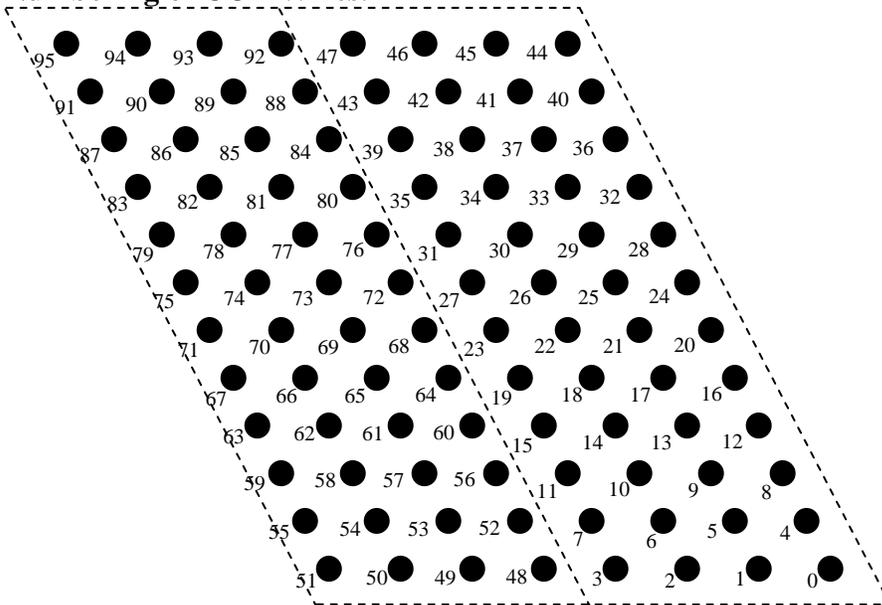


Drawing from Brian Winer's 2003 spec

**Numbering of COT Wires.**



Tables from T. Shaw document.

**Data Fiber # 1 carries information from TDC wires 0-47**

<b>Data Word</b>	<b>Beam_Zero Marker 1 bit</b>	<b>Word Zero Flag 1 bit</b>	<b>Group Identifies 2 bits</b>	<b>Wire data time slice (0-5) 12 bits</b>
1	beam_zero	1	00	t0 (0-11)
2	beam_zero	1	01	t0 (12-23)
3	beam_zero	1	10	t0 (24-35)
4	beam_zero	1	11	t0 (36-47)
5	beam_zero	0	00	t1 (0-11)
6	beam_zero	0	01	t1 (12-23)
7	beam_zero	0	10	t1 (24-35)
8	beam_zero	0	11	t1 (36-47)
9	beam_zero	0	00	t2 (0-11)
10	beam_zero	0	01	t2 (12-23)
11	beam_zero	0	10	t2 (24-35)
12	beam_zero	0	11	t2 (36-47)
13	beam_zero	0	00	t3 (0-11)
14	beam_zero	0	01	t3 (12-23)
15	beam_zero	0	10	t3 (24-35)
16	beam_zero	0	11	t3 (36-47)
17	beam_zero	0	00	t4 (0-11)
18	beam_zero	0	01	t4 (12-23)
19	beam_zero	0	10	t4 (24-35)
20	beam_zero	0	11	t4 (36-47)
21	beam_zero	0	00	t5 (0-11)
22	beam_zero	0	01	t5 (12-23)
23	beam_zero	0	10	t5 (24-35)
24	beam_zero	0	11	t5 (36-47)

**Data Fiber # 2 carries information from TDC wires 48-95**

<b>Data Word</b>	<b>Beam_Zero Marker 1 bit</b>	<b>Word Zero Flag 1 bit</b>	<b>Group Identifies 2 bits</b>	<b>Wire data time slice (0-5) 12 bits</b>
1	beam_zero	1	00	t0 (48-59)
2	beam_zero	1	01	t0 (60-71)
3	beam_zero	1	10	t0 (72-83)
4	beam_zero	1	11	t0 (84-95)
5	beam_zero	0	00	t1 (48-59)
6	beam_zero	0	01	t1 (60-71)
7	beam_zero	0	10	t1 (72-83)
8	beam_zero	0	11	t1 (84-95)
9	beam_zero	0	00	t2 (48-59)
10	beam_zero	0	01	t2 (60-71)
11	beam_zero	0	10	t2 (72-83)
12	beam_zero	0	11	t2 (84-95)
13	beam_zero	0	00	t3 (48-59)
14	beam_zero	0	01	t3 (60-71)
15	beam_zero	0	10	t3 (72-83)
16	beam_zero	0	11	t3 (84-95)
17	beam_zero	0	00	t4 (48-59)
18	beam_zero	0	01	t4 (60-71)
19	beam_zero	0	10	t4 (72-83)
20	beam_zero	0	11	t4 (84-95)
21	beam_zero	0	00	t5 (48-59)
22	beam_zero	0	01	t5 (60-71)
23	beam_zero	0	10	t5 (72-83)
24	beam_zero	0	11	t5 (84-95)

## Ben's new Wire mask names.

```
% wire names %
w0_T2C1      : NODE;
w0_T0C1      : NODE;
w0_T3C1      : NODE;
w0_T4C1      : NODE;
w0_T1C1      : NODE;
w0_T4C2      : NODE;
w0_T3C2      : NODE;
w0_T2C2      : NODE;
w0_T1C2      : NODE;
w0_T0C2      : NODE;
w1_T4C1      : NODE;
w1_T2C1      : NODE;
w1_T3C1      : NODE;
w1_T1C1      : NODE;
w1_T0C1      : NODE;
w1_T4C0      : NODE;
w1_T4C2      : NODE;
w1_T5C0      : NODE;
w1_T5C1      : NODE;
w1_T3C2      : NODE;
w1_T2C2      : NODE;
w2_T3C0      : NODE;
w2_T4C0      : NODE;
w2_T0C1      : NODE;
w2_T3C1      : NODE;
w2_T4C1      : NODE;
w2_T1C1      : NODE;
w2_T2C1      : NODE;
w2_T2C0      : NODE;
w2_T5C1      : NODE;
w2_T5C0      : NODE;
w3_T1C0      : NODE;
w3_T2C0      : NODE;
w3_T2C1      : NODE;
w3_T3C0      : NODE;
w3_T4C0      : NODE;
w3_T1C1      : NODE;
w3_T3C1      : NODE;
w3_T0C1      : NODE;
w3_T4C1      : NODE;
w3_T0C0      : NODE;
w3_T5C1      : NODE;
w3_T5C0      : NODE;
w4_T1C0      : NODE;
w4_T0C0      : NODE;
w4_T4C1      : NODE;
w4_T2C0      : NODE;
w4_T3C1      : NODE;
w4_T4C0      : NODE;
w4_T3C0      : NODE;
w4_T2C1      : NODE;
w4_T5C1      : NODE;
w4_T1C1      : NODE;
w4_T5C0      : NODE;
w5_T3C0      : NODE;
w5_T2C0      : NODE;
w5_T4C0      : NODE;
w5_T1C0      : NODE;
w5_T0C0      : NODE;
w5_T4C1      : NODE;
w5_T5C0      : NODE;
w5_T5C1      : NODE;
w5_T3C1      : NODE;
w6_T4Cn1     : NODE;
w6_T4C0      : NODE;
w6_T1C0      : NODE;
w6_T2C0      : NODE;
w6_T3C0      : NODE;
w6_T5C0      : NODE;
w6_T0C0      : NODE;
w6_T5Cn1     : NODE;
w6_T3Cn1     : NODE;
w7_T2Cn1     : NODE;
w7_T3Cn1     : NODE;
w7_T1C0      : NODE;
w7_T0C0      : NODE;
w7_T4Cn1     : NODE;
w7_T4C0      : NODE;
w7_T5C0      : NODE;
w7_T2C0      : NODE;
w7_T5Cn1     : NODE;
w7_T3C0      : NODE;
w7_T1Cn1     : NODE;
w8_T1Cn1     : NODE;
w8_T0Cn1     : NODE;
w8_T2C0      : NODE;
w8_T2Cn1     : NODE;
w8_T3Cn1     : NODE;
w8_T3C0      : NODE;
w8_T1C0      : NODE;
w8_T5C0      : NODE;
w8_T4C0      : NODE;
w8_T4Cn1     : NODE;
w8_T0C0      : NODE;
w8_T5Cn1     : NODE;
w9_T3Cn1     : NODE;
w9_T2Cn1     : NODE;
w9_T4C0      : NODE;
w9_T0Cn1     : NODE;
w9_T1Cn1     : NODE;
w9_T4Cn1     : NODE;
w9_T3C0      : NODE;
w9_T5C0      : NODE;
w9_T5Cn1     : NODE;
w9_T2C0      : NODE;
w10_T5Cn2    : NODE;
w10_T4Cn1    : NODE;
w10_T3Cn1    : NODE;
w10_T2Cn1    : NODE;
w10_T1Cn1    : NODE;
w10_T0Cn1    : NODE;
w10_T4C0     : NODE;
w10_T5C0     : NODE;
w10_T5Cn1    : NODE;
w10_T4Cn2    : NODE;
w10_T3C0     : NODE;
w10_T3Cn2    : NODE;
w10_T2Cn2    : NODE;
w11_T3Cn2    : NODE;
w11_T4Cn2    : NODE;
w11_T2Cn1    : NODE;
w11_T4Cn1    : NODE;
w11_T3Cn1    : NODE;
w11_T0Cn1    : NODE;
w11_T1Cn1    : NODE;
w11_T5Cn2    : NODE;
w11_T5Cn1    : NODE;
w11_T2Cn2    : NODE;
w11_T1Cn2    : NODE;
w11_T0Cn2    : NODE;
```

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