

Pulsar Firmware for L2 trigger upgrade

Appendix 3: Statemachine diagrams

List of statemachine diagrams

Figure 1: Muon DataIO Statemachine 1 (8-bit input to 32-bit words)	2
Figure 2: Muon DataIO Statemachine 2 (DAQ RAM writing)	3
Figure 3: Muon DataIO Statemachine 3 (input merging)	4
Figure 4: ShowerMax DataIO Statemachine 1 (8-bit input to 32-bit words).....	5
Figure 5: ShowerMax DataIO Statemachine 2 (DAQ RAM writing)	6
Figure 6: ShowerMax DataIO Statemachine 3 (input merging)	7
Figure 7: XFT DataIO Statemachine 1 (16-bit input to 32-bit words, DAQ RAM writing)	8
Figure 8: XFT DataIO Statemachine 2 (input merging)	9
Figure 9: TS Handshake statemachine	10

Muon

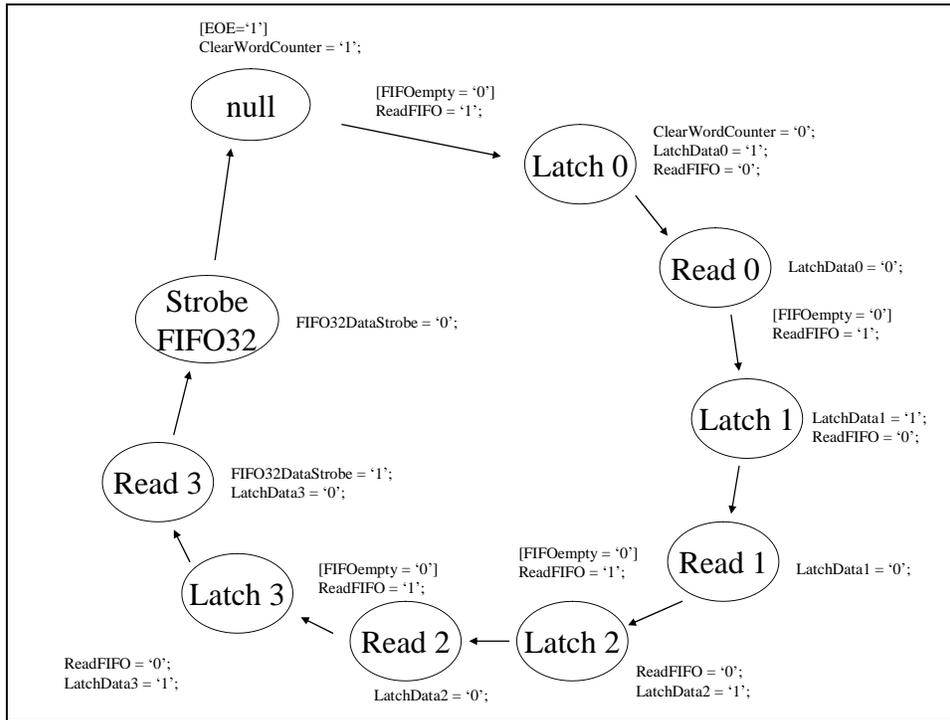


Figure 1: Muon DataIO Statemachine 1 (8-bit input to 32-bit words)

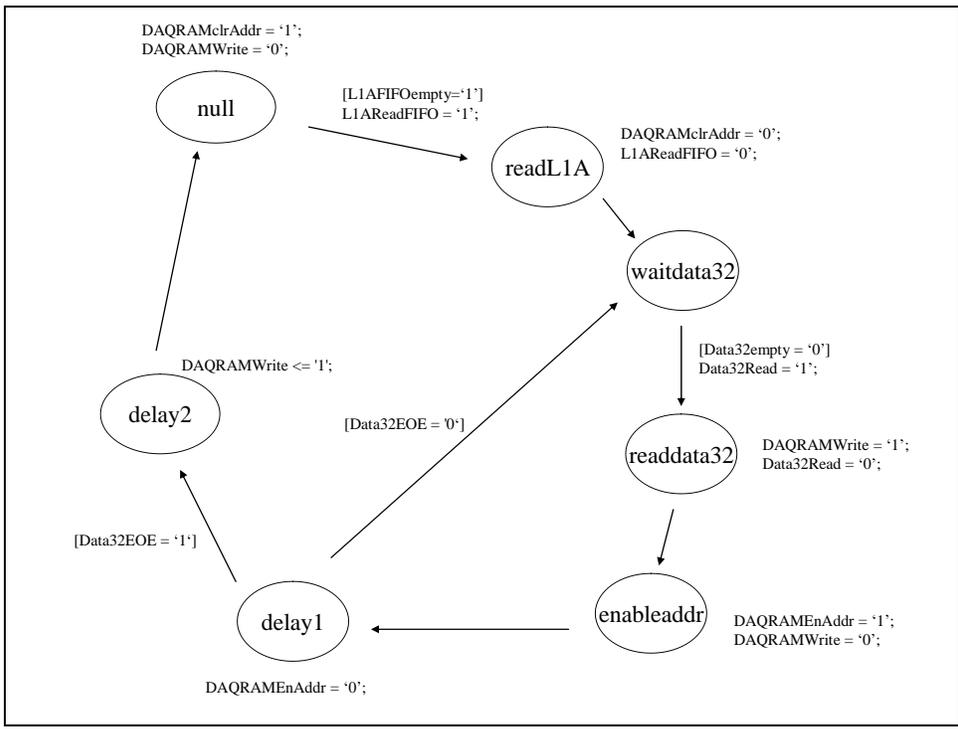


Figure 2: Muon DataIO State machine 2 (DAQ RAM writing)

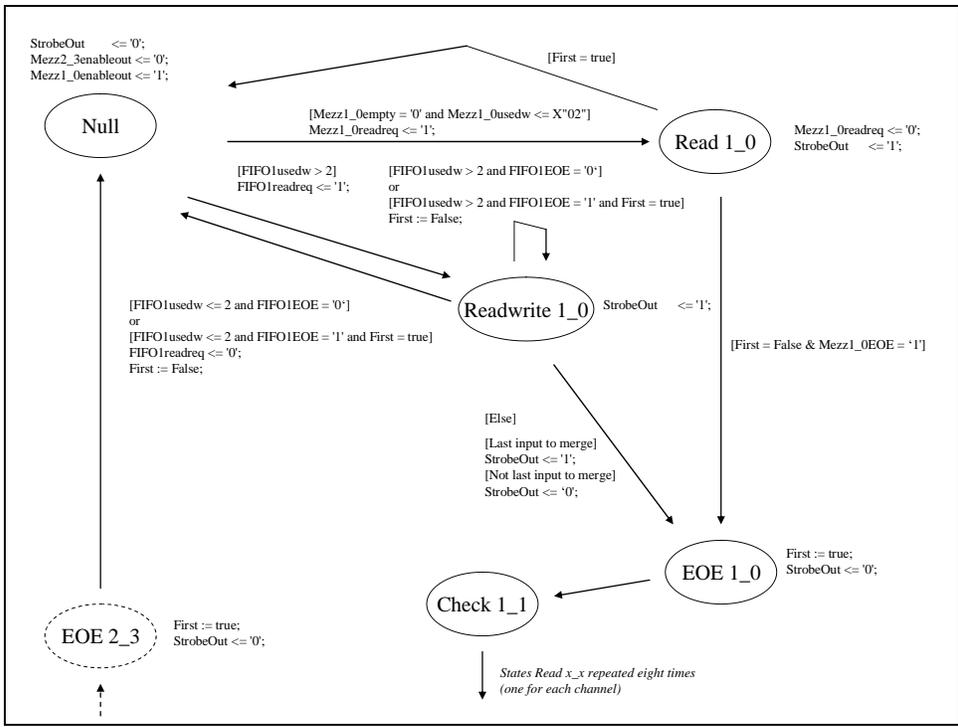


Figure 3: Muon DataIO State machine 3 (input merging)

ShowerMax

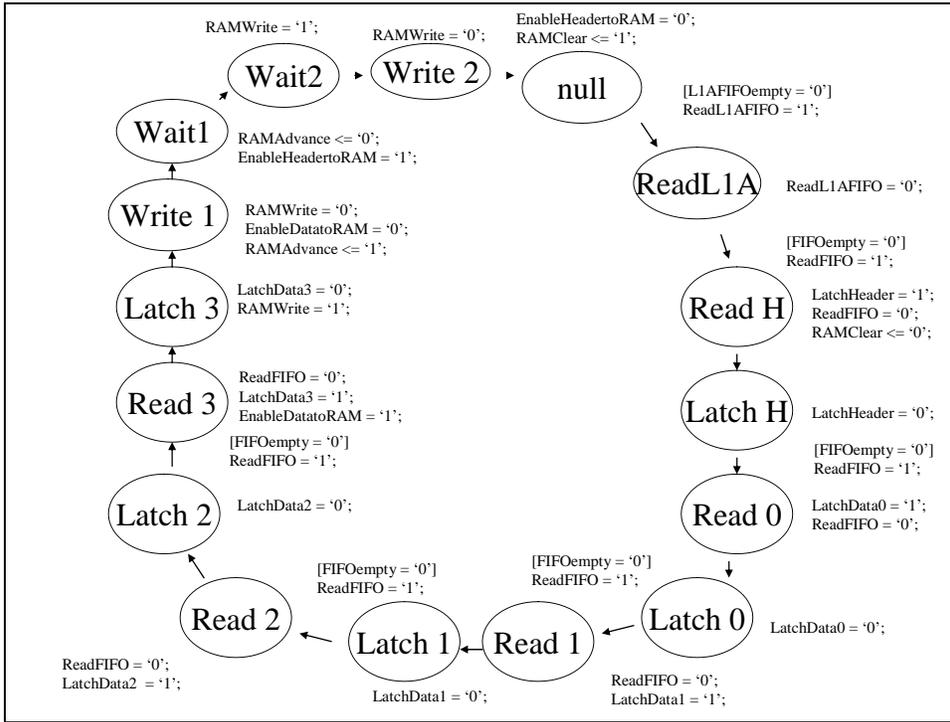


Figure 4: ShowerMax DataIO State machine 1 (8-bit input to 32-bit words)

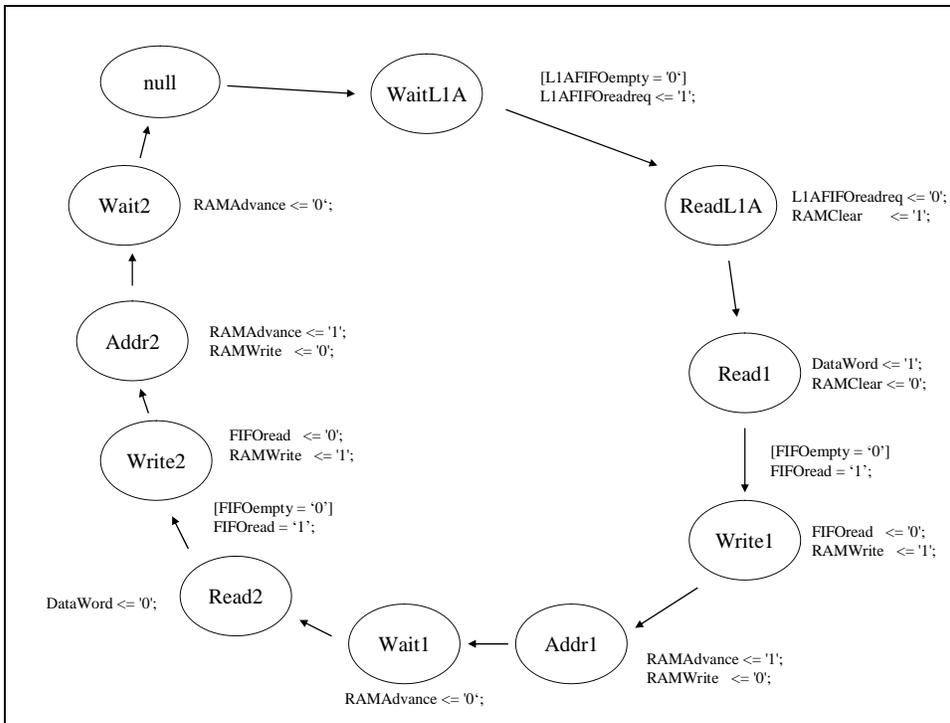


Figure 5: ShowerMax DataIO State machine 2 (DAQ RAM writing)

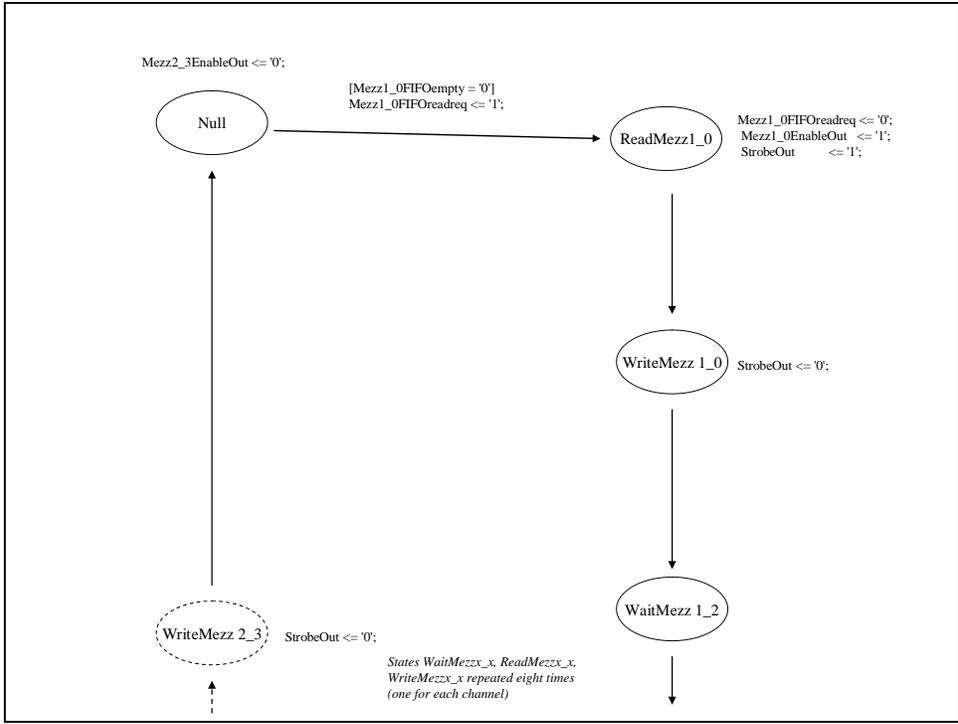


Figure 6: ShowerMax DataIO State machine 3 (input merging)

XFT

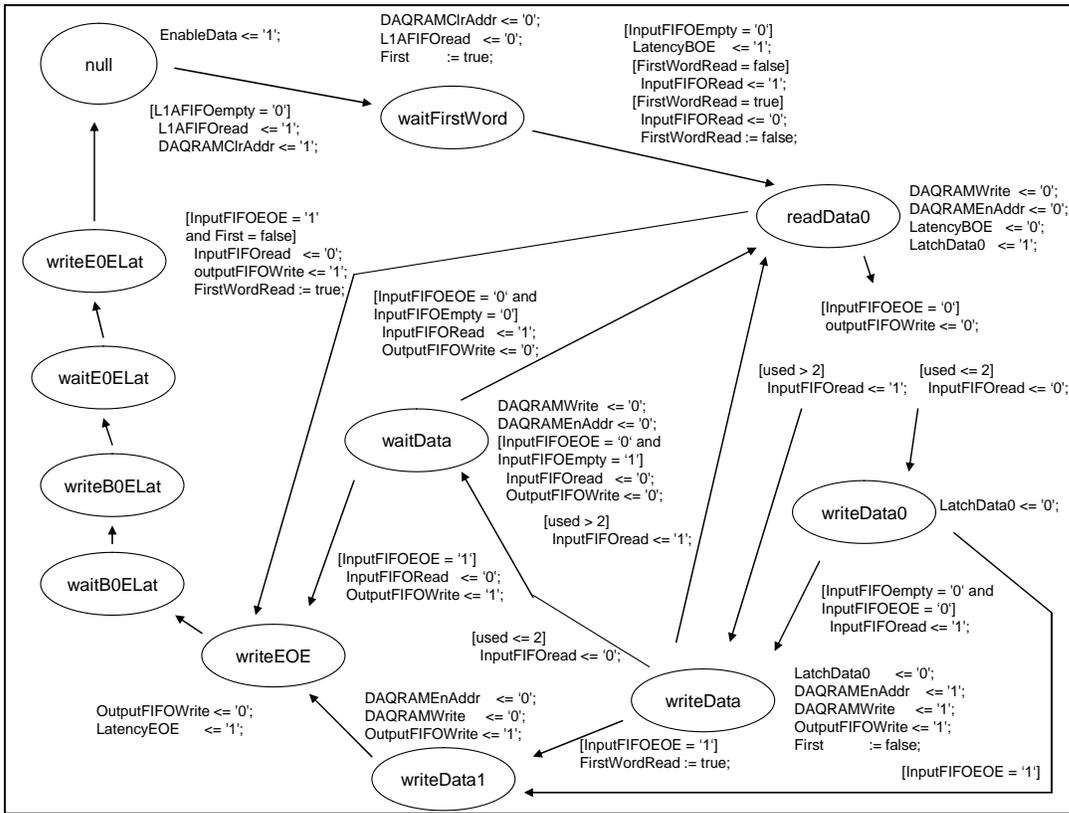


Figure 7: XFT DataIO State machine 1 (16-bit input to 32-bit words, DAQ RAM writing)

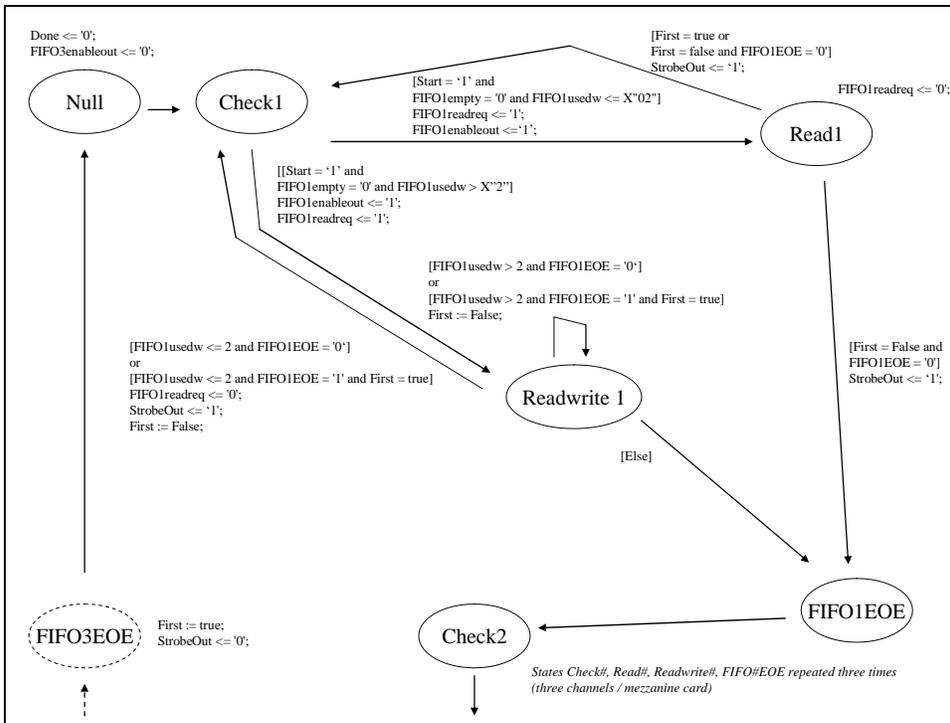


Figure 8: XFT DataIO State machine 2 (input merging)

L2toTS

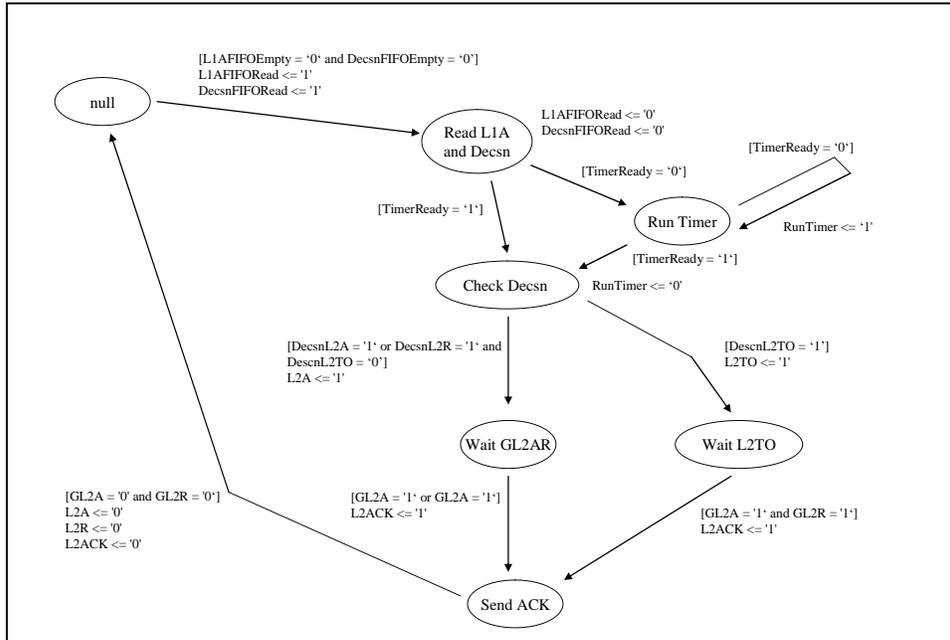


Figure 9: TS Handshake statemachine

This document was created with Win2PDF available at <http://www.daneprairie.com>.
The unregistered version of Win2PDF is for evaluation or non-commercial use only.