

Substrate-Induced Photofield Effect in Graphene Phototransistors

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Abstract—A single atomic layer of graphene, integrated onto an undoped bulk substrate in a back-gated transistor configuration, demonstrates surprising strong photoconduction, and yet, the physical origin of the photoresponse is not fully understood. Here, we use a detailed computational model to demonstrate that the photoconductivity arises from the electrostatic doping of graphene, induced by the surface accumulation of photogenerated carriers at the graphene/substrate interface. The accumulated charge density depends strongly on the rate of charge transfer between the substrate and the graphene; the suppression of the transfer rate below that of carrier's thermal velocity is an essential prerequisite for a substantial photoinduced doping in the graphene channel under this mechanism. The contact-to-graphene coupling (defined by the ratio of graphene-metal contact capacitance to graphene's quantum capacitance) determines the magnitude of photoinduced doping in graphene at the source/drain contacts. High-performance graphene phototransistors would, therefore, require careful engineering of the graphene-substrate interface and optimization of graphene-metal contacts.

Index Terms—Electrostatic doping, graphene, photodetector, phototransistor.

I. INTRODUCTION

GRAPHENE is composed of a single layer of carbon atoms, which are arranged in a hexagonal crystalline form. The material possesses remarkable electrical, optical, and mechanical properties, making it attractive for a broad spectrum of nanoelectronic and photonic devices [1]–[3]. Ever since the first experimental demonstration of monolayer graphene in 2004 [4], it has found innovative applications in a variety of devices appropriate for microprocessors, displays, sensors, and optoelectronics/photronics [5]–[8]. Among these, graphene-based photodetectors and phototransistors have recently attracted considerable attention as broadband, high-performance, highly reliable, and (potentially) low-cost alternatives to traditional devices. A variety of device

configurations have been explored: some configurations offer ultrahigh-speed switching, while others target highly sensitive response [8]–[15]. In general, the device has a back-gated FET structure, in which graphene forms the transport layer on the top of either a semiconductor substrate or a dielectric.

Based on the performance parameters and the dominant physical mechanism, graphene-based photodetectors could broadly be divided into two categories: 1) Type-I devices rely on photoabsorption in graphene and the photocurrent is attributed to the combination of photovoltaic, photothermoelectric, and bolometric responses of graphene itself. The substrates in these devices do not contribute to the photoresponse. These devices have demonstrated a very high speed (in the range of gigahertz); their photoresponsivity has, however, been relatively low (in the range of milliamperes/watt) [9]. A number of design approaches are being explored to improve the photoresponsivity of Type-I photodetectors, e.g., by integrating various innovative receptors, such as plasmonic nanostructures, quantum dots, microcavities, and trap centers, and through band engineering [13], [16]–[20]. 2) The second configuration (Type-II) of graphene-based photodetection involves a phototransistor that is composed of a graphene channel integrated on the top of an undoped semiconductor substrate. In these devices, the undoped semiconductor substrate acts as the light absorber, and the photogenerated charge carriers in the substrate (rather than in graphene) play the dominant role in photodetection [21]–[24]. These devices have shown a very high photoresponsivity up to few amperes per watt. The photodetection mechanism in Type-II devices has been attributed to the conductivity modulation of graphene as the photogenerated carriers in the substrate modulate the electric field at the graphene/substrate interface [21], [23], [24]. Such a photodetection mechanism has not been treated by rigorous, self-consistent theoretical models; as a result, the principle of device operation and the origin of extremely high photoconduction are not fully understood.

Here, we present a computational model that explains the essential physics of a series of experimental observations and develops an intuitive understanding of the device operation. In particular, we address the following key questions in Type-II graphene phototransistors.

- 1) What is the origin of surprisingly high photoresponse?

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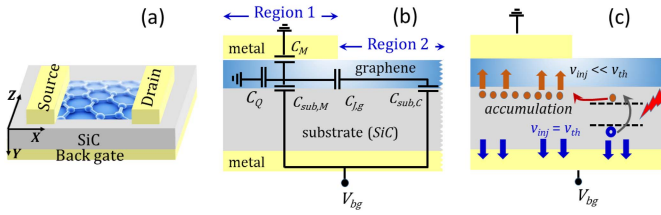


Fig. 1. (a) Graphene field-effect phototransistor. A single-layer graphene forms the channel between the source/drain metal contacts. An undoped semiconductor substrate (SiC in our model) acts as the photoabsorber, which modulates graphene conductivity through electrostatic coupling. A back-gate voltage (V_{bg}) provides an electric field in the substrate for photogenerated charge separation. (b) Capacitance model for the graphene phototransistor. The cross section for one-half of the device is shown. (c) Photogenerated carrier transport. A drift-diffusion model describes the carrier transport in the substrate, where the carriers are injected into the graphene with velocity (v_{inj}).

- 2) What material/structural knobs are available to optimize the photoresponse?
- 3) How does the photoresponse depend on the incident light intensity?
- 4) What is the origin of symmetrical vs. asymmetrical response of the ambipolar photoconduction?

The remainder of this paper is organized as follows. Section II presents our theoretical approach. The results are discussed in Section III. The conclusions are provided in Section IV.

II. THEORETICAL APPROACH

A. Model System

An illustration of the investigated device is shown in Fig. 1(a). The structure is a field-effect phototransistor, in which graphene forms the channel that is back gated through an intrinsic (undoped) silicon carbide (6H-SiC) substrate and is contacted at the top by source/drain metal. The model is independent from any specific process/deposition method of graphene. The interfacial properties (such as the binding energy) of graphene with the substrate/metal, which might be process dependent, are treated in a generalized approach. To elaborate our model system, we divide the device cross section into two regions, as highlighted in Fig. 1(b). Region 1 defines the cross section under the metal contacts, whereas the cross section under the channel is defined by Region 2. Under illumination, we anticipate uniform photogeneration throughout the device.

B. Electrostatic Model

A quantitative model for electrostatics is implemented by the numerical solution of 2-D Poisson equation [see (A1) in the Appendix], which includes the effect of applied voltages and the photogenerated charges in the substrate. A qualitative understanding can, however, be developed using a simple capacitance model, as shown in Fig. 1(b). The device cross section highlights various capacitances that could affect the electrostatics of graphene. $C_{sub,M}$ and $C_{sub,C}$ define the graphene-to-substrate capacitances in Region 1 and Region 2, respectively. The capacitance C_Q represents the quantum

capacitance in graphene, and the capacitance $C_{J,g}$ represents the junction capacitance between Region 1 and Region 2 in graphene if the regions differ in photoinduced electrostatic doping. The electrostatic coupling of graphene with the metal at the source/drain contacts is represented by C_M . The nature of metal-graphene interfacial chemical bonding, especially for different types of deposition conditions, is not fully understood. If the bonding is weak and the contact is nonohmic, as suggested in [25] and [26], then the interface can be modeled by a thin interfacial dielectric layer between the metal and the underlying graphene, resulting in a finite C_M . In this formulation, an ohmic contact between the graphene-metal will be described by $C_M \rightarrow \infty$. Note that the electrostatic doping of graphene in Region 1 depends on the relative magnitude of C_M and C_Q : with respect to this, there could be two limiting scenarios: 1) For $C_M \rightarrow \infty$, the graphene's potential, i.e., Dirac point energy (E_D) in Region 1 would be completely controlled by the potential of the metal contact, and electrostatic doping of Region 1 would be impossible. 2) If, on the other hand, an interfacial dielectric barrier exists between the metal and the graphene, the limiting case could be $C_M \ll C_Q$. E_D for graphene in Region 1 under this limit would experience the maximum effect of quantum capacitance provided that there is a substantial field effect induced by the density of photogenerated carriers at the SiC surface under the source/drain contacts. Away from the source/drain contacts in Region 2, the electrostatic effects of the contacts are fully screened; therefore, E_D floats with the surface potential of the substrate (here SiC). The photoinduced doping of Region 2, therefore, depends on the modulation of the surface potential in SiC underneath the channel.

C. Transport Model

To model the charge transport in graphene-SiC field-effect phototransistor, we consider two distinct components of the photogenerated carrier transport, as shown in Fig. 1(c). The first component is the semiclassical transport of photogenerated carriers in SiC which requires the solution of the drift-diffusion equation. The second component is the quantum mechanical charge transfer from the SiC into the graphene. To incorporate both of these transport components in a single computational model, we have adopted a phenomenological approach, in which graphene is treated as an electrode and the carrier transfer flux at the graphene/SiC interface provides a boundary condition to the solution of the drift-diffusion transport in SiC [see (A2) and (A3) in the Appendix]. To model the quantum mechanical carrier transfer flux between the SiC and the graphene, we calculate the velocity (v_{inj}) of carrier injection from the SiC into the graphene that depends on the rate of carrier transfer at the SiC/graphene interface. For a standard metal/semiconductor Schottky contact, v_{inj} is approximately equal to the carrier's thermal velocity (v_{th}) which depends only on the temperature and bandstructure of the semiconductor. Recent experimental data [24] on graphene phototransistors integrated on the SiC substrate have, however, indicated the presence of a thin

TABLE I
CALCULATION OF INJECTION VELOCITY *

t_{ox} (Å)	$v_{th,elec}$ (cm/s)	$v_{th,hole}$ (cm/s)	ϕ_{elec} (eV)	ϕ_{hole} (eV)	$v_{inj,elec}$ (cm/s)	$v_{inj,hole}$ (cm/s)
1	1.5×10^3	1.2×10^3	2.4	3.5	3.2×10^2	1.8×10^2
3	1.5×10^3	1.2×10^3	2.4	3.5	1.4×10^1	4.4×10^0
5	1.5×10^3	1.2×10^3	2.4	3.5	6.5×10^{-1}	1.0×10^{-1}
7	1.5×10^3	1.2×10^3	2.4	3.5	2.9×10^{-2}	2.5×10^{-3}
10	1.5×10^3	1.2×10^3	2.4	3.5	2.8×10^{-4}	9.0×10^{-6}

* t_{ox} refers to SiO₂ thickness between graphene and SiC, $v_{th,elec}$ ($v_{th,hole}$) are thermal velocities in SiC, ϕ_{elec} (ϕ_{hole}) are energy barrier height at SiC/SiO₂ interface, $v_{inj,elec}$ ($v_{inj,hole}$) are the injection velocity at graphene/SiC interface for electrons (holes).

interfacial barrier at the graphene/SiC interface. It has been reported that a native oxide layer often forms naturally on the SiC surface [24], [27], [28] that could act as an energy barrier to the carriers at the graphene/SiC interface. An effective energy barrier at the interface may also exist due to a relatively weak chemical bonding between the graphene and the SiC as compared with a bulk Schottky junction [26]. The presence of an energy barrier at the graphene/SiC interface results in $v_{inj} \ll v_{th}$, which now becomes a function of thickness and height of the interfacial barrier. An estimate of v_{inj} in the presence of an interfacial oxide could be calculated analytically using Wentzel–Kramers–Brillouin approximation for tunneling through a rectangular energy barrier [29]

$$v_{inj} = v_{th} e^{-\beta \sqrt{\phi} t_{ox}} \quad (1)$$

where t_{ox} is the interfacial barrier thickness and given in Å, β is the tunneling constant, and ϕ is the energy barrier height in electron volts. The calculated v_{inj} from (1), assuming a thin SiO₂ layer at the graphene/SiC interface, is tabulated in the Appendix (Table I). A rigorous approach to model the charge transport through the semiconductor/graphene interface would require quantum mechanical models, e.g., the nonequilibrium Green function formalism [30], which are beyond the scope of this paper. Our approach is nevertheless appropriate for the phenomenological understanding of the physics of conductivity modulation in graphene and for qualitatively defining a design space for graphene-based phototransistors.

III. RESULTS AND DISCUSSION

A. Origin of High Photoresponsivity

Fig. 2 qualitatively shows the phenomenon of the photoinduced doping of graphene due to the field effect from the substrate. The energy band diagrams drawn at the SiC/graphene interface along the direction perpendicular to the channel in Region 1 with the illustrated surface charge density in SiC are shown for various limits of v_{inj} under dark and illumination. When the carriers are photogenerated in SiC under illumination, holes drift toward the source/drain contacts due to the applied gate voltage ($V_{bg} = 20$ V). For the

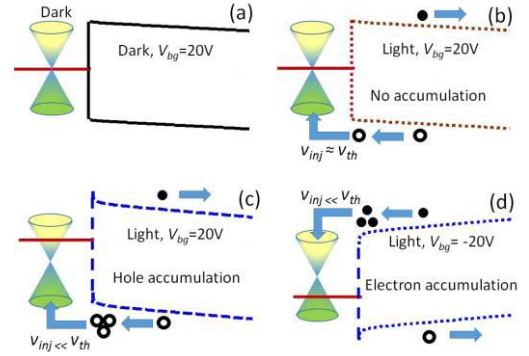


Fig. 2. Energy band diagram near the graphene/substrate interface in Region 1 (a) in dark and (b)–(d) under illumination. No electrostatic doping in graphene is observed in dark. In (b), photogenerated carriers do not accumulate at the graphene/SiC interface, since $v_{inj} \approx v_{th}$, and hence no electrostatic doping in graphene. In (c) and (d), photogenerated holes and electrons, respectively, accumulate near the graphene/SiC interface (underneath the energy barrier, not drawn for simplicity) due to $v_{inj} \ll v_{th}$ for $V_{bg} = 20$ V and $V_{bg} = -20$ V, respectively. Graphene is doped n-type in (c) and p-type in (d), and $C_M \ll C_Q$ is assumed.

case when no interfacial barrier exists at the graphene/SiC interface, $v_{inj} \approx v_{th}$, and there is no accumulation of holes at the interface. Under this situation, there is no significant space charge present at the graphene/SiC interface, and hence there is negligible substrate-induced electrostatic effect in the graphene, as shown in Fig. 2(b). In the presence of an energy barrier at the interface ($v_{inj} \ll v_{th}$), the drift flux of photogenerated carriers flowing from the substrate results in charge accumulation near the SiC surface (underneath the barrier) in Region 1. The magnitude of the surface accumulation density in Region 1 develops such that it maintains the self-consistency between the steady-state photogenerated carrier flux drifting from the bulk and the surface carrier flux being injected into the metal contacts. This accumulated space charge near the graphene/SiC interface (more precisely at the interface between the SiC and the energy barrier which is not drawn for simplicity) could be significant enough to electrostatically dope graphene, as shown in Fig. 2(c) and (d).

The net charge concentration at the SiC surface (underneath graphene) along the channel direction (X) and along the direction (y) perpendicular to the channel (into the SiC) obtained from the numerical simulations are shown in Fig. 3(a) and (b), respectively, for $v_{inj} = 10^{-3}$ cm/s, both under dark and illumination. Assuming an SiO₂ barrier, $v_{inj} = 10^{-3}$ cm/s corresponds to $t_{ox} \approx 7$ Å (see Table I) and is much smaller than $v_{th} \approx 10^3$ cm/s for carriers in intrinsic SiC [31]. The maximum charge density under illumination is found to be at the SiC surface in Region 1 due to the surface accumulation of the photogenerated carriers that drift from the substrate toward the contact. The photogenerated charge density decreases exponentially while going away from the SiC surface in Region 1, both laterally into Region 2 [Fig. 3(a) (solid line)] and perpendicularly into the SiC thickness in Region 1 [Fig. 3(b) (dashed line)]. It should be noted that for $V_{bg} = 0$ V, there is no surface accumulation, since the charge carriers generated in the field-free substrate recombine before reaching to the SiC

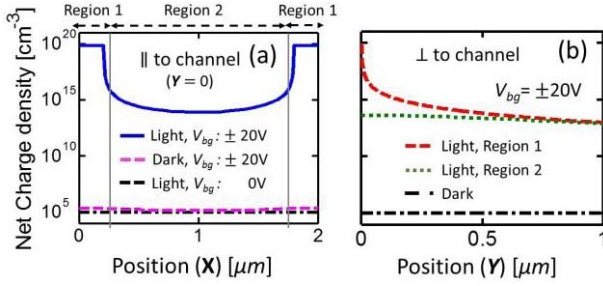


Fig. 3. Net charge density at the SiC surface underneath the energy barrier below the graphene/SiC interface along (a) channel direction and (b) direction perpendicular to the channel in dark and under illumination for $v_{inj} = 10^{-3}$ cm/s. The photogenerated carriers accumulate at the SiC surface in Region 1 for $v_{inj} \ll v_{th}$ and $V_{bg} = \pm 20$ V under illumination. For $V_{bg} = 0$ V and in dark, no photogenerated charge accumulation occurs. Vertical solid line in (a): boundaries of Regions 1 and Region 2. The back gate is at $Y = 500 \mu\text{m}$ and the incident light power density (P_{inc}) = 18 mW/cm².

surface [Fig. 3(a) (dashed line)]. As a result, the charge density for this case remains equal to that in the dark [Fig. 3(a) and (b) (dashed-dotted lines)]. In Region 2, the density of photogenerated carriers at the SiC surface [Fig. 3(b) (dotted line)] is significantly lower than that in Region 1 (carrier accumulation in Region 2 is insignificant), since the lateral flow of the drift current toward the source/drain contacts prevents any substantial accumulation of carriers in Region 2.

The photogenerated carrier accumulation at the SiC surface due to weak injection velocity of carriers into the source/drain contacts self-consistently enhances the lateral electric field and the potential drop between Region 1 and Region 2 along the SiC surface. The polarity and the magnitude of this modulation in the surface potential depend on the polarity (*n* or *p*) and the density of the accumulated charge carriers at the surface, respectively. The lateral modulation of the surface potential in Region 2 results in an electrostatic doping of graphene in the channel region, as shown in Fig. 4, for both the dark and under illumination. Under dark or at zero-gate voltage ($V_{bg} = 0$ V), no electrostatic doping is observed, as expected [Fig. 4(a) and (b) respectively]. For a finite-gate voltage ($V_{bg} = \pm 20$ V) and under illumination, only the channel region is electrostatically doped if $C_M \gg C_Q$ [Fig. 4(c) and (d)], whereas both the channel and the source/drain regions are electrostatically doped if $C_M \ll C_Q$ [Fig. 4(e) and (f)].

B. Dependence of Photoresponse on Device Parameters

Physical properties of the device, in particular, the interface of graphene with the source/drain metal and the substrate, which influence C_M and v_{inj} , respectively, strongly affect the photoresponse. The effect of relative magnitudes of C_Q and C_M on $E_D - E_F$ for graphene under the source/drain metal (Region 1) and in the channel (Region 2) are shown in Fig. 5(a) for $V_{bg} = \pm 20$ V, $v_{inj} = 10^{-3}$ cm/s, and incident power density of illumination (P_{inc}) of 18 mW/cm². The accumulated density of the photogenerated carriers at the SiC surface remains essentially unchanged with the modulation of C_M/C_Q , however the electrostatic doping of graphene in Region 1 and Region 2 varies with the magnitude of C_M/C_Q .

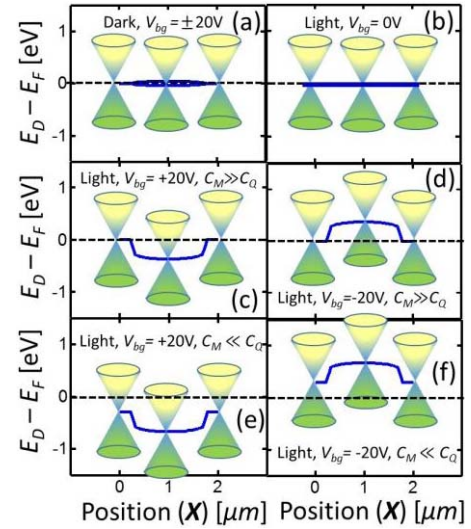


Fig. 4. (a)–(f) Substrate-induced electrostatic shift in Dirac energy (E_D) versus Fermi level (E_F) of graphene along the channel. v_{inj} is kept equal to 10^{-3} cm/s in (a)–(f). For (a) dark, an insignificant shift in ($E_D - E_F$) is due to the small electrostatic effect of V_{bg} on graphene. (b) Under illumination at $V_{bg} = 0$ V, photogenerated carriers recombine before they could reach to the contacts. Holes and electrons accumulate at the SiC surface in Region 1 under illumination for (c) and (e) $V_{bg} = +20$ V and (d) and (f) $V_{bg} = -20$ V, respectively. For (c) and (d) $C_M \gg C_Q$, E_D in graphene under metal remains fixed at the potential at the metal contact. The surface carrier accumulation in Region 1 electrostatically doped graphene channel (c) *n*-type for $V_{bg} = +20$ V and (d) *p*-type for $V_{bg} = -20$ V. For (e) and (f) $C_M \ll C_Q$, graphene in Region 1 also gets electrostatically doped.

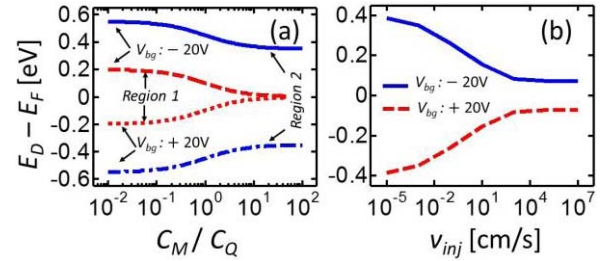


Fig. 5. (a) Shift in E_D for graphene under metal (Region 1) and in the channel (Region 2) as a function of C_M/C_Q for $V_{bg} = \pm 20$ V, $v_{inj} = 10^{-3}$ cm/s, and $P_{inc} = 18$ mW/cm². For $C_M/C_Q \ll 1$, the shift in E_D is maximum, which gradually reduces as C_M/C_Q is increased. For $C_M/C_Q \gg 1$, there is essentially no shift in E_D in Region 1, since the potential is completely controlled by the voltage of the metal contact. (b) Shift in E_D for graphene in the channel region as a function of v_{inj} for $V_{bg} = \pm 20$ V, $P_{inc} = 18$ mW/cm², and $C_M/C_Q \gg 1$. The shift in E_D increases for $v_{inj} < 10^3$ cm/s due to the increase in the accumulation of photogenerated carriers at the SiC surface. For $v_{inj} > 10^3$ cm/s, the effect of photoinduced doping is negligible, and the residual small shift in E_D is due to the effect of V_{bg} .

The two limiting cases, i.e., $C_M/C_Q \ll 1$ and $C_M/C_Q \gg 1$, correspond to the maximum possible electrostatic doping and the minimum doping in the graphene, respectively, for given values of P_{inc} and v_{inj} . When $C_M/C_Q \ll 1$, E_D in Region 1 is relatively independent of the potential of the source/drain metal and the photogenerated carrier accumulation at the SiC surface in Region 1 induces an electrostatic doping in the graphene under the source/drain metal through the effect of quantum capacitance ($E_D - E_F = qN_{int}/C_Q$), where N_{int} is the integrated charge density at the SiC surface in Region 1.

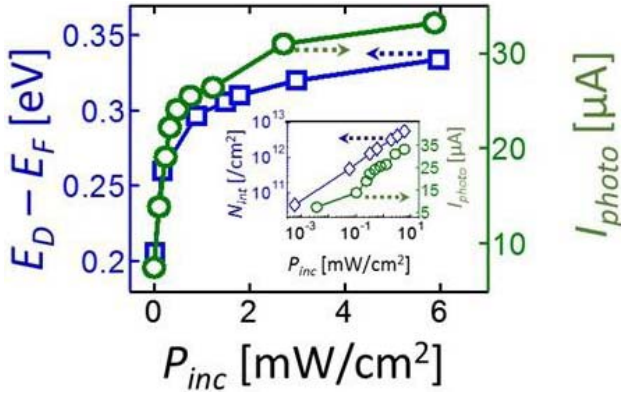


Fig. 6. Shift in E_D for graphene in the channel region as a function of P_{inc} for $v_{inj} = 10^{-3}$ cm/s, $V_{bg} = 20$ V, and $C_M/C_Q \gg 1$. The experimental photocurrent (I_{photo}) versus P_{inc} taken from [24] follows a similar trend. Inset: dependence of experimental I_{photo} and the surface charge density in SiC (integrated over the y -direction) on $\log(P_{inc})$.

For $C_M/C_Q \gg 1$, there is essentially no shift in E_D in Region 1, since the graphene potential in Region 1 in this case is completely controlled by the voltage of the source/drain metal contacts. It should be noted that the shift in E_D in Region 1 as a function of C_M/C_Q also modulates the graphene potential in Region 2, as shown in Fig. 5(a).

Fig. 5(b) shows the effect of v_{inj} on electrostatic doping of graphene in the channel for $V_{bg} = \pm 20$ V, $C_M/C_Q \gg 1$, and a fixed incident power ($P_{inc} = 18$ mW/cm²). As v_{inj} increases, charge accumulation density at the surface reduces, which lowers the electrostatic doping (smaller $E_D - E_F$). It is worth noting that $E_D - E_F$ starts showing a significant increase as v_{inj} is reduced below 10^3 cm/s. As shown in Table A1, this corresponds to the presence of an ultrathin energy barrier between the surface of SiC and graphene. For $v_{inj} > 10^3$ cm/s, the effect of photoinduced doping is negligible. The small shift in E_D observed for $v_{inj} > 10^3$ cm/s in Fig. 5(b) is due to the residual effect of V_{bg} , which remains independent of v_{inj} .

C. Dependence of Photoresponse on Input Light Intensity

The dependence of graphene's electrostatic doping in the channel on P_{inc} is shown in Fig. 6 at $V_{bg} = 20$ V and $v_{inj} = 10^{-3}$ cm/s. The photoresponse is maximum at lower P_{inc} and gradually flattens out at higher P_{inc} . This trend is qualitatively similar to the experimental observation of photocurrent (I_{photo}) versus P_{inc} [24], which is plotted in Fig. 6 for comparison. As P_{inc} is increased, the charge accumulation density at the SiC/graphene increases [shown in Fig. 6 (inset)], hence increasing the photoinduced doping in graphene. As the charge accumulation density increases, the subsequent flow of photogenerated carriers toward the SiC surface becomes slower which results in a gradual flattening of the response of $E_D - E_F$ and I_{photo} as a function of P_{inc} . This is expected since the higher accumulation of carriers at the SiC surface could result in a back-diffusion of carrier flux from the surface toward the substrate's bulk which opposes the photogenerated carrier flux drifting from the bulk toward the surface. In addition, the electric field in SiC bulk is reduced due to high

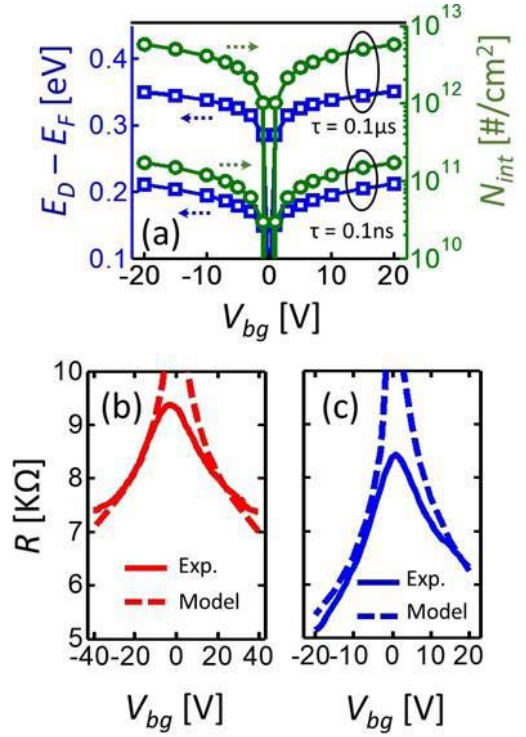


Fig. 7. (a) Magnitude of shift in E_D for graphene in the channel region and the integrated surface charge (N_{int}) in SiC under the source/drain contact (Region 1) as a function of V_{bg} for $v_{inj} = 10^{-3}$ cm/s, $P_{inc} = 18$ mW/cm², and $C_M/C_Q \gg 1$. N_{int} is due to the surface accumulation of holes (electrons) for the positive (negative) polarity of V_{bg} . The results are shown for two different carrier lifetimes (assumed the same for electrons and holes) in SiC. (b) Experimental back-gate-voltage modulation of graphene resistance ($R - V_{bg}$) in a graphene phototransistor with channel dimensions of $W \times L = 3 \mu\text{m} \times 2 \mu\text{m}$ under visible light illumination ($P_{inc} \approx 40$ mW/cm²) is compared with the simulated $R - V_{bg}$. For the simulated result, the same injection velocity ($v_{inj} = 10^{-3}$ cm/s) for electron and holes is used that shows a good match with the experimental ambipolar symmetric $R - V_{bg}$ characteristics. (c) Simulated $R - V_{bg}$ is compared with the experimental data from [23]. The experimental data are for a graphene phototransistor with channel dimensions of $W \times L = 4 \mu\text{m} \times 1 \mu\text{m}$ under visible light illumination ($P_{inc} \approx 7$ mW/cm²). Different injection velocities ($v_{inj} = 5 \times 10^{-2}$ cm/s and $v_{inj} = 10^{-1}$ cm/s for the electrons and holes, respectively) are assumed at the graphene/SiC interface in the simulated result. The simulation results in (b) and (c) are calculated from (2), where $C_M/C_Q \gg 1$ is assumed.

carrier accumulation at the surface which could reduce the collection of photogenerated carriers from the bulk at the surface.

D. Origin of Symmetric/Asymmetric Ambipolar Photoconductivity

The ambipolar photoconduction in graphene phototransistors could have either symmetric or asymmetric dependence on the polarity of V_{bg} , as observed in the experiments [23], [24]. To understand the origin of this dependence, we explore the gate-voltage dependence of $E_D - E_F$ in the graphene channel region and that of the integrated photoinduced charge density accumulated at the SiC surface. Fig. 7 (a) shows these results for $P_{inc} = 18$ mW/cm² and $v_{inj} = 10^{-3}$ cm/s. The integrated surface charge density and $E_D - E_F$ show a similar trend, i.e., an ambipolar increase with increasing magnitude of V_{bg} . This is expected because at higher V_{bg} , the electric field across

substrate's thickness increases which enhances the drift length for the photogenerated carriers in the substrate. This results in an enhanced photogenerated carrier flux and the corresponding higher accumulated carrier density which increases the electrostatic doping in graphene as V_{bg} is increased. As the polarity of V_{bg} is reversed, the type (electron or hole) of the carriers accumulating at the SiC/graphene interface is switched, and the graphene is correspondingly doped (p or n) hence resulting into a symmetric ambipolar photoconduction. Fig. 7(a) also shows the relative importance of carrier lifetime (τ) in the SiC on the electrostatic doping of graphene. In general, carrier lifetime for crystalline 6H – SiC is reported in the range 0.1 ns – 0.1 μ s [32], where a high-quality and pure SiC substrate is characterized by a relatively longer lifetime ($\tau = 0.1 \mu$ s), while a lower quality and highly defective SiC substrate is best described by much shorter lifetime ($\tau = 0.1$ ns). Longer carrier lifetime increases the drift length of the carriers, and thereby increases the density of accumulated photogenerated carriers at the SiC surface. The corresponding electrostatic doping in the graphene is somewhat higher for $\tau = 0.1 \mu$ s as compared with $\tau = 0.1$ ns, although the qualitative trend remains the same.

Fig. 7(b) and (c) compare the experimental photoinduced gate-voltage modulation of graphene resistance (R), i.e., the $R - V_{bg}$ characteristics of graphene/SiC phototransistor, with the simulated $R - V_{bg}$. The experimental data shown in Fig. 7(b) was measured on a device with channel dimensions of $W \times L = 3 \mu\text{m} \times 2 \mu\text{m}$, where the whole sample was exposed under a visible light at an estimated $P_{inc} \approx 40 \text{ mW/cm}^2$. The fabrication and characterization of this device are similar to what are discussed in [24]. The experimental data in Fig. 7(c) are taken from [23] on a device with $W \times L = 4 \mu\text{m} \times 1 \mu\text{m}$ under visible light illumination at an estimated $P_{inc} \approx 7 \text{ mW/cm}^2$. The calculated resistances are obtained assuming the low bias model for diffusive transport in graphene [33]

$$R = \alpha \left[\left(\frac{2e^2}{h} \right) \frac{\lambda}{L} \frac{2W}{\pi \hbar v_F} (\kappa_B T) (s + 1) \Gamma(s + 1) (F_s(\eta_F) + F_s(-\eta_F)) \right]^{-1} \quad (2)$$

where Γ is the gamma function, F_s is the Fermi–Dirac integral of order s , $\eta_F = (E_D - E_F)/\kappa_B T$, and λ is the mean free path of the carriers in the graphene. The prefactor $\alpha \geq 1$ captures the effect of subunity transmission probability between the source/drain metal and the underlying graphene [34]. To calculate R shown in Fig. 7(b) and (c), η_F is obtained from the simulations, while $s = 1/2$, $\alpha = 5$, and $\lambda = 10$ nm are used as fitting parameters to match the experimental data. The assumed carrier (electron and hole) lifetime of 0.1 ns for SiC in the simulated results of Fig. 7(b) and (c) was found to best fit the experimental data.

The symmetric ambipolar $R - V_{bg}$ in Fig. 7(b) shows a good match between the experiment and the model, where $v_{inj} = 10^{-3} \text{ cm/s}$ is assumed for both the electrons and the holes. The Dirac point for the experimental data is shifted to $V_{bg} \approx -3$ V, which might correspond to the presence of

charged impurities in the graphene or the surface defects at the graphene/SiC interface (these effects are not incorporated in our simulation). The $R - V_{bg}$ data in Fig. 7(c), on the other hand, show an asymmetric ambipolar dependence of R on the polarity of V_{bg} . The origin of the asymmetry is yet to be interpreted in terms of the substrate's photoinduced field effect. We propose that the asymmetry arises from a difference in the magnitude of v_{inj} between the electrons and the holes. A different v_{inj} for electrons and holes would correspond to a mismatch in the effective energy barrier at the graphene/SiC interface for the two carriers according to (1). The dissimilarity in v_{inj} implies a polarity dependence for the density of accumulated carriers at the graphene/Si interface and the corresponding electrostatic doping in graphene, as shown in Figs. 2 and 5. This according to (2) could result in an asymmetric ambipolar $R - V_{bg}$ behavior, as shown in Fig. 7(c). For the simulated result in Fig. 7(c), a relatively small dissimilarity in v_{inj} for the electrons and holes ($5 \times 10^{-2} \text{ cm/s}$ and 10^{-1} cm/s , respectively) provides a good match with the asymmetric behavior of R versus V_{bg} in the experimental data. In Fig. 7(b) and (c), a sharp rise in the simulated resistance in the vicinity of $V_{bg} = 0$ V corresponds to zero density of states near the Dirac energy in the graphene. Such behavior is not observed in the experimental results probably due to the presence of spatial fluctuations or charge puddles resulting in a finite density of states and energy band broadening in graphene as E_F approaches the Dirac point [25]. The simulation model, by construction, does not account for these nonidealities.

In practice, the calculation of graphene resistance in the phototransistor may involve other considerations beyond the simple approach used in (2). For example, an opposite polarity (or unequal magnitude) of doping in the contact and the channel regions of graphene may exist requiring to consider the transport through p - n (or n - n and p - p) junctions across the graphene layer. The simple approach given by (2), however, provides a first-order comparison between the model and the experiment, while rigorous models could be considered as a part of future studies.

IV. CONCLUSION

We have developed a computational model for the physical origin of photoresponsivity in graphene phototransistors that are fabricated on an undoped substrate. We showed that the photoresponse is due to the electrostatic doping of the graphene channel induced by the accumulation of photogenerated carriers at the graphene/substrate interface. The accumulation of the charge density strongly depends on the injection velocity (transfer rate) of carrier between the graphene and the substrate. The injection velocity physically depends on the graphene/substrate energy barrier and needs to be significantly smaller than the thermal velocity of carriers to enable a substantial photoresponse. The graphene at the source/drain metal contacts could be electrostatically doped if the relative strength of graphene to contact capacitance is weaker compared with the graphene quantum capacitance. These findings provide important guidelines to optimize the material/interfaces in graphene phototransistors for improving performance/cost. The high photoresponsivity for graphene

phototransistor requires a careful design of the graphene–substrate interface and graphene–metal contacts, e.g., through engineering of thin interfacial oxides and selection of different metals. In addition, different substrate materials could be explored as the photoabsorber layer to optimize cost and the bandwidth of the photoresponse. Finally, the physics of graphene phototransistor, in principle, invites opportunities to explore other 2-D materials besides graphene in a similar device configuration for low-cost/high-performance photodetectors.

APPENDIX

A. Simulation Details

The 2-D numerical simulations under illumination and in dark are performed in a semiclassical simulation tool called PADRE developed at Bell Labs [35]. The coupled set of electrostatics (Poisson) and carrier continuity equations is solved as a function of the applied back-gate voltage (V_{bg}). The carrier transport is treated in the drift-diffusion formalism. The Poisson equation is given by

$$\nabla^2 V(x, y) = \frac{q}{\epsilon_{si}} [N_D - N_A + p(x, y) - n(x, y)] \quad (A1)$$

where N_D and N_A are the donor and the acceptor doping density, respectively, and p and n are the position-dependent hole and electron density, respectively. The steady-state carrier continuity equation is given as

$$D \frac{\partial^2 n}{\partial x^2} + \mu \varepsilon(x, y) \frac{\partial n}{\partial x} + G(x, y) - R(x, y) = 0 \quad (A2)$$

$$D \frac{\partial^2 p}{\partial x^2} - \mu \varepsilon(x, y) \frac{\partial p}{\partial x} + G(x, y) - R(x, y) = 0 \quad (A3)$$

where G and R are the carrier generation and recombination rates, respectively, and μ and D are the carrier mobility and diffusion constant, respectively. The carrier generation rate is calculated from the incident power density of the photons and the absorption coefficient (α) in SiC using $G(x, \lambda) = \alpha(\lambda) \varphi(\lambda) e^{-\alpha(\lambda)x}$ [29], where $\varphi(\lambda) = P_{inc}/E_{ph}$ is the incident flux and energy of the incident photons, respectively, at wavelength (λ).

A 500- μm -thick intrinsic SiC substrate acts as the photoabsorber layer having a back metal gate at the bottom and graphene on the top. The bandgap and electron affinity for SiC are 2.9 and 3.3 eV, respectively [31]. For simplicity, the carrier mobility and recombination lifetime in the SiC substrate are assumed to be the same for electrons and holes. The values of 100 $\text{cm}^2/(\text{V}\cdot\text{s})$ and 0.1 μs are used for the mobility and recombination lifetime (except for Fig. 6, as specified in Section III-D), respectively, which are close to the experimentally reported values for 6H-SiC [32], [36]. The channel length is maintained at 1.5 μm . A neutral contact is assumed for the back-gate contact, while the work function of the front metal electrodes is kept the same as for graphene. For undoped graphene, we have used work function of 4.75 eV [37]. In all simulations, source/drain potentials are assumed to be at the ground. The device is illuminated with a monochromatic light at a wavelength of 400 nm. The absorption coefficient of 40/cm is used for the SiC [38].

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