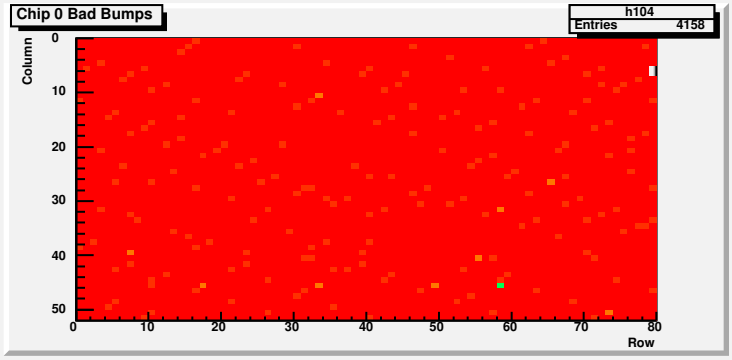
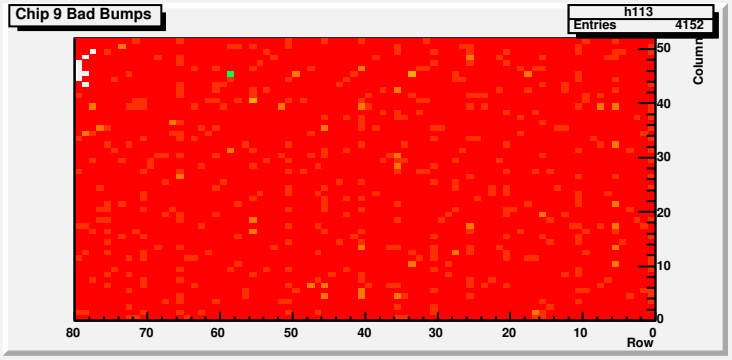

Chip 0 Bad Bumps
h104
Entries 4158
Column
Row


Chip 9 Bad Bumps
h113
Entries 4152
Column
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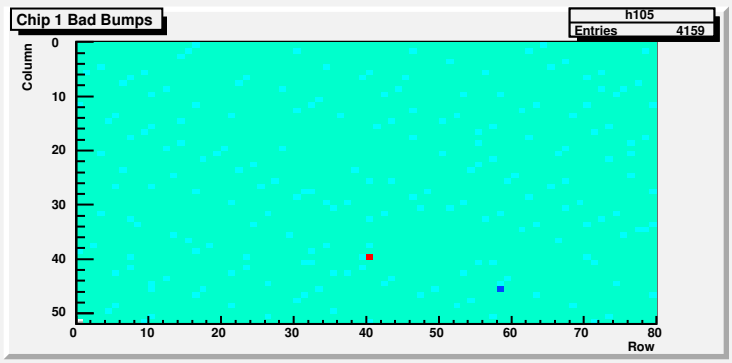
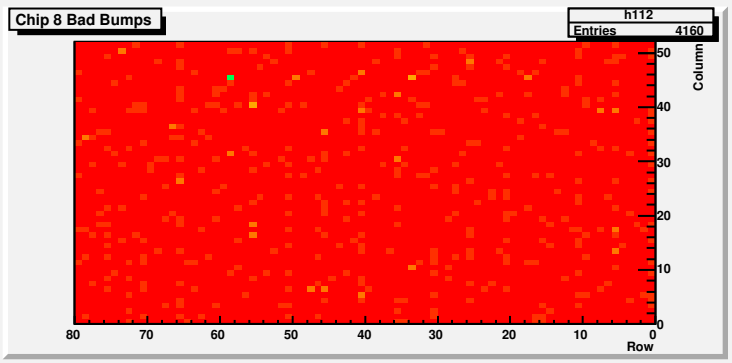
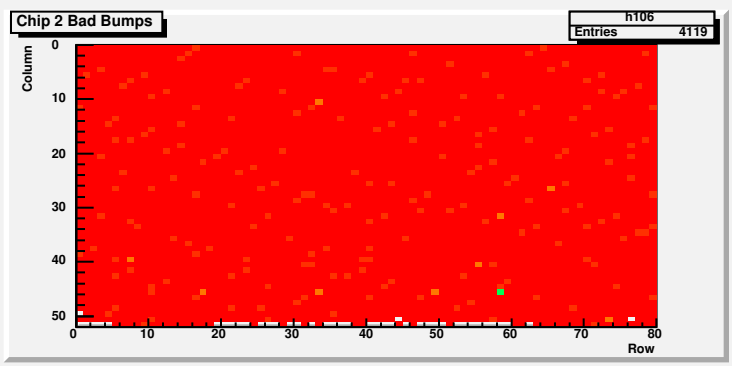
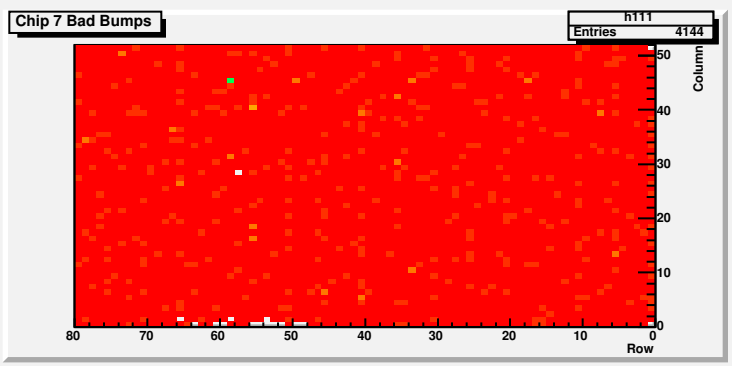
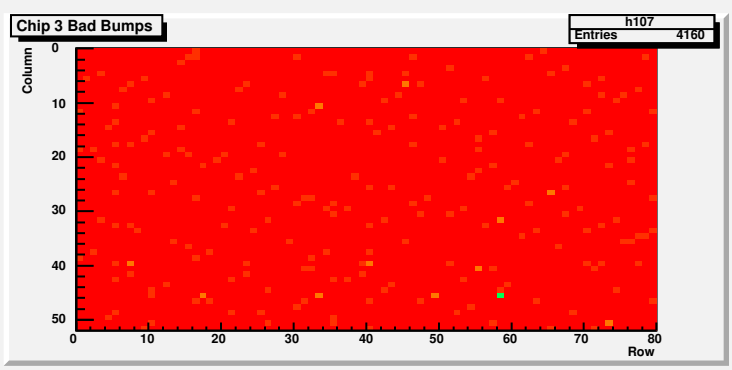
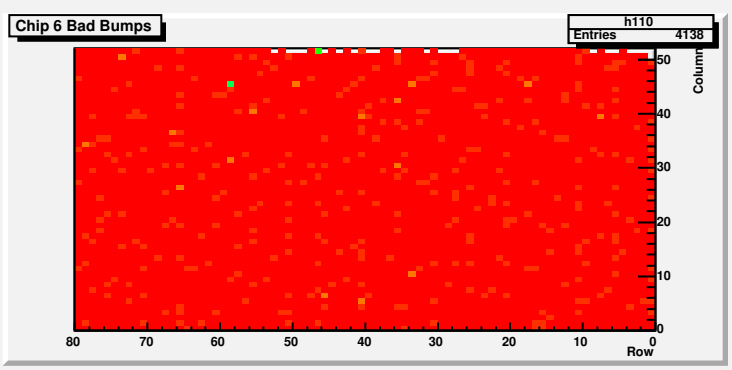

Chip 1 Bad Bumps
h105
Entries 4159
Column
Row

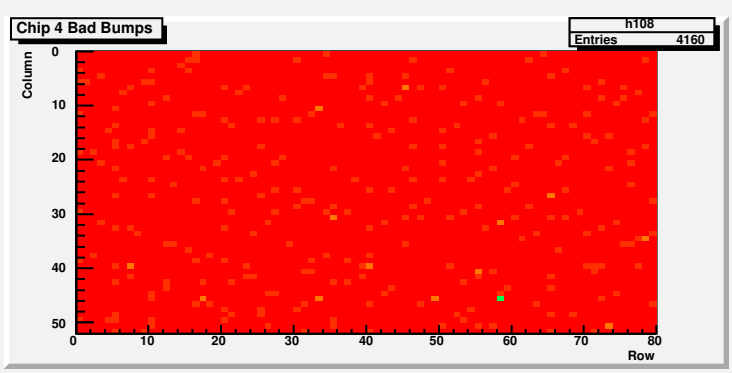

Chip 8 Bad Bumps
h112
Entries 4160
Column
Row

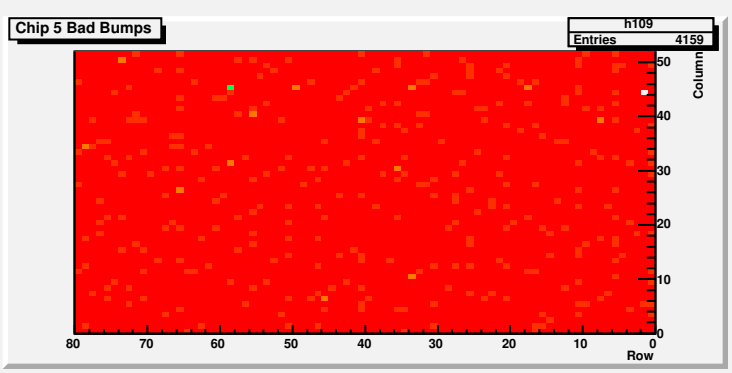

Chip 2 Bad Bumps
h106
Entries 4119
Column
Row

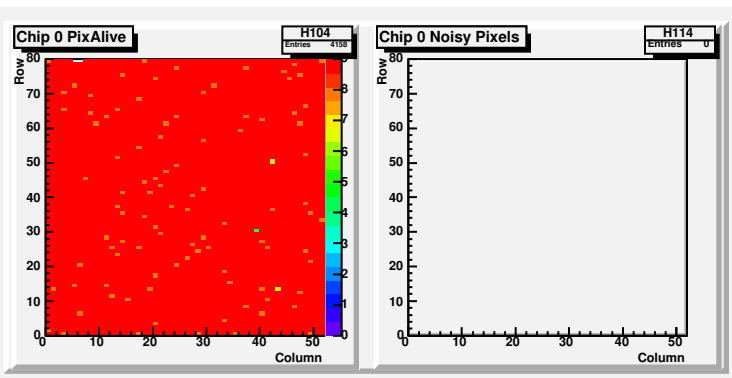

Chip 7 Bad Bumps
h111
Entries 4144
Column
Row

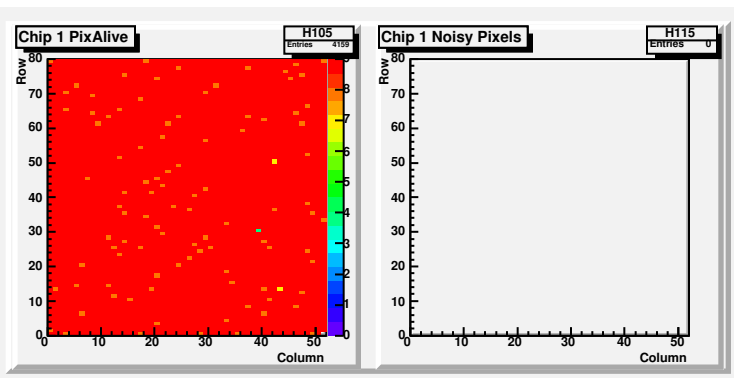

Chip 3 Bad Bumps
h107
Entries 4160
Column
Row

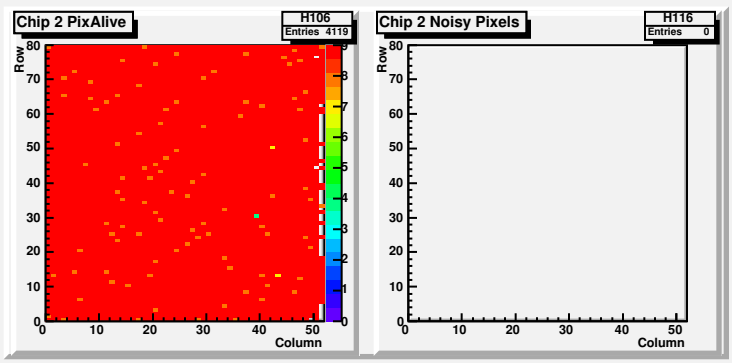

Chip 6 Bad Bumps
h110
Entries 4138
Column
Row

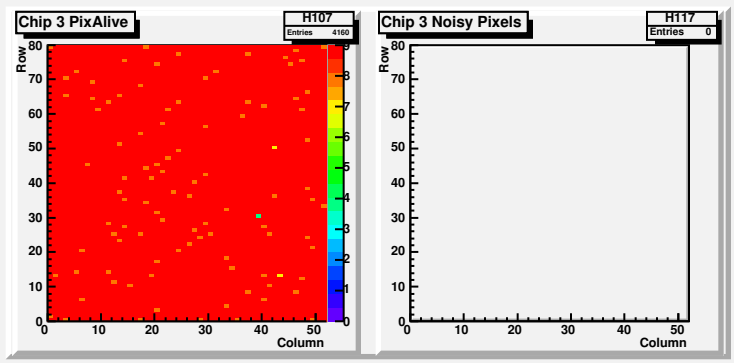

Chip 4 Bad Bumps
h108
Entries 4160
Column
Row

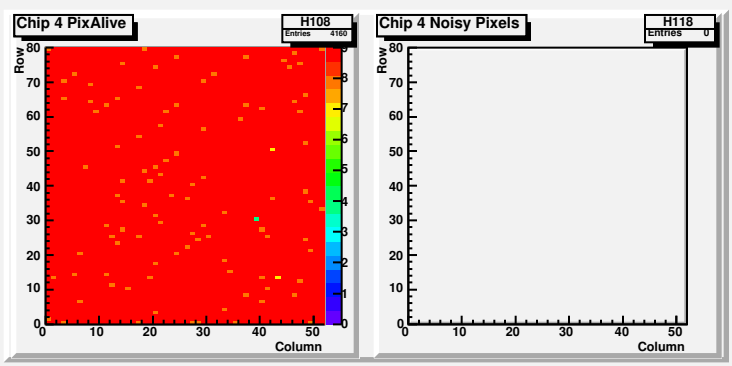

Chip 5 Bad Bumps
h109
Entries 4159
Column
Row

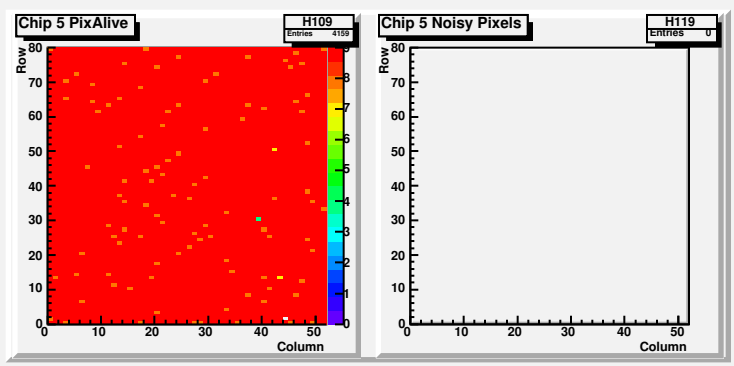

Chip 0 PixAlive
H104
Entries 4158
Chip 0 Noisy Pixels
H114
Entries 0
Row
Column

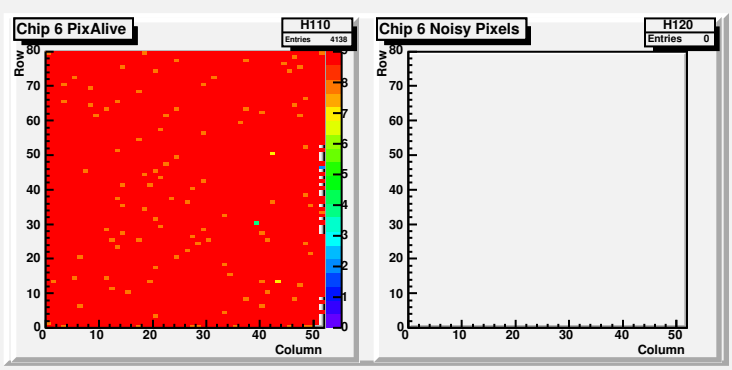

Chip 1 PixAlive
H105
Entries 4159
Chip 1 Noisy Pixels
H115
Entries 0
Row
Column

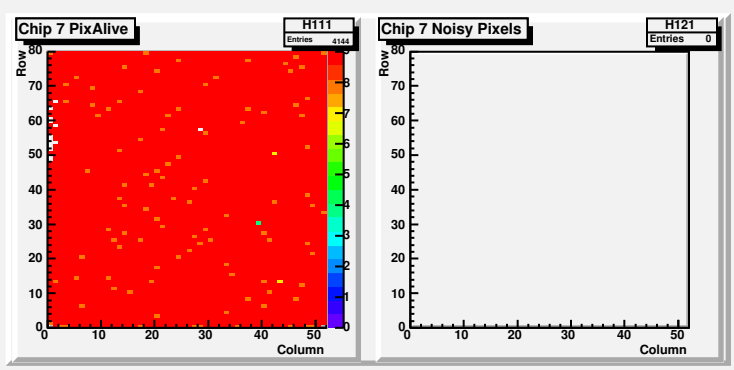

Chip 2 PixAlive
H106
Entries 4119
Chip 2 Noisy Pixels
H116
Entries 0
Row
Column

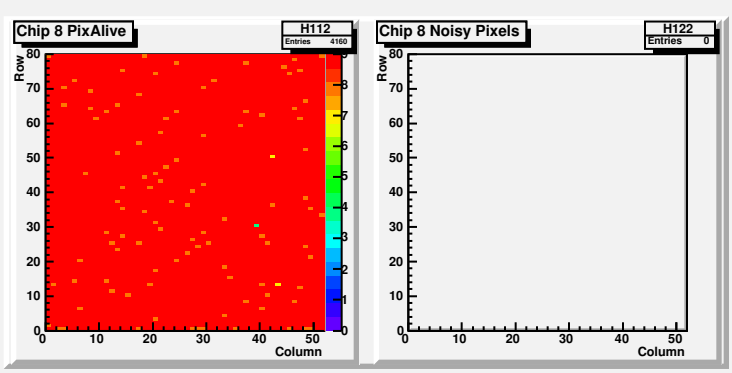

Chip 3 PixAlive
H107
Entries 4160
Chip 3 Noisy Pixels
H117
Entries 0
Row
Column

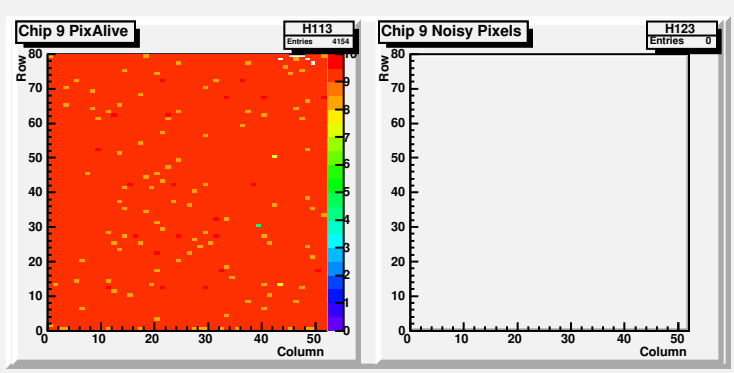

Chip 4 PixAlive
H108
Entries 4160
Chip 4 Noisy Pixels
H118
Entries 0
Row
Column

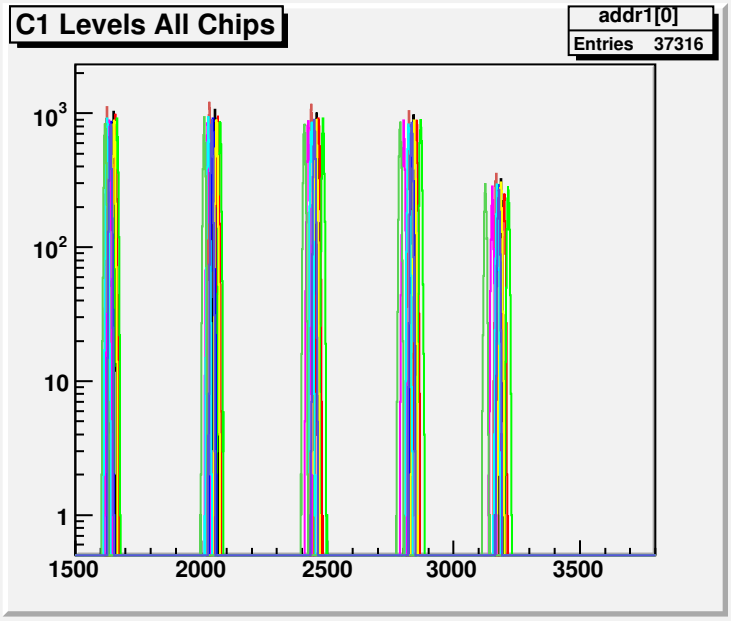

Chip 5 PixAlive
H109
Entries 4159
Chip 5 Noisy Pixels
H119
Entries 0
Row
Column

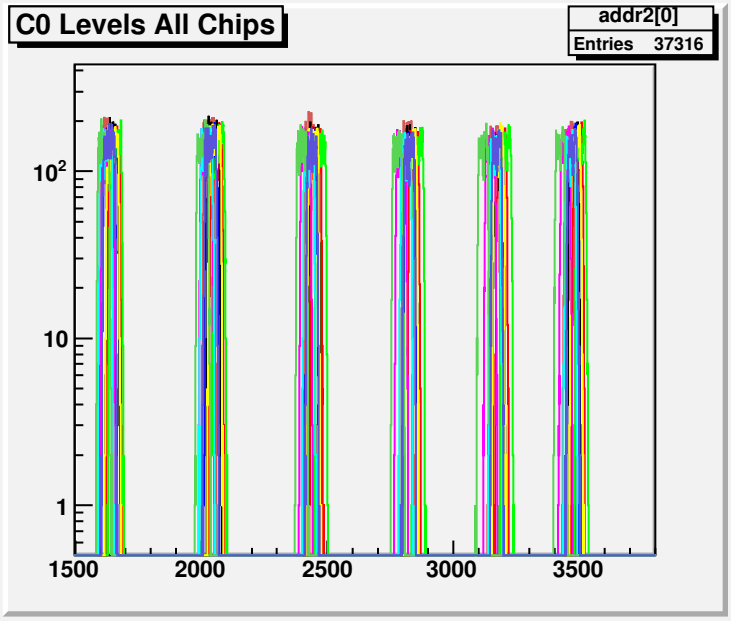

Chip 6 PixAlive
H110
Entries 4138
Chip 6 Noisy Pixels
H120
Entries 0
Row
Column

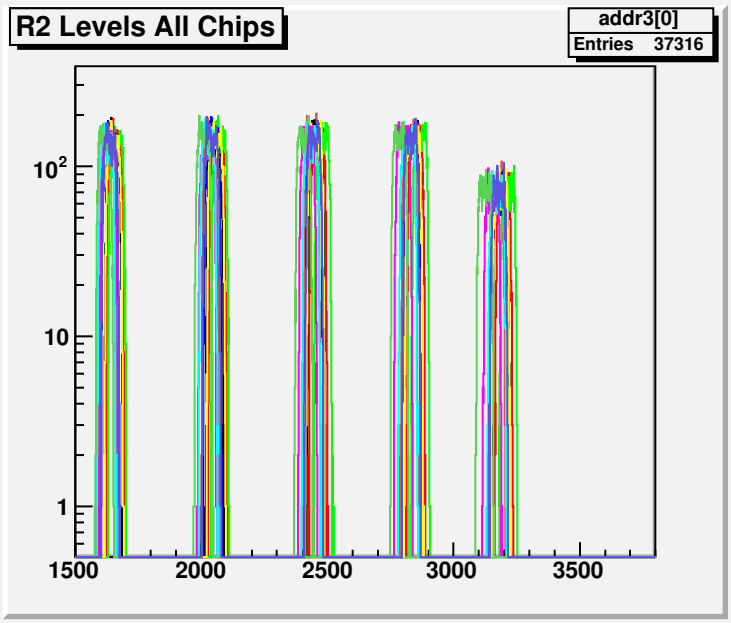

Chip 7 PixAlive
H111
Entries 4144
Chip 7 Noisy Pixels
H121
Entries 0
Row
Column

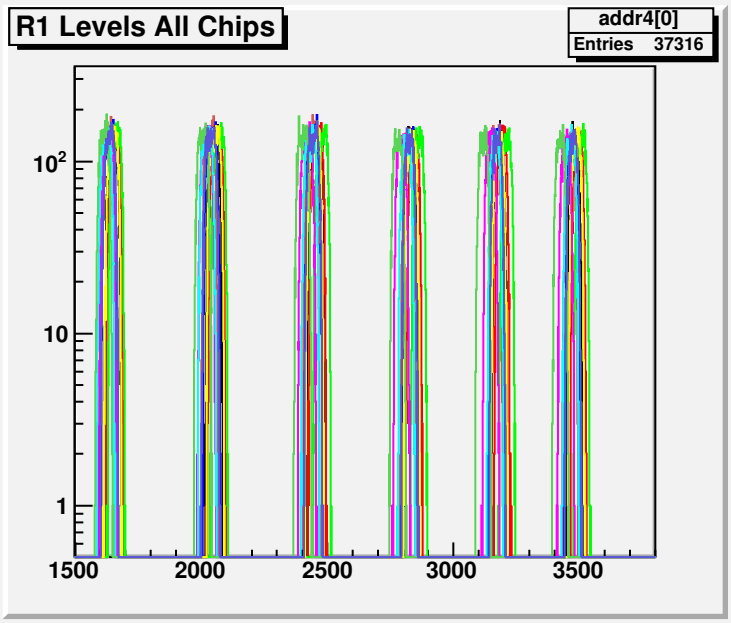

Chip 8 PixAlive
H112
Entries 4160
Chip 8 Noisy Pixels
H122
Entries 0
Row
Column

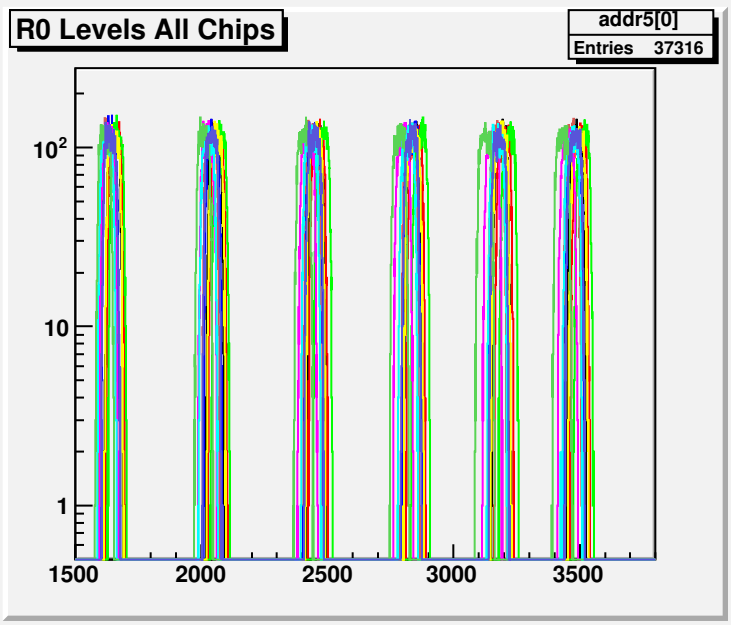

Chip 9 PixAlive
H113
Entries 4154
Chip 9 Noisy Pixels
H123
Entries 0
Row
Column

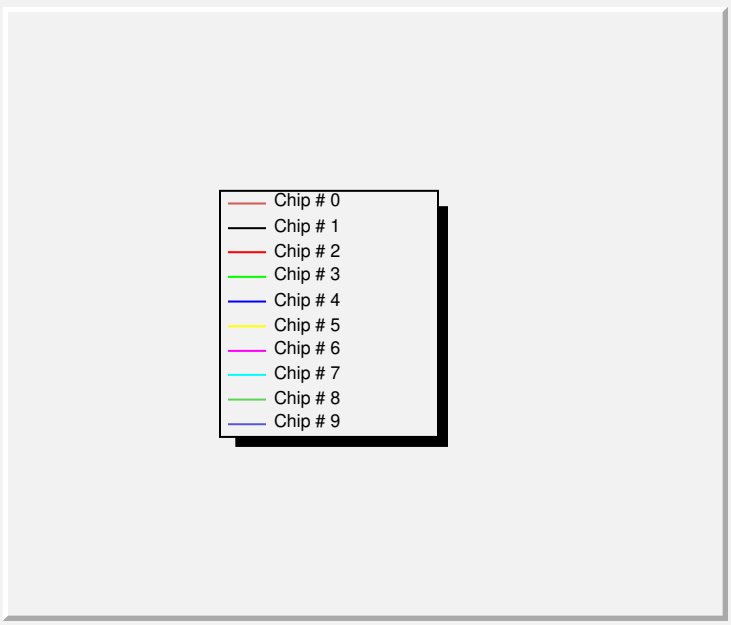

C1 Levels All Chips
addr1[0]
Entries 37316
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37316
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37316
10^2
10
1
1500
2000
2500
3000
3500


R1 Levels All Chips
addr4[0]
Entries 37316
10^2
10
1
1500
2000
2500
3000
3500


R0 Levels All Chips
addr5[0]
Entries 37316
10^2
10
1
1500
2000
2500
3000
3500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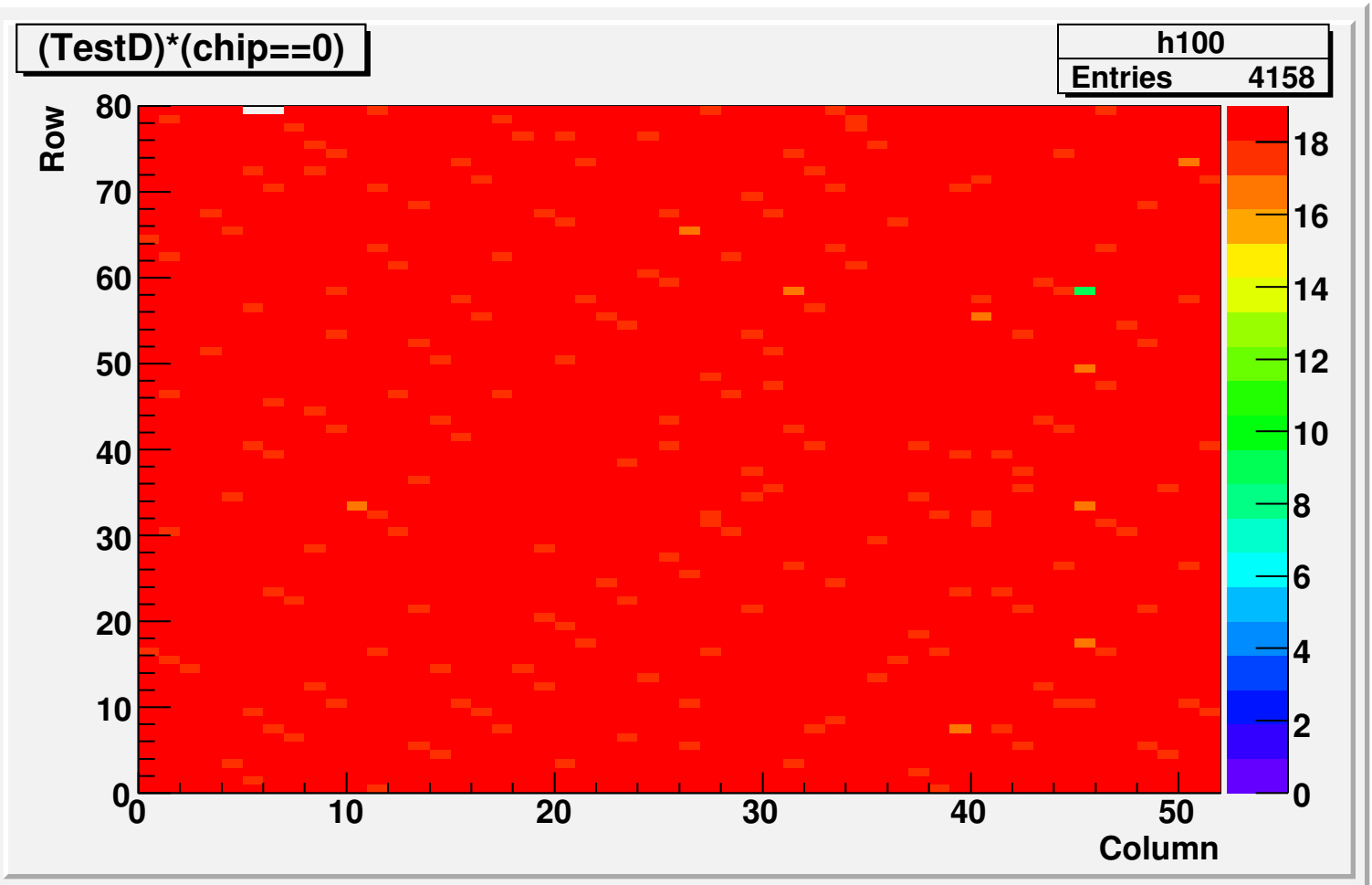
Chip # 1
Chip # 2
Chip # 3
Chip # 4
Chip # 5
Chip # 6
Chip # 7
Chip # 8
Chip # 9


(TestD)*(chip==0)
h100
Entries 4158
Row
Column
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60
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80
0
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12
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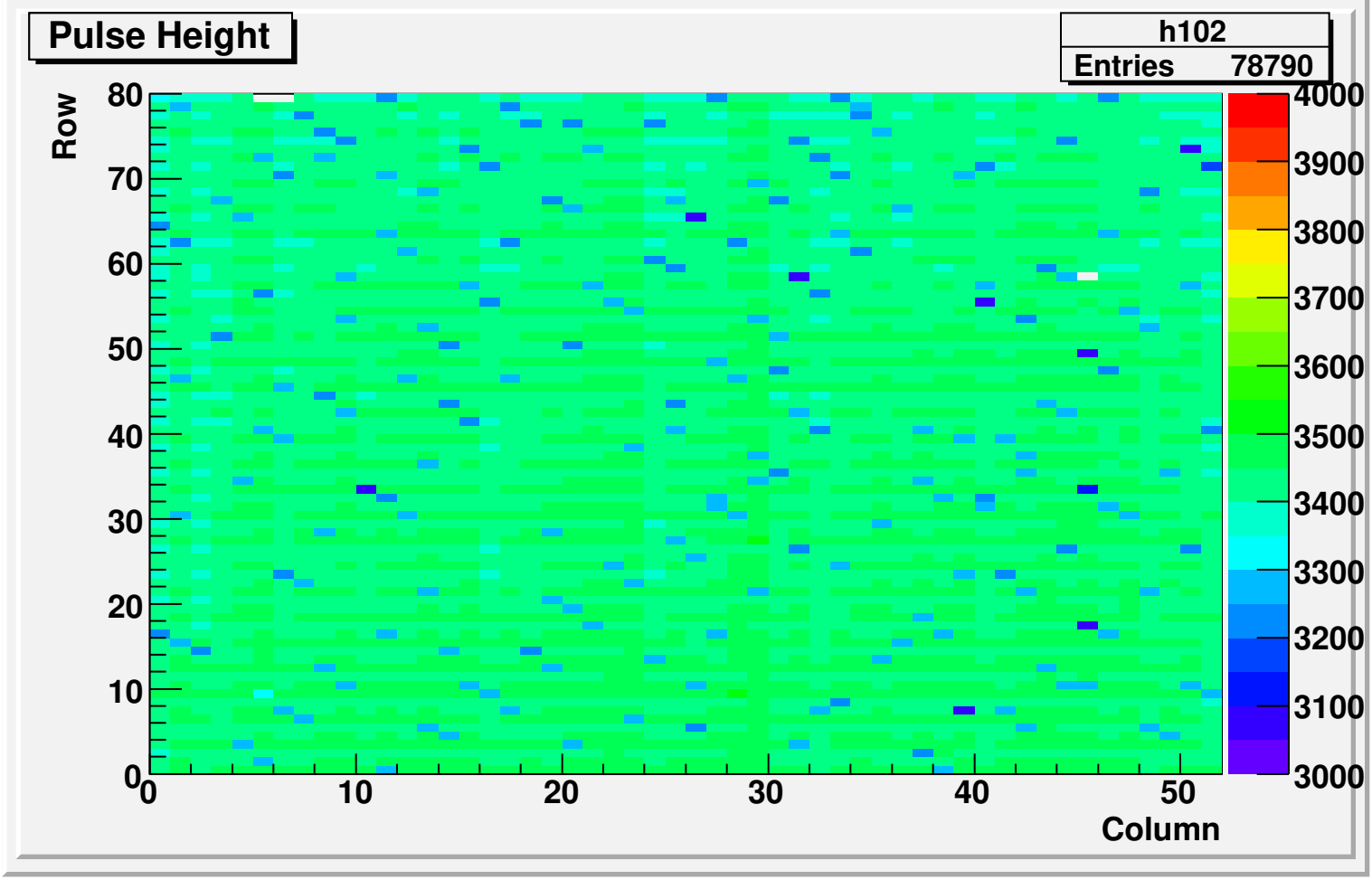
16
18


Pulse Height
h102
Entries 78790
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Column
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3100
3200
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3800
3900
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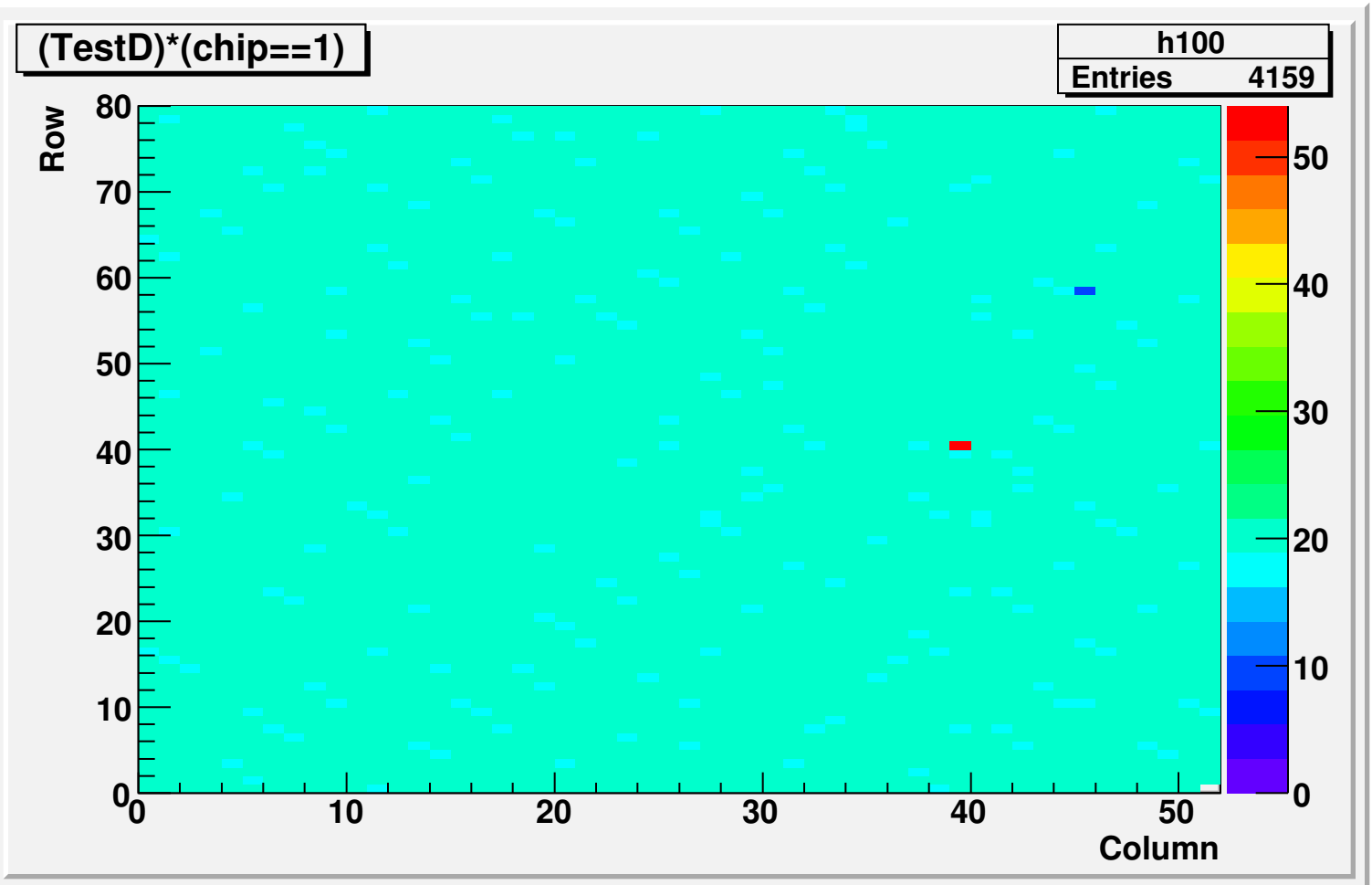
(TestD)*(chip==1)
h100
Entries 4159
Row
Column
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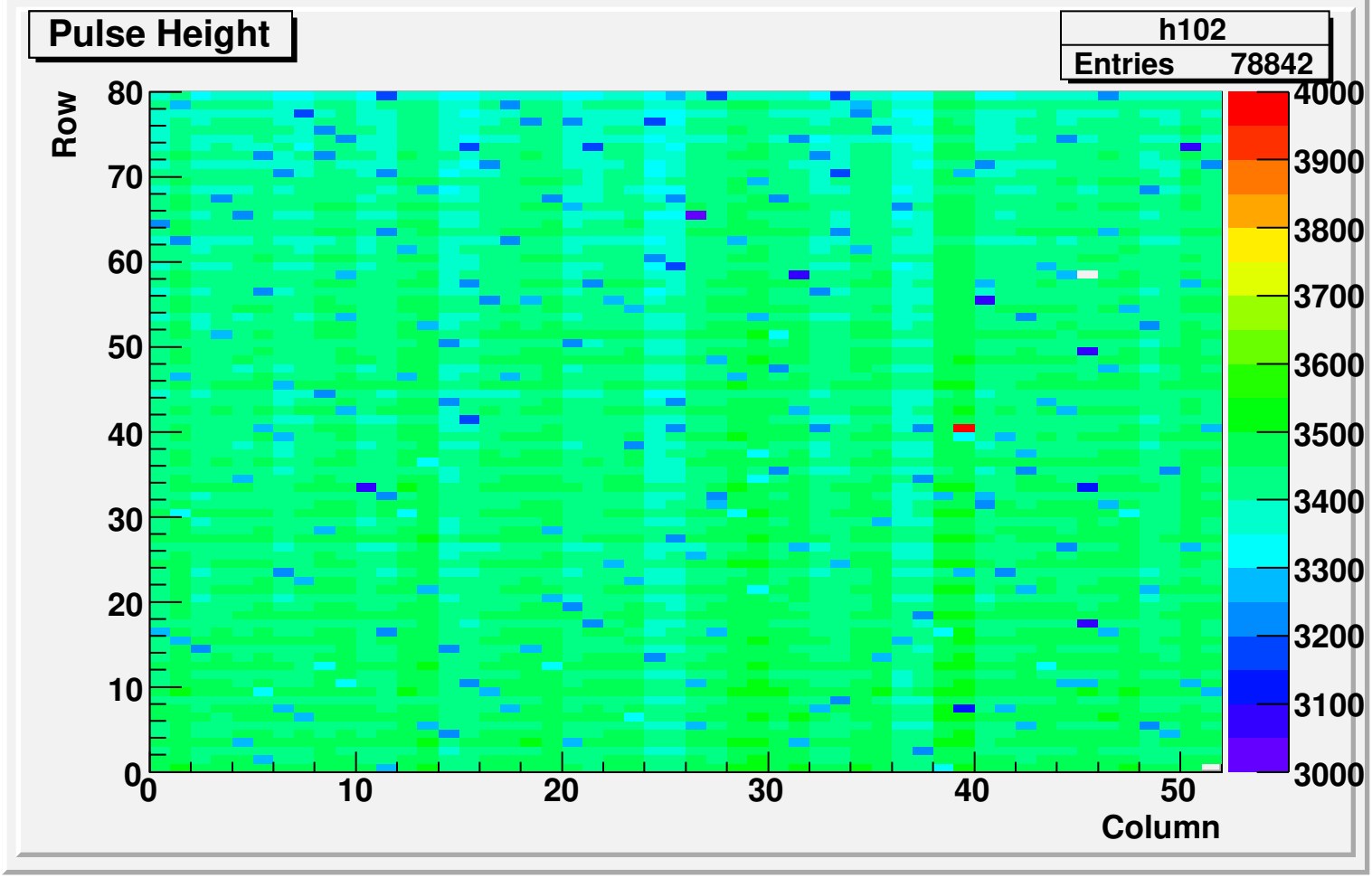
70
80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h102
Entries 78842
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Column
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3100
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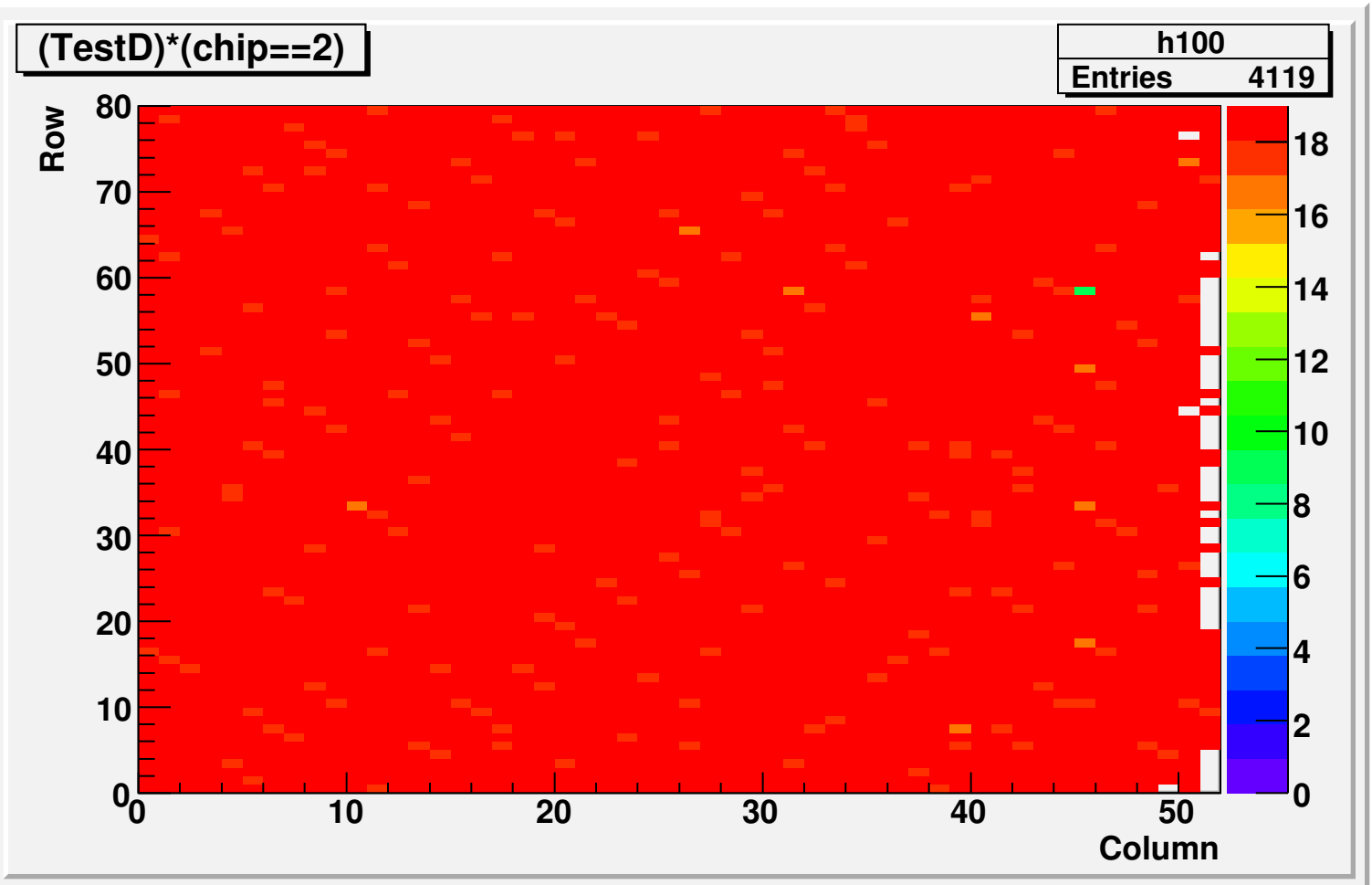
(TestD)*(chip==2)
h100
Entries 4119
Row
Column
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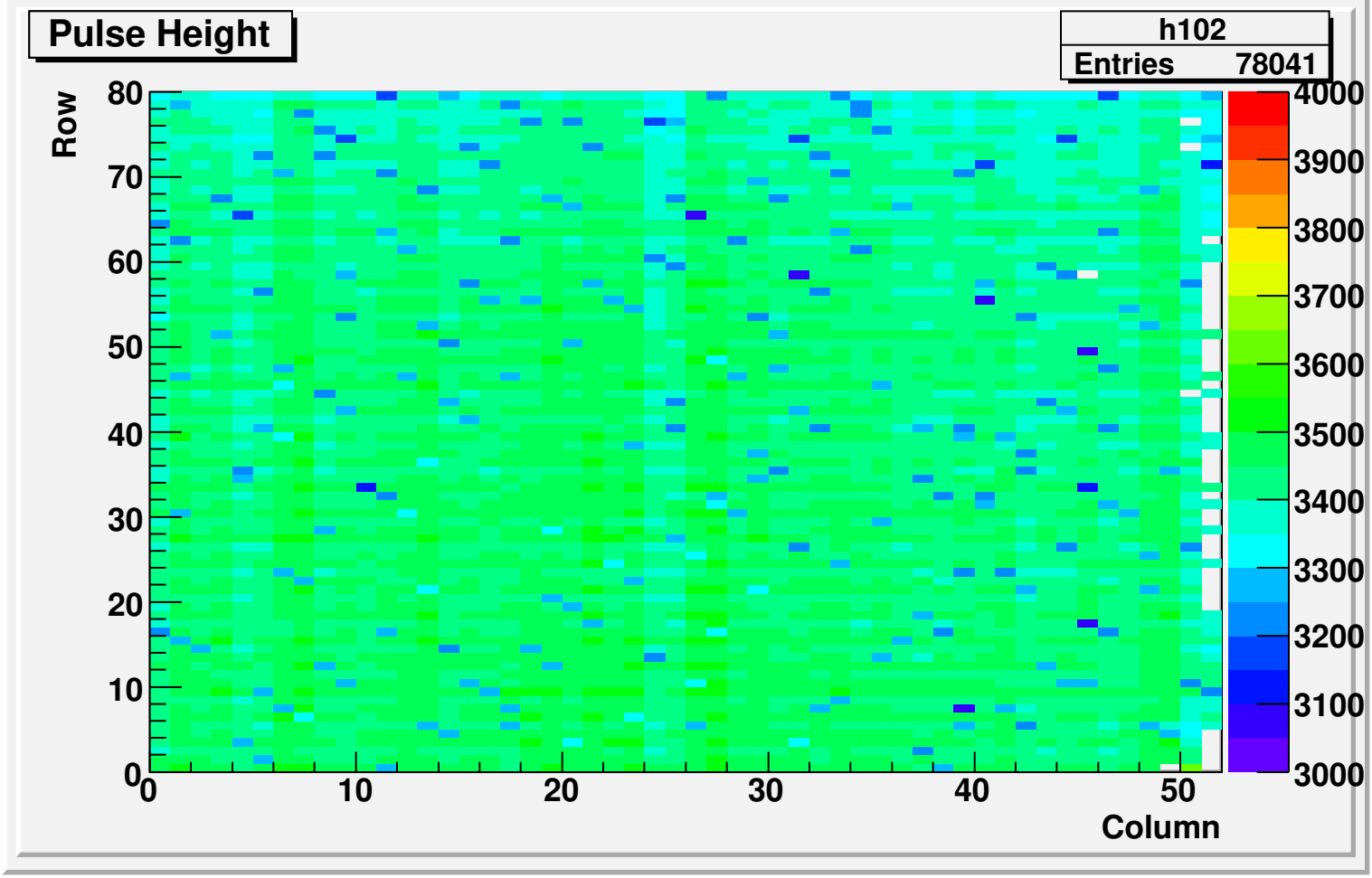
Pulse Height
h102
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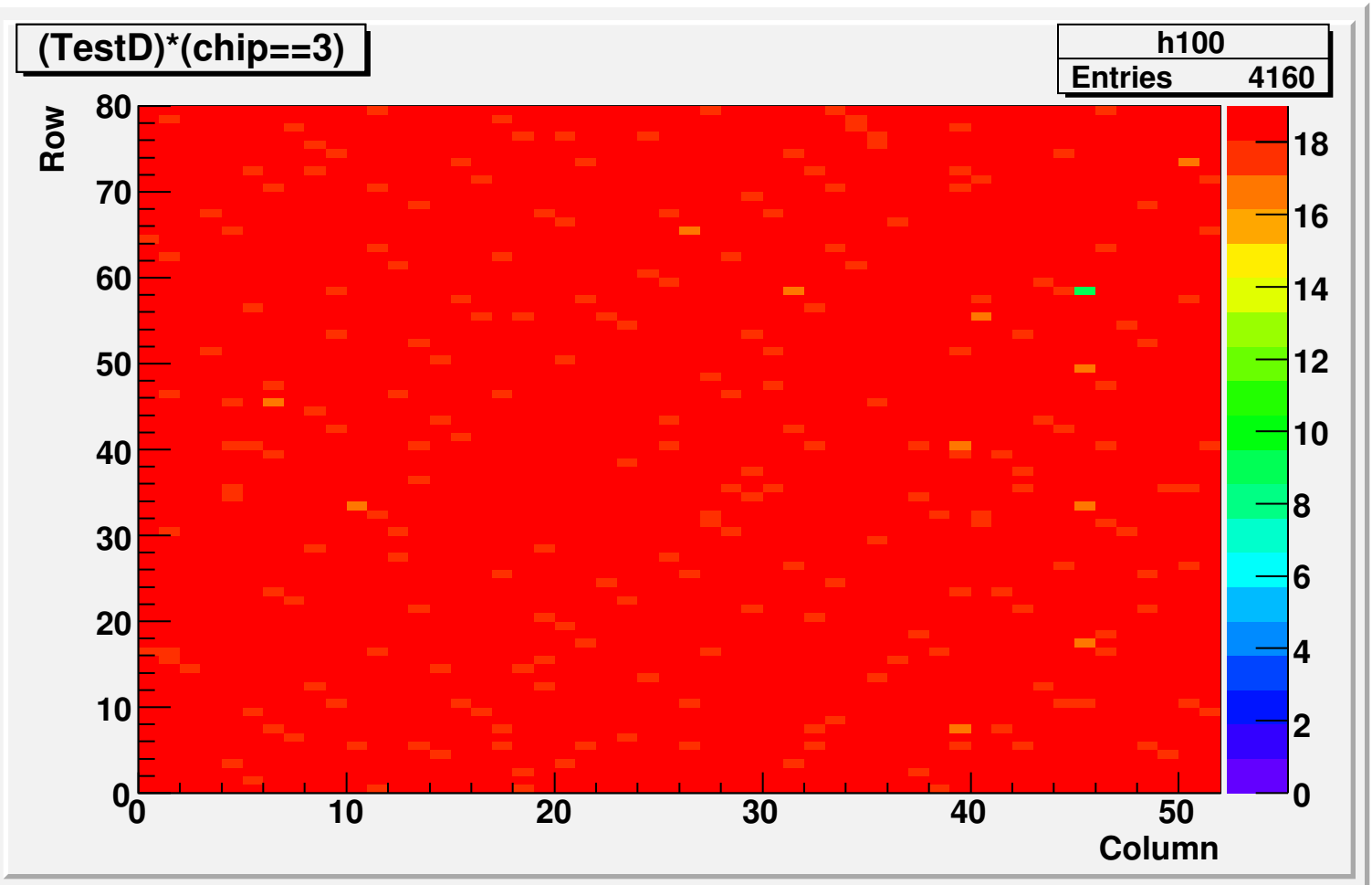
(TestD)*(chip==3)
h100
Entries 4160
Row
Column
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60
70
80
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8
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12
14
16
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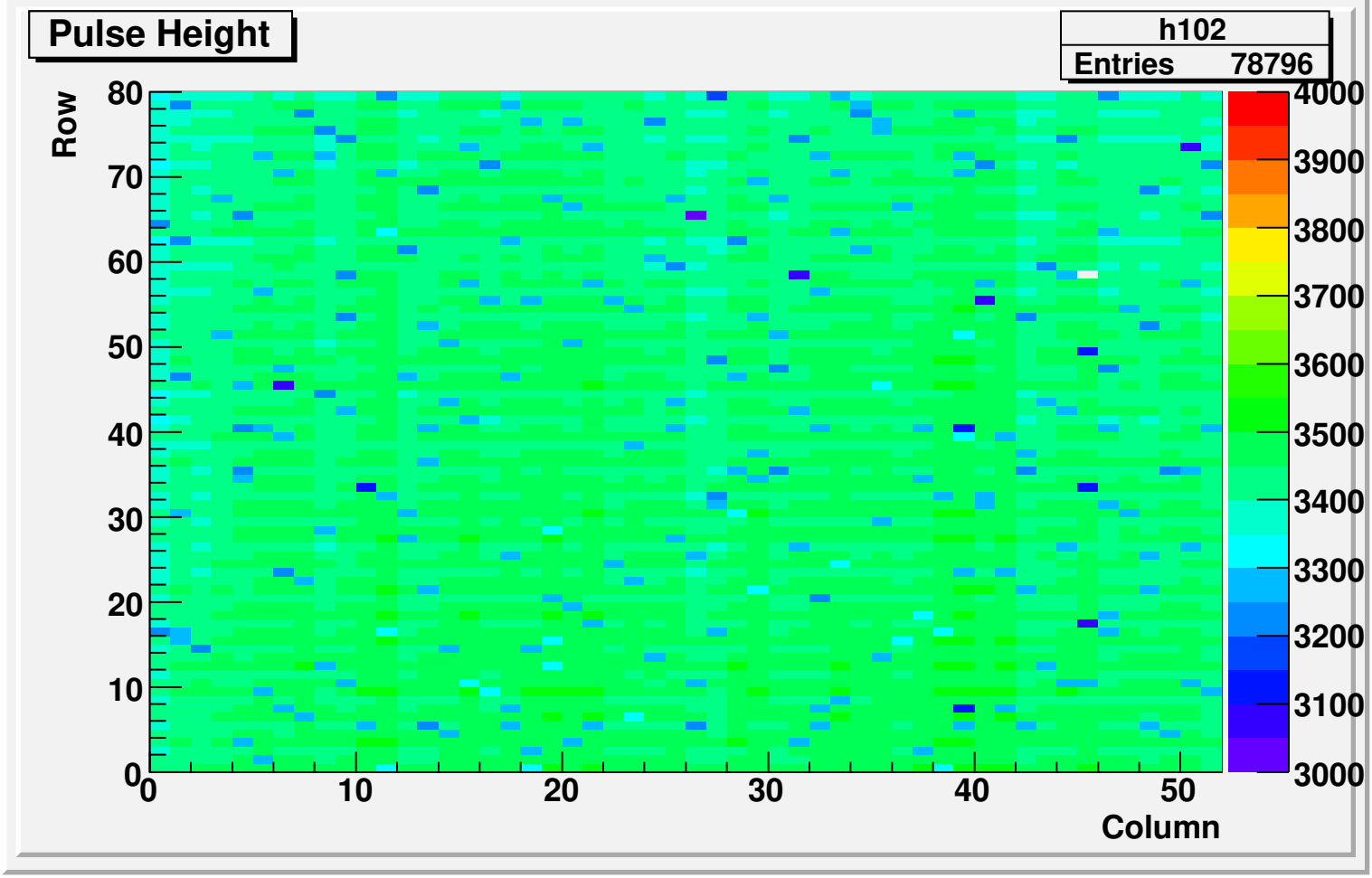
Pulse Height
h102
Entries 78796
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Column
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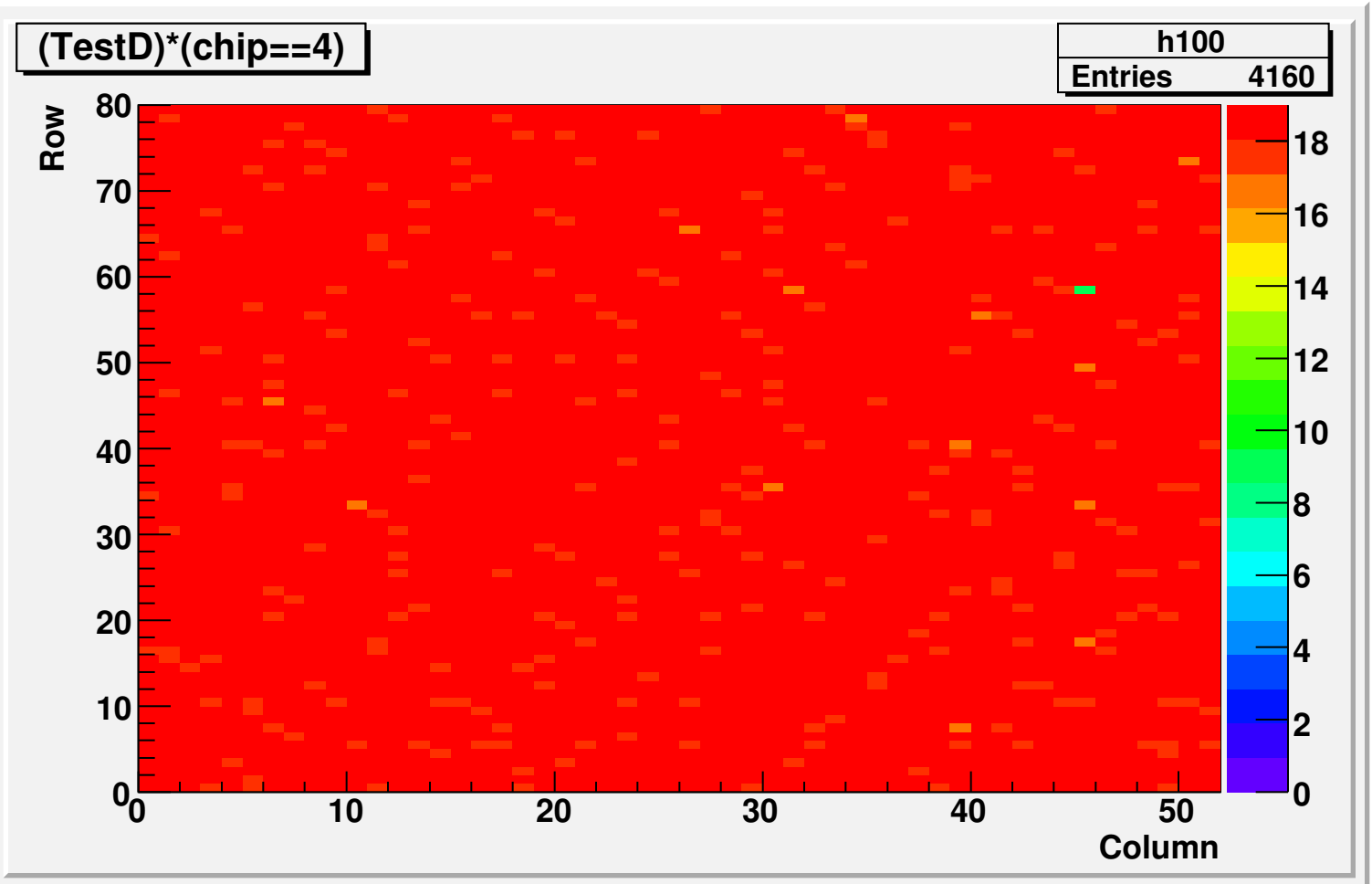
(TestD)*(chip==4)
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Entries 4160
Row
Column
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40
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80
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12
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16
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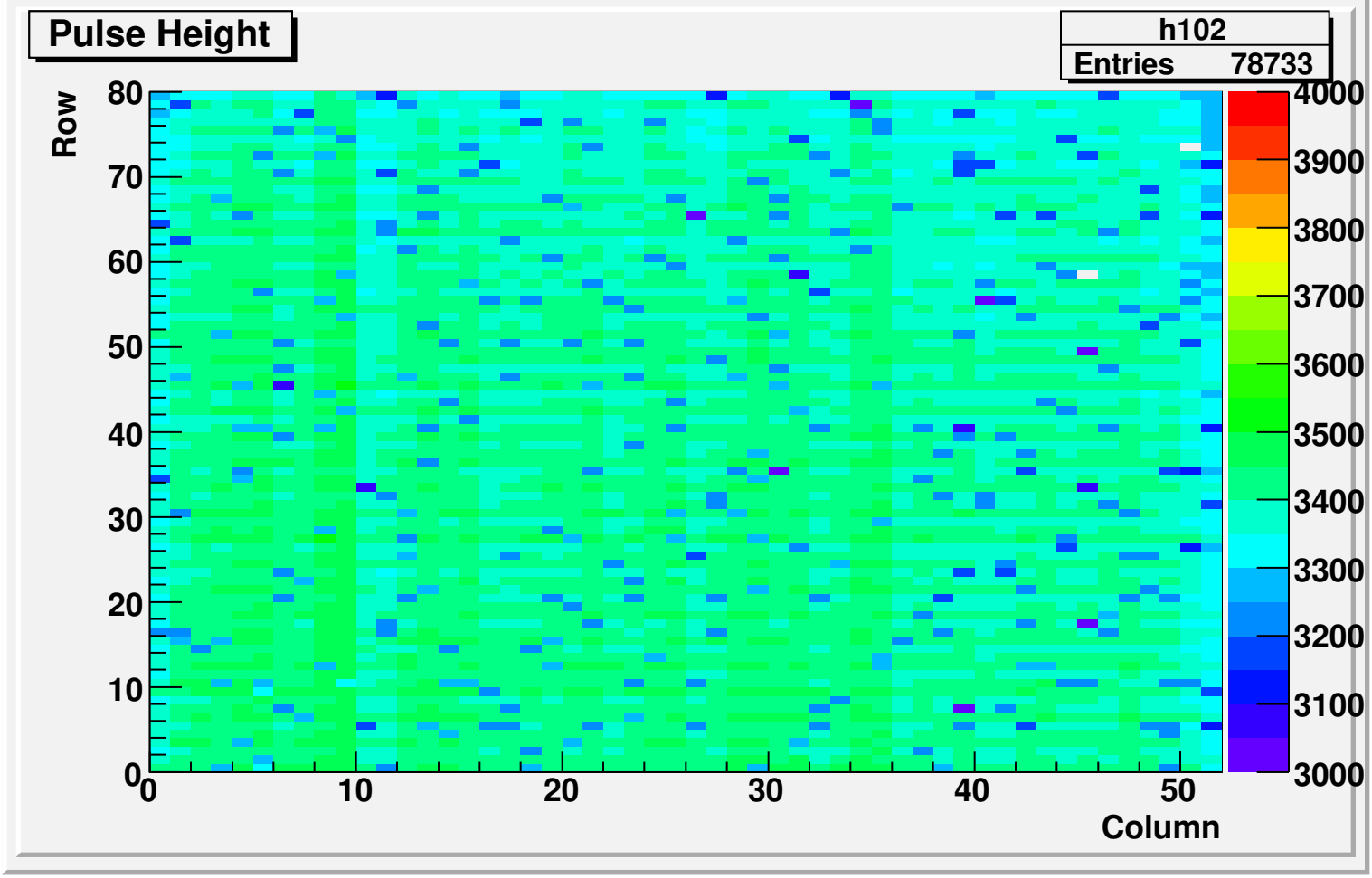
Pulse Height
h102
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Column
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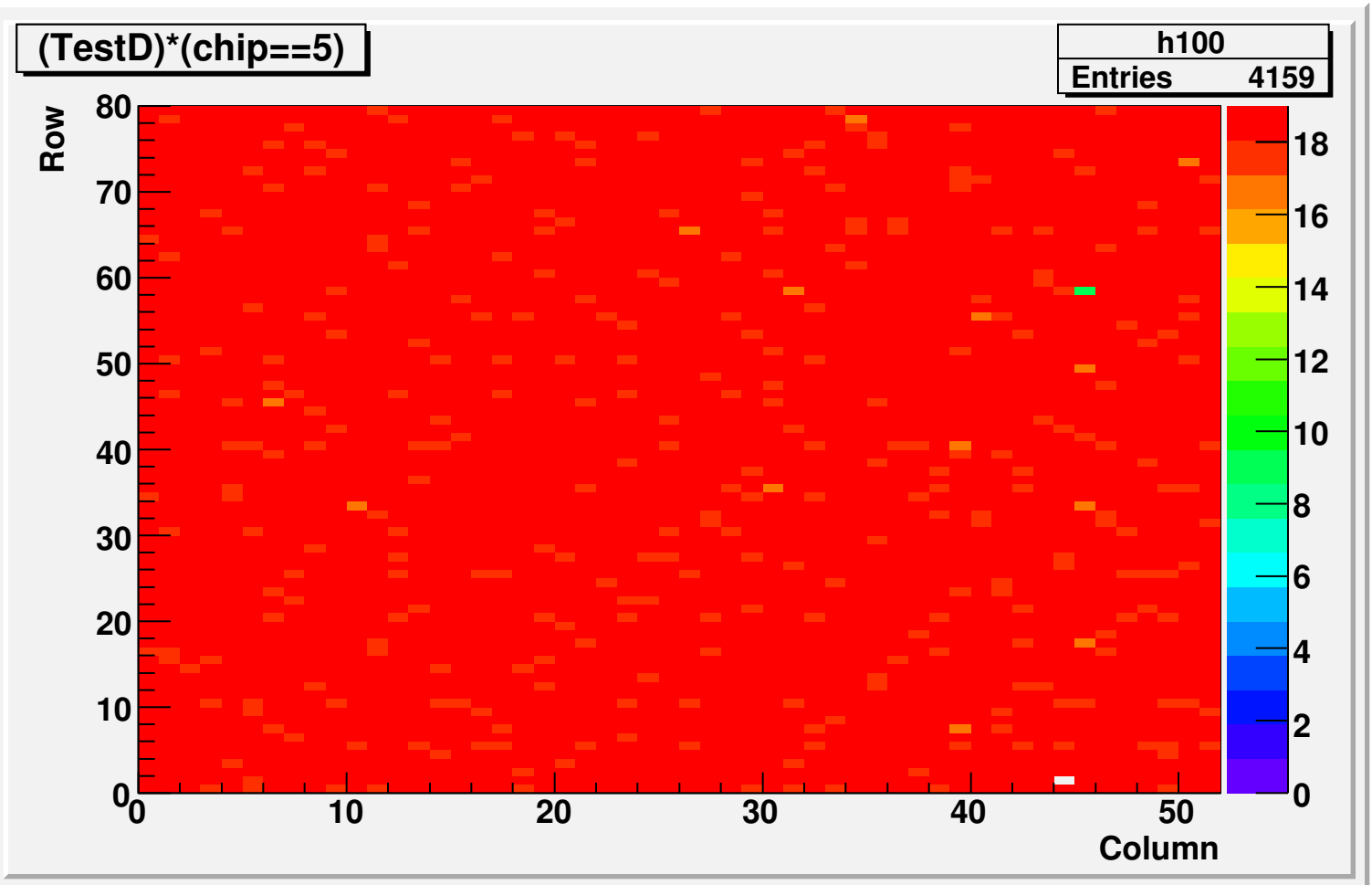
(TestD)*(chip==5)
h100
Entries 4159
Row
Column
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60
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80
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12
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16
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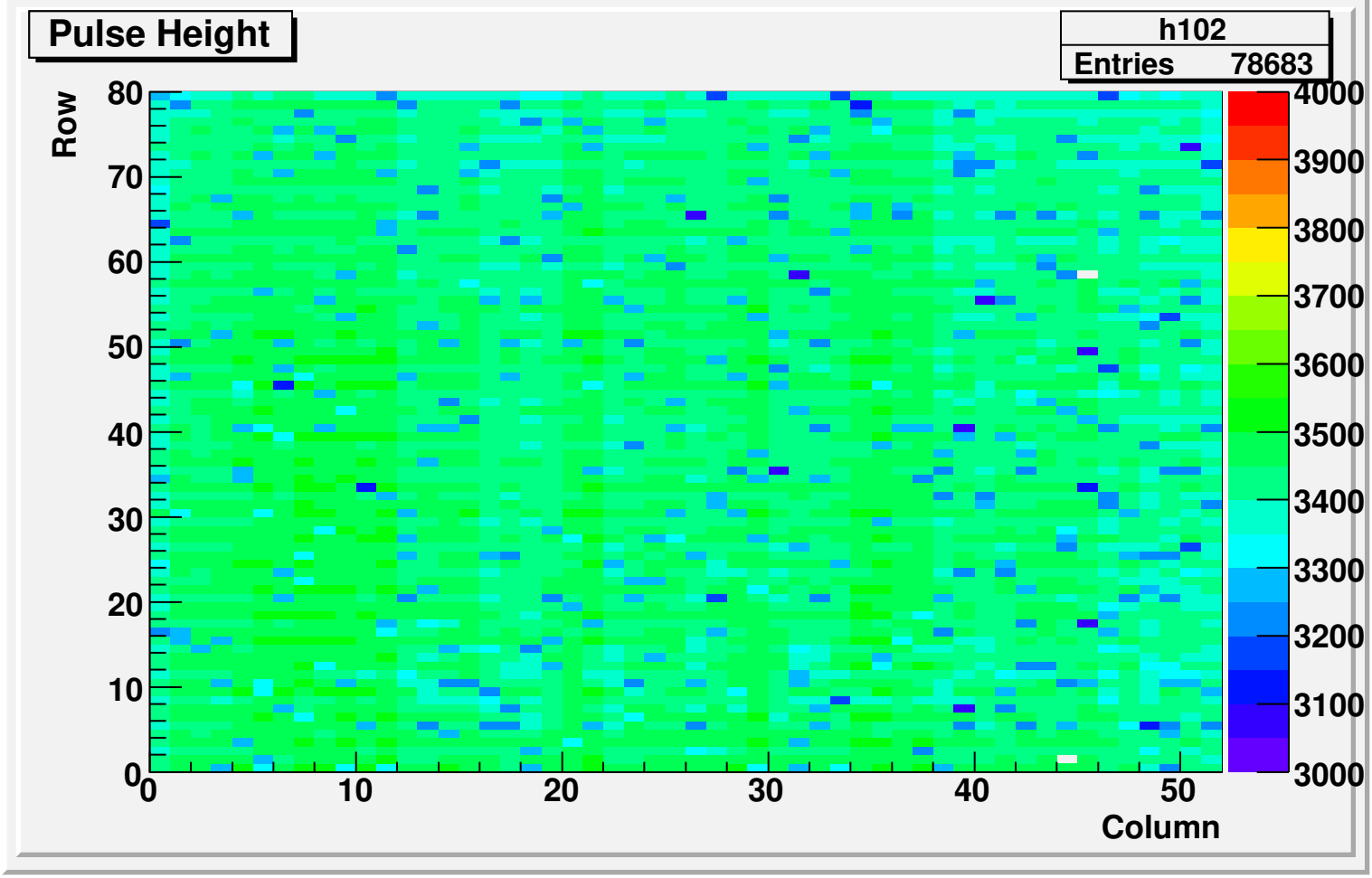
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Column
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3900
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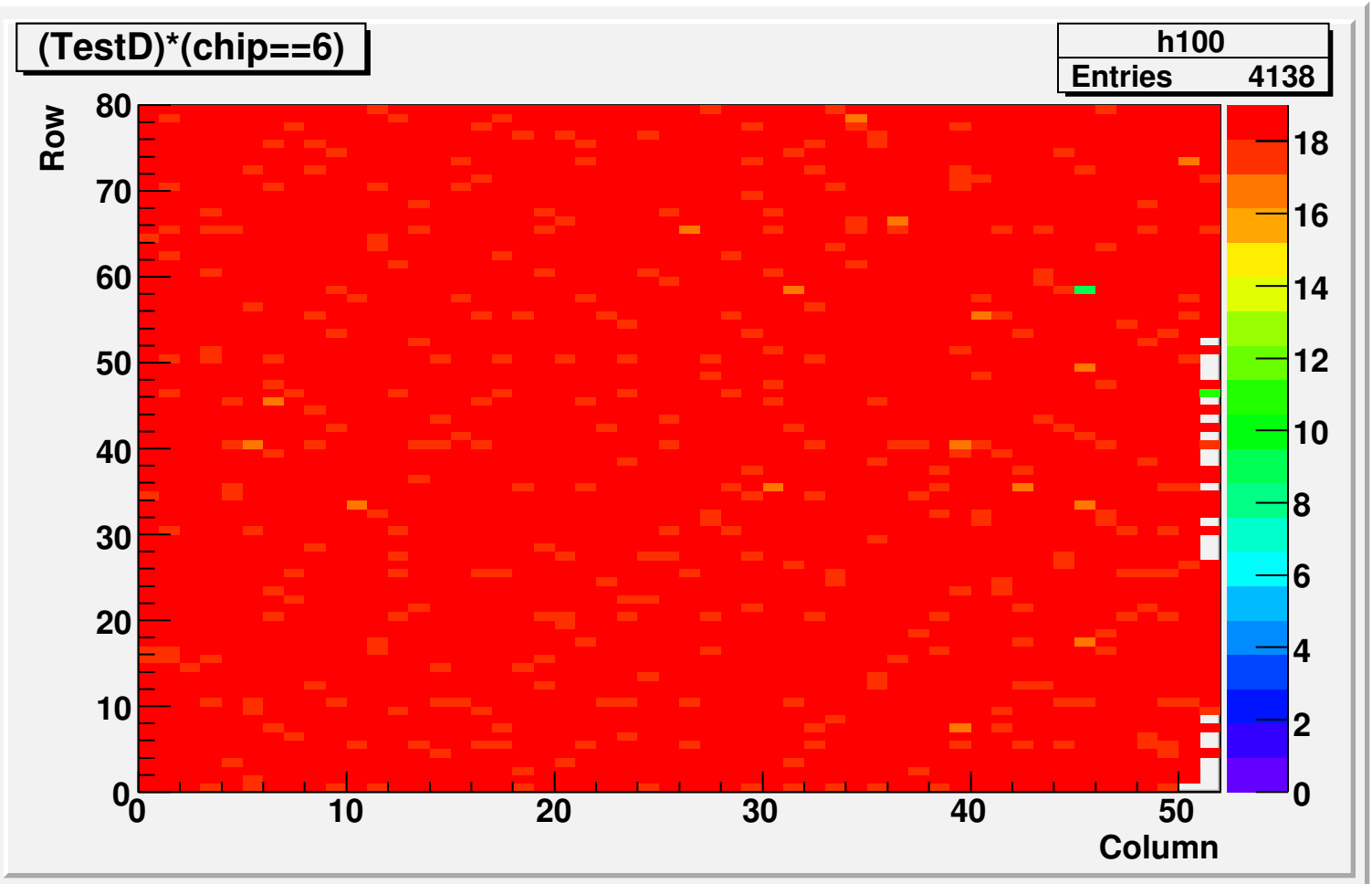
(TestD)*(chip==6)
h100
Entries 4138
Row
Column
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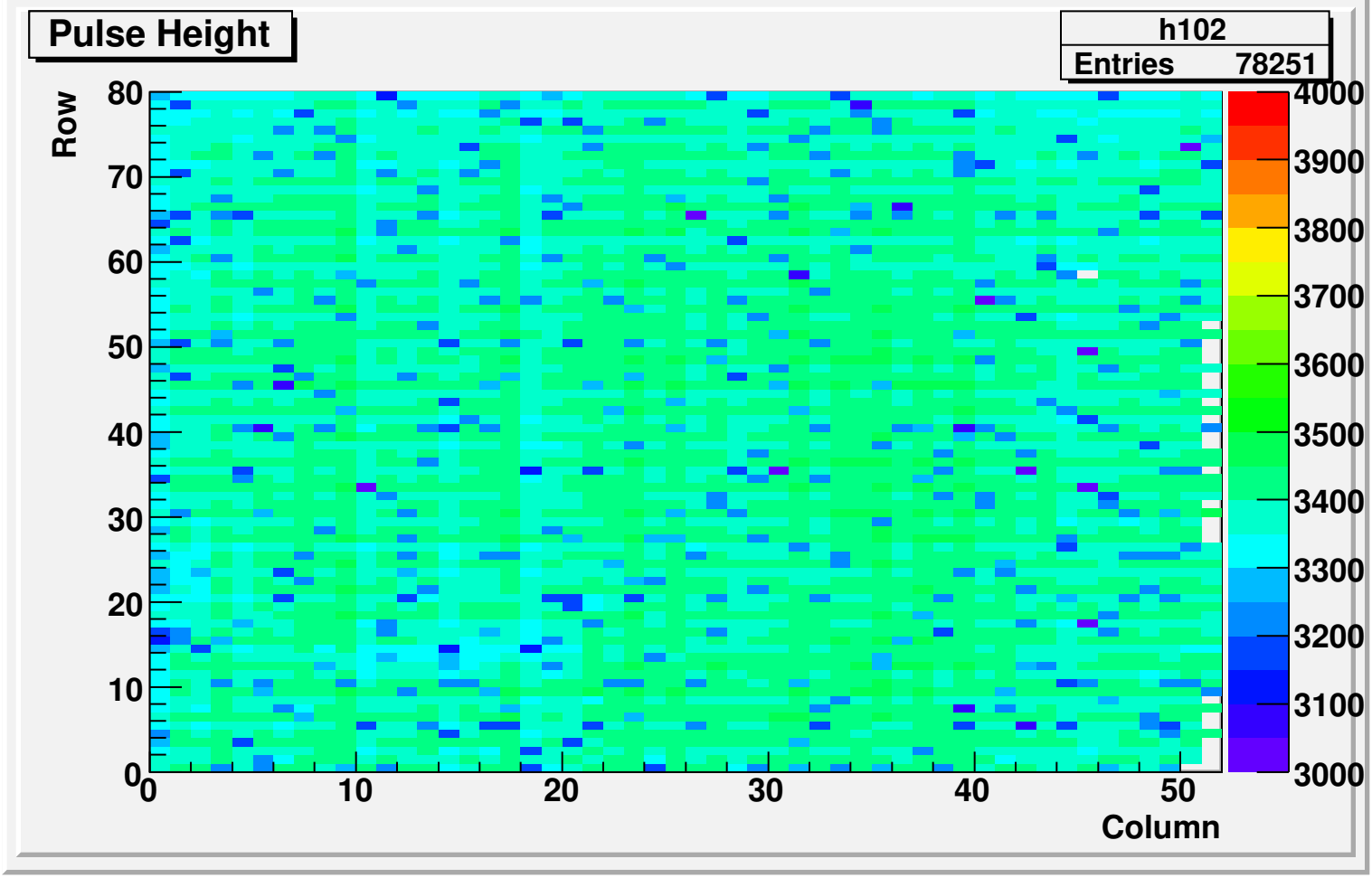
Pulse Height
h102
Entries 78251
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Column
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3900
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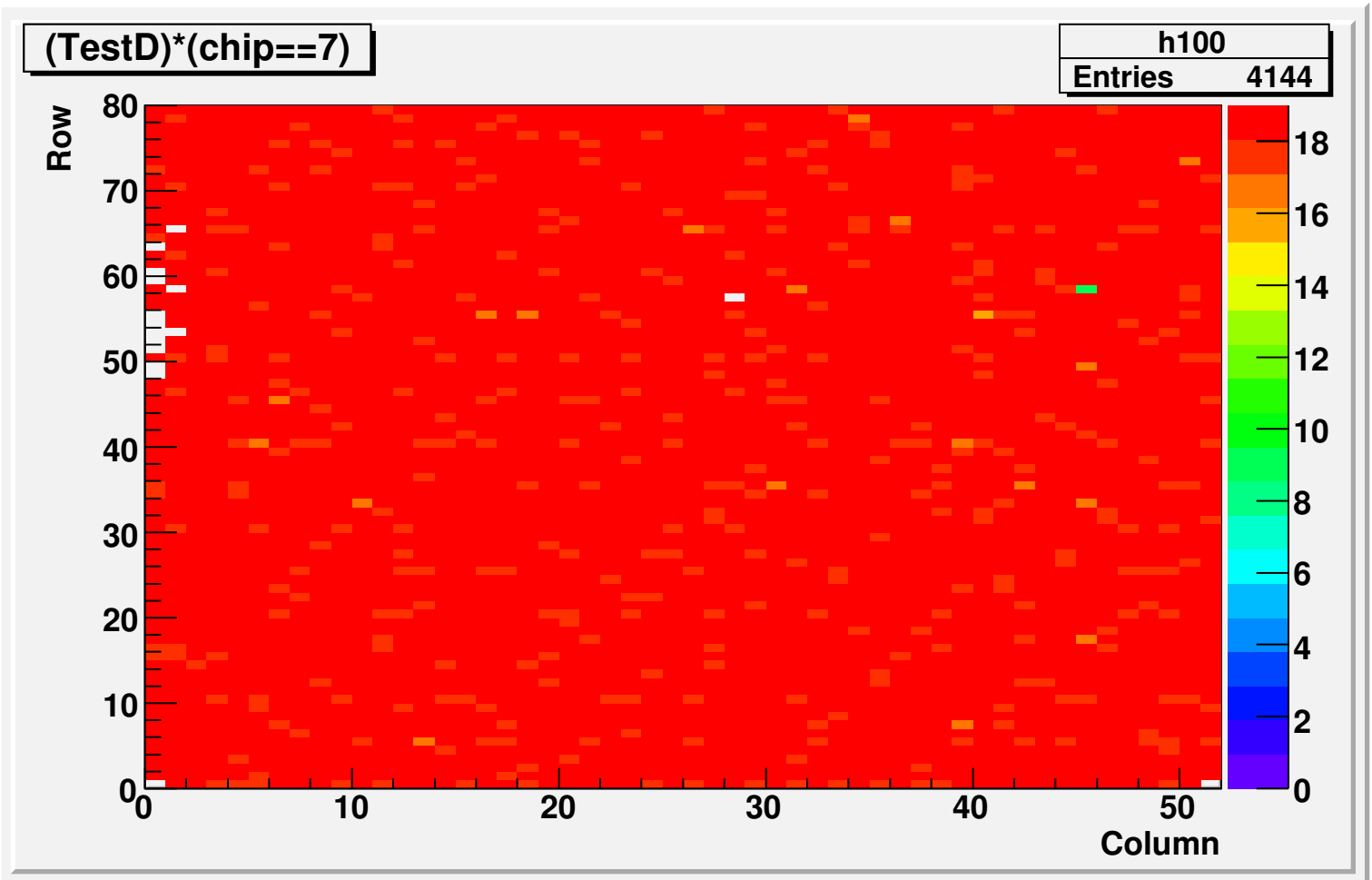
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h100
Entries 4144
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Column
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12
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16
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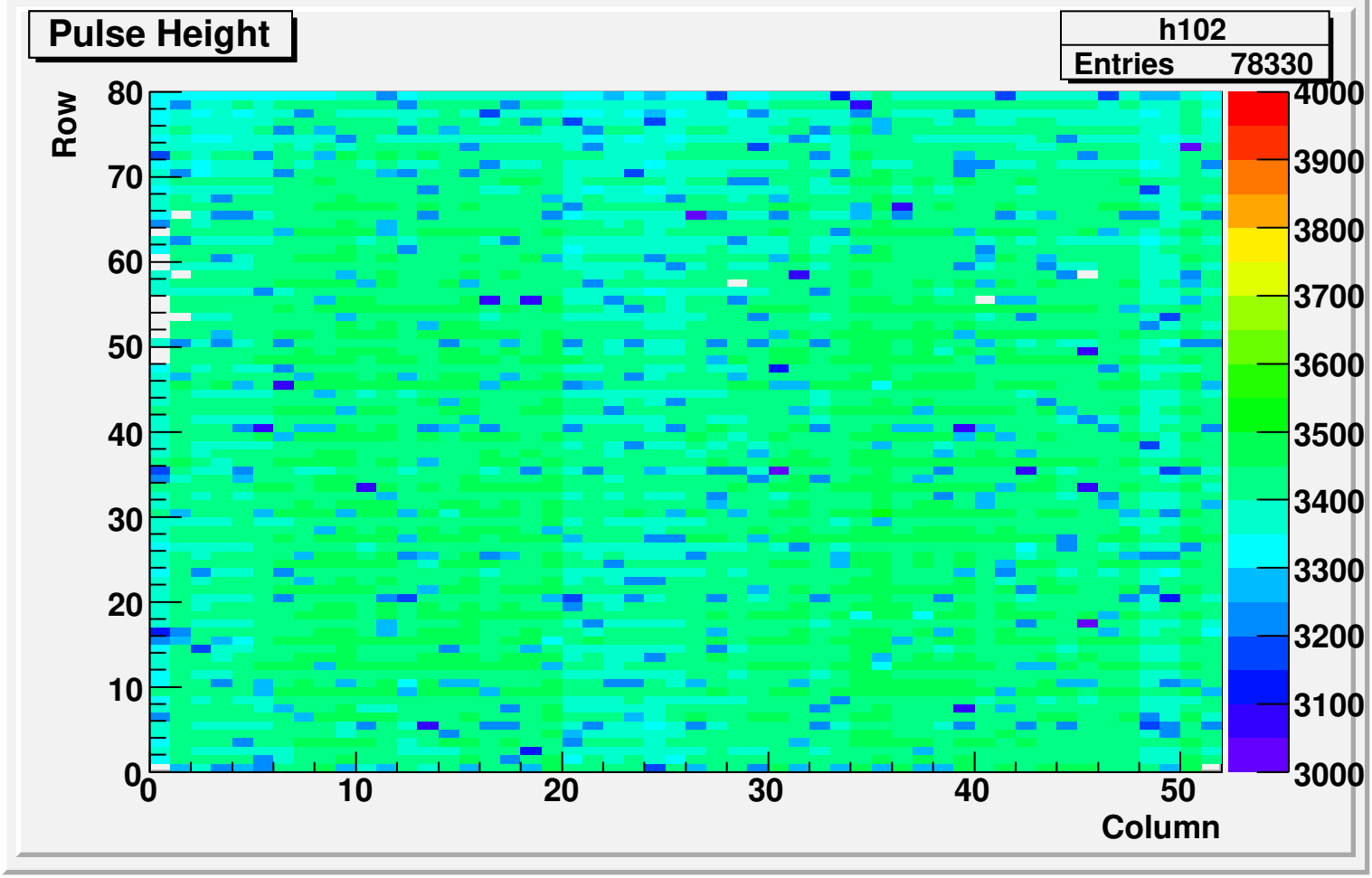
Pulse Height
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Entries 78330
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Column
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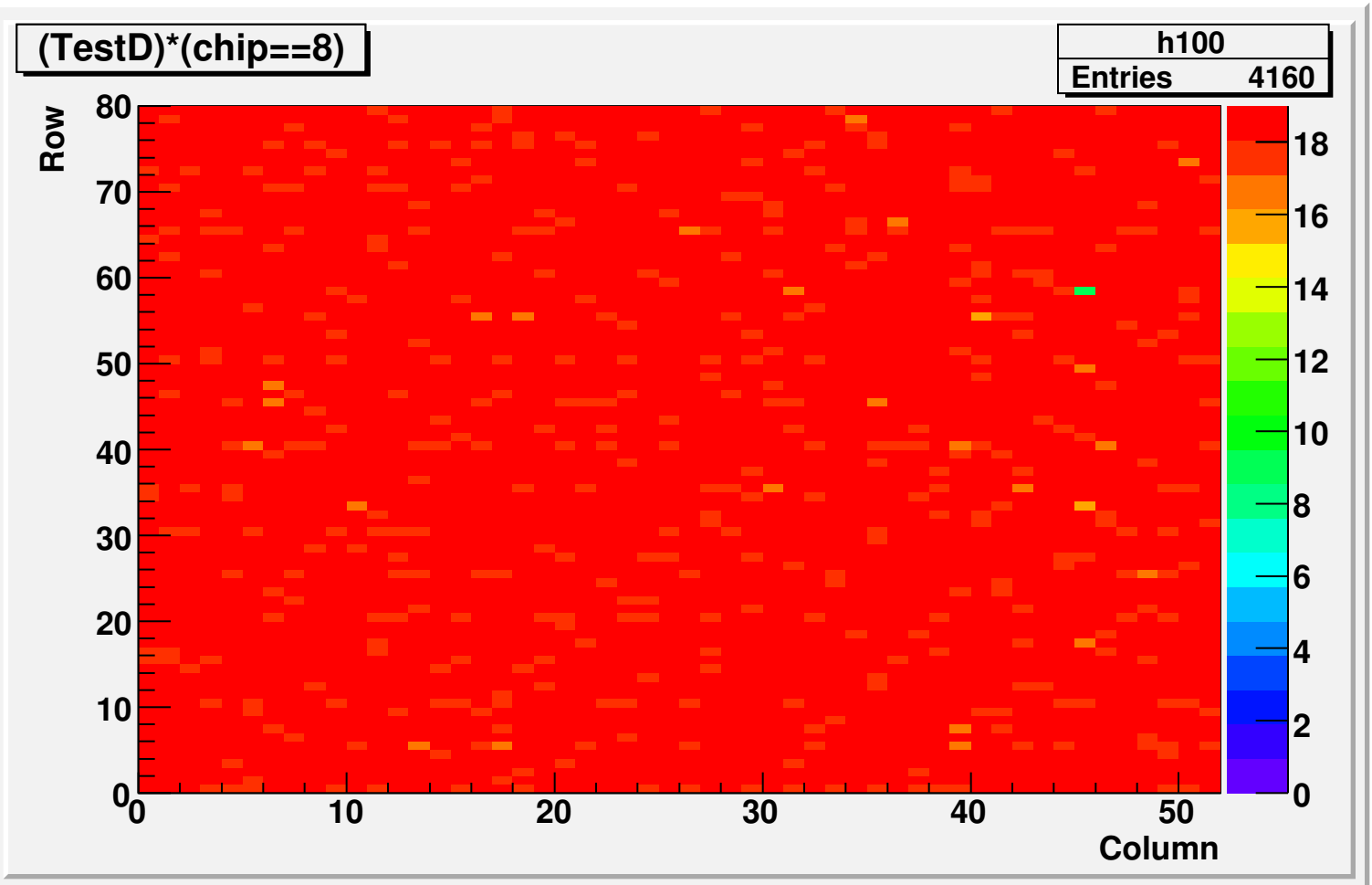
(TestD)*(chip==8)
h100
Entries 4160
Row
Column
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80
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8
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12
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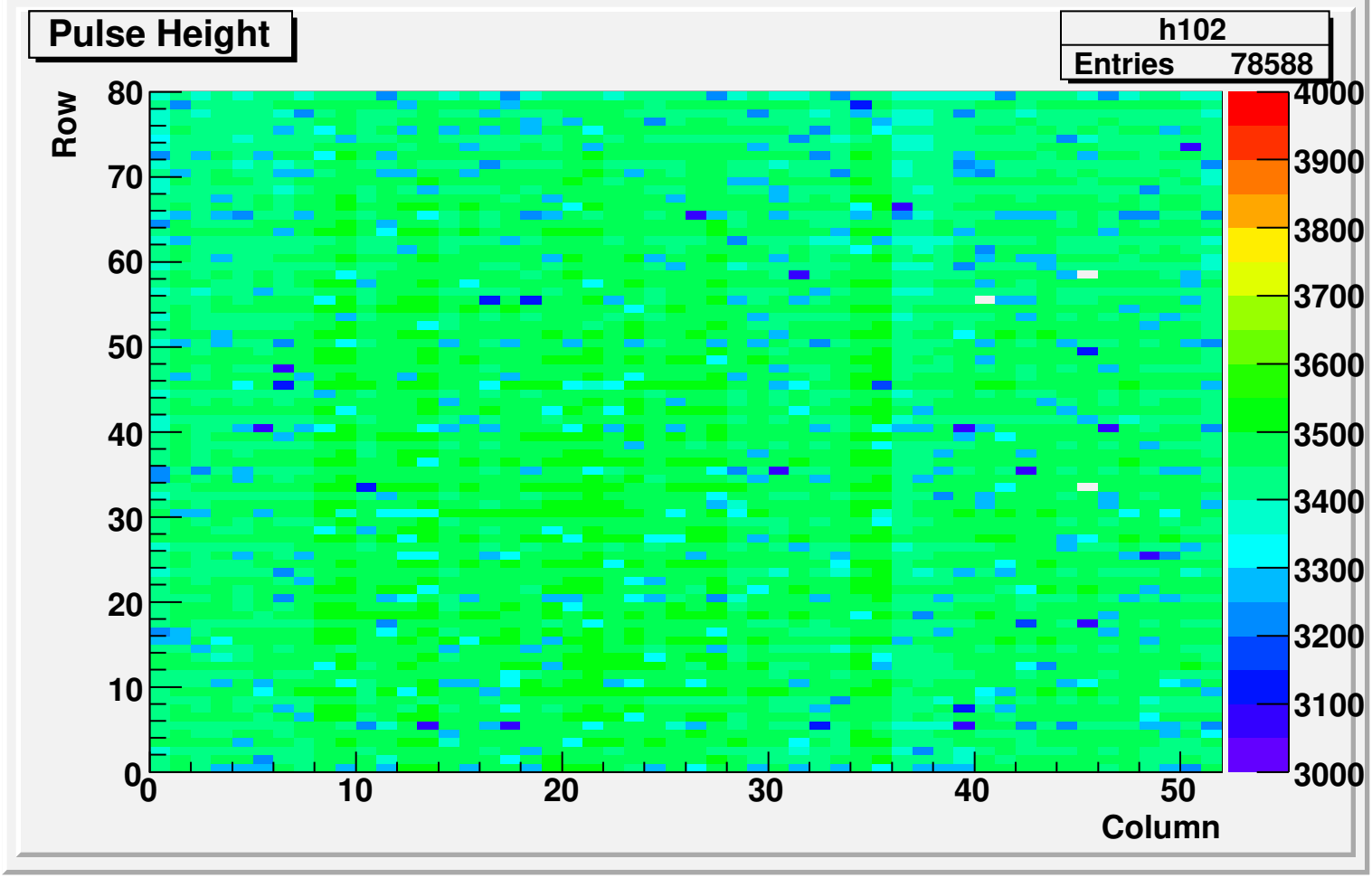
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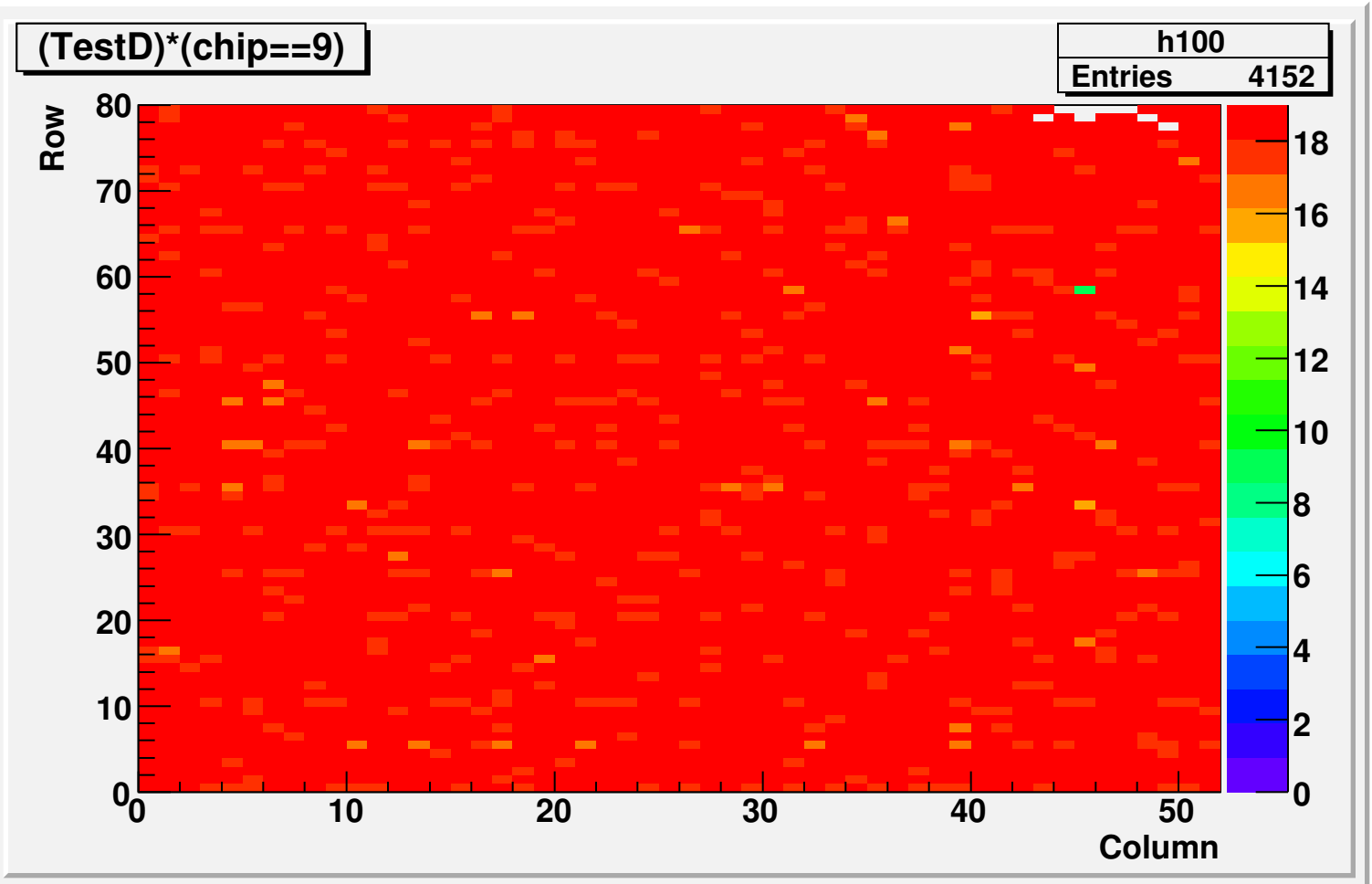
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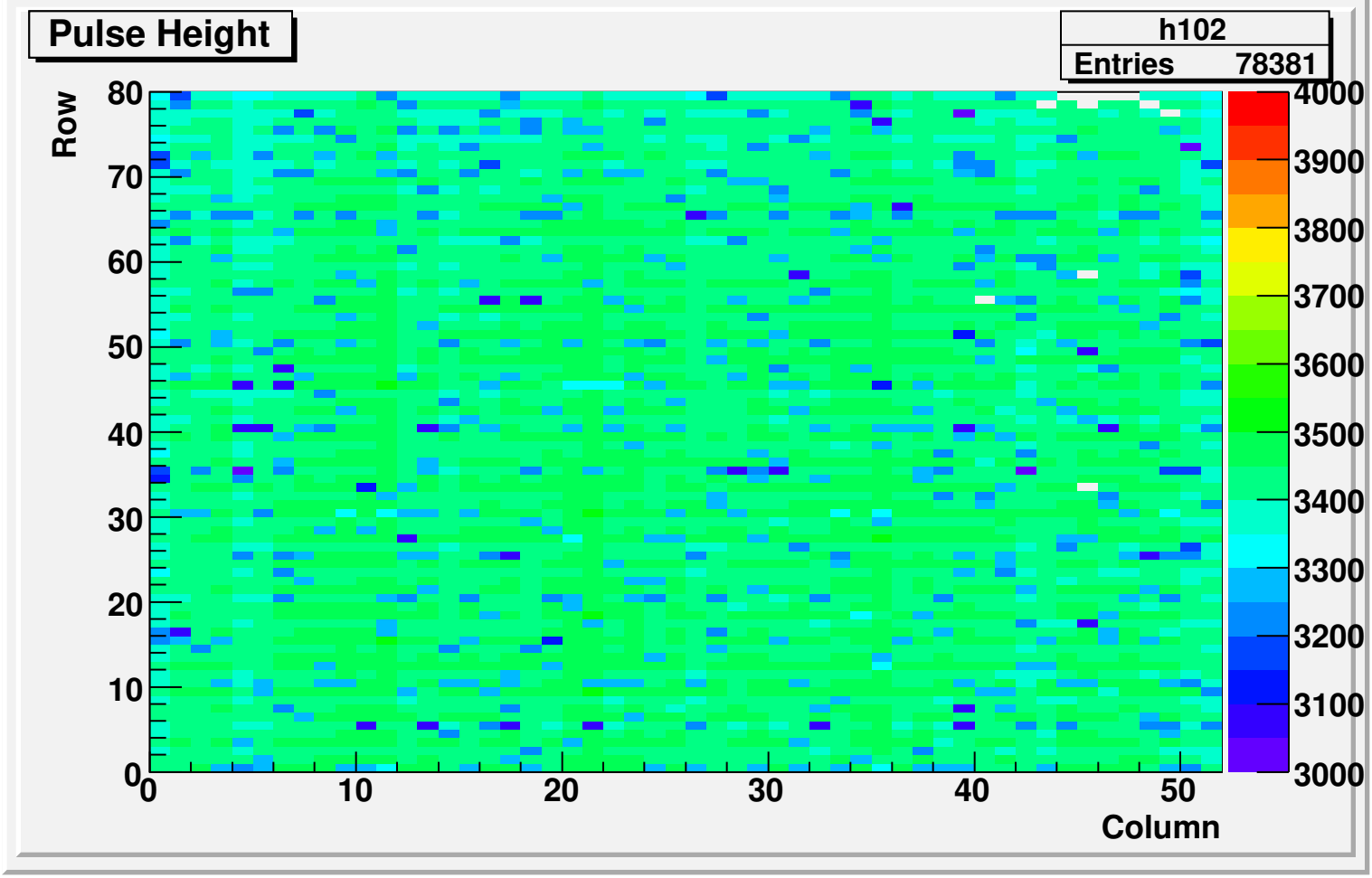
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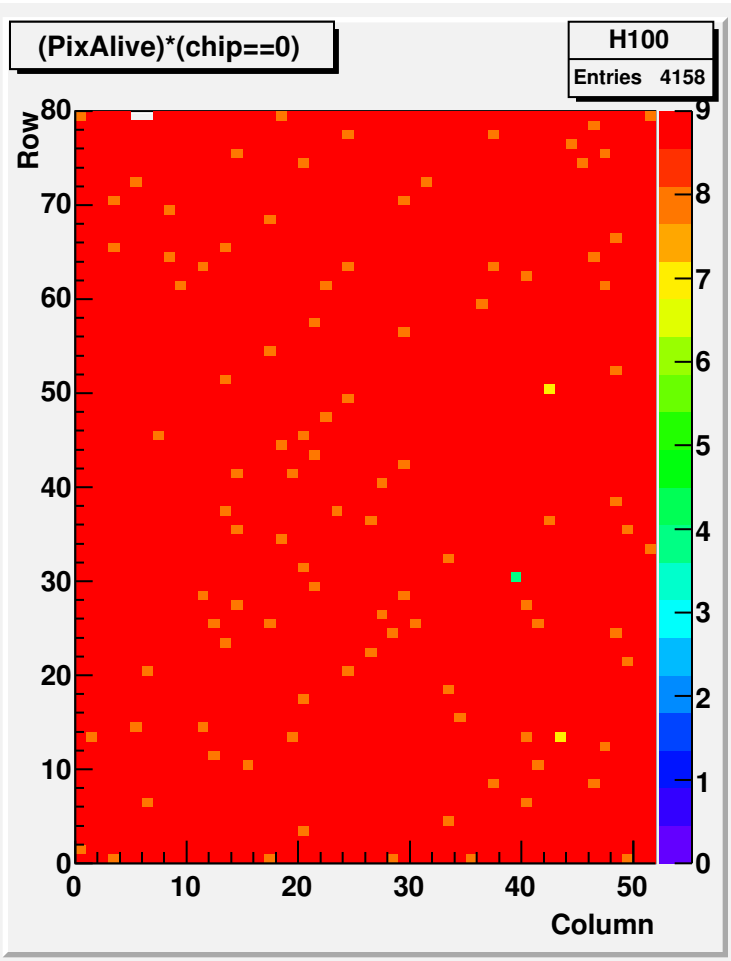
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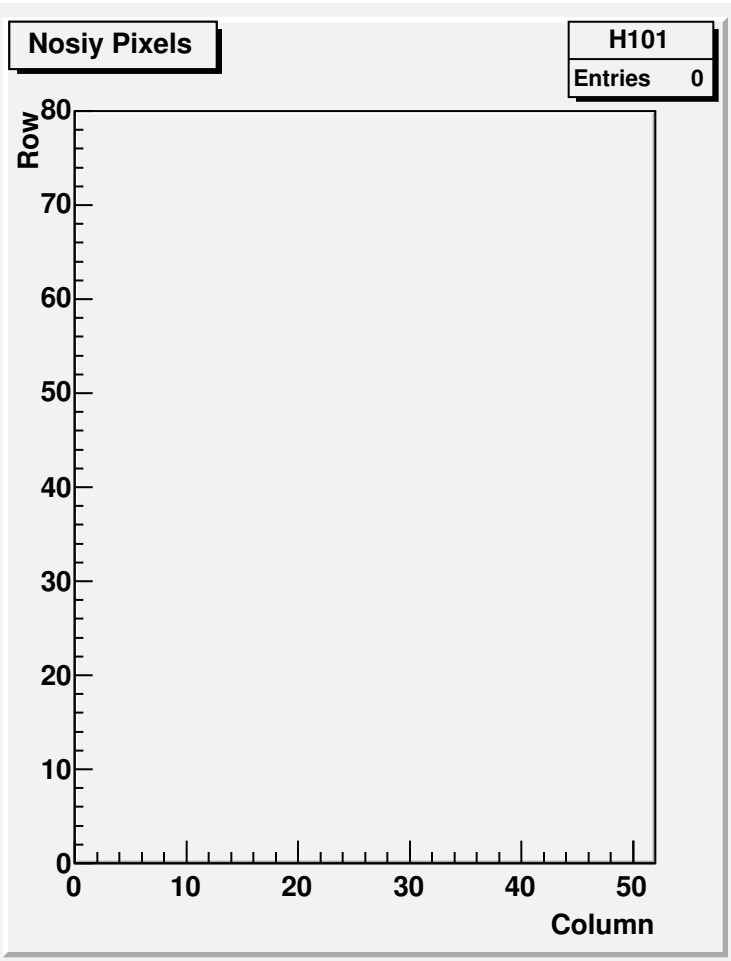
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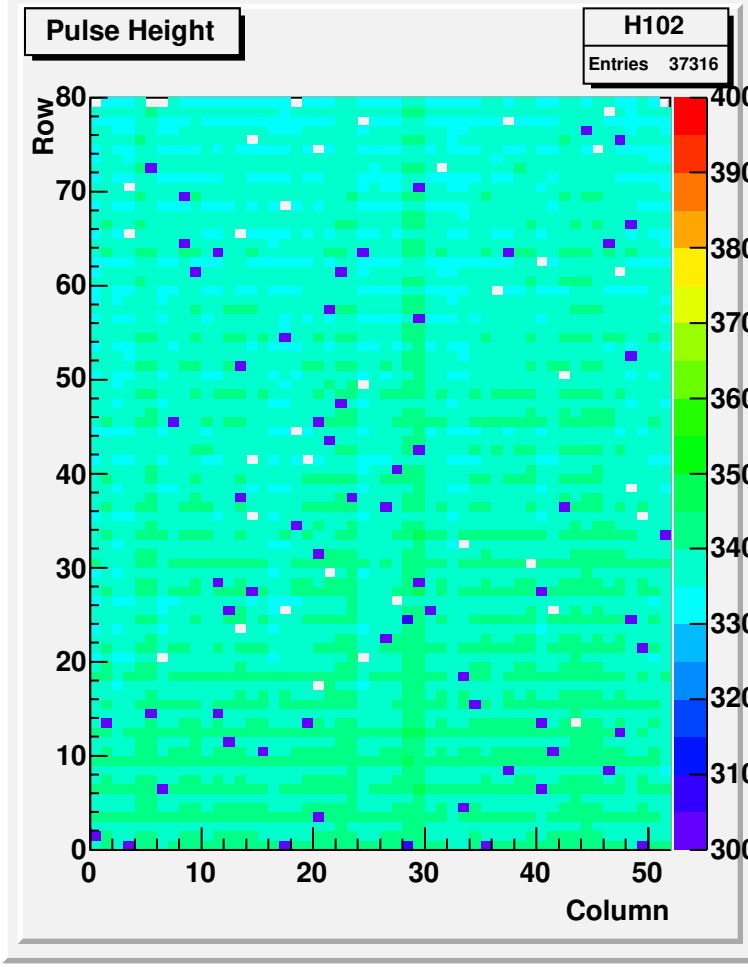
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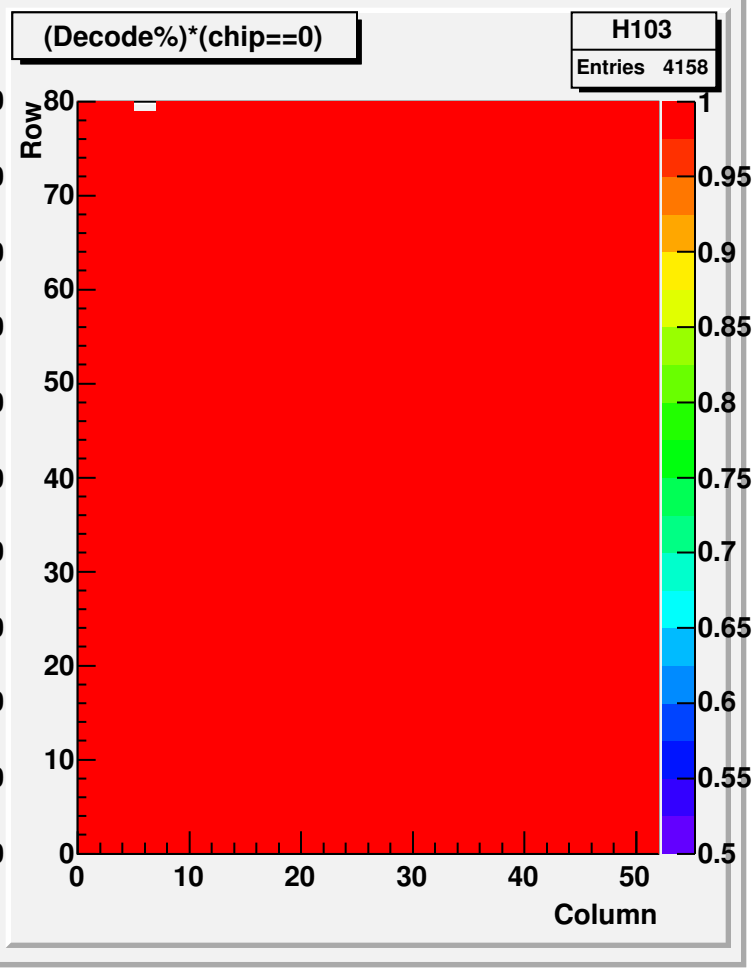
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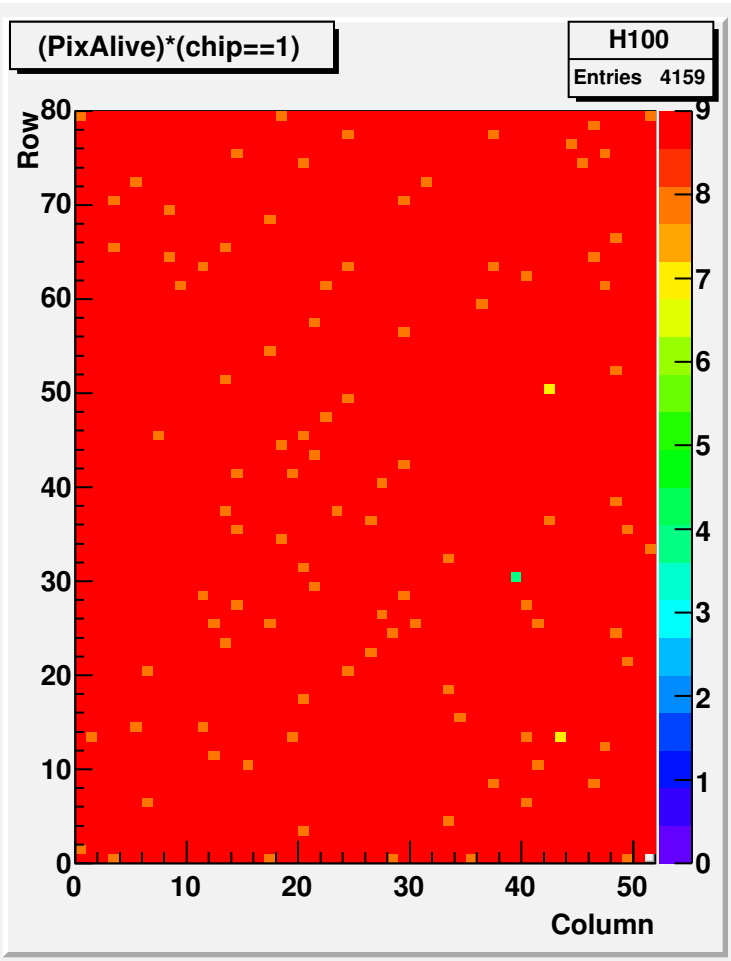
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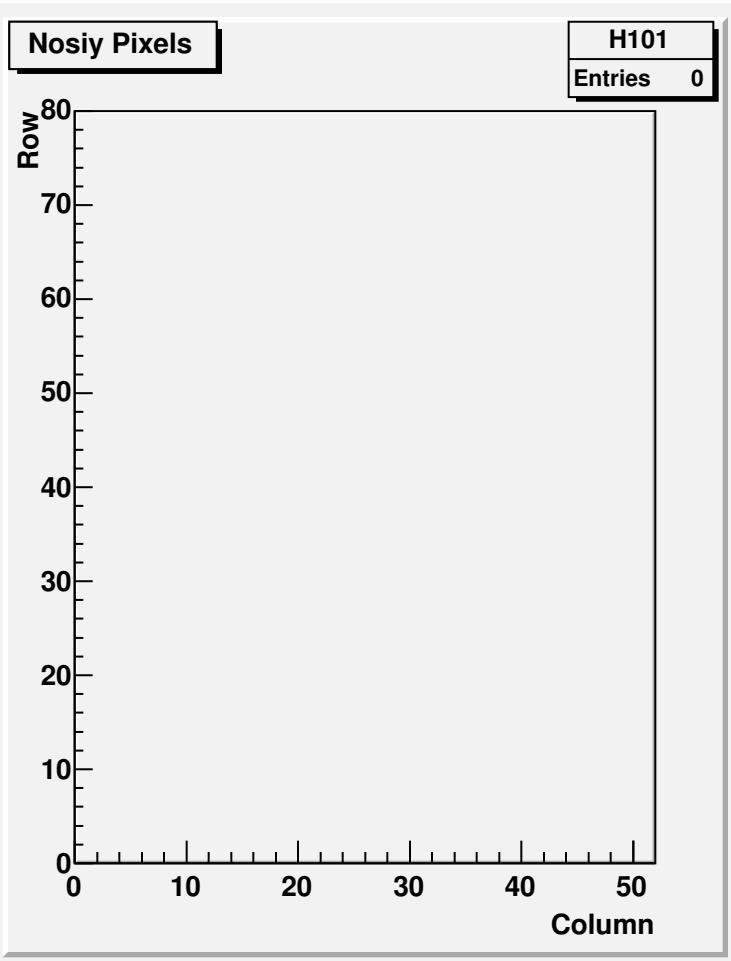
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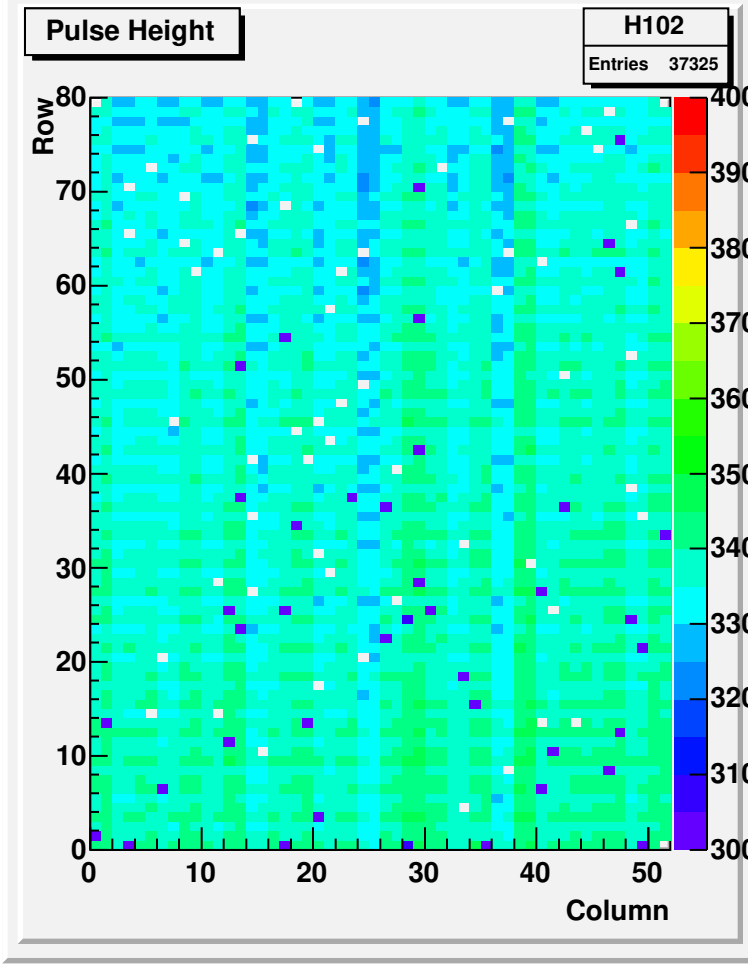
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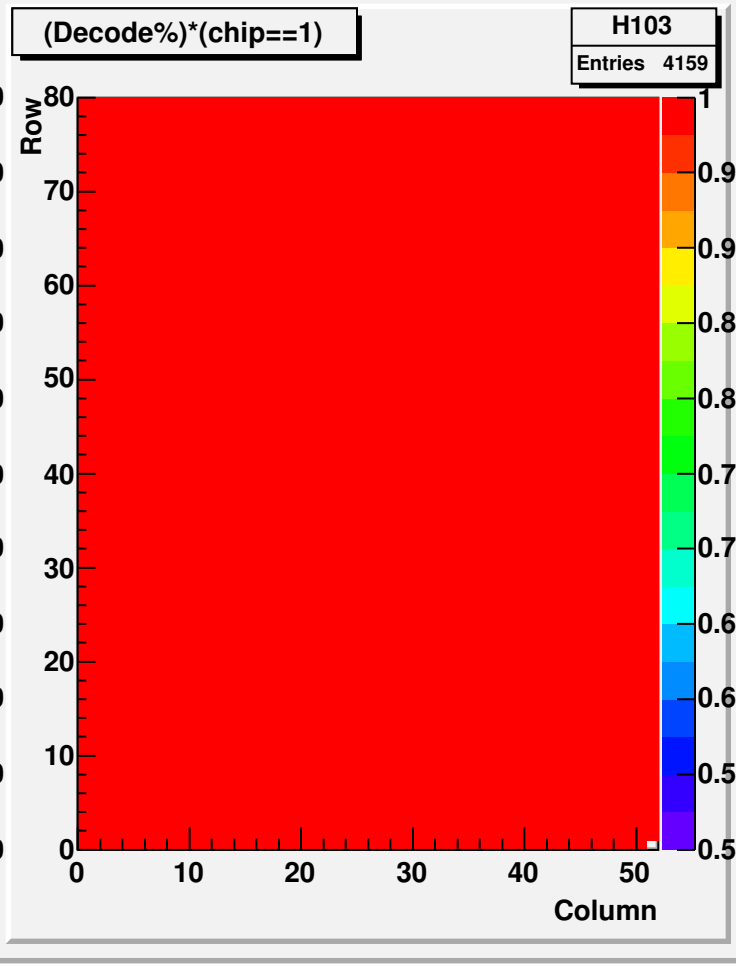
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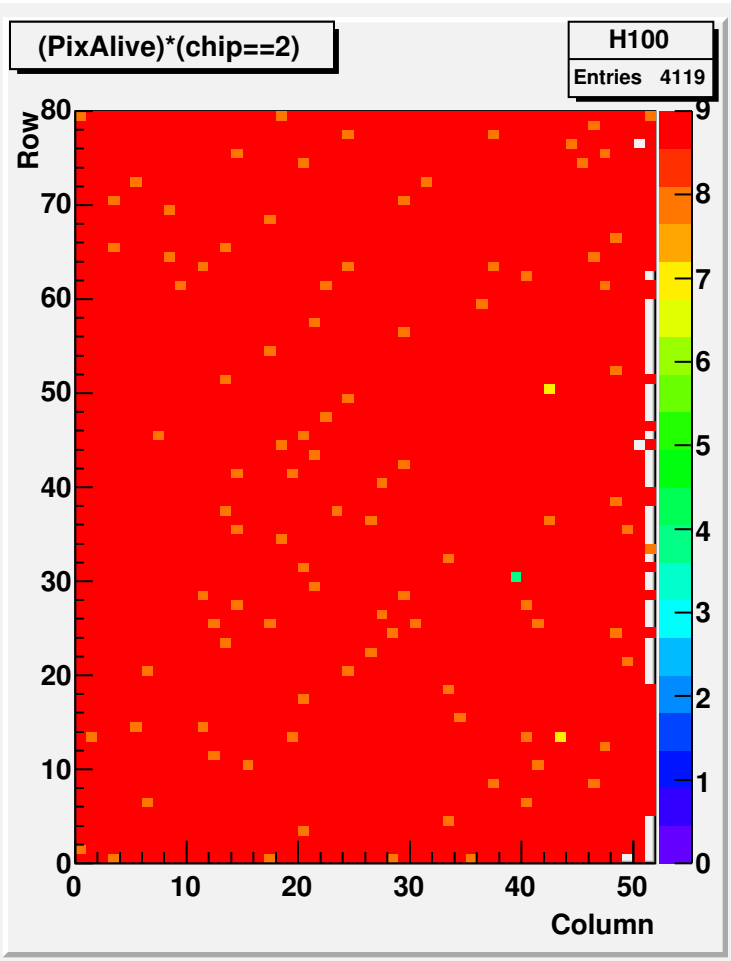
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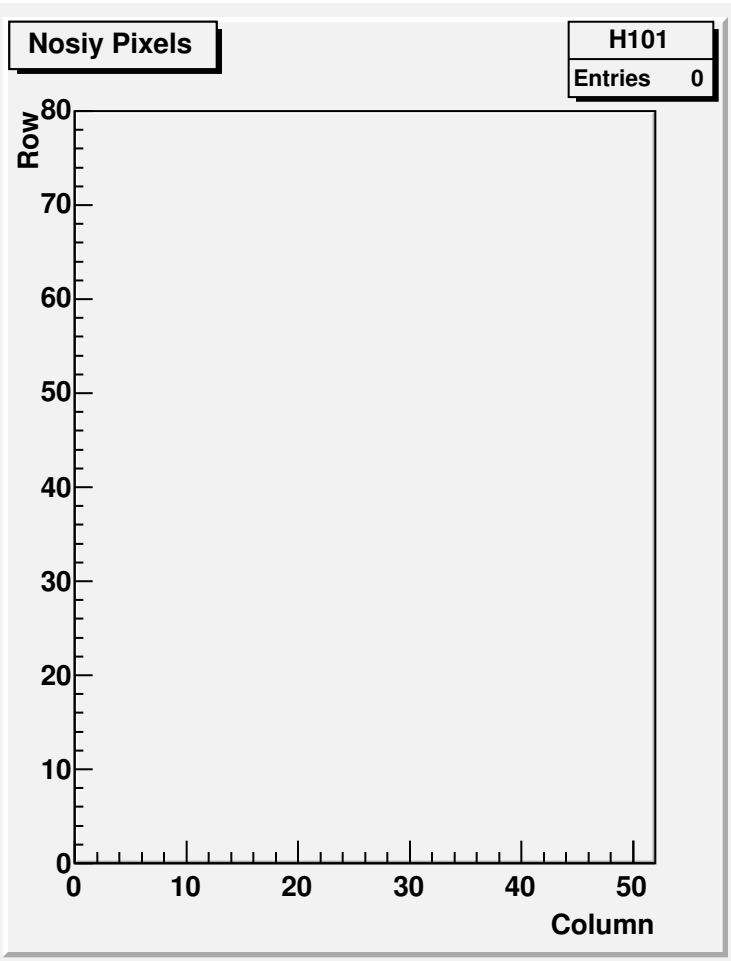
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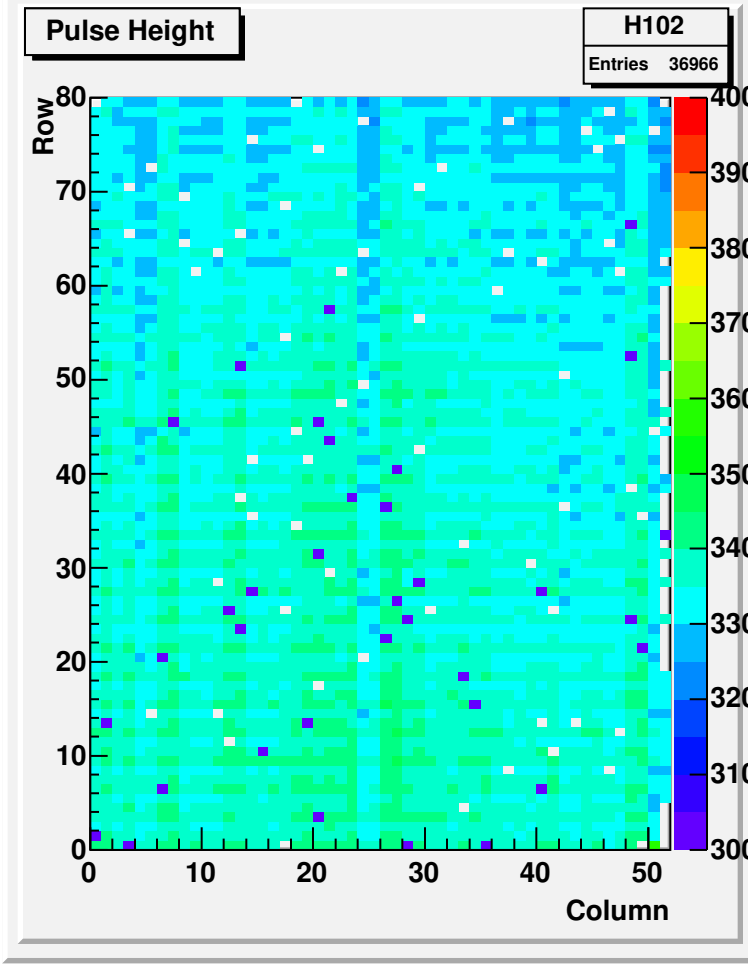
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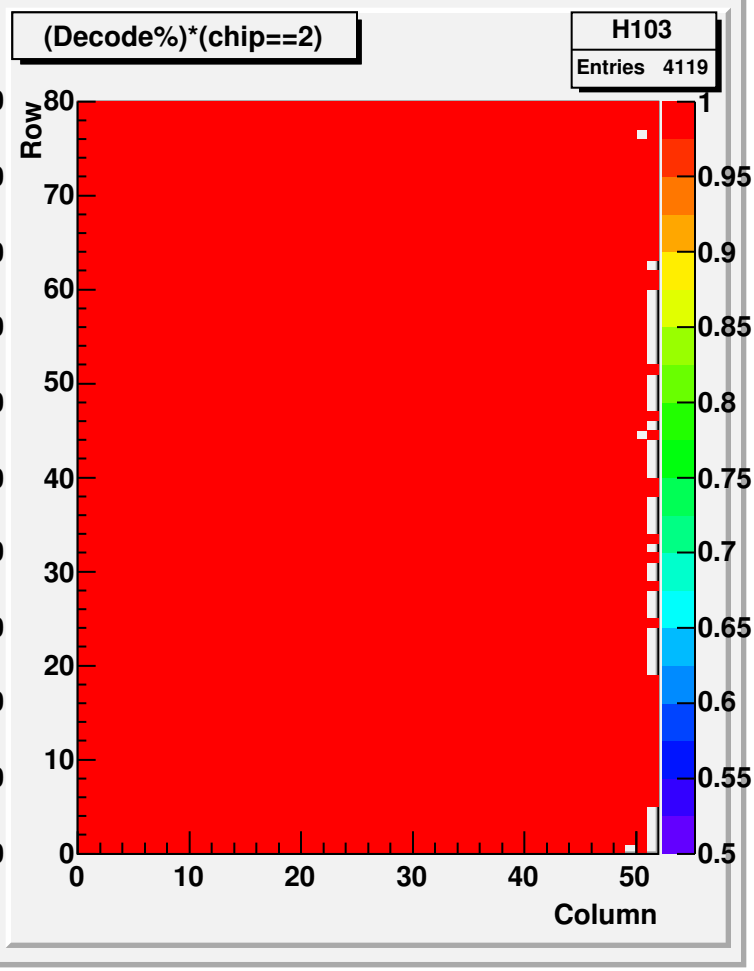
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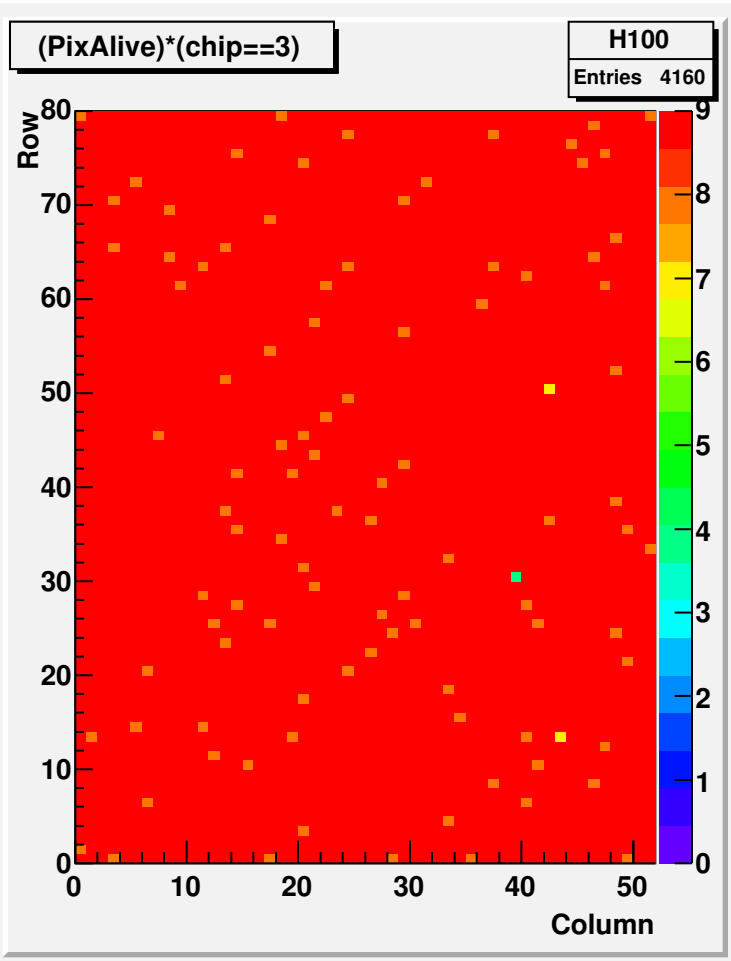
Column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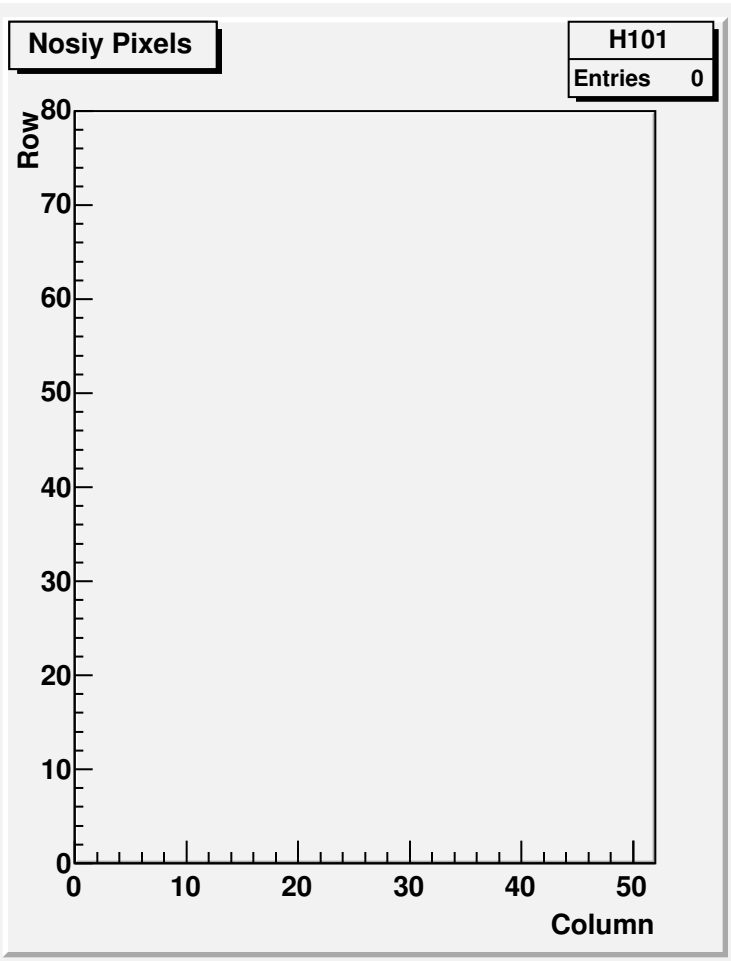
Column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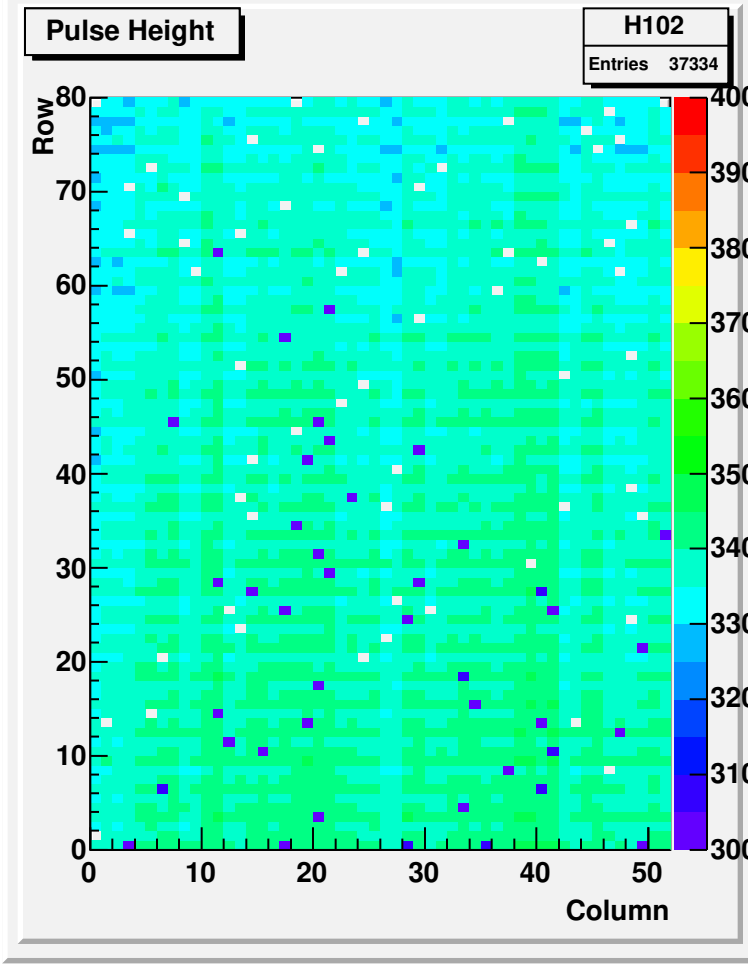
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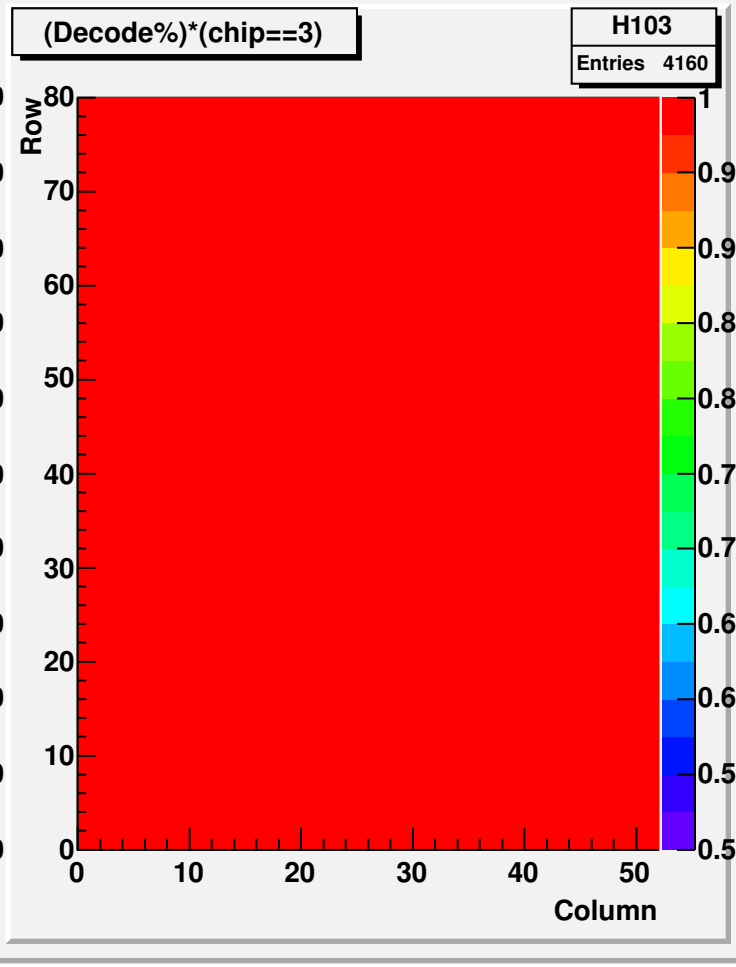
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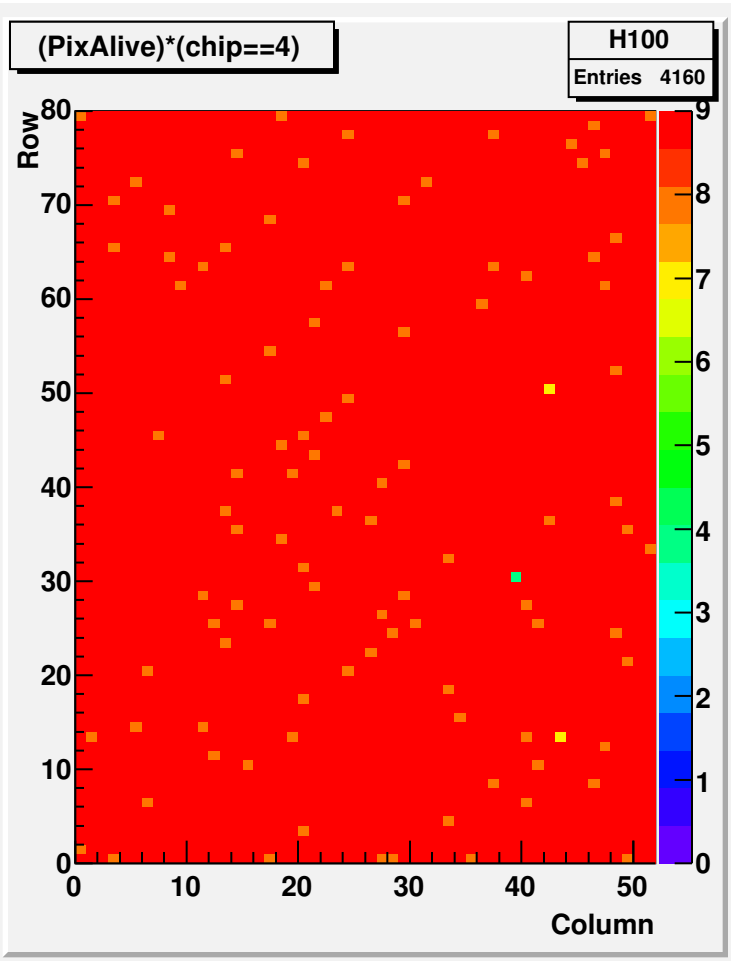
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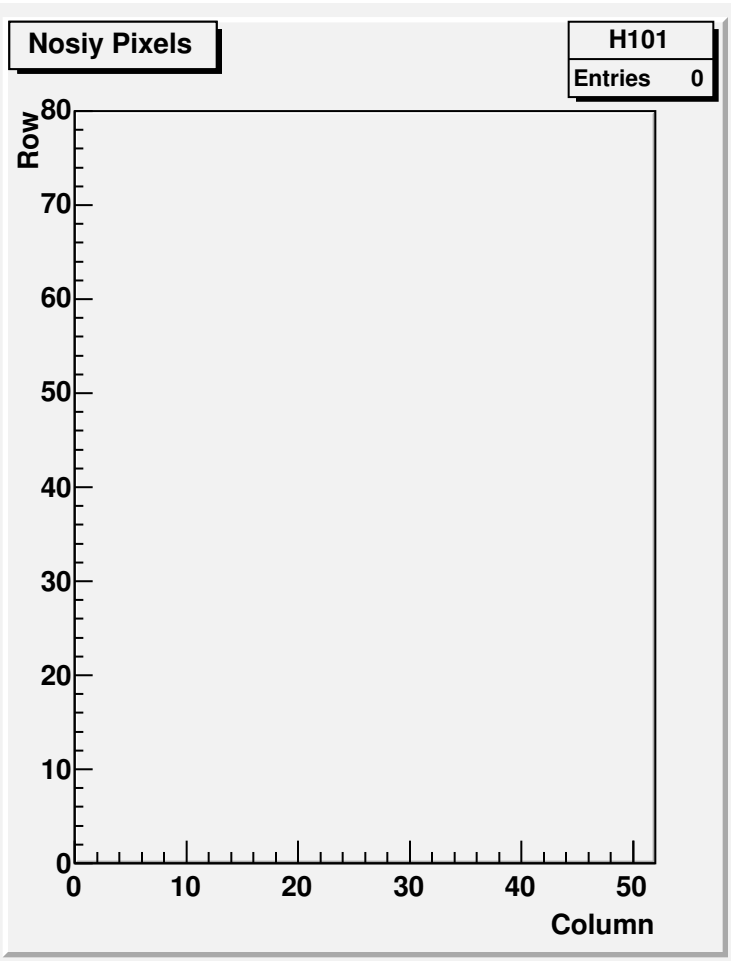
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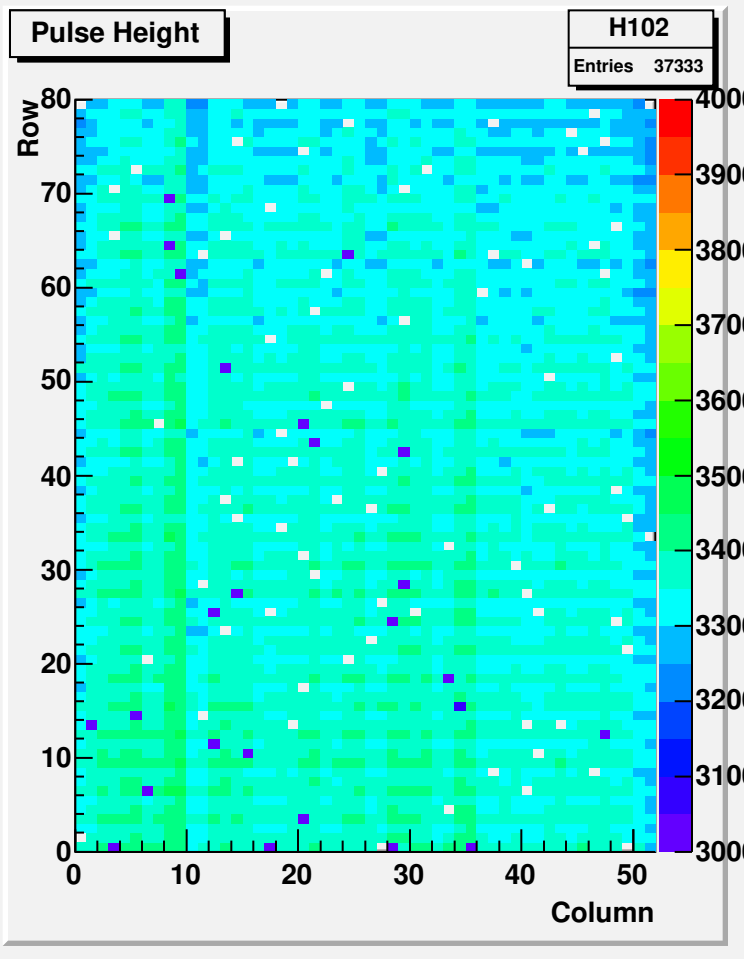
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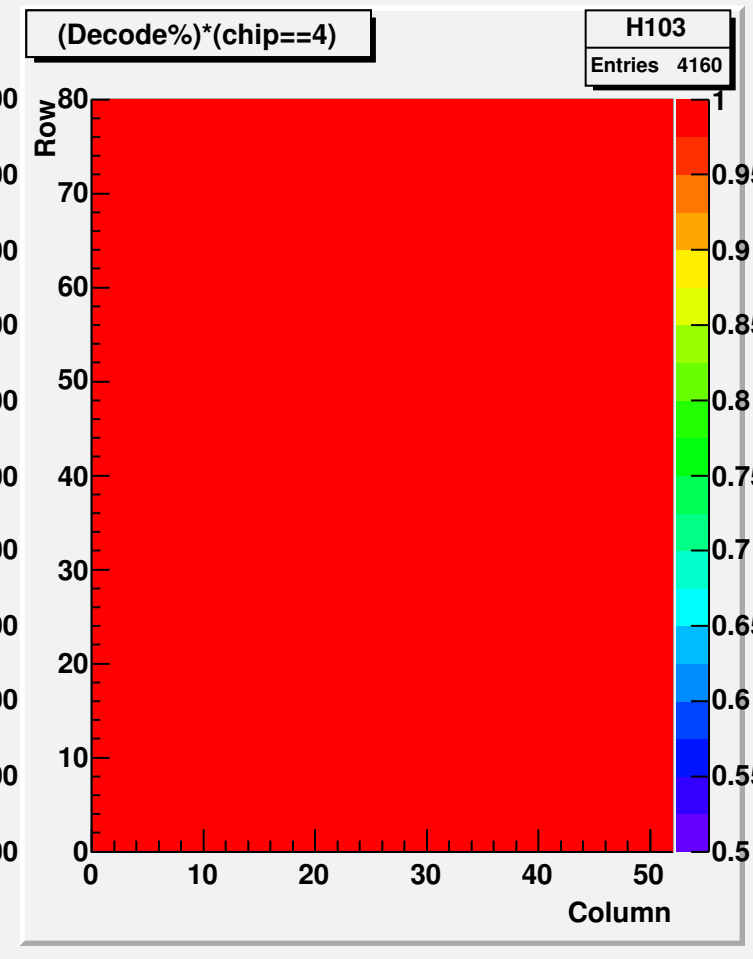
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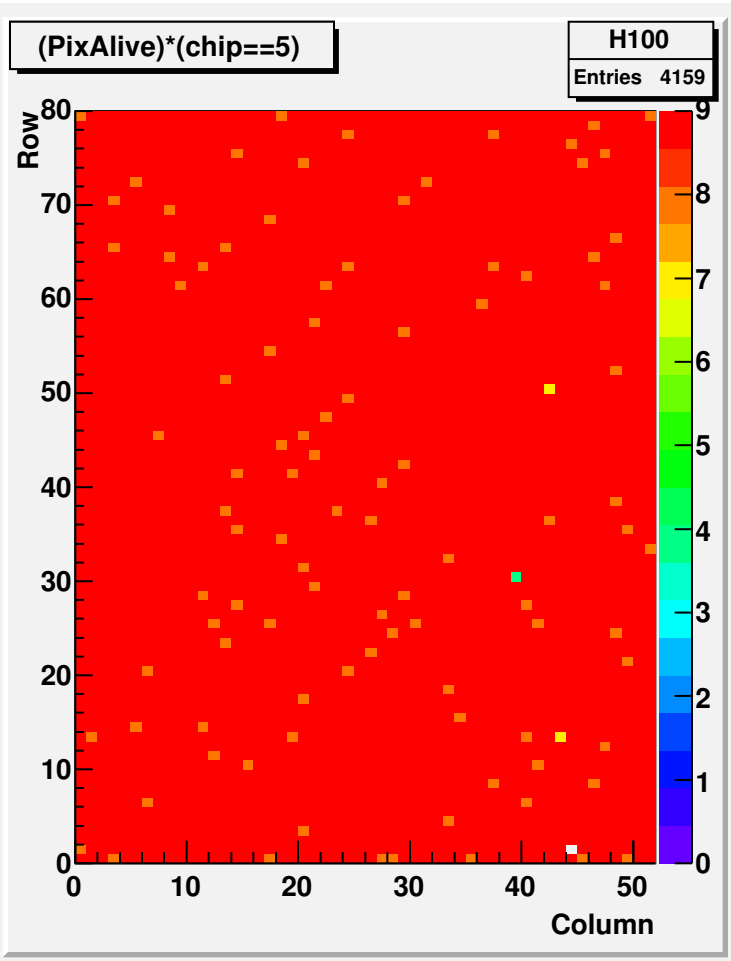
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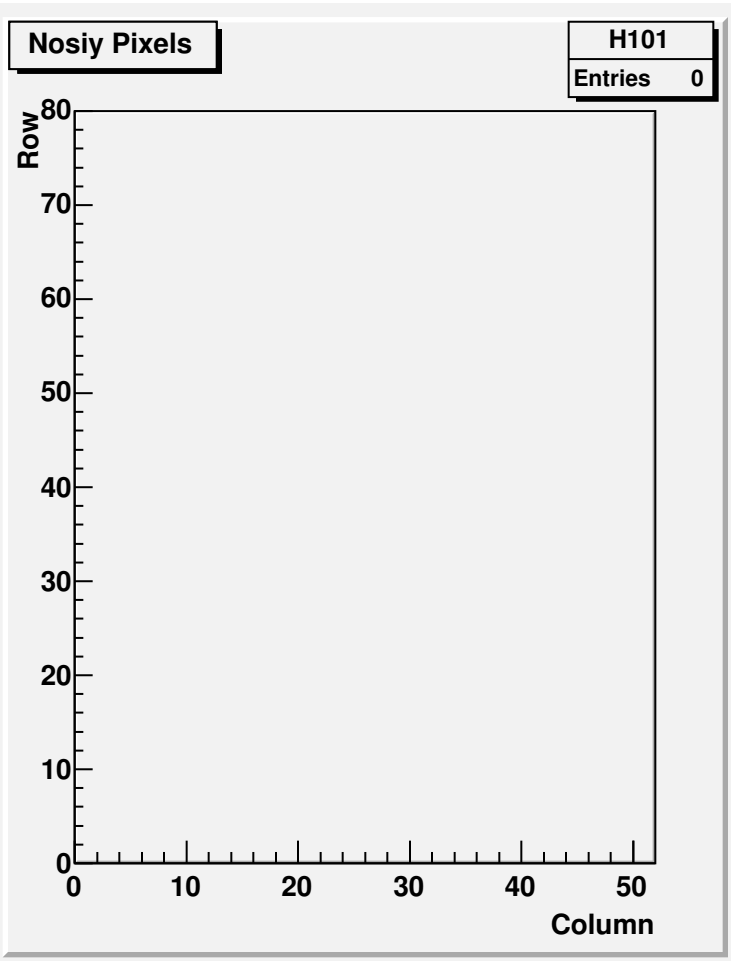
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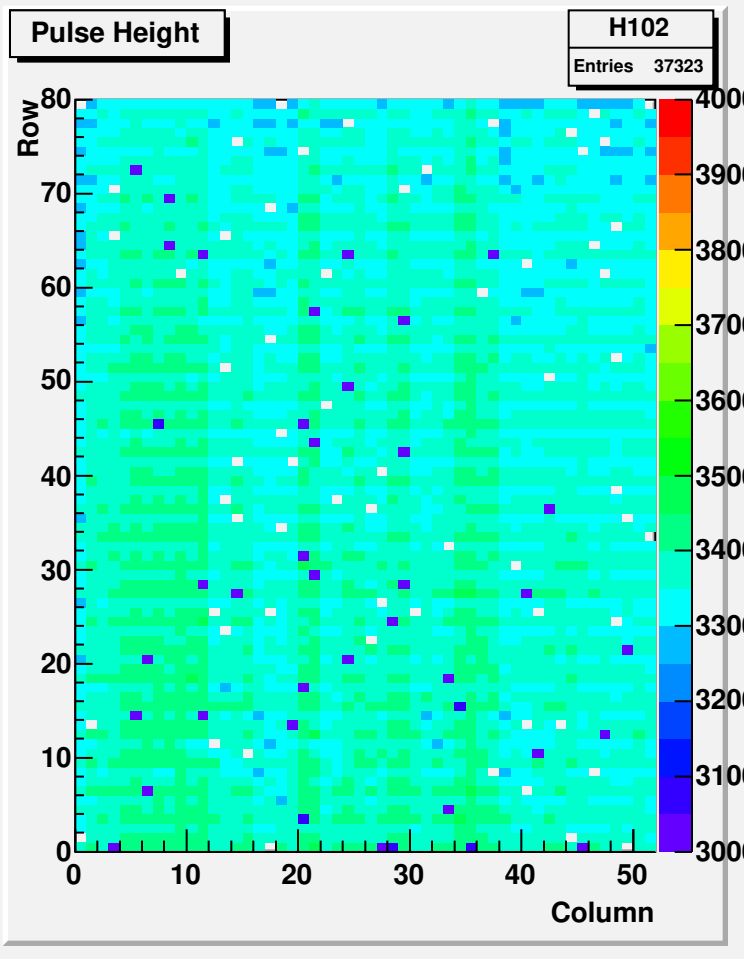
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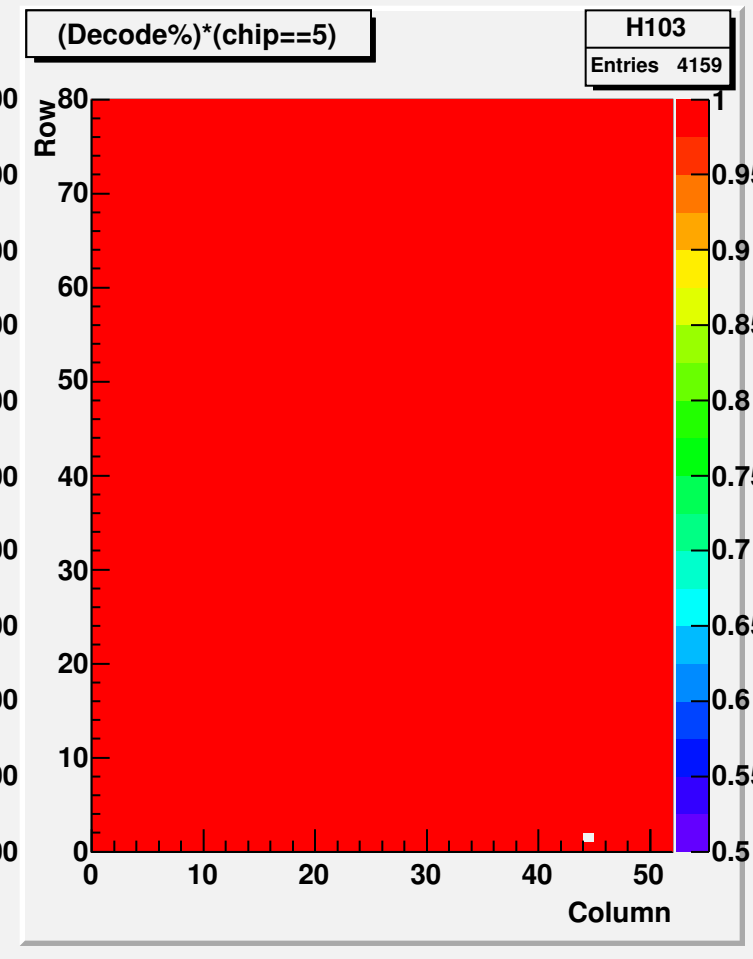
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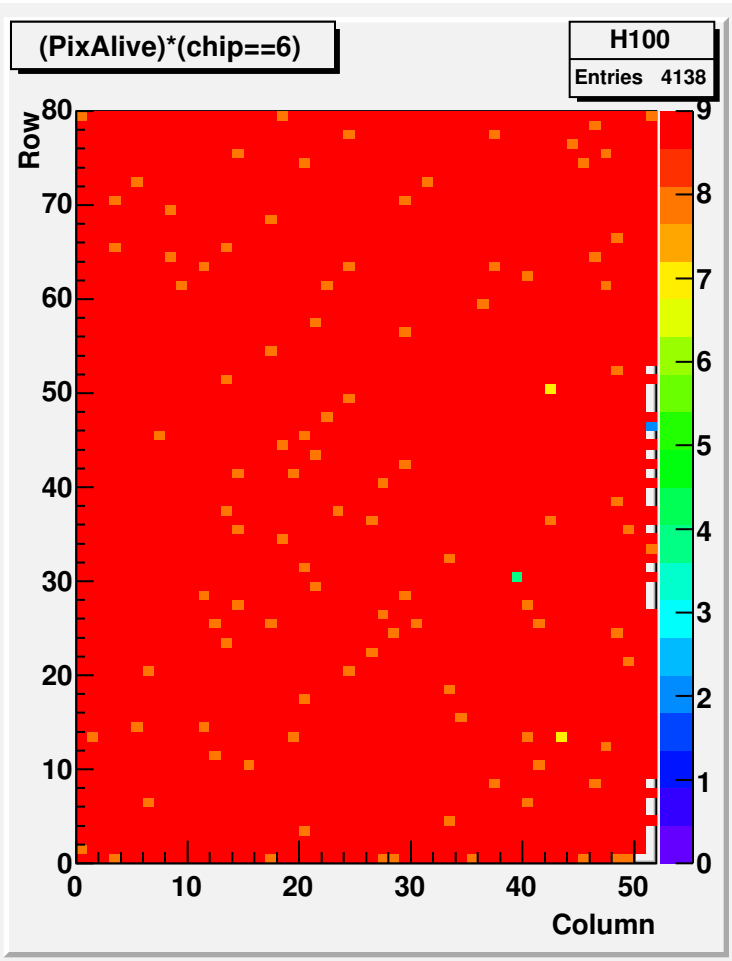
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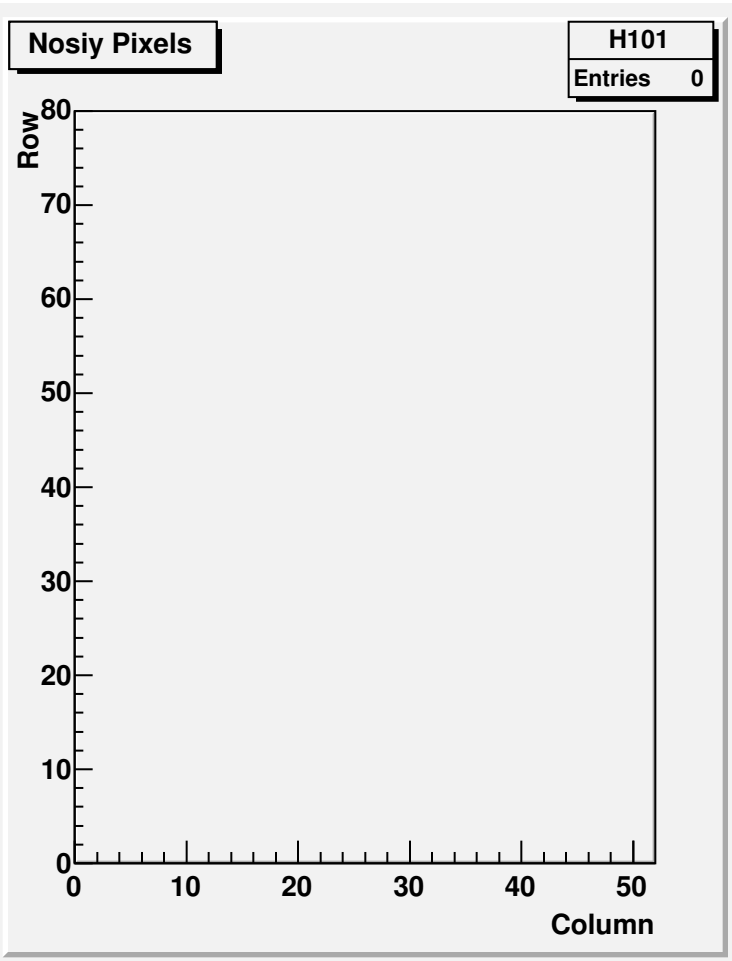
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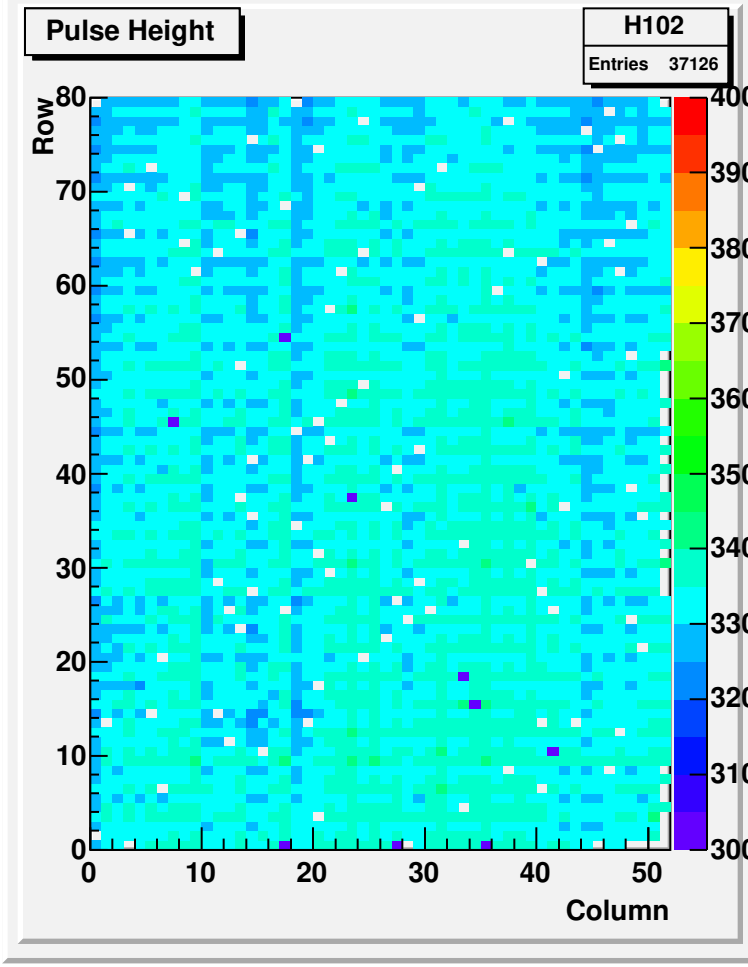
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H102
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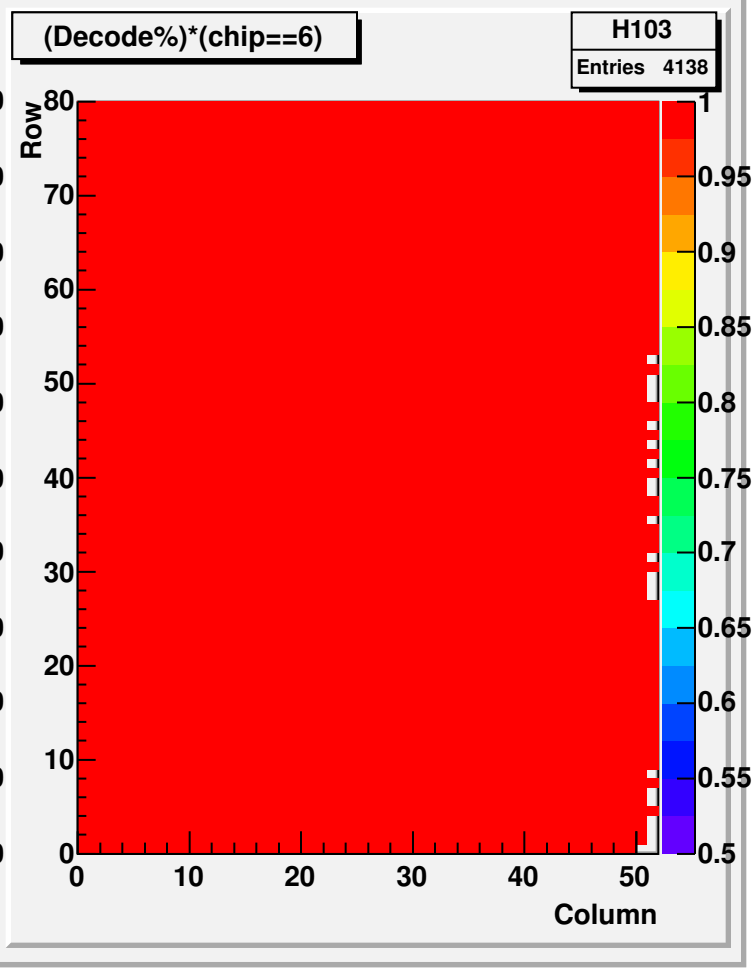
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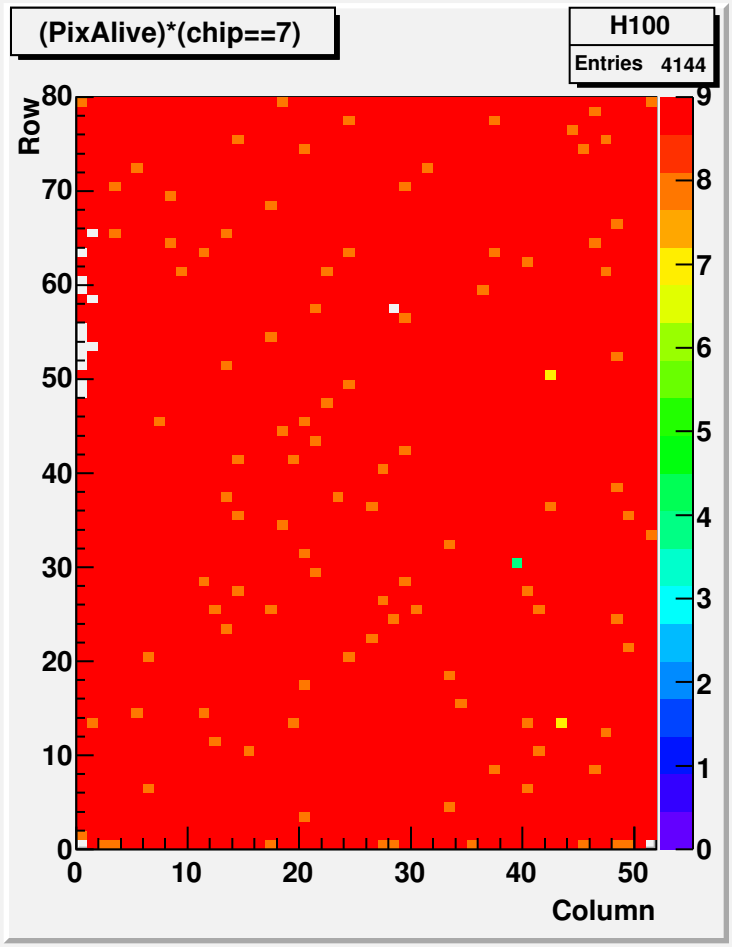
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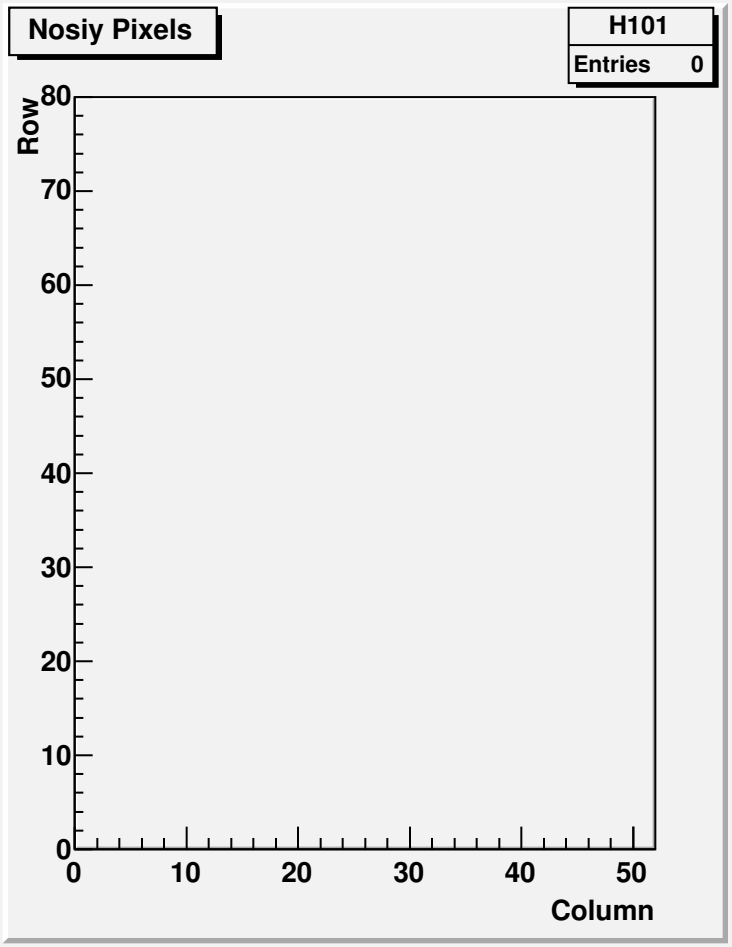
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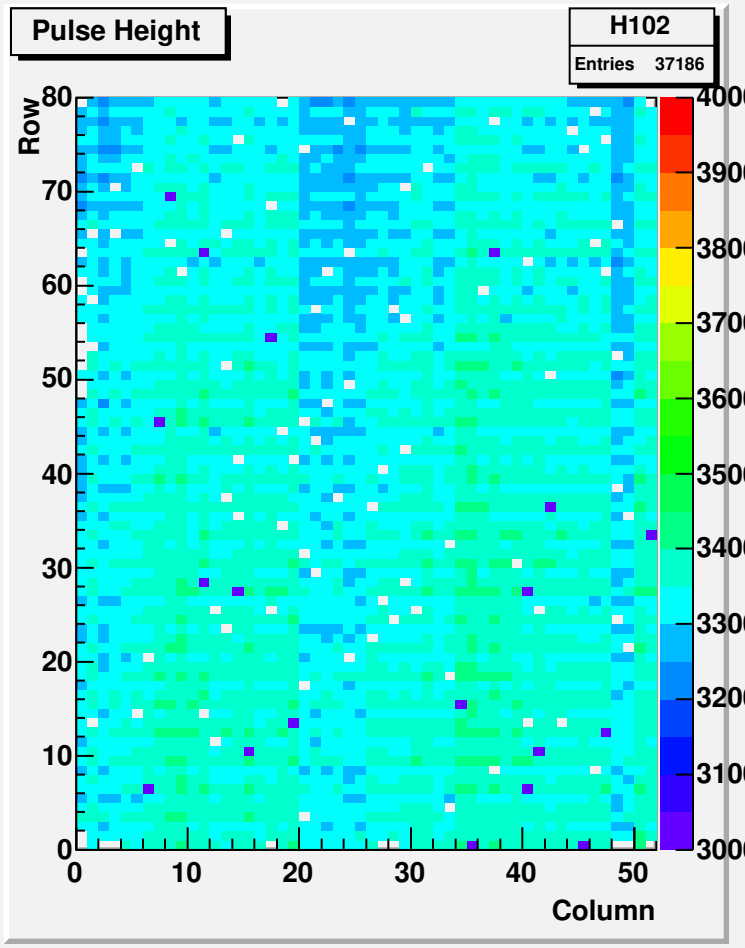
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H102
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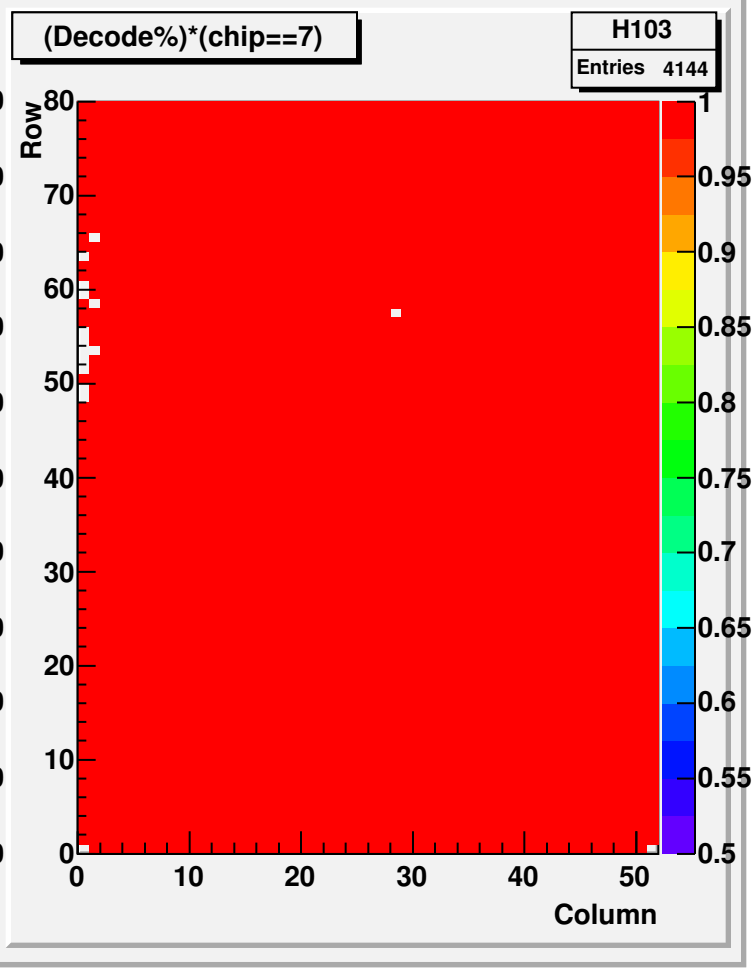
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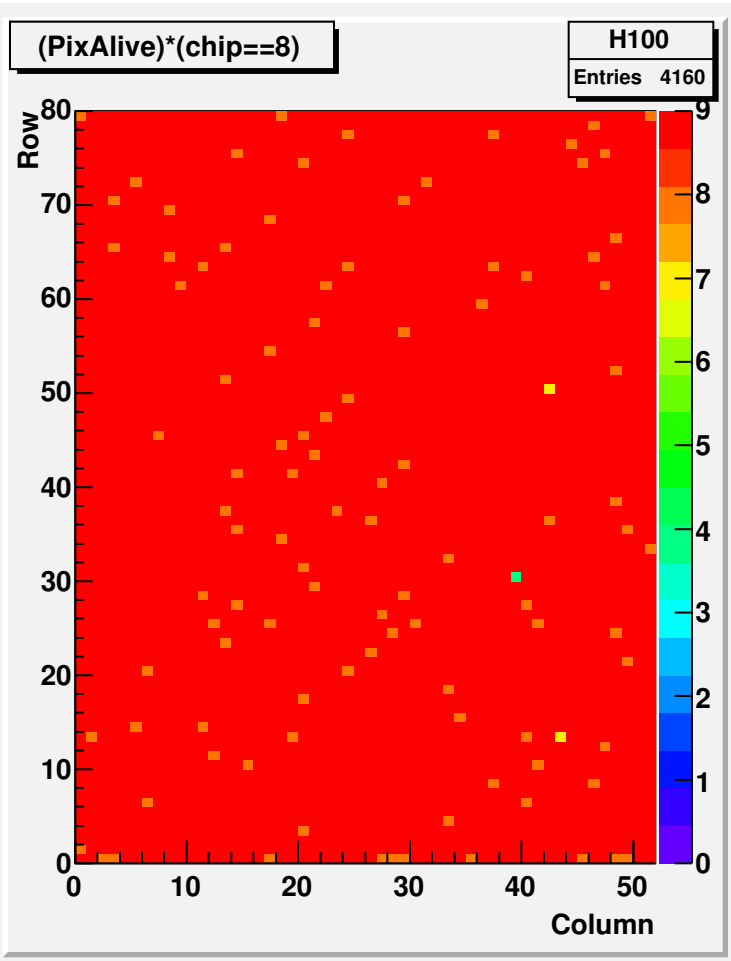
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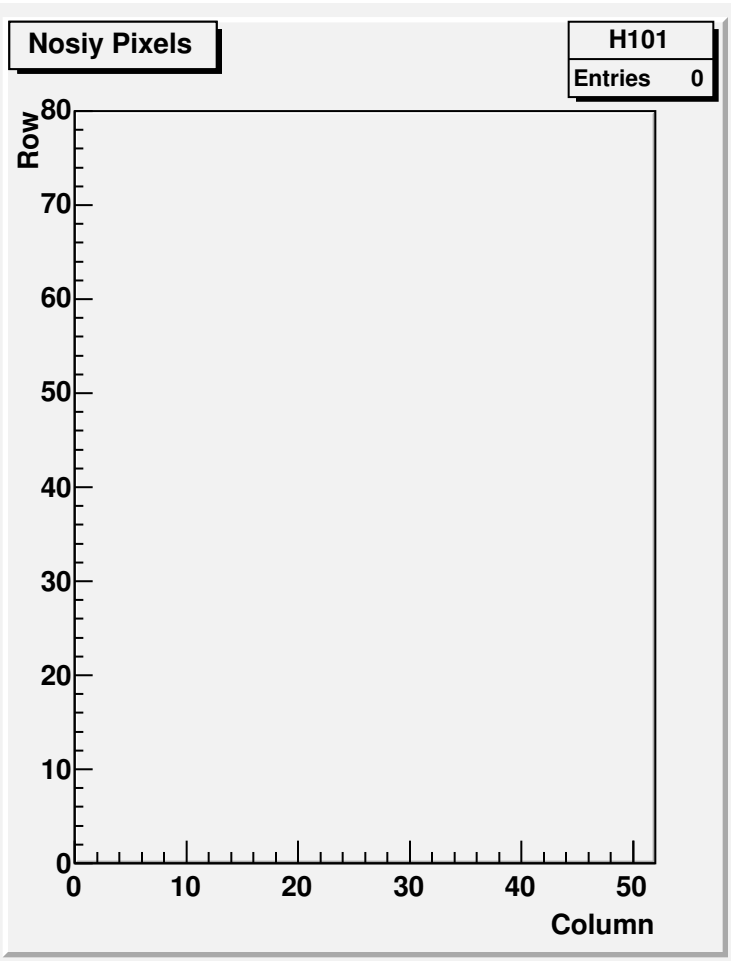
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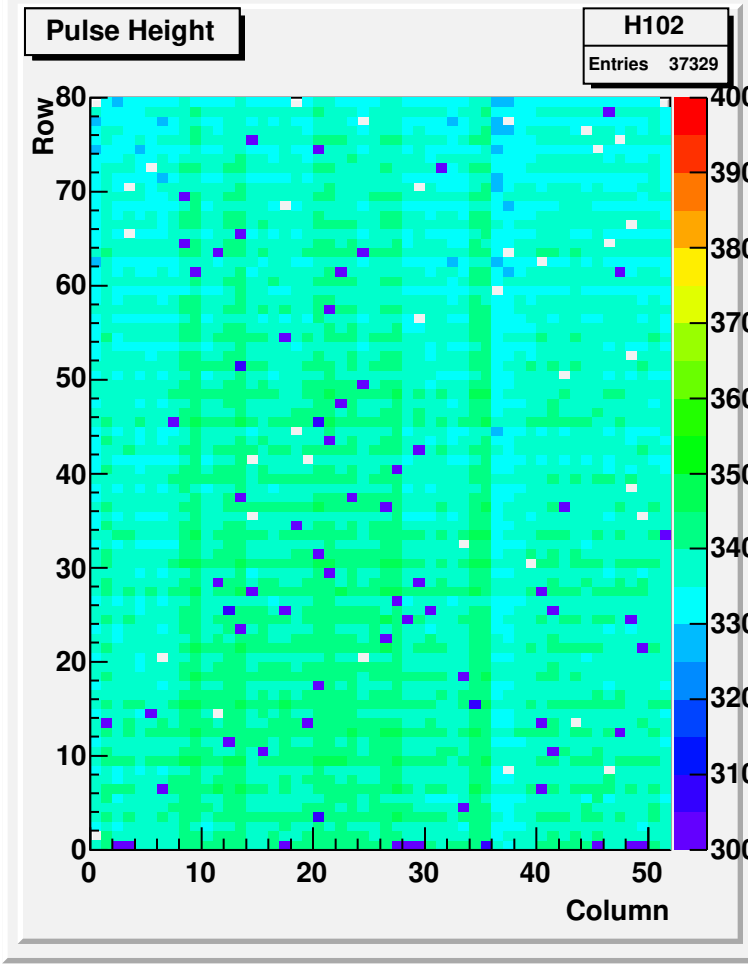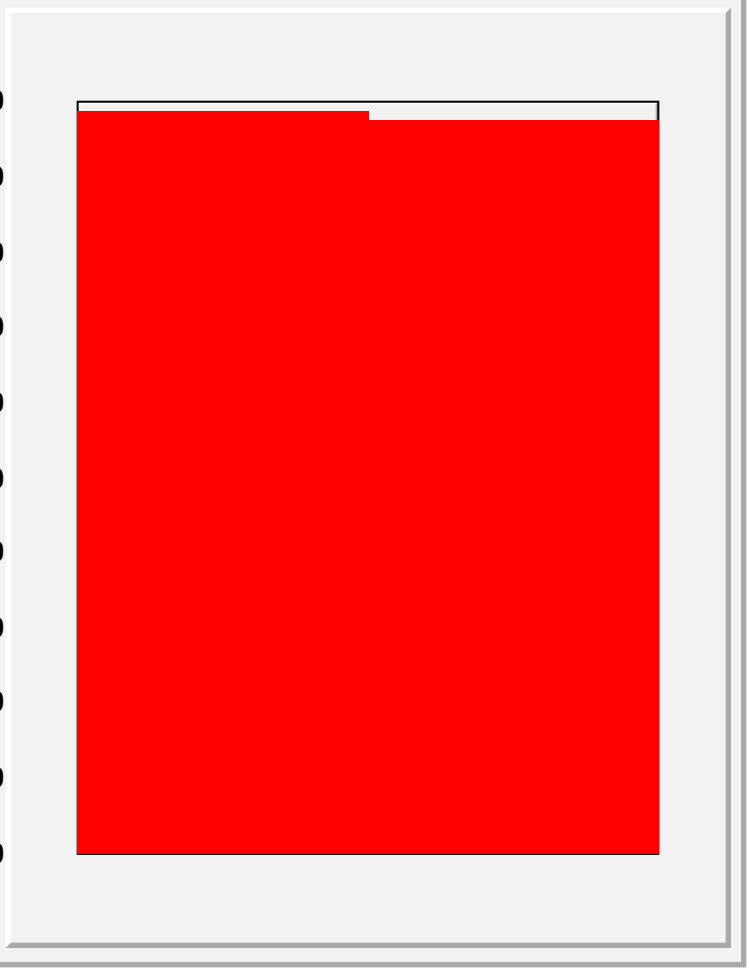
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