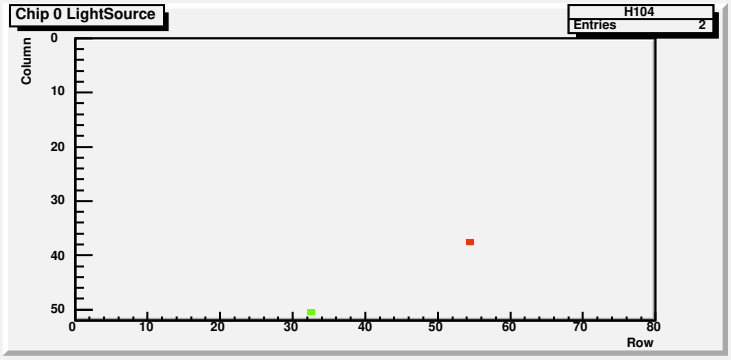
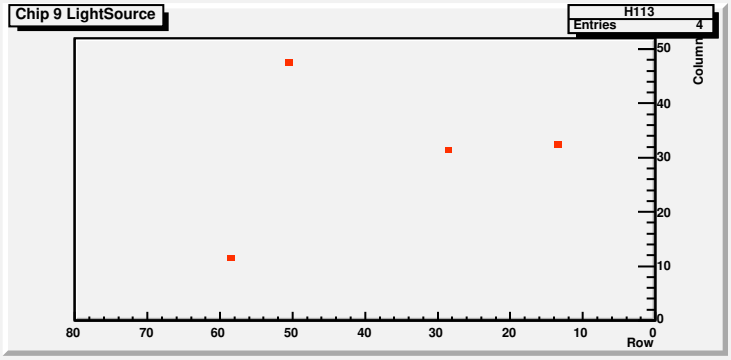
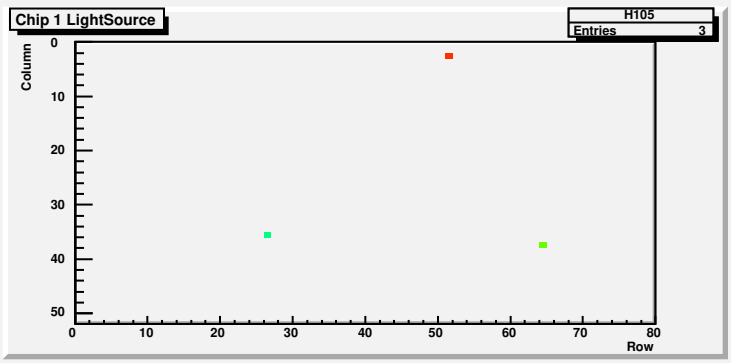
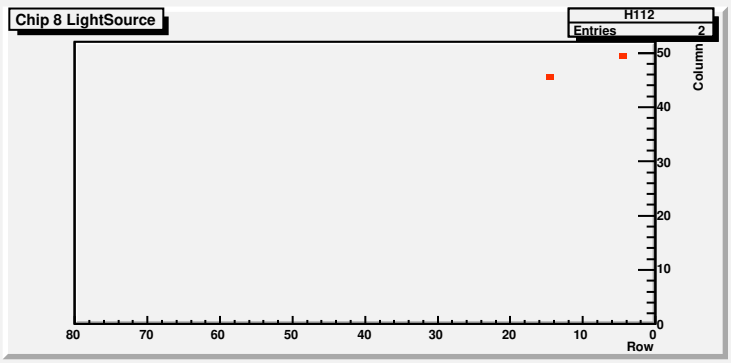
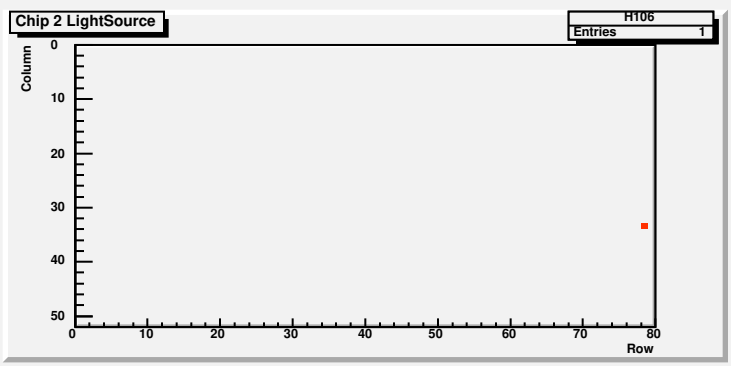
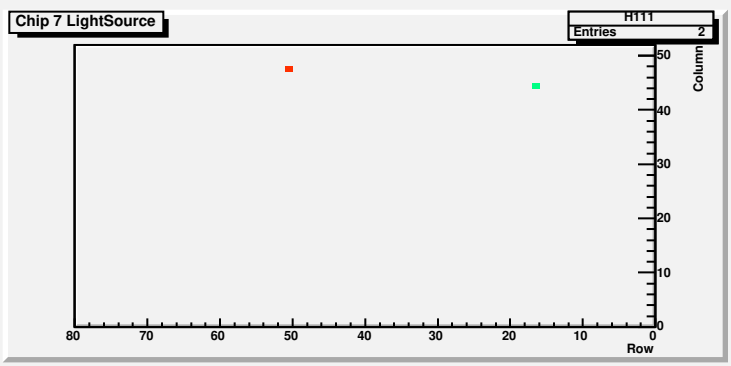

Chip 0 LightSource
H104
Entries 2
Column
Row

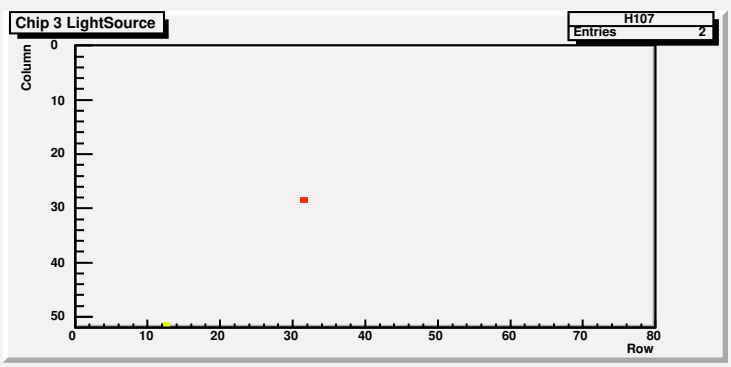

Chip 9 LightSource
H113
Entries 4
Column
Row

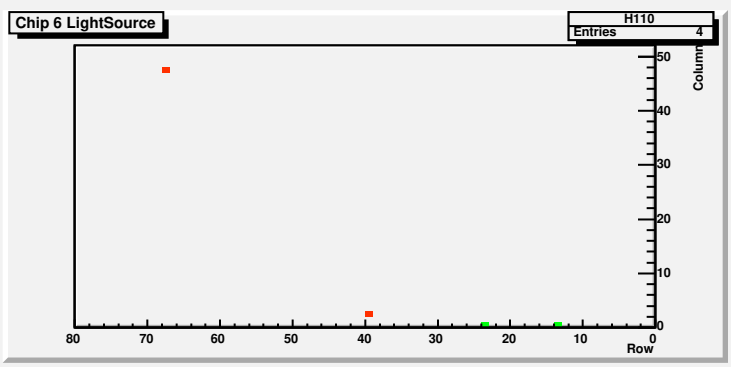

Chip 1 LightSource
H105
Entries 3
Column
Row

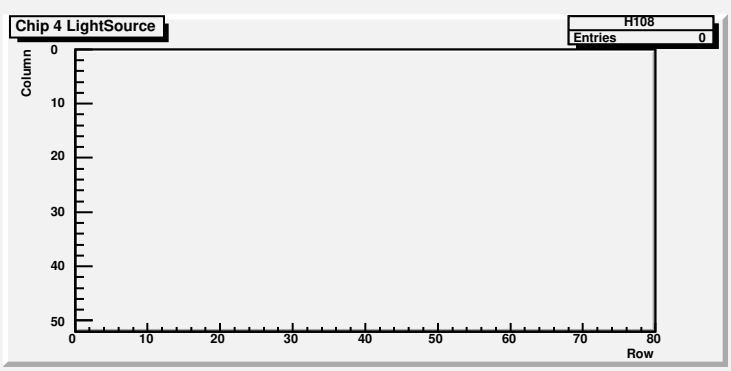

Chip 8 LightSource
H112
Entries 2
Column
Row

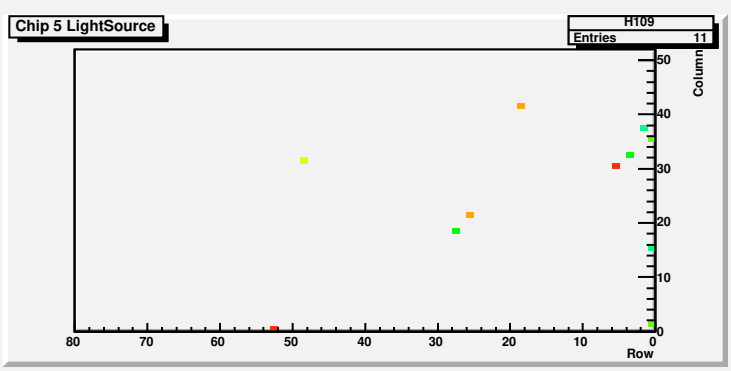

Chip 2 LightSource
H106
Entries 1
Column
Row

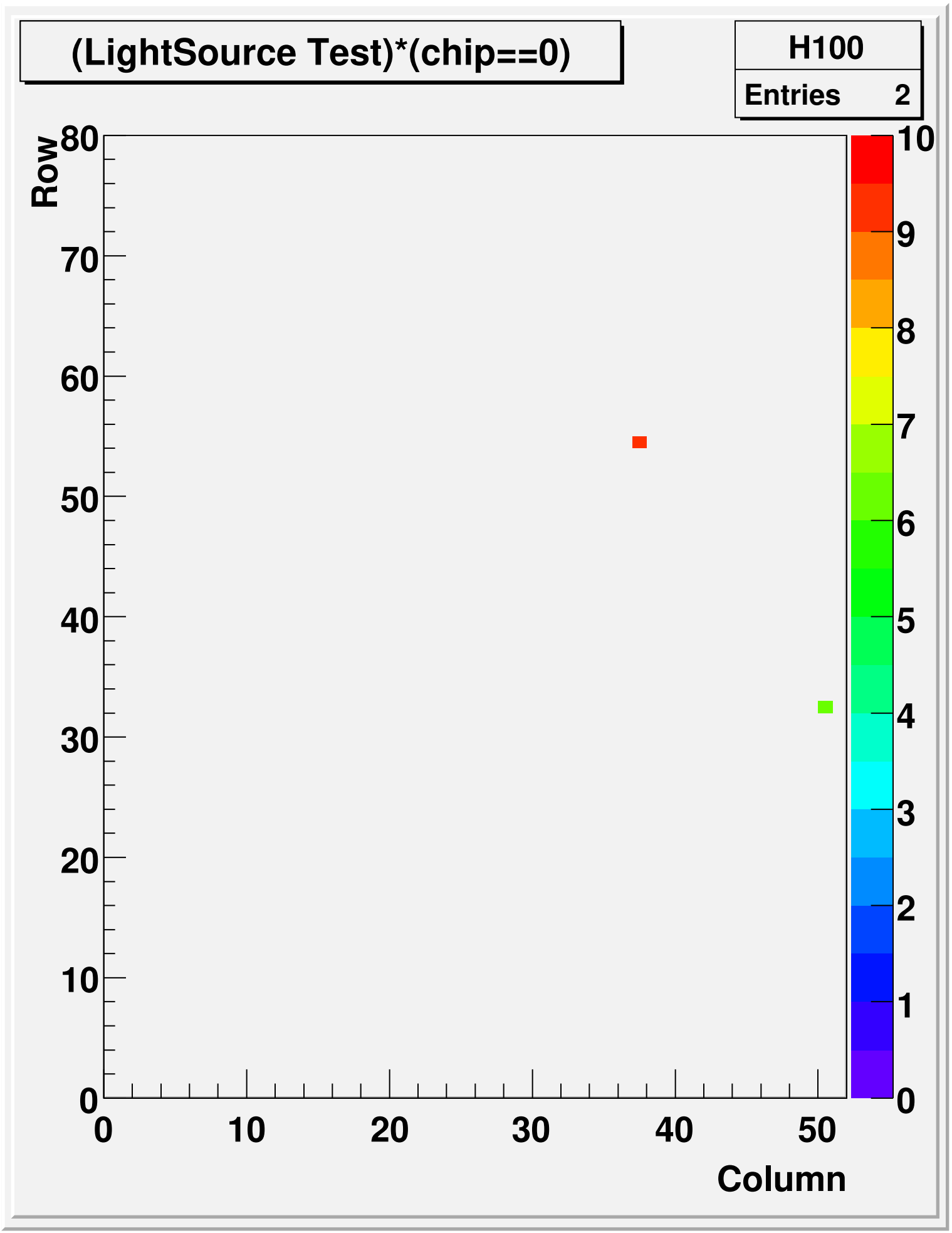

Chip 7 LightSource
H111
Entries 2
Column
Row

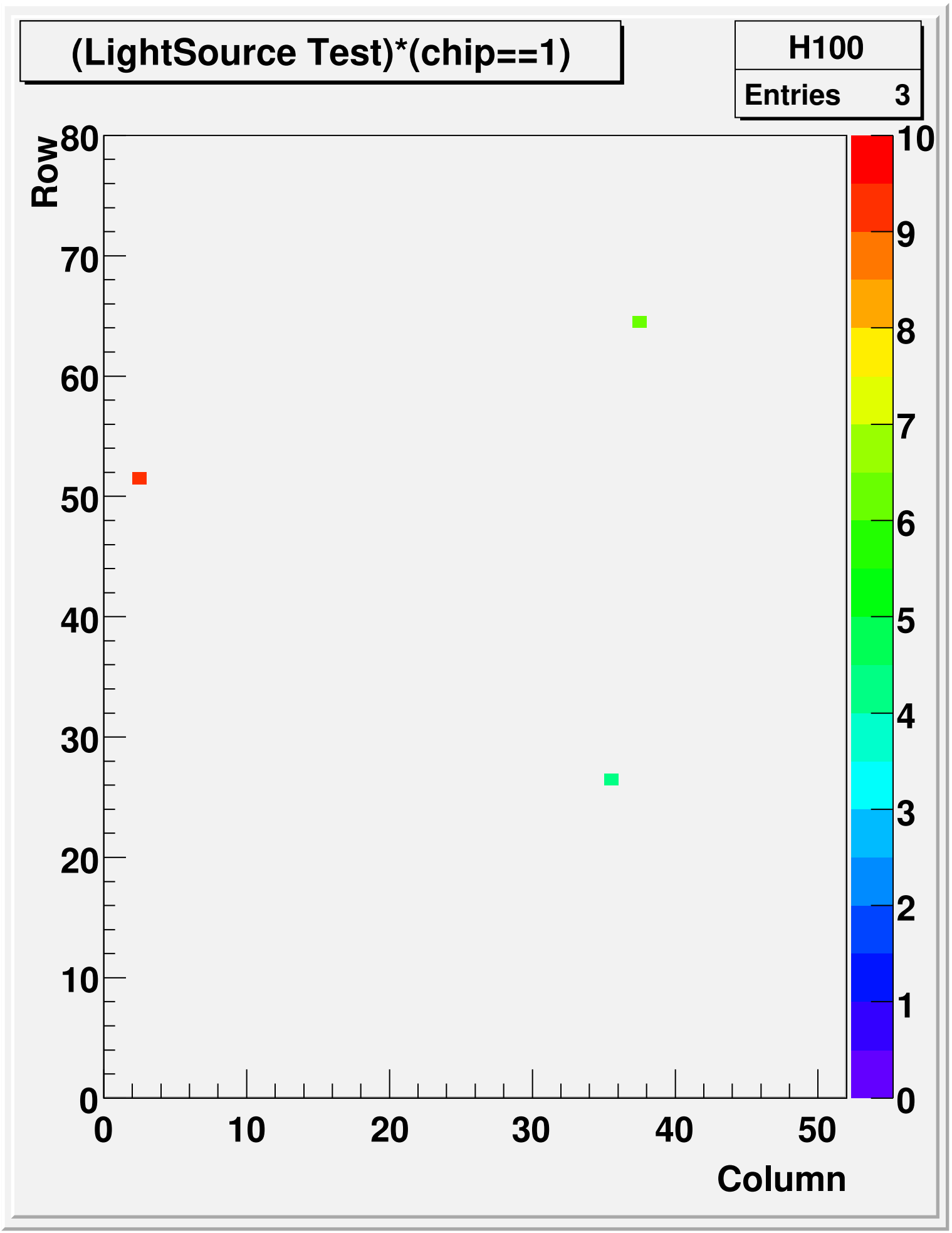

Chip 3 LightSource
H107
Entries 2
Column
Row


Chip 6 LightSource
H110
Entries 4
Column
Row


Chip 4 LightSource
H108
Entries 0
Column
Row


Chip 5 LightSource
H109
Entries 11
Column
Row


(LightSource Test)*(chip==0)
H100
Entries 2
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==1)
H100
Entries 3
Row
80
70
60
50
40
30
20
10
0
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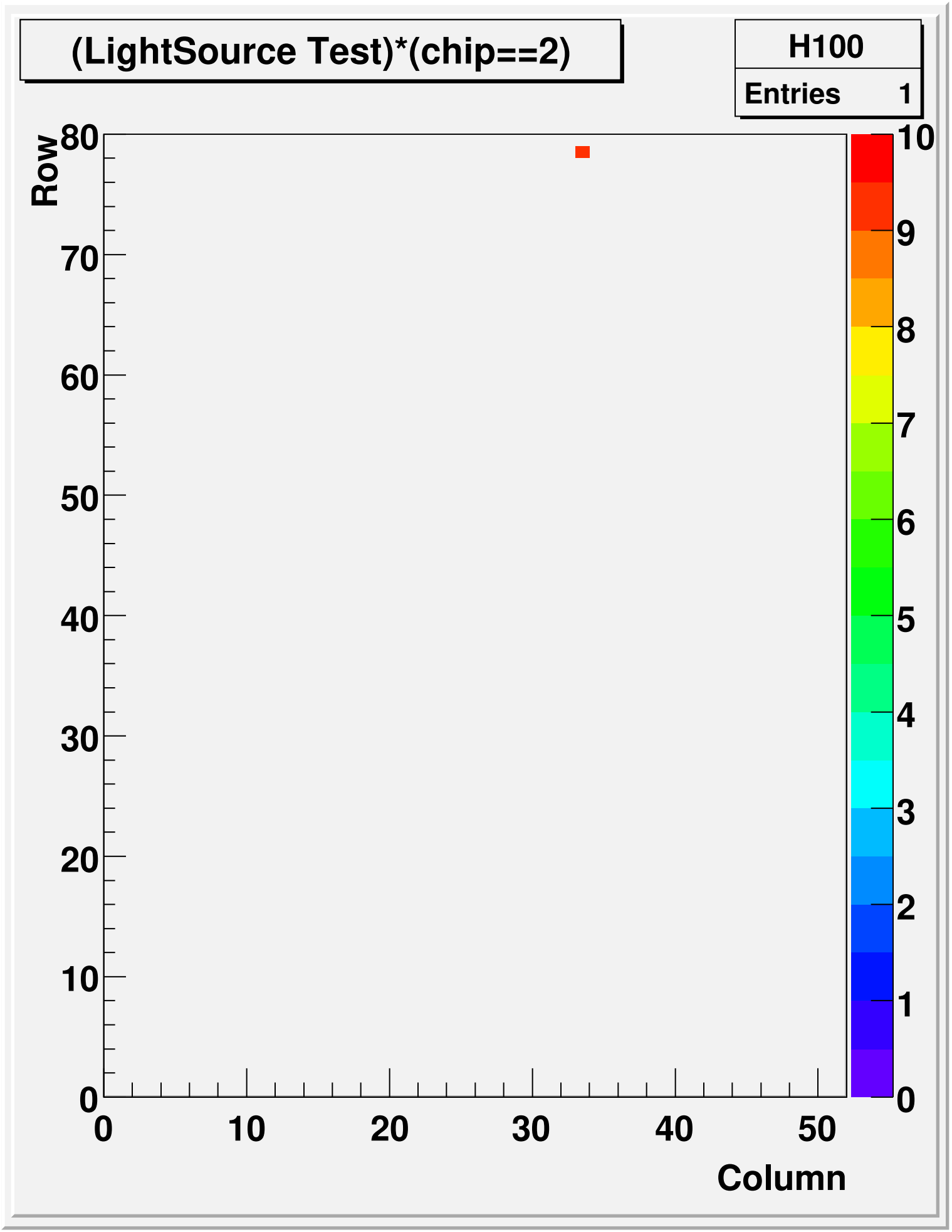
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==2)
H100
Entries 1
Row
80
70
60
50
40
30
20
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0
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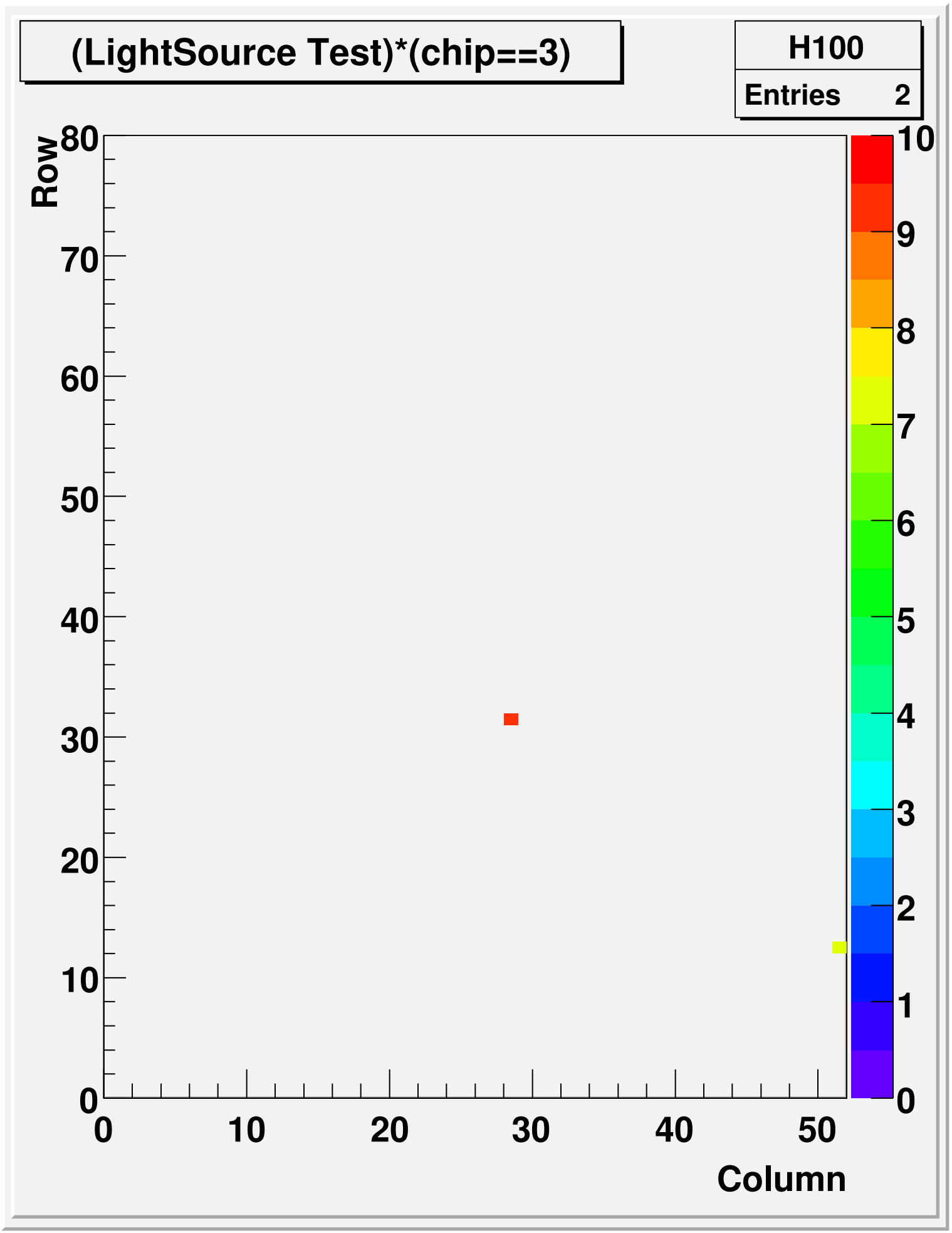
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==3)
H100
Entries 2
Row
80
70
60
50
40
30
20
10
0
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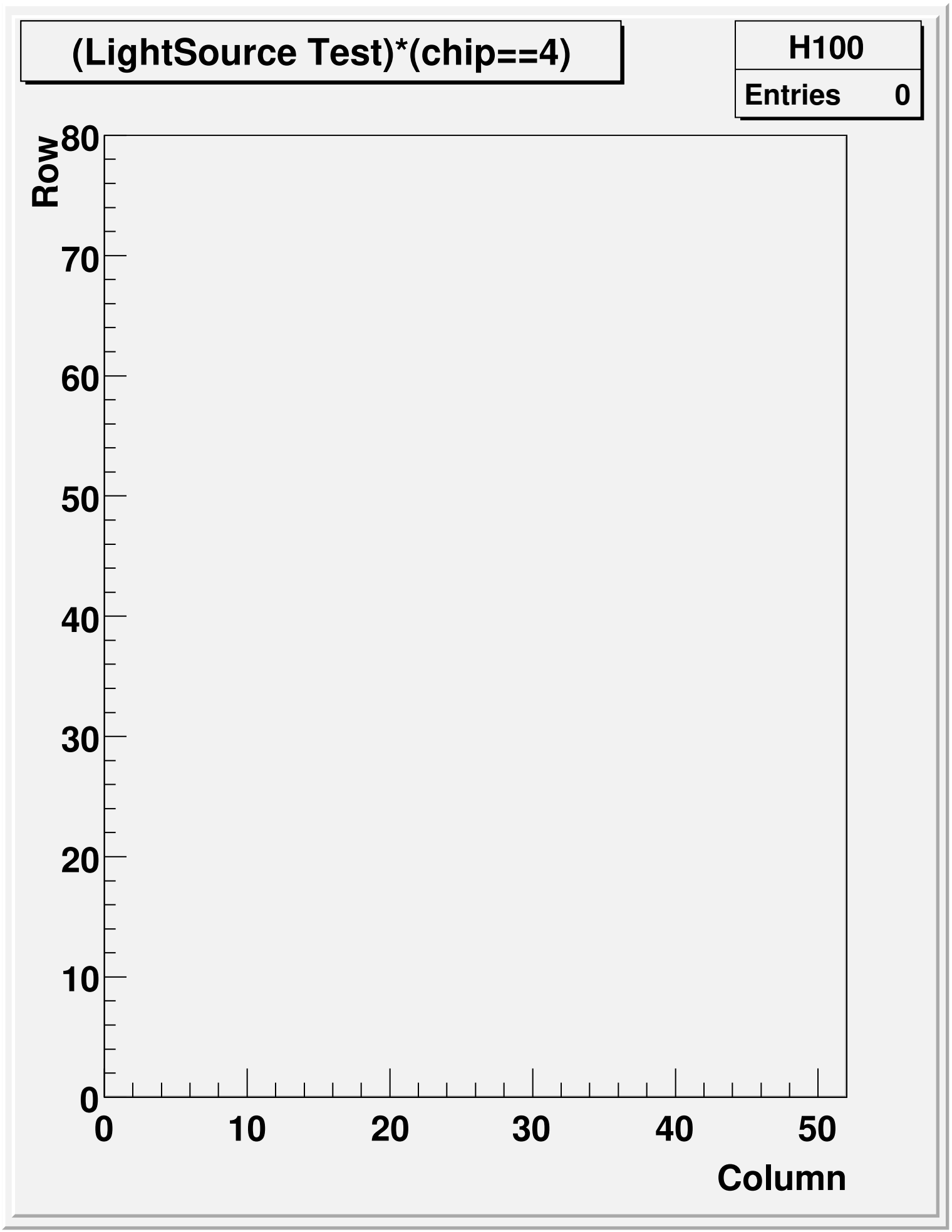
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==4)
H100
Entries 0
Row
80
70
60
50
40
30
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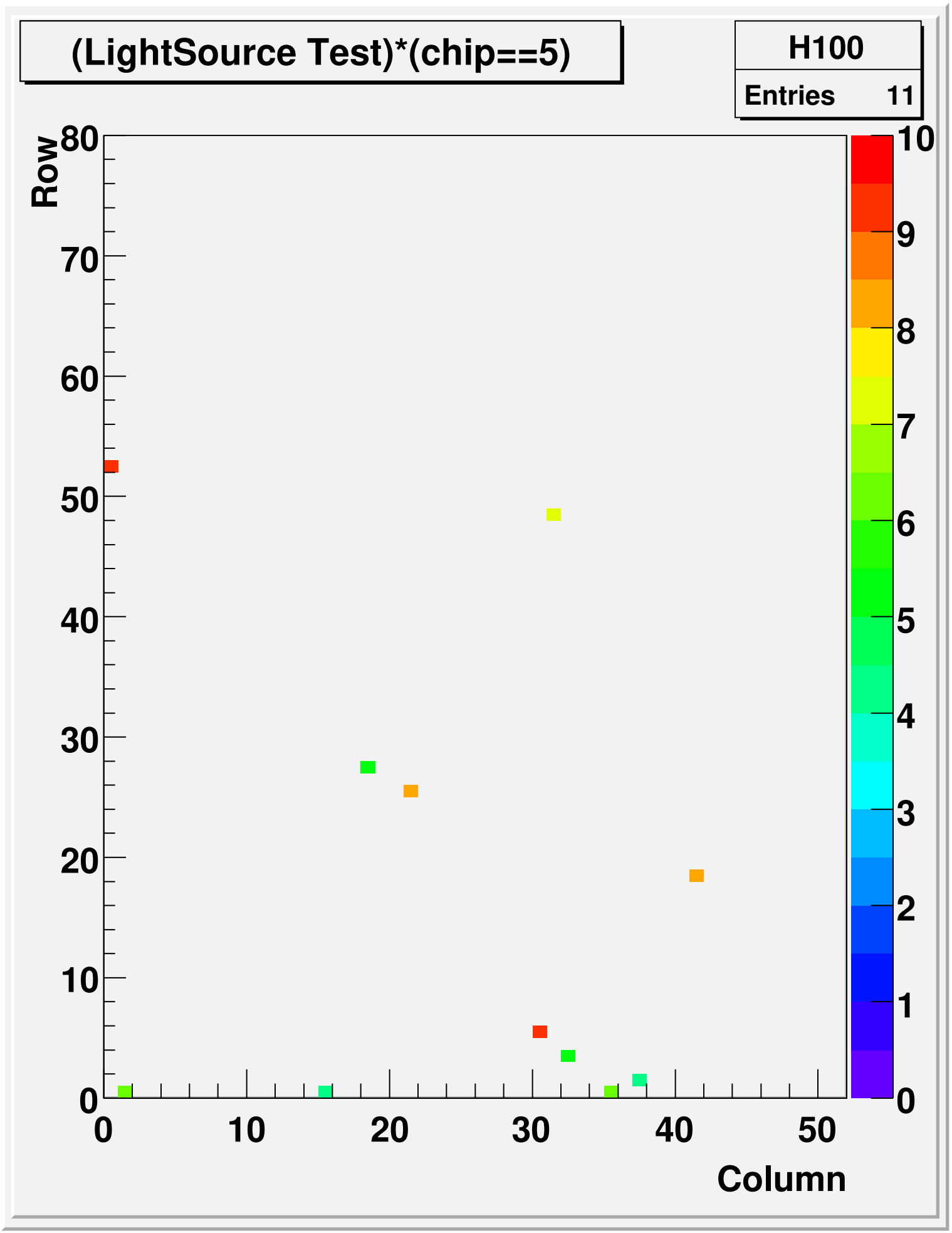
0
0
10
20
30
40
50
Column


(LightSource Test)*(chip==5)
H100
Entries 11
Row
Column
0
10
20
30
40
50
60
70
80
0
1
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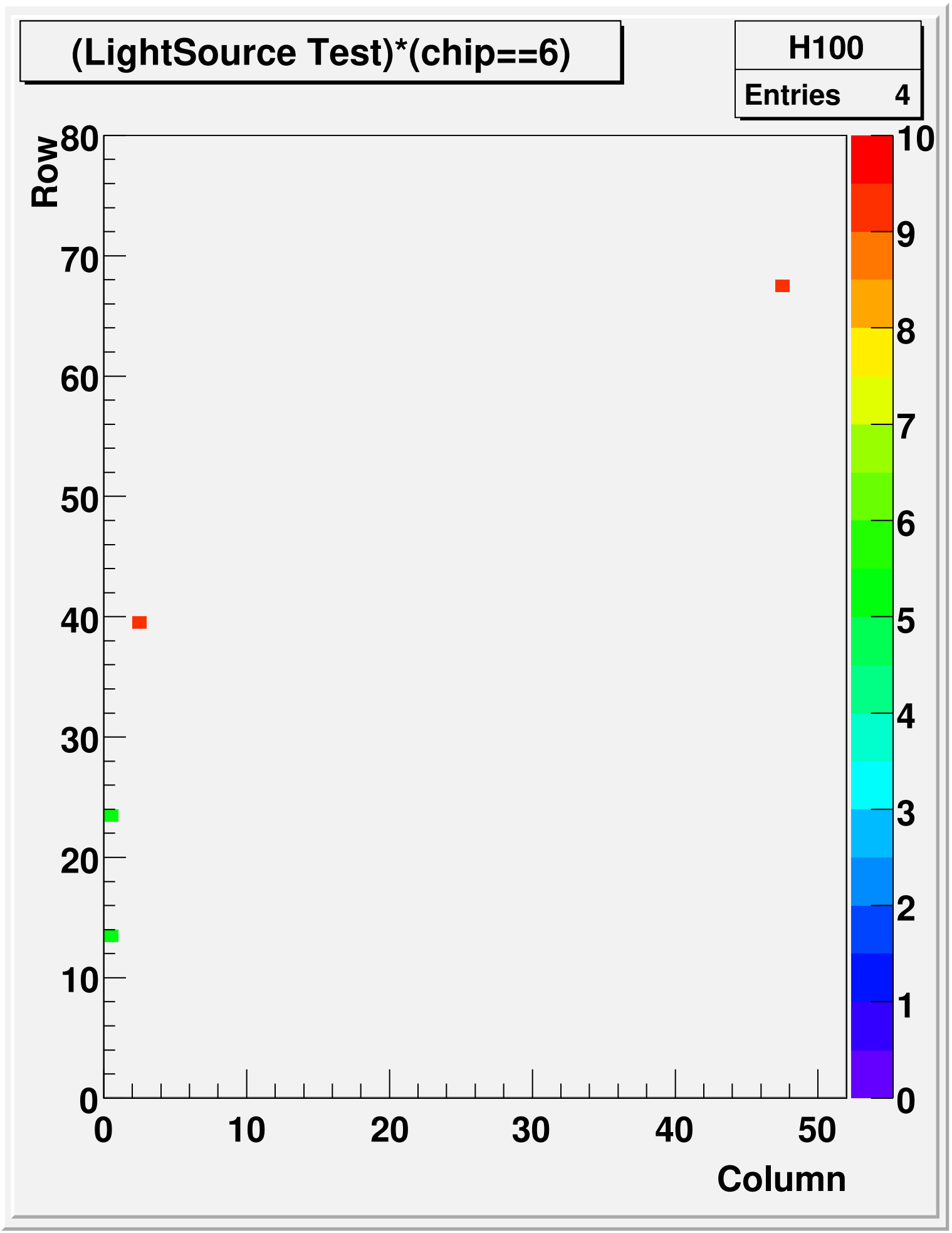
5
6
7
8
9
10


(LightSource Test)*(chip==6)
H100
Entries 4
Row
Column
0
10
20
30
40
50
60
70
80
0
1
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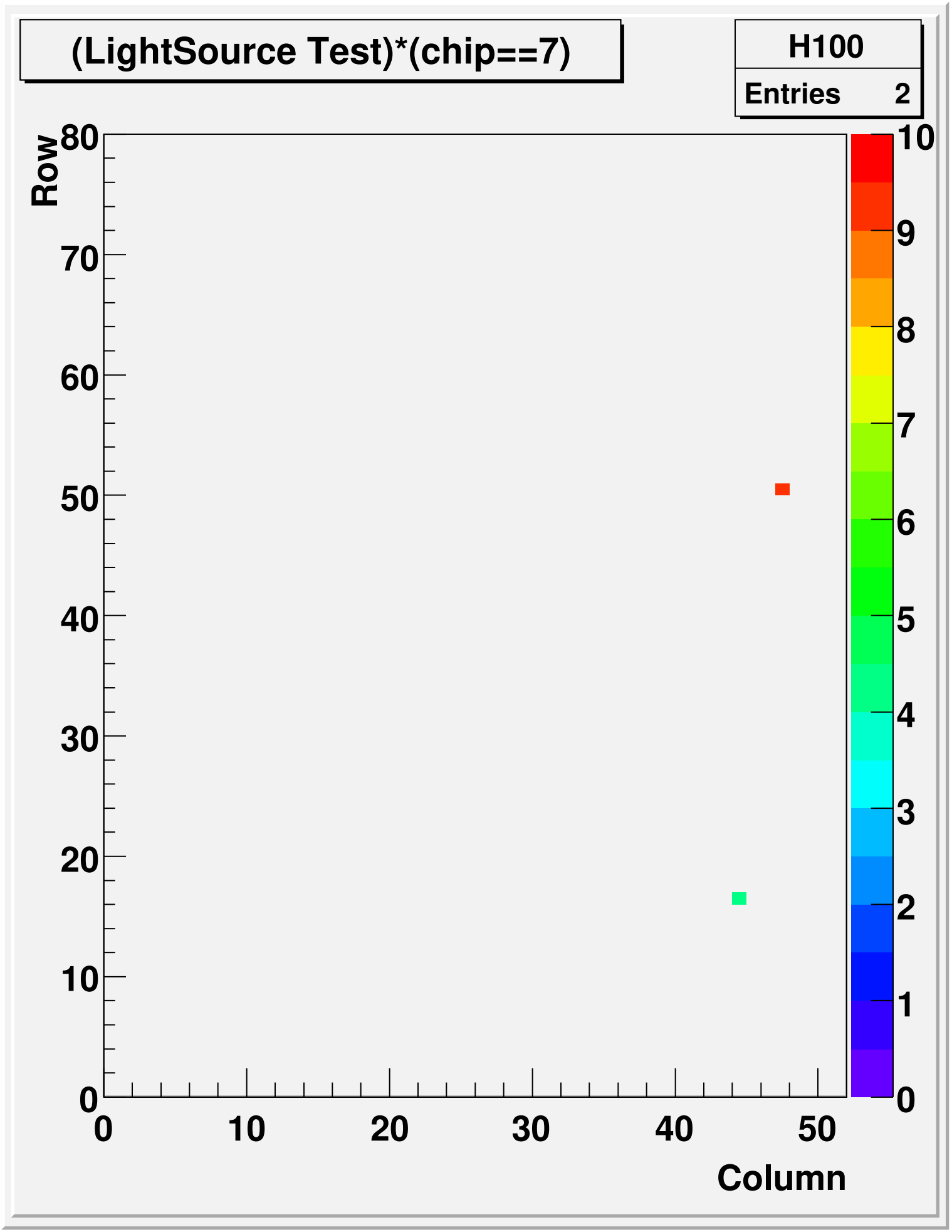
5
6
7
8
9
10


(LightSource Test)*(chip==7)
H100
Entries 2
Row
80
70
60
50
40
30
20
10
0
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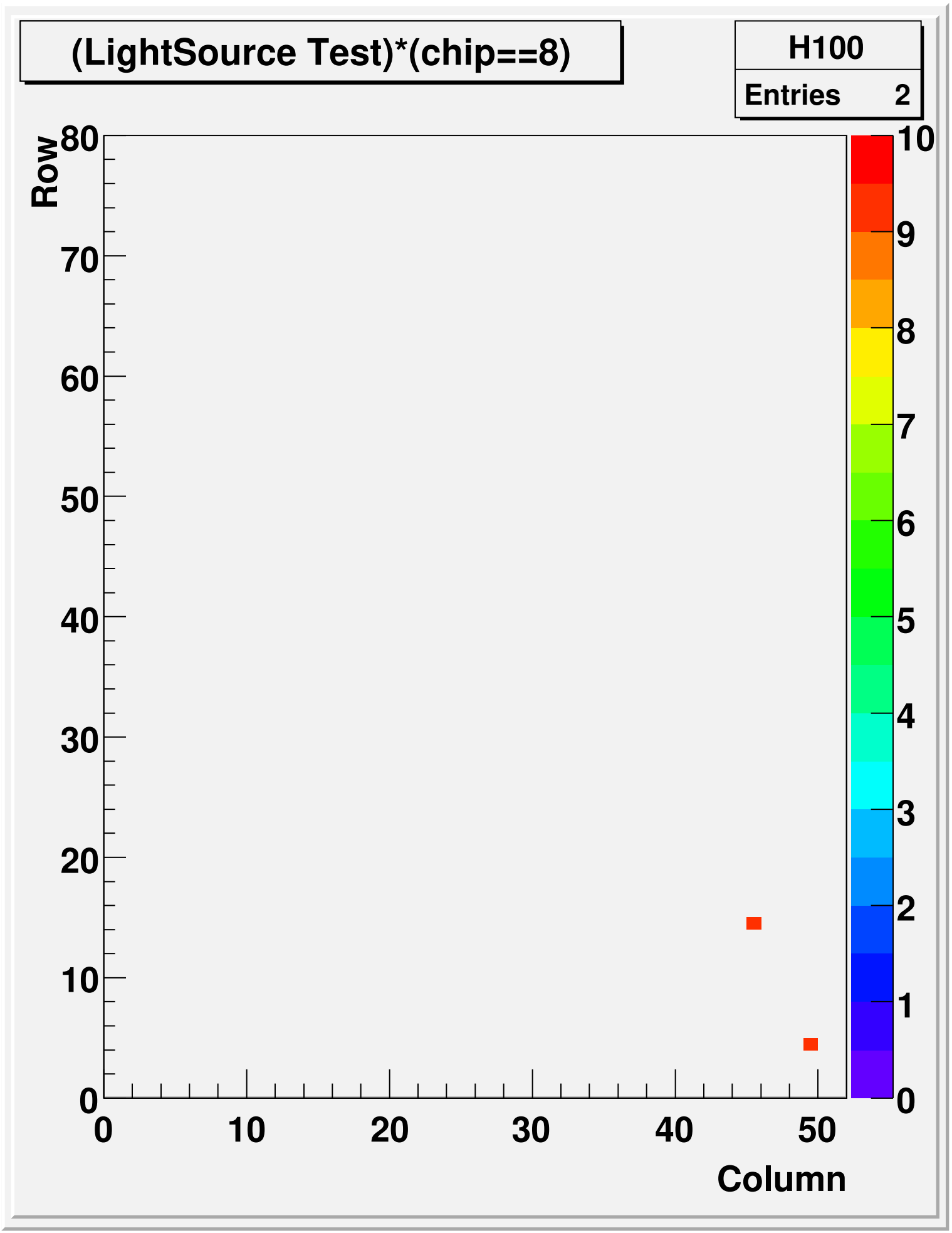
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==8)
H100
Entries 2
Row
80
70
60
50
40
30
20
10
0
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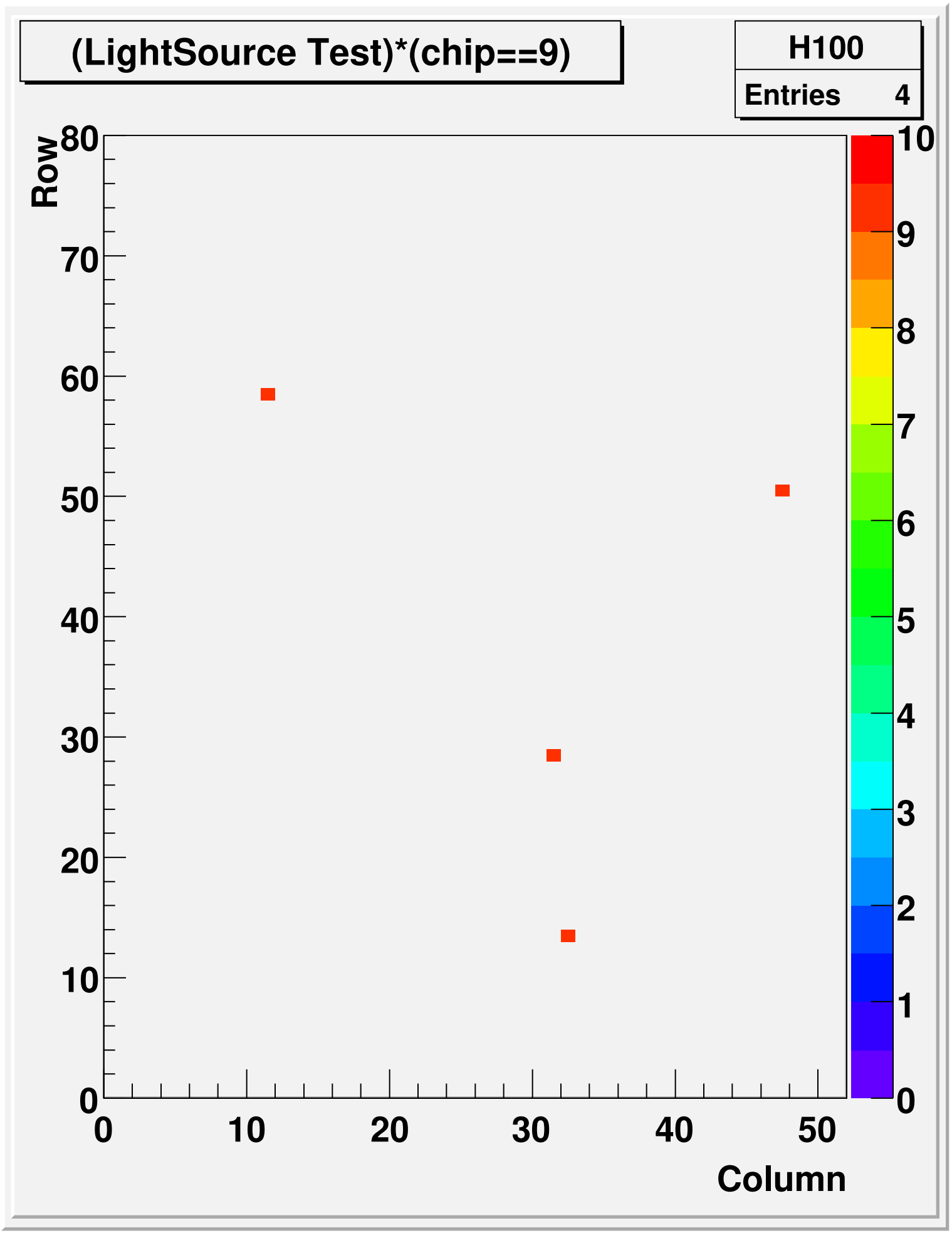
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==9)
H100
Entries 4
Row
Column
0
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20
30
40
50
60
70
80
0
1
2
3
4
5
6
7
8
9
10