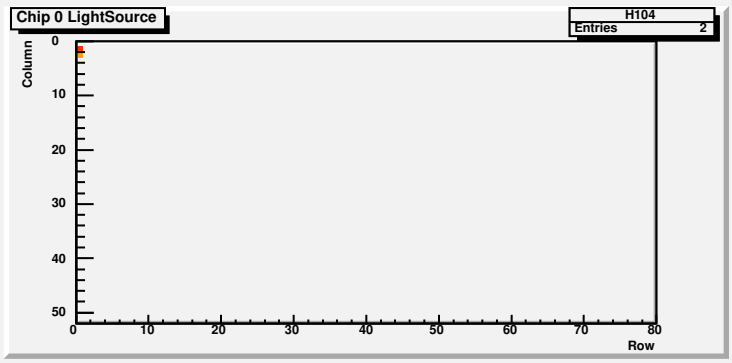
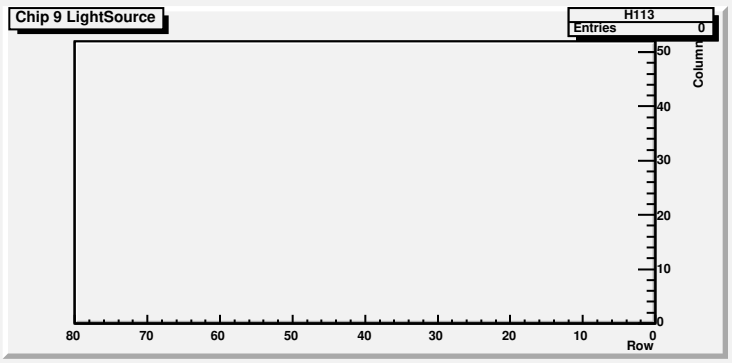

Chip 0 LightSource
H104
Entries 2
Column
Row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Column
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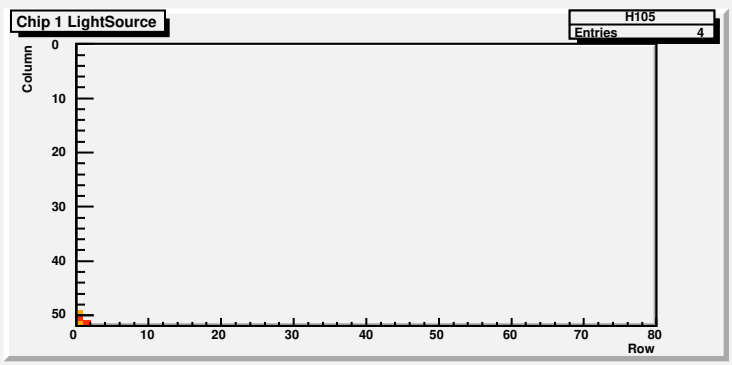
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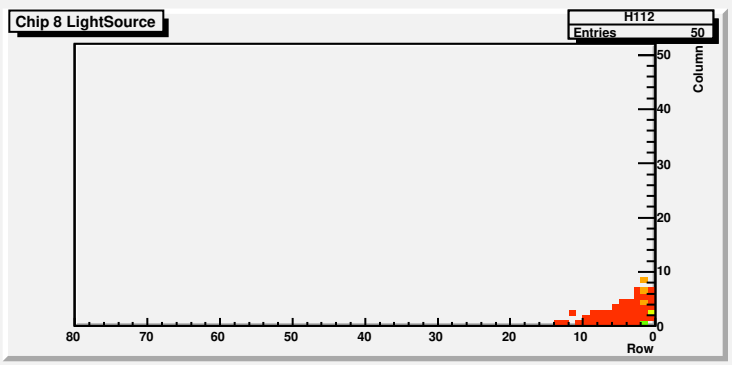
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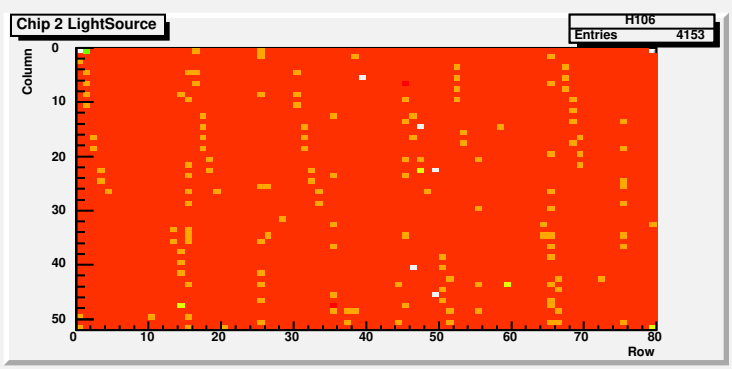
Column
Row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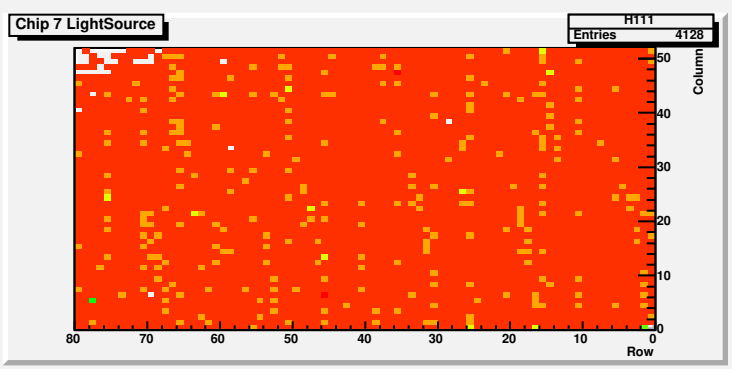
H106
Entries 4153
Column
Row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Entries 4128
Column
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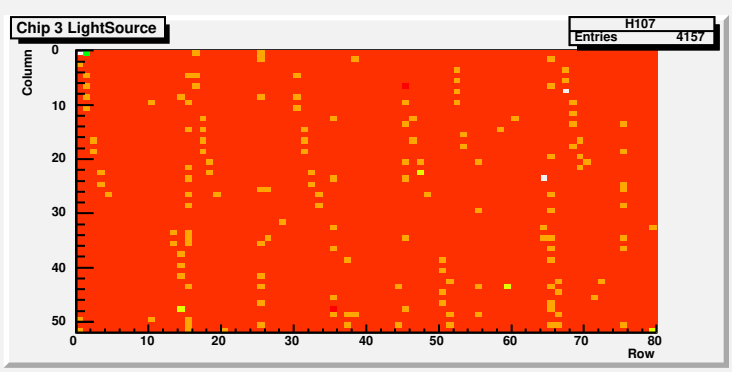
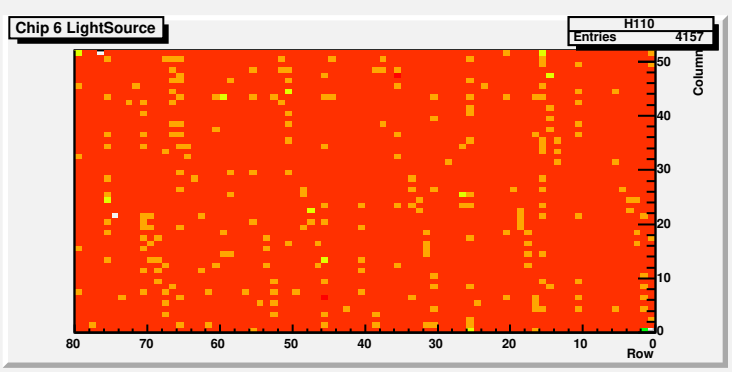

Chip 3 LightSource
H107
Entries 4157
Column
Row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H110
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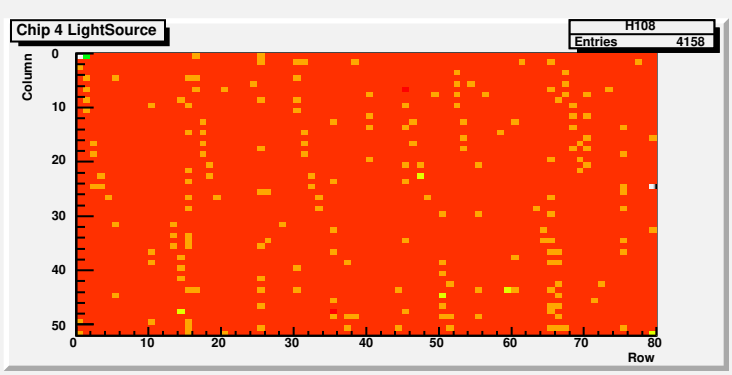
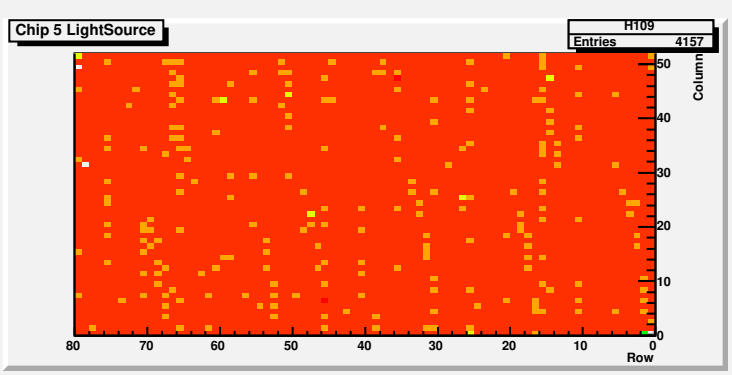
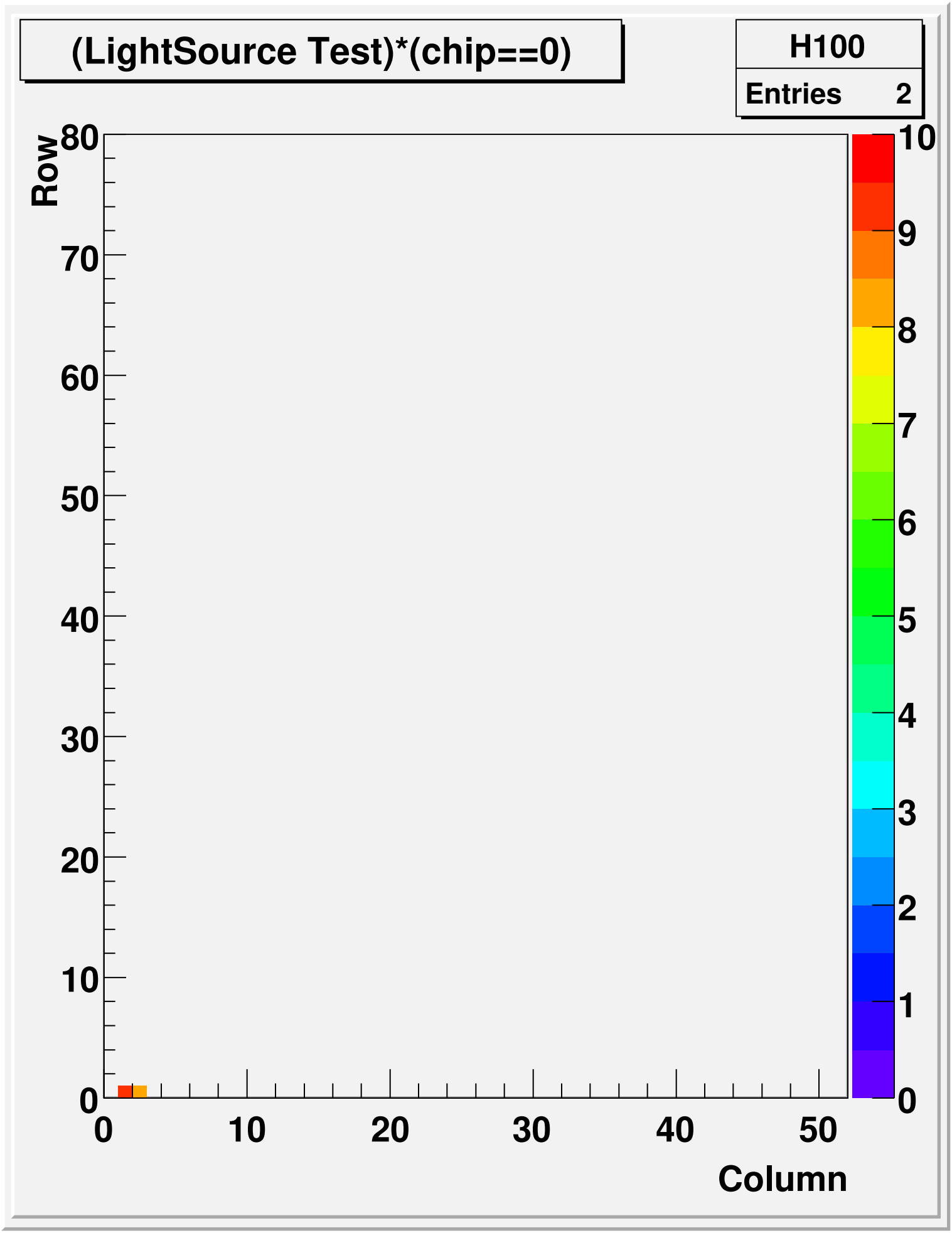
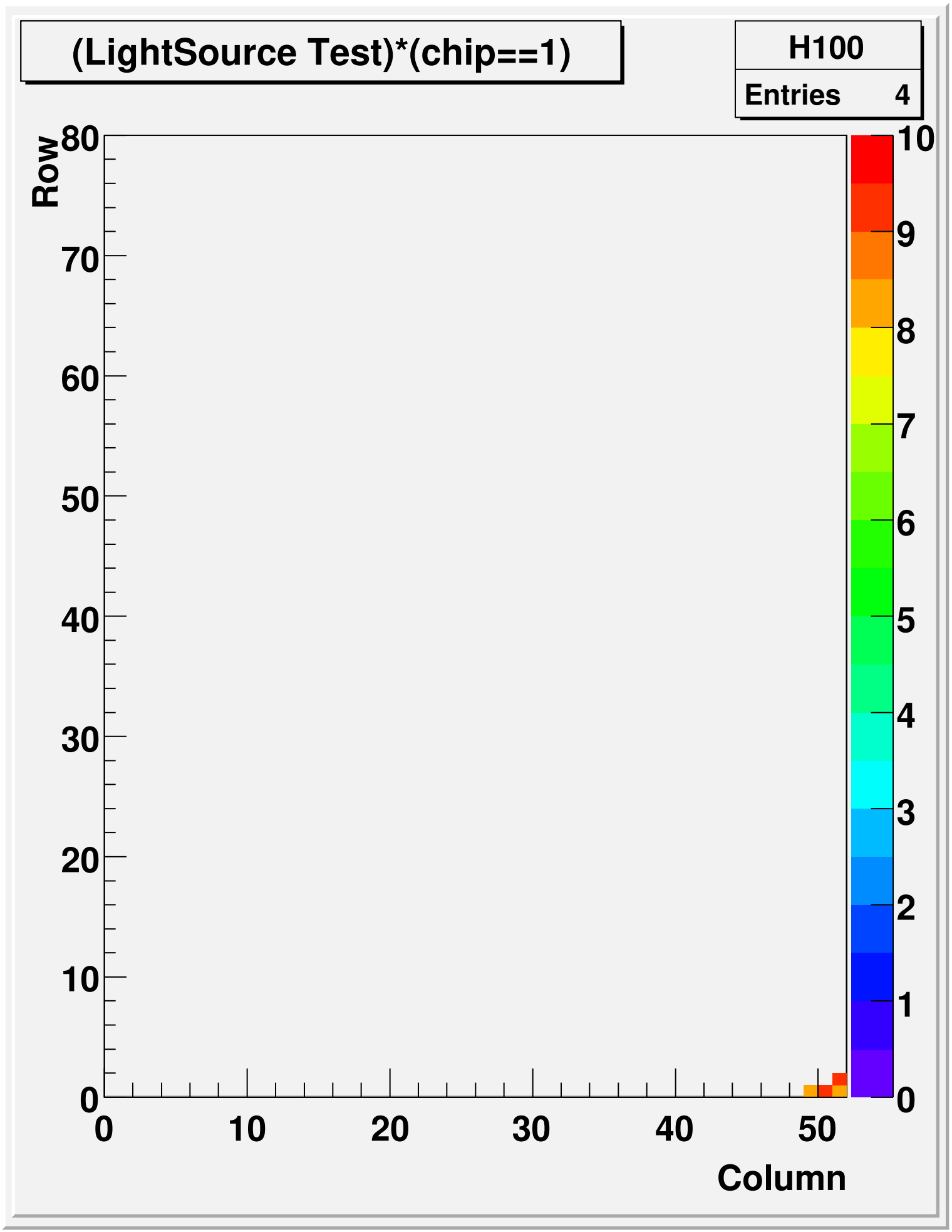
Column
Row


Chip 4 LightSource
H108
Entries 4158
Column
Row


Chip 5 LightSource
H109
Entries 4157
Column
Row


(LightSource Test)*(chip==0)
H100
Entries 2
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==1)
H100
Entries 4
Row
80
70
60
50
40
30
20
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0
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20
30
40
50
Column
10
9
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7
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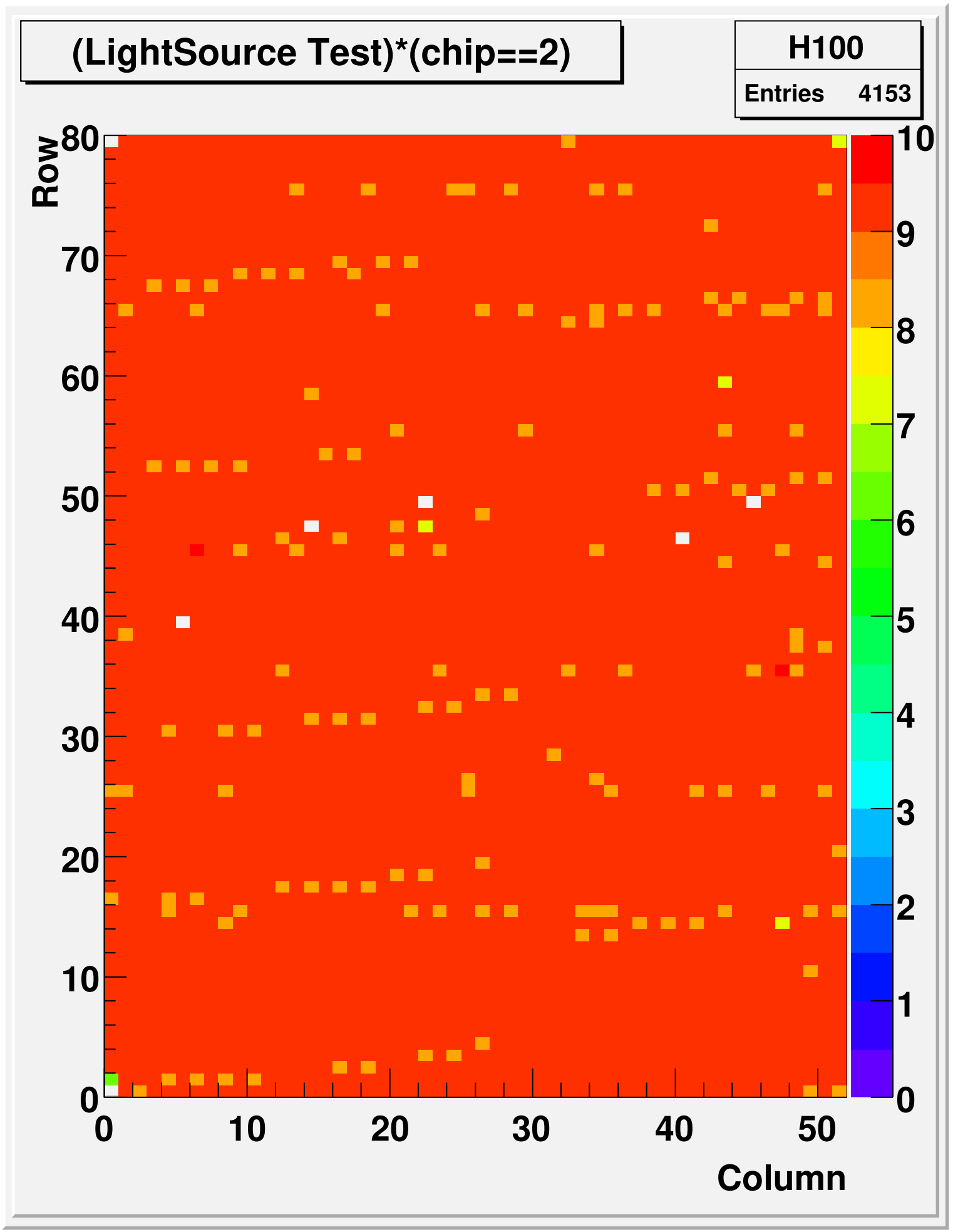
5
4
3
2
1
0


(LightSource Test)*(chip==2)
H100
Entries 4153
Row
Column
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10
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30
40
50
60
70
80
0
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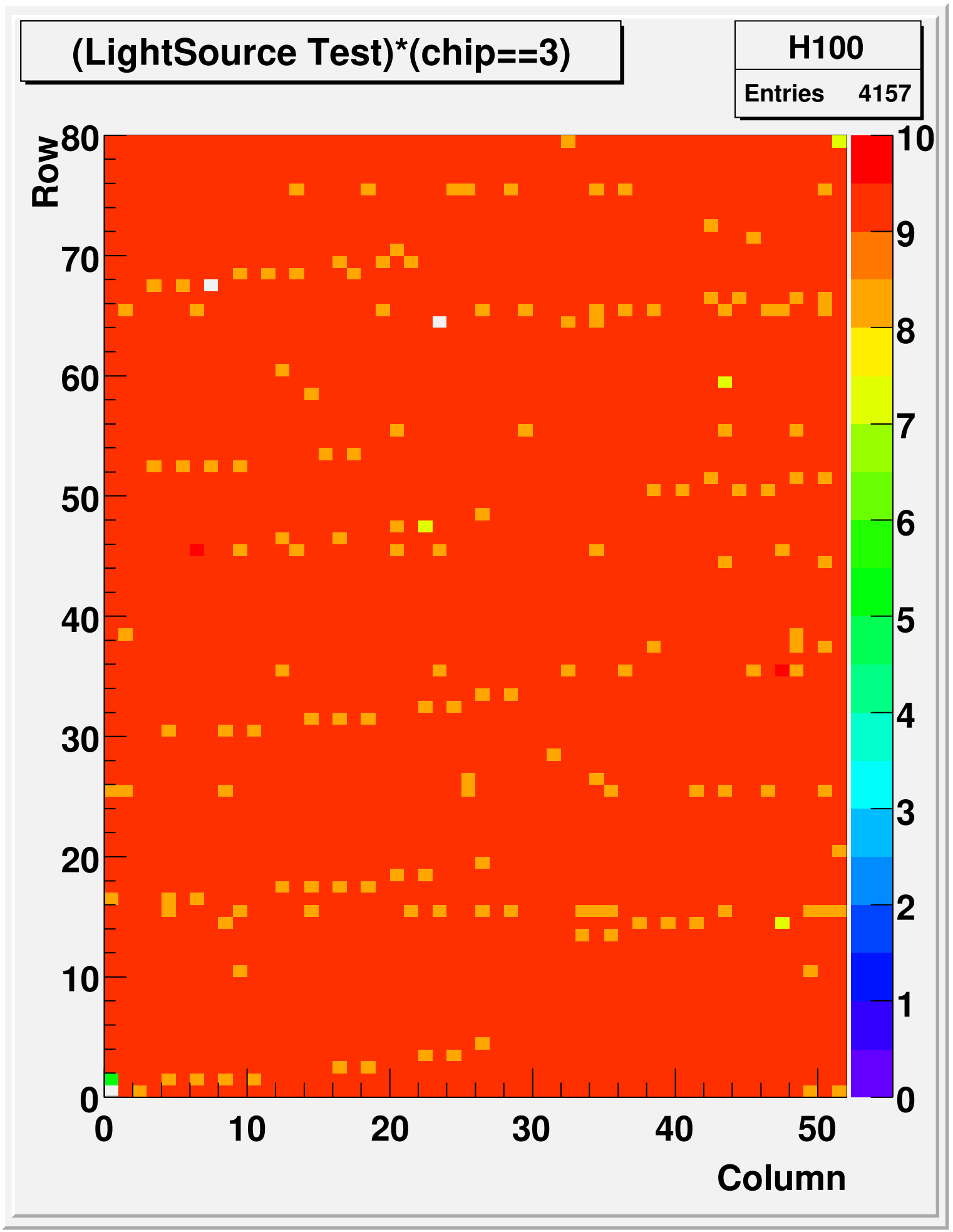
5
6
7
8
9
10


(LightSource Test)*(chip==3)
H100
Entries 4157
Row
Column
0
10
20
30
40
50
60
70
80
0
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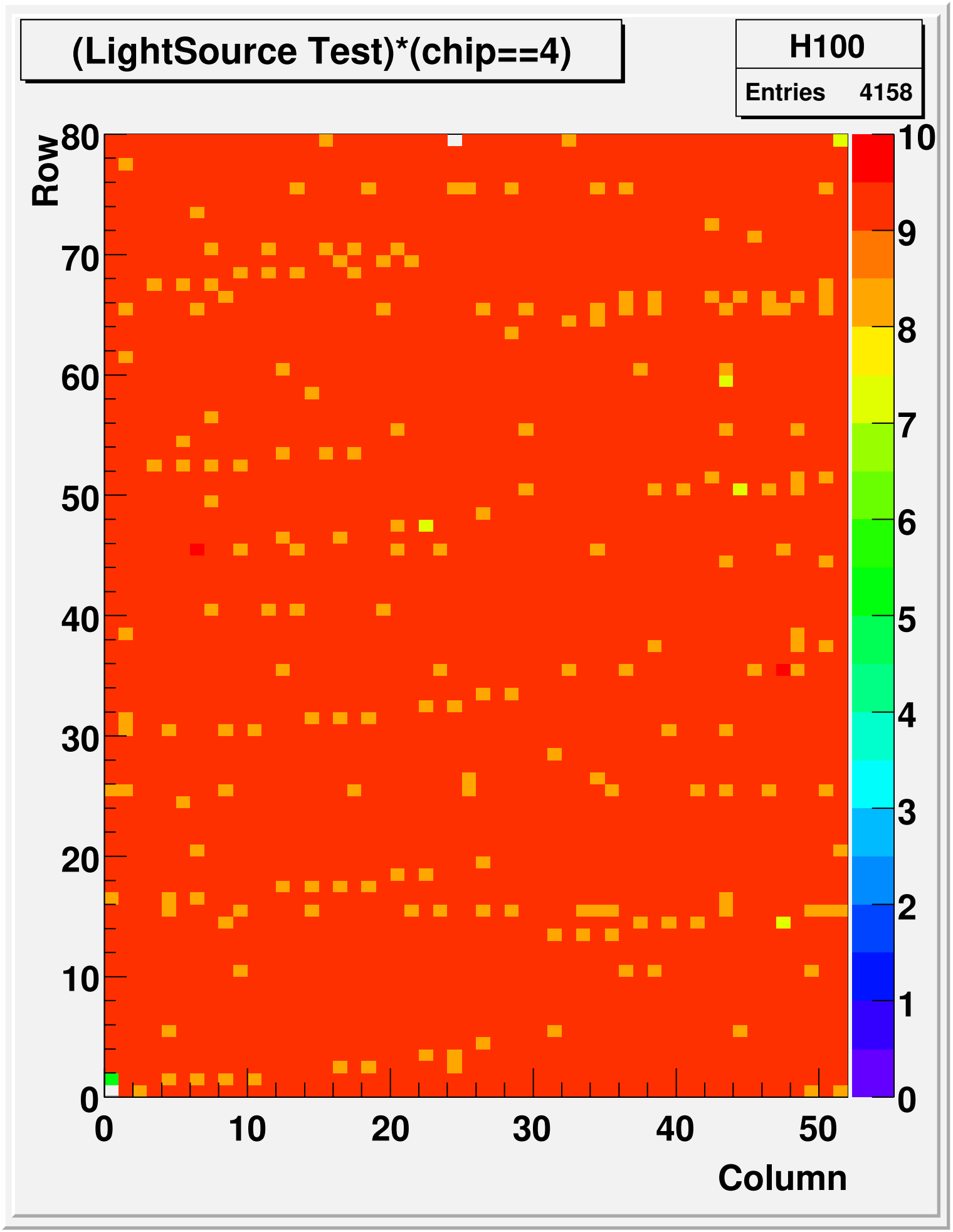
5
6
7
8
9
10


(LightSource Test)*(chip==4)
H100
Entries 4158
Row
Column
0
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20
30
40
50
60
70
80
0
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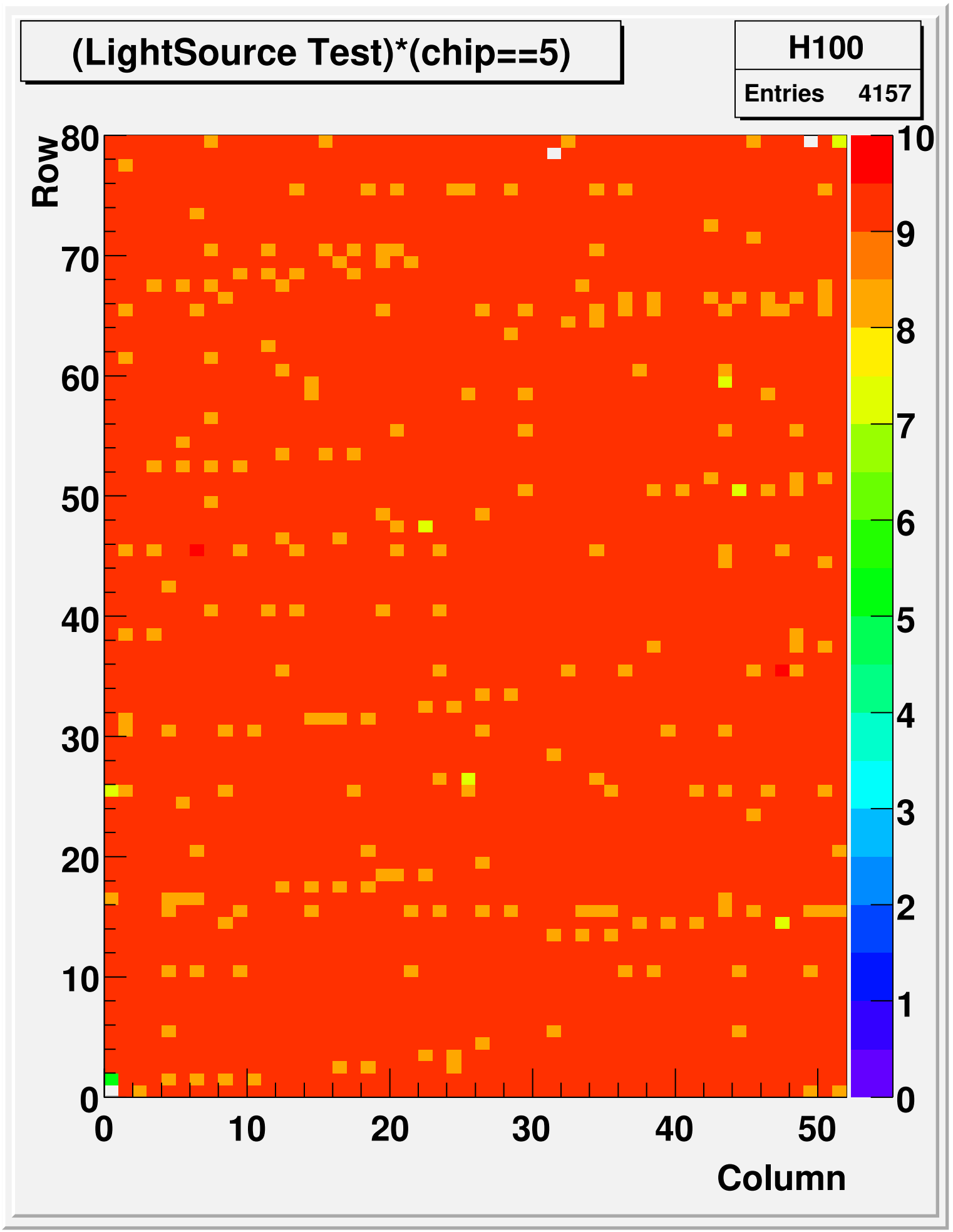
5
6
7
8
9
10


(LightSource Test)*(chip==5)
H100
Entries 4157
Row
Column
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80
0
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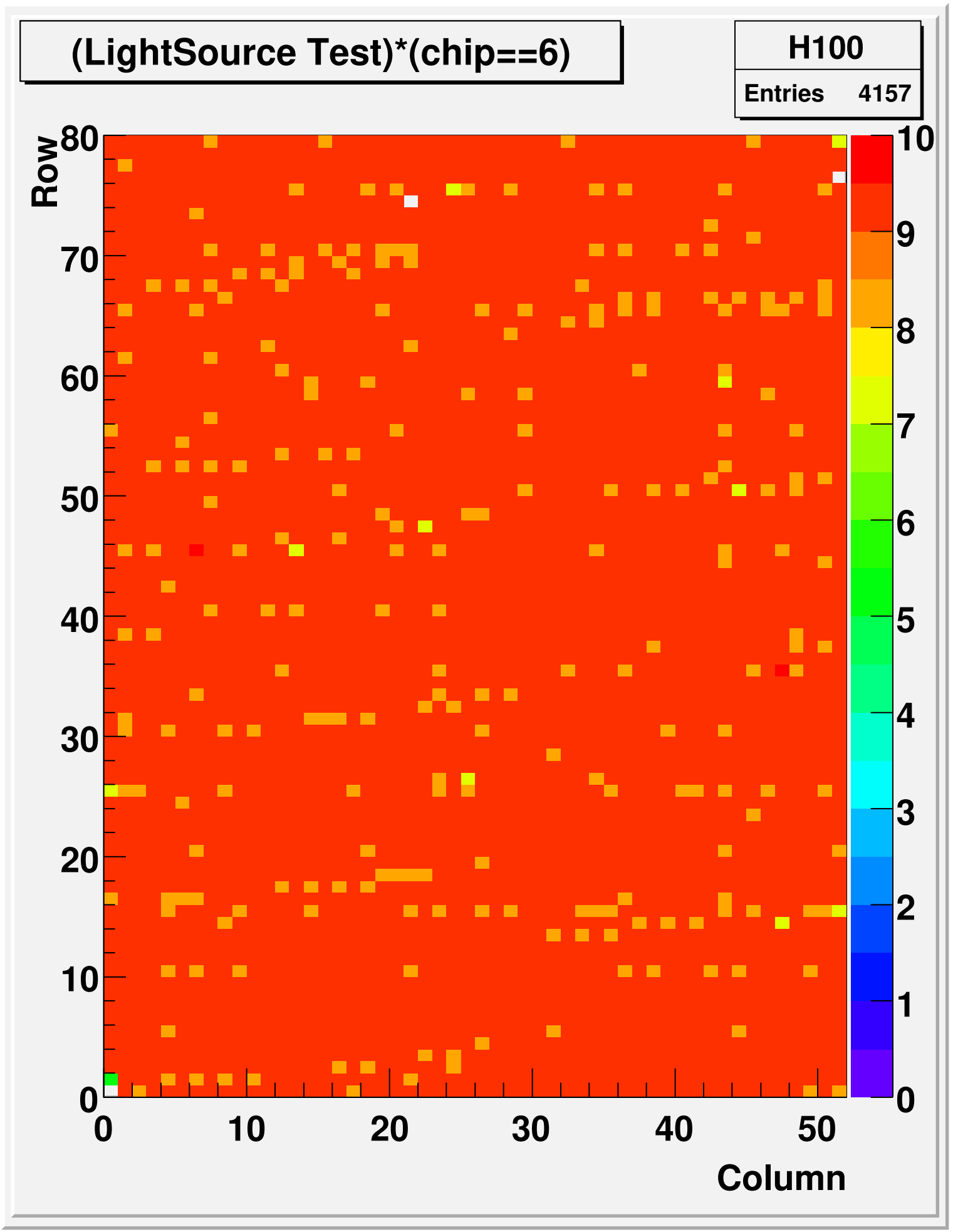
7
8
9
10


(LightSource Test)*(chip==6)
H100
Entries 4157
Row
Column
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60
70
80
0
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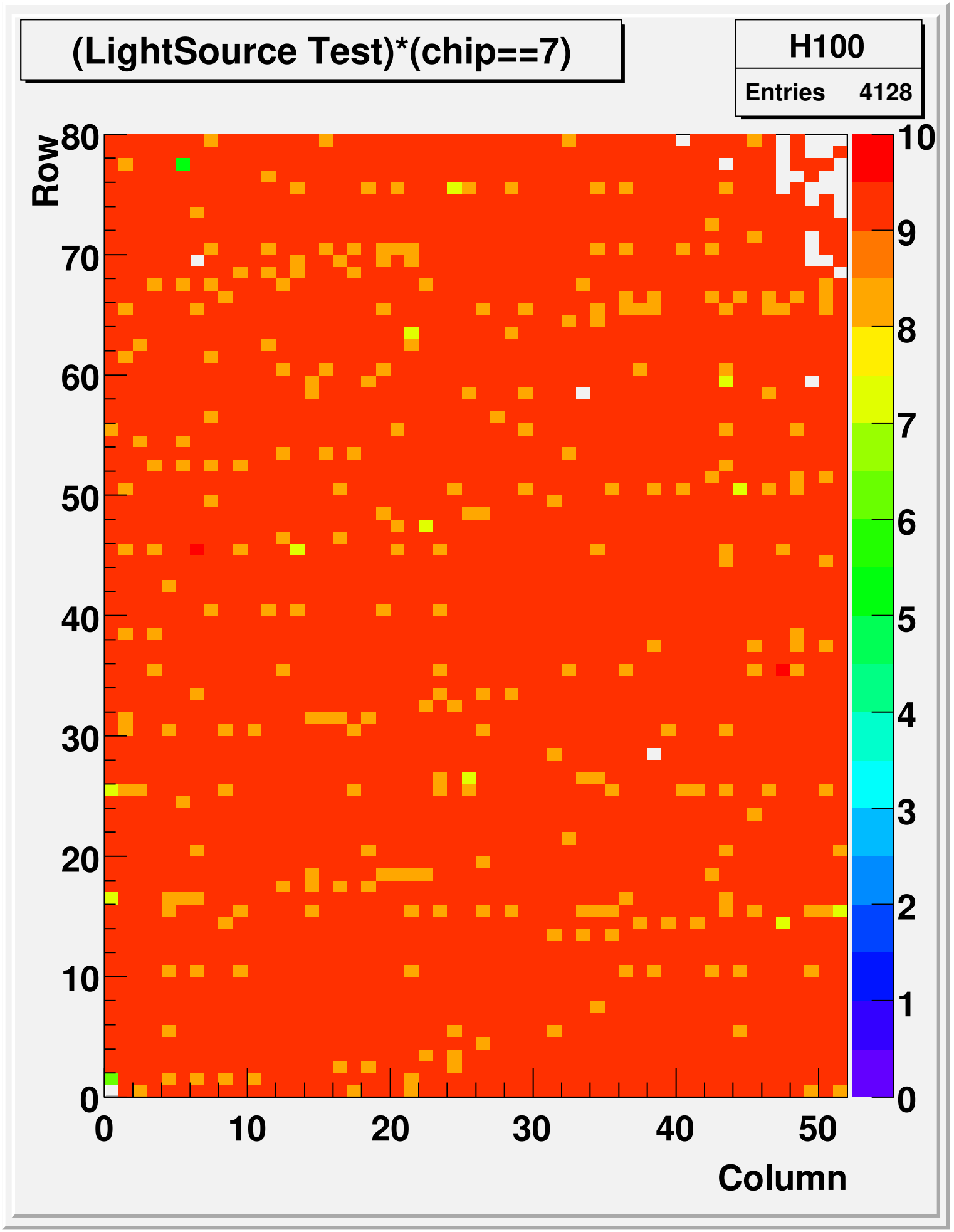
5
6
7
8
9
10


(LightSource Test)*(chip==7)
H100
Entries 4128
Row
Column
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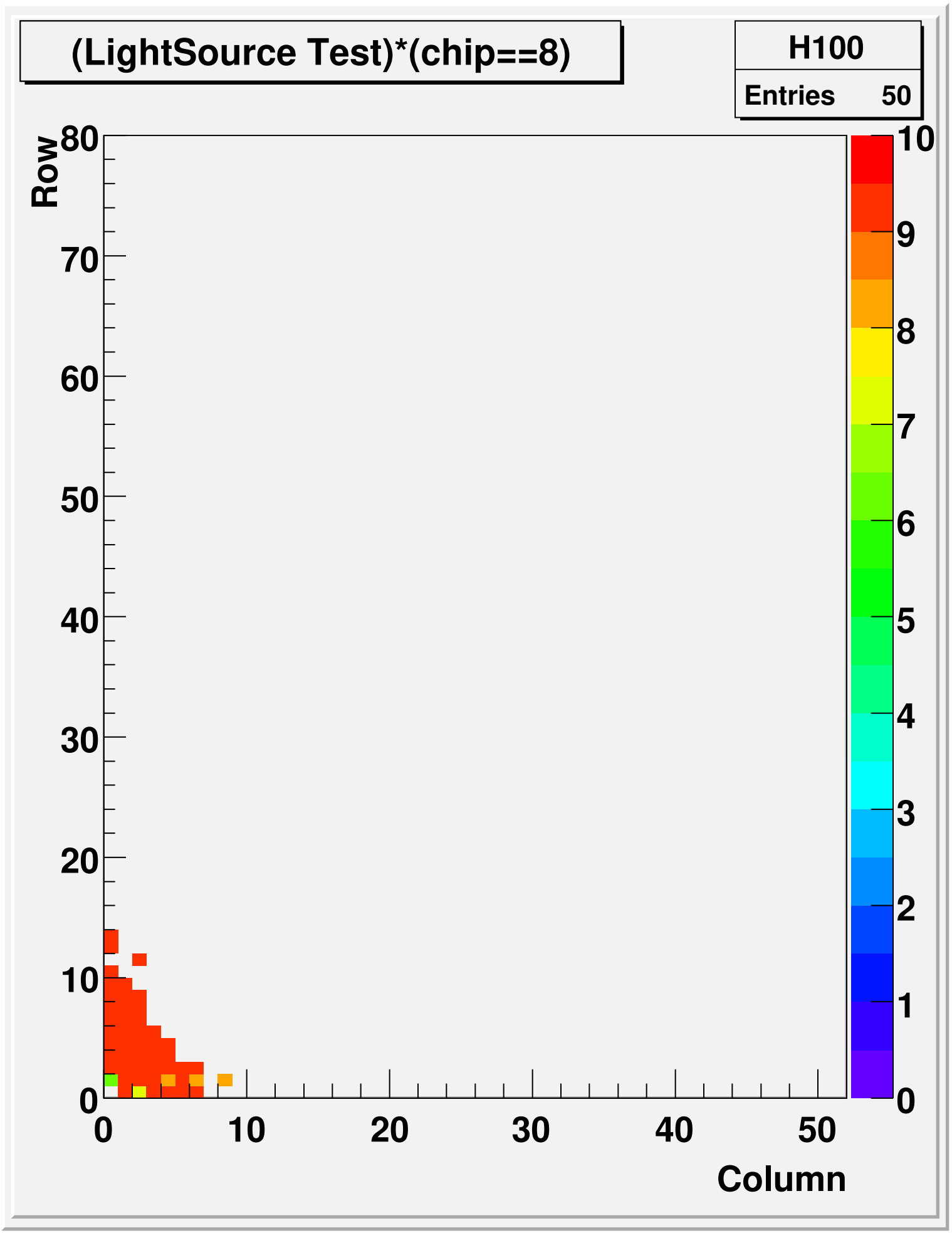
7
8
9
10


(LightSource Test)*(chip==8)
H100
Entries 50
Row
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70
60
50
40
30
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20
30
40
50
Column
10
9
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7
6
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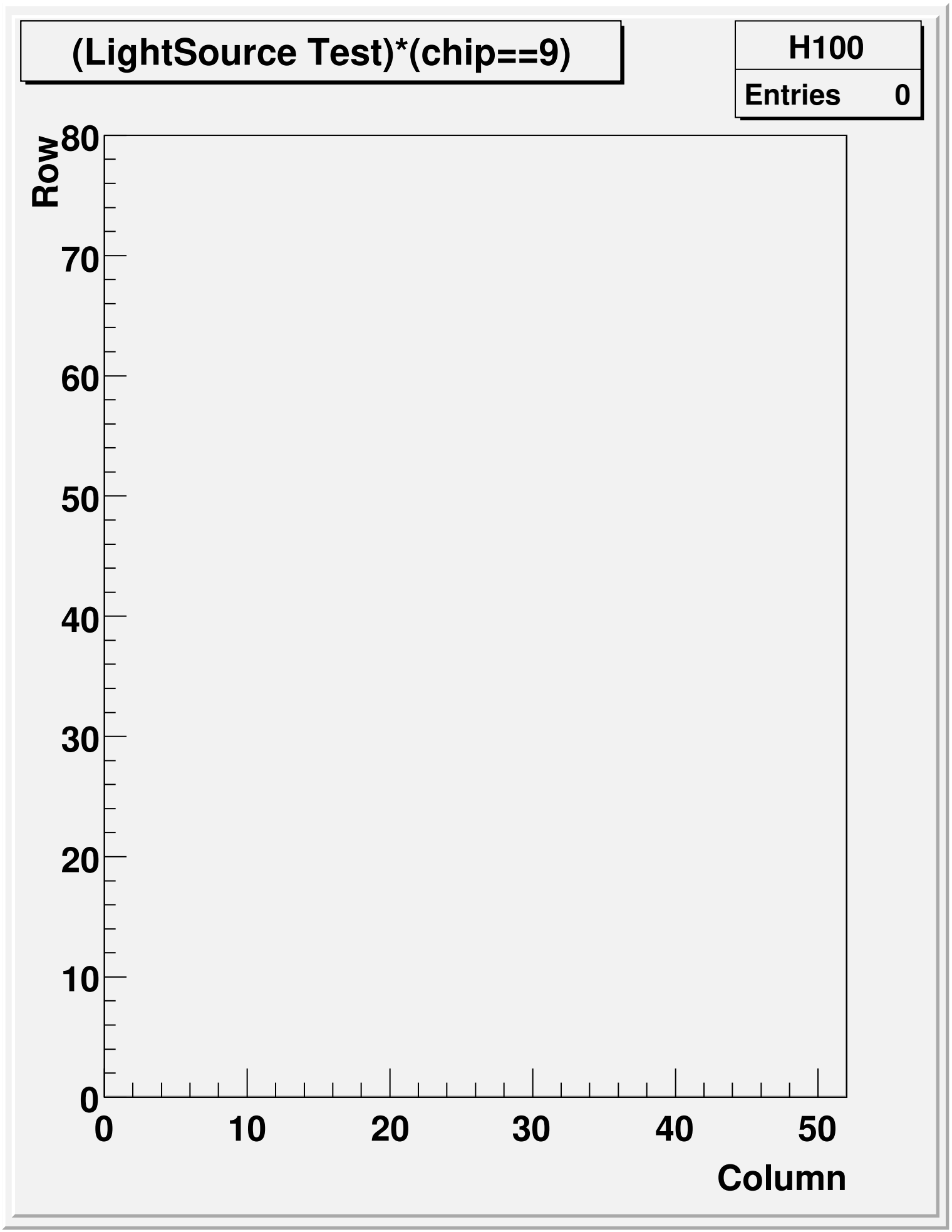
3
2
1
0


(LightSource Test)*(chip==9)
H100
Entries 0
Row
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60
50
40
30
20
10
0
0
10
20
30
40
50
Column