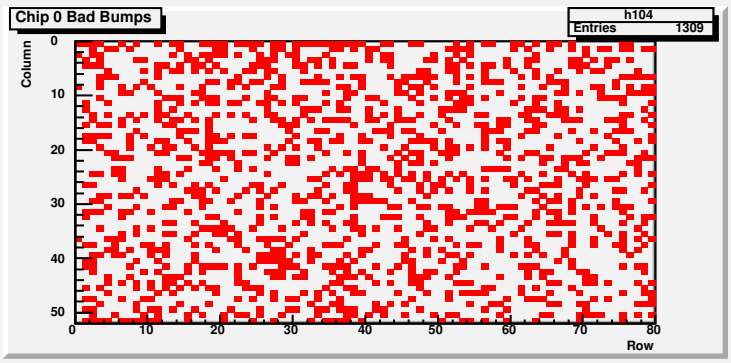
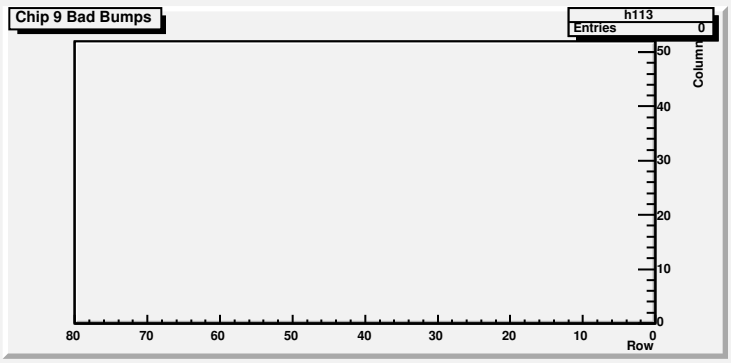

Chip 0 Bad Bumps
h104
Entries 1309
Column
Row


Chip 9 Bad Bumps
h113
Entries 0
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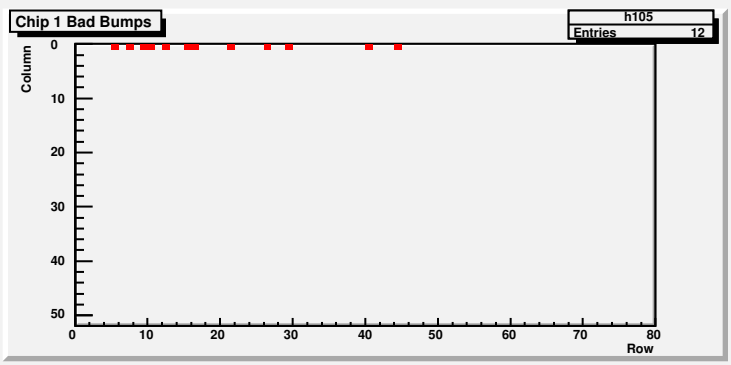
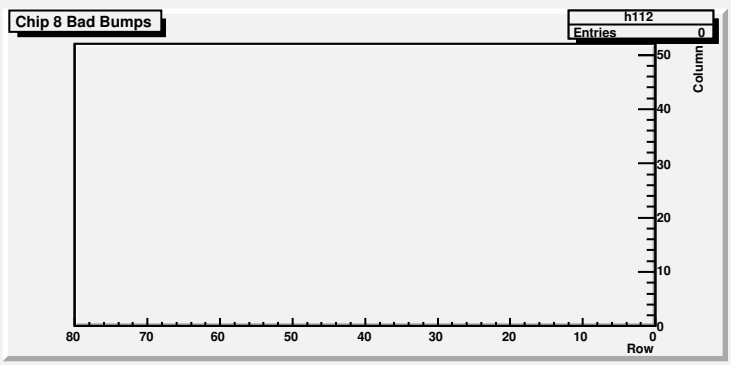
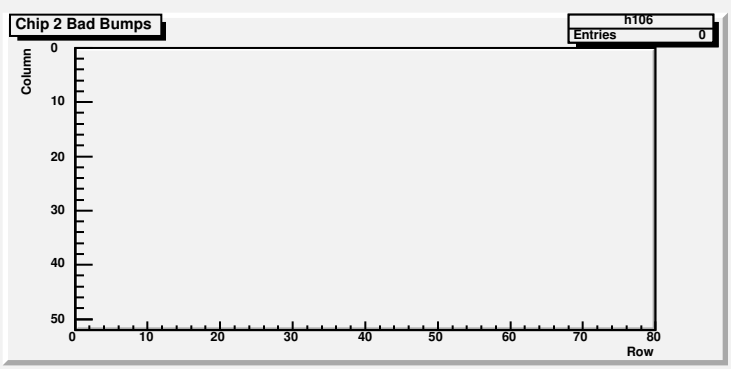
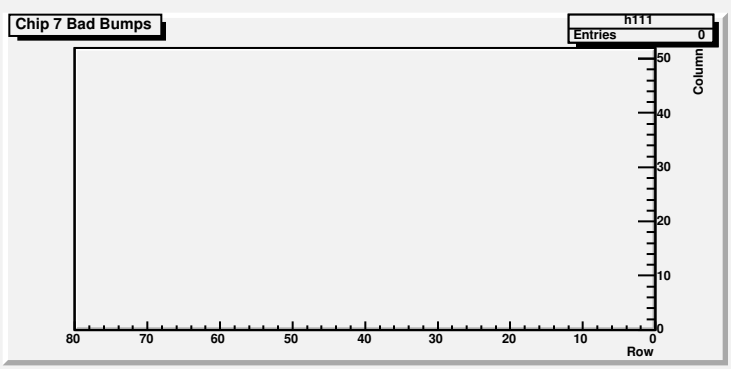
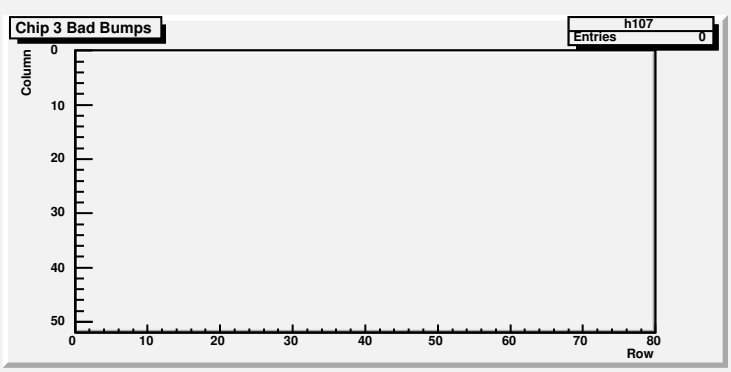
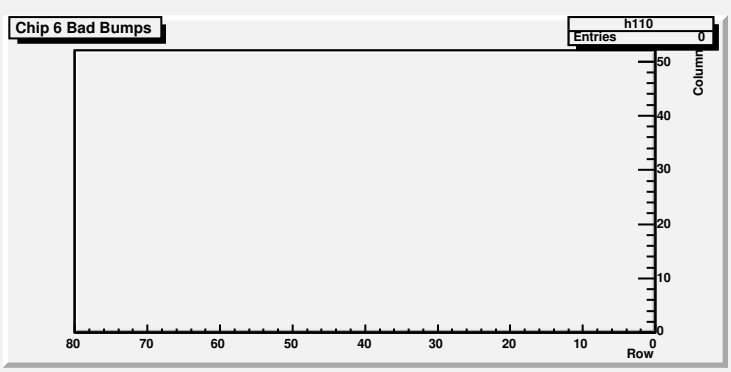

Chip 1 Bad Bumps
h105
Entries 12
Column
Row

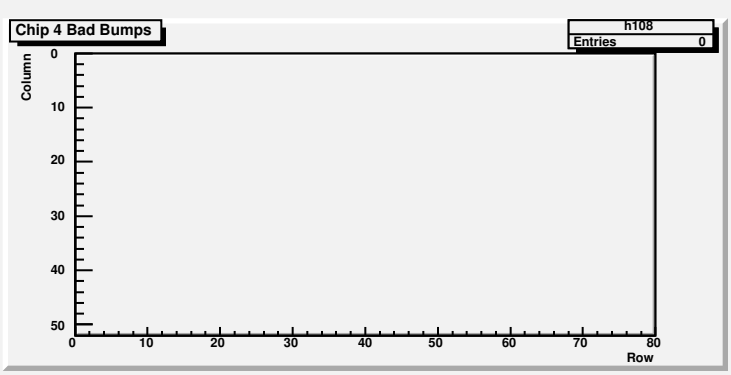

Chip 8 Bad Bumps
h112
Entries 0
Column
Row

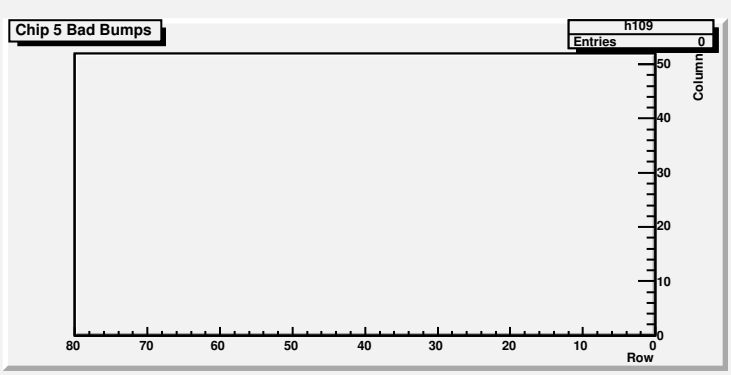

Chip 2 Bad Bumps
h106
Entries 0
Column
Row

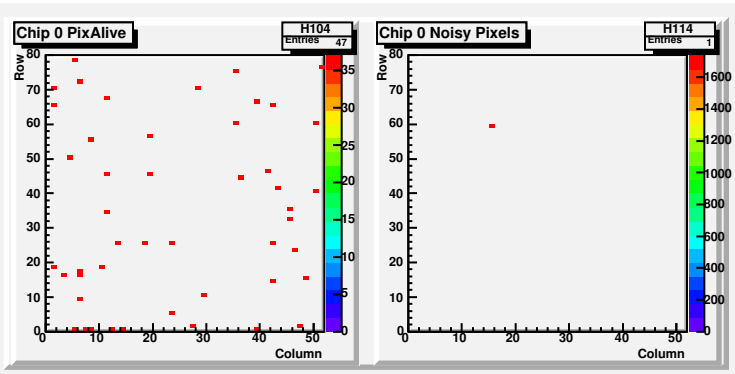

Chip 7 Bad Bumps
h111
Entries 0
Column
Row


Chip 3 Bad Bumps
h107
Entries 0
Column
Row

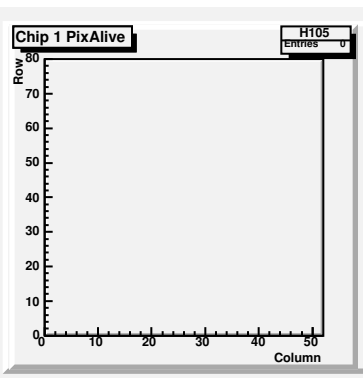

Chip 6 Bad Bumps
h110
Entries 0
Column
Row

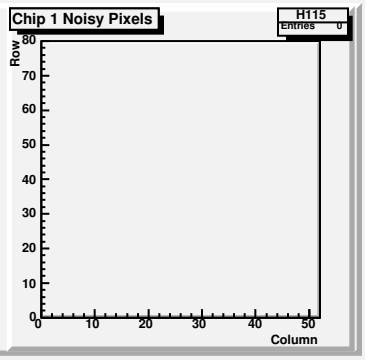

Chip 4 Bad Bumps
h108
Entries 0
Column
Row

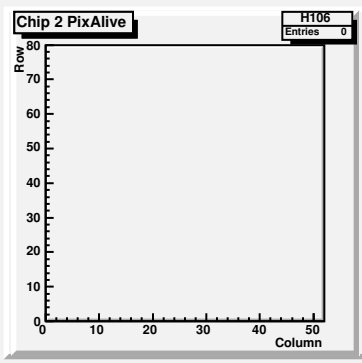

Chip 5 Bad Bumps
h109
Entries 0
Column
Row

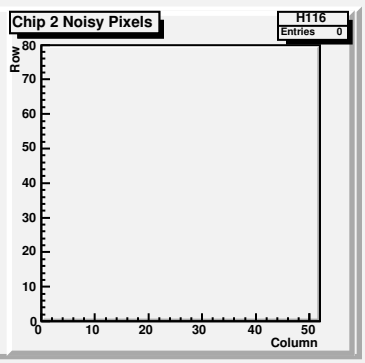

Chip 0 PixAlive
H104
Entries 47
Row
Column

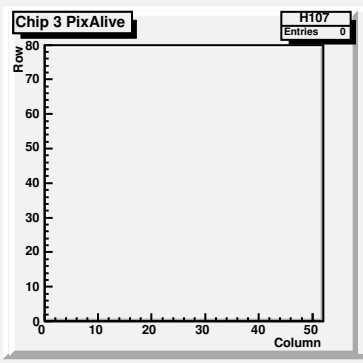
Chip 0 Noisy Pixels
H114
Entries 1
Row
Column

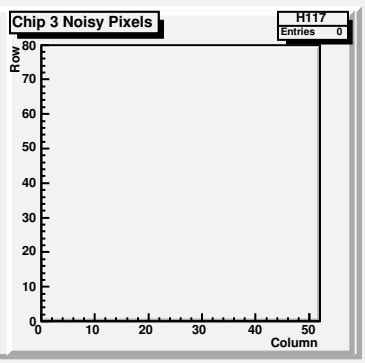

Chip 1 PixAlive
H105
Entries 0
Row
Column

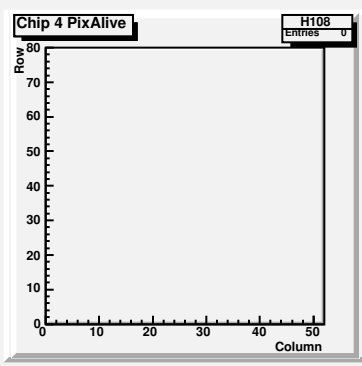

Chip 1 Noisy Pixels
H115
Entries 0
Row
Column

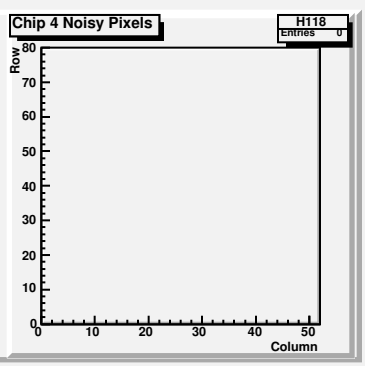

Chip 2 PixAlive
H106
Entries 0
Row
Column


Chip 2 Noisy Pixels
H116
Entries 0
Row
Column


Chip 3 PixAlive
H107
Entries 0
Row
Column


Chip 3 Noisy Pixels
H117
Entries 0
Row
Column


Chip 4 PixAlive
H108
Entries 0
Row
Column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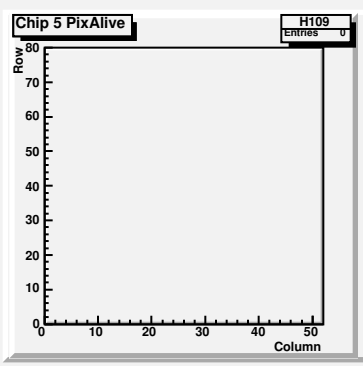
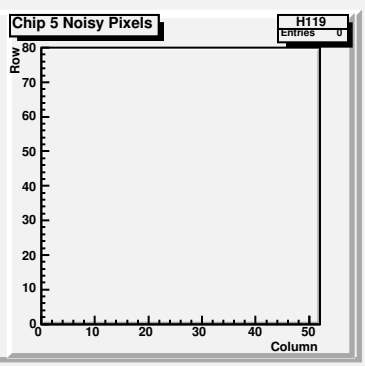
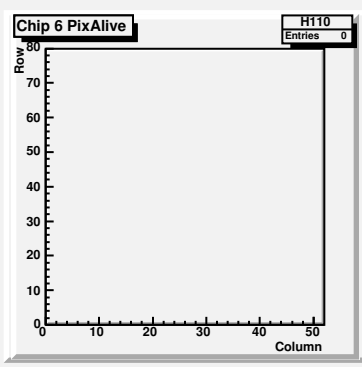
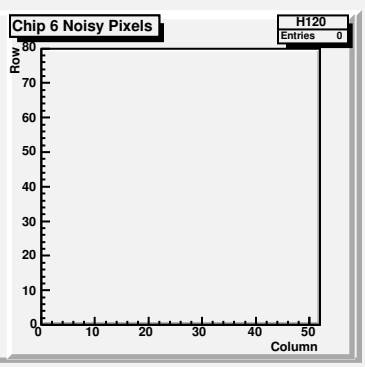
H118
Entries 0
Row
Column


Chip 5 PixAlive
H109
Entries 0
Row
Column


Chip 5 Noisy Pixels
H119
Entries 0
Row
Column


Chip 6 PixAlive
H110
Entries 0
Row
Column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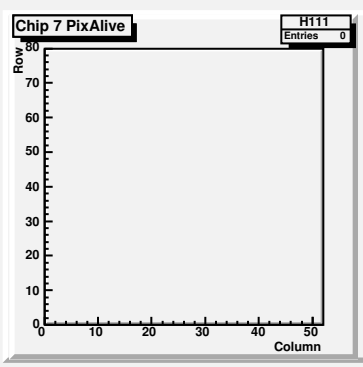
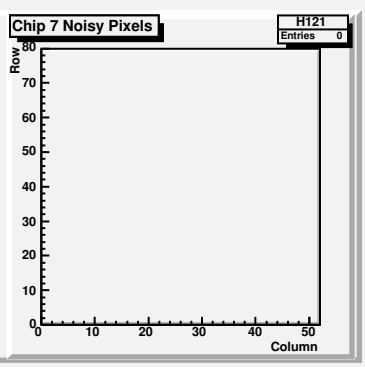
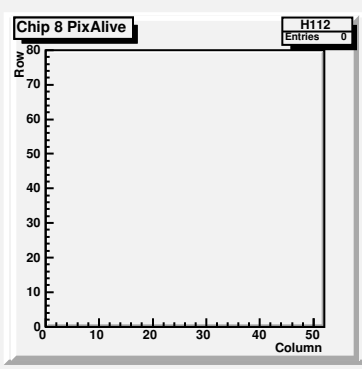
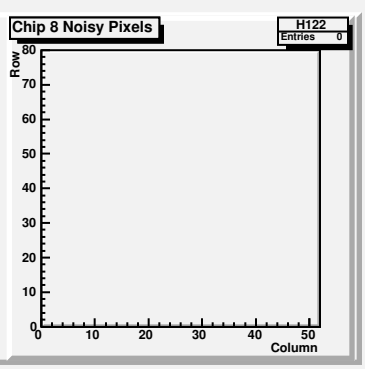
Column


Chip 7 PixAlive
H111
Entries 0
Row
Column


Chip 7 Noisy Pixels
H121
Entries 0
Row
Column


Chip 8 PixAlive
H112
Entries 0
Row
Column


Chip 8 Noisy Pixels
H122
Entries 0
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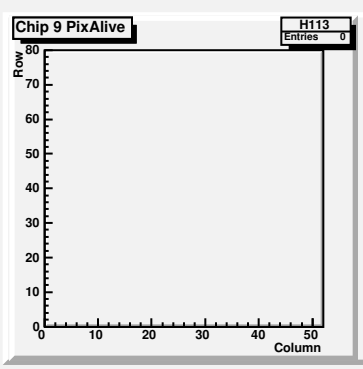
Column


Chip 9 PixAlive
H113
Entries 0
Row
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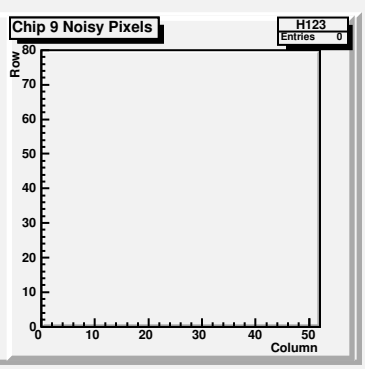
Chip 9 Noisy Pixels
H123
Entries 0
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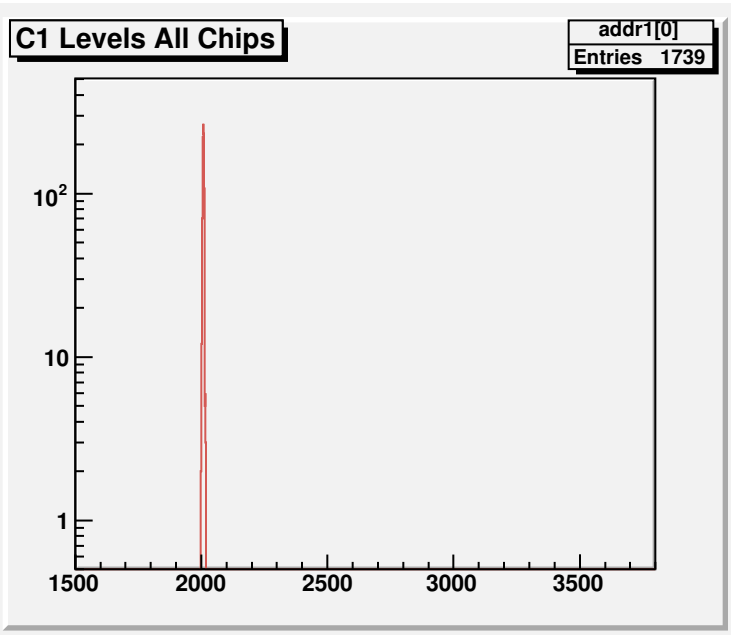
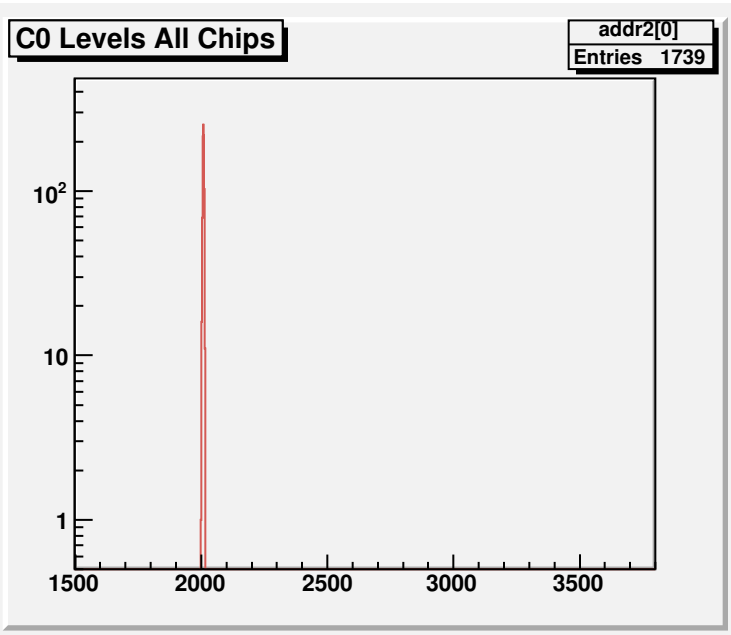
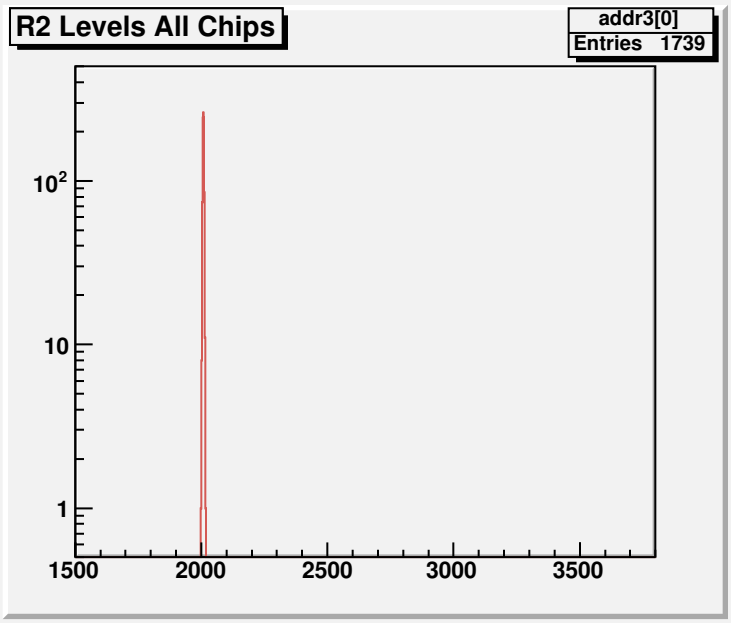
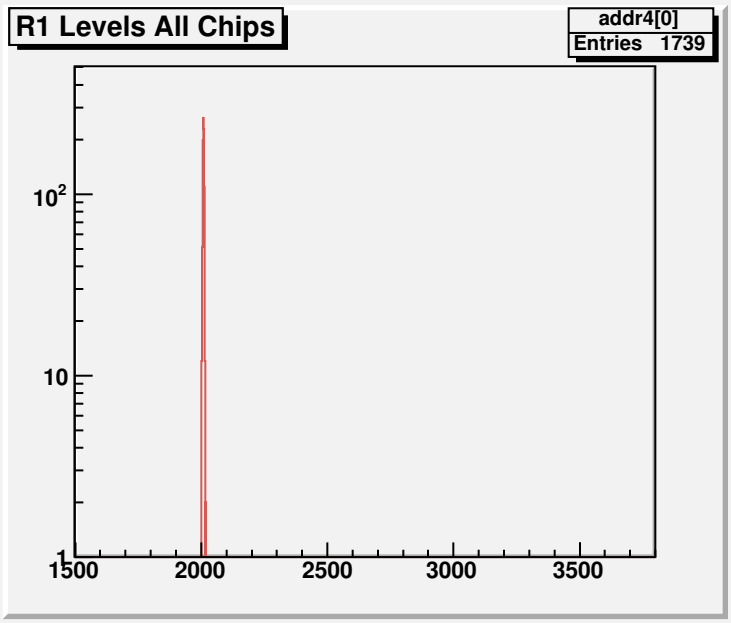
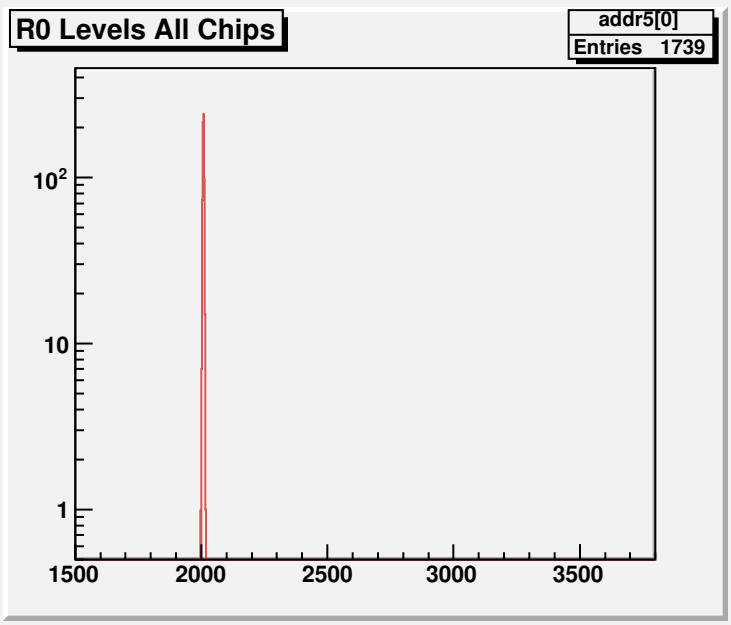
Column

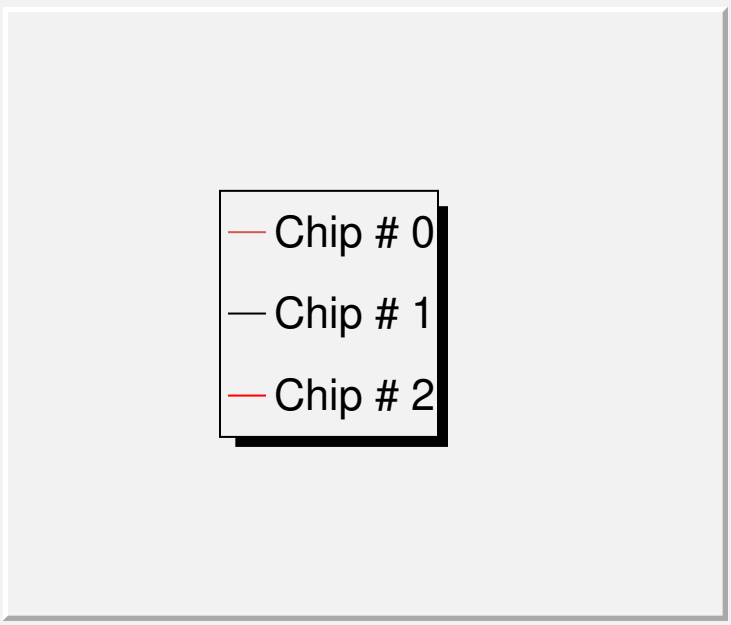

C1 Levels All Chips
addr1[0]
Entries 1739
10^2
10
1
1500
2000
2500
3000
3500

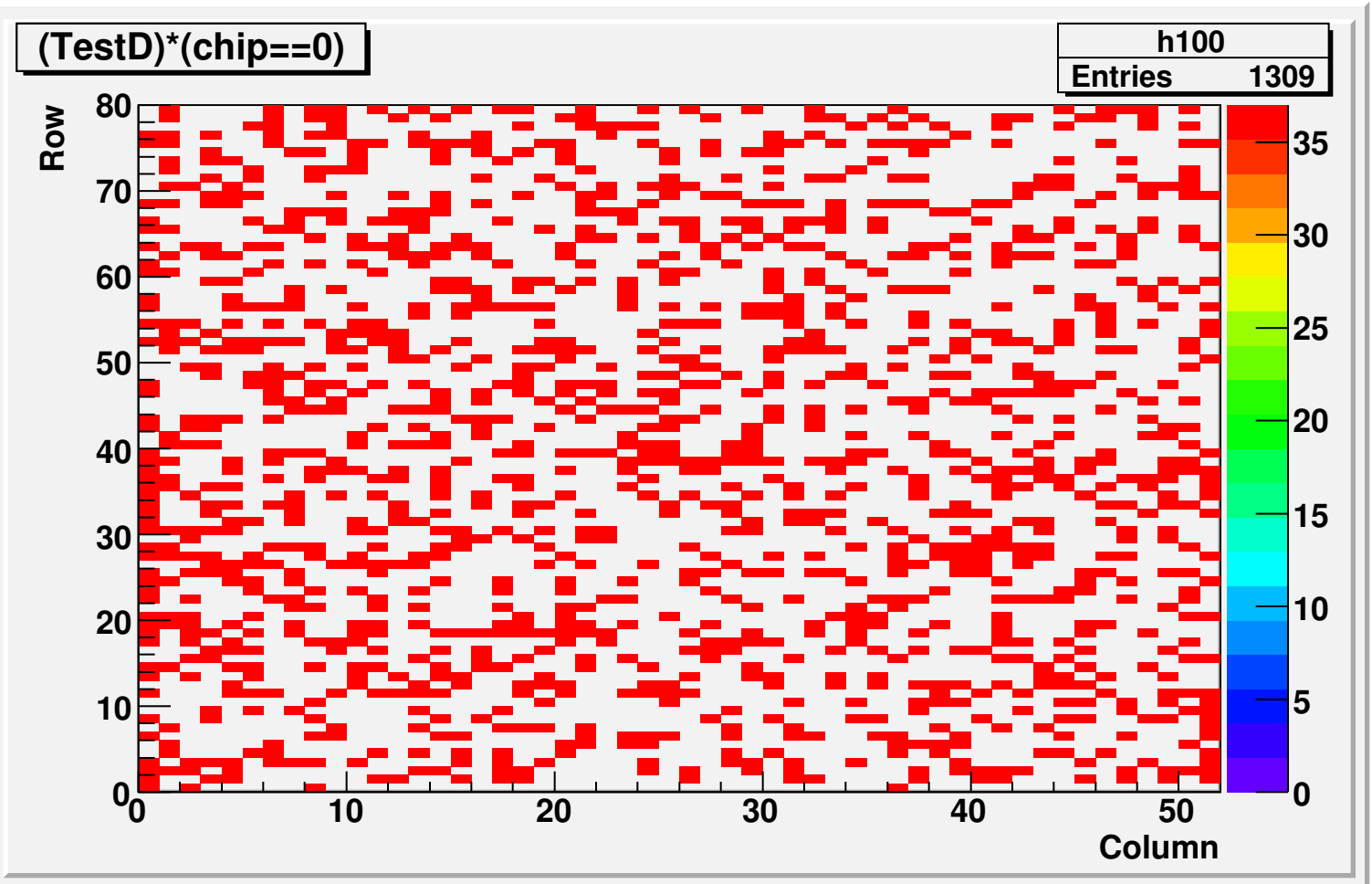

C0 Levels All Chips
addr2[0]
Entries 1739
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 1739
10^2
10
1
1500
2000
2500
3000
3500


R1 Levels All Chips
addr4[0]
Entries 1739
10^2
10
1
1500
2000
2500
3000
3500


R0 Levels All Chips
addr5[0]
Entries 1739
10^2
10
1
1500
2000
2500
3000
3500


Chip # 0
Chip # 1
Chip # 2


(TestD)*(chip==0)
h100
Entries 1309
Row
Column
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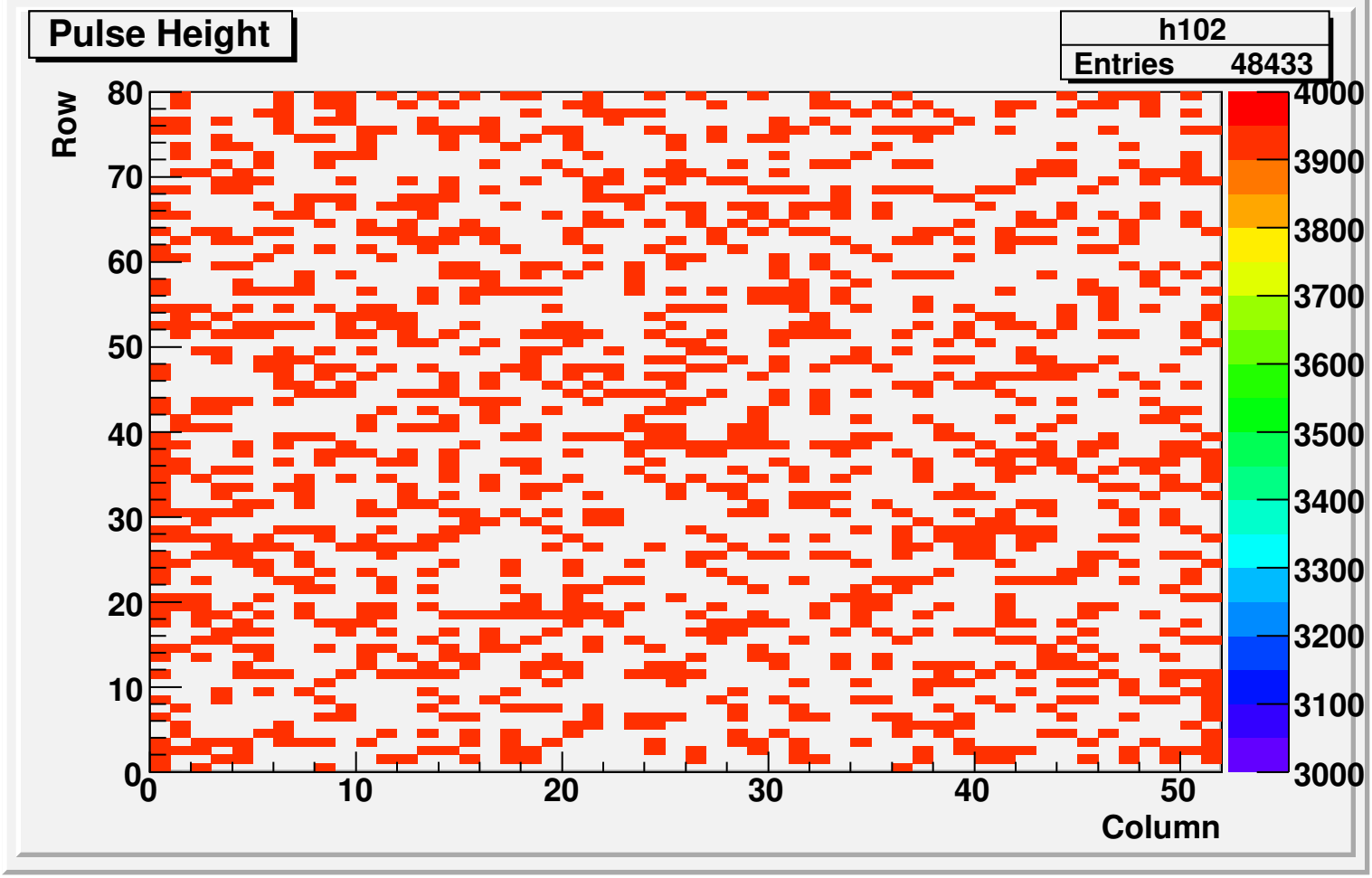
20
30
40
50
60
70
80
0
5
10
15
20
25
30
35


Pulse Height
h102
Entries 48433
Row
Column
0
10
20
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40
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80
3000
3100
3200
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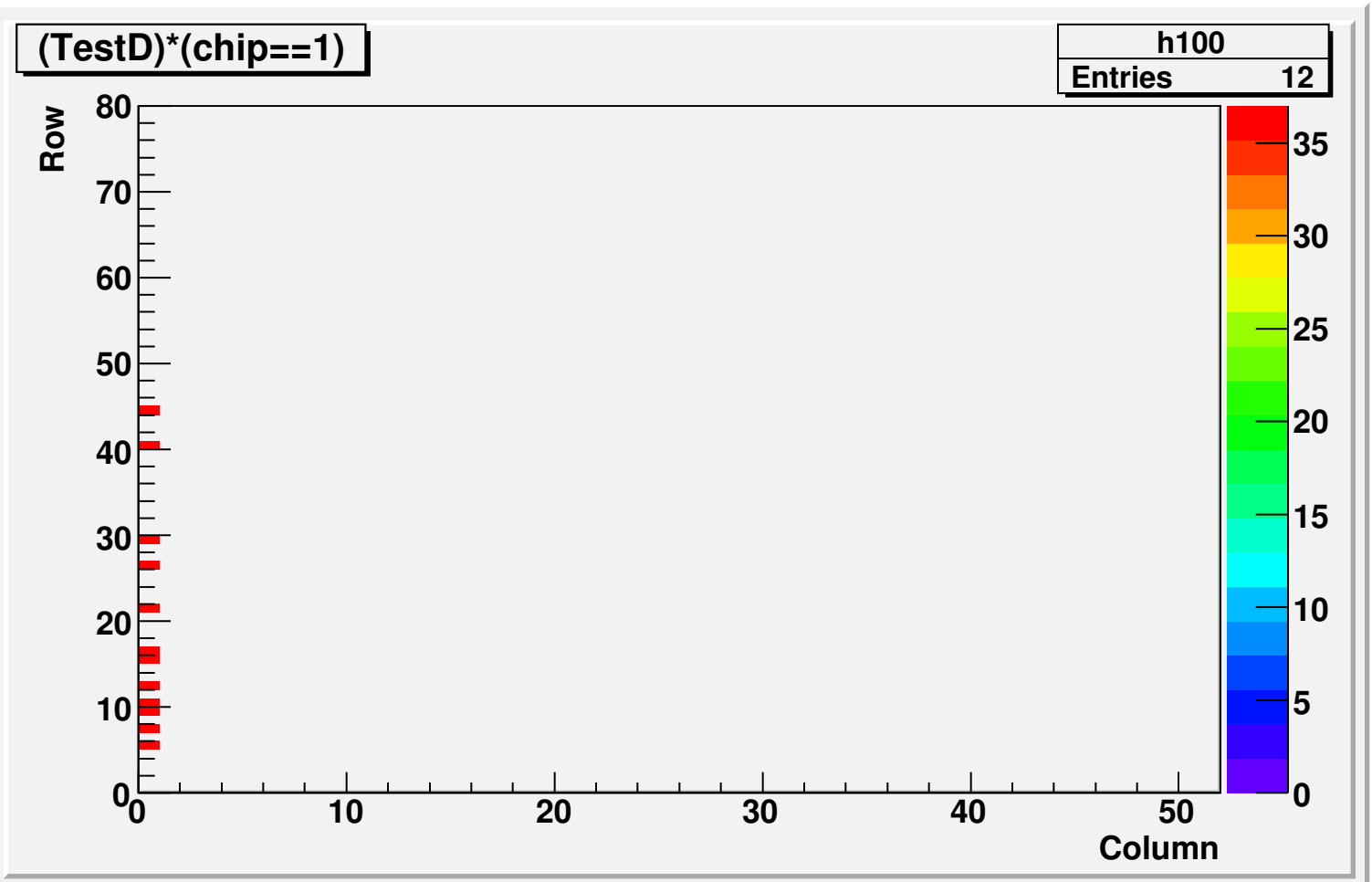
3600
3700
3800
3900
4000


(TestD)*(chip==1)
h100
Entries 12
Row
Column
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20
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0
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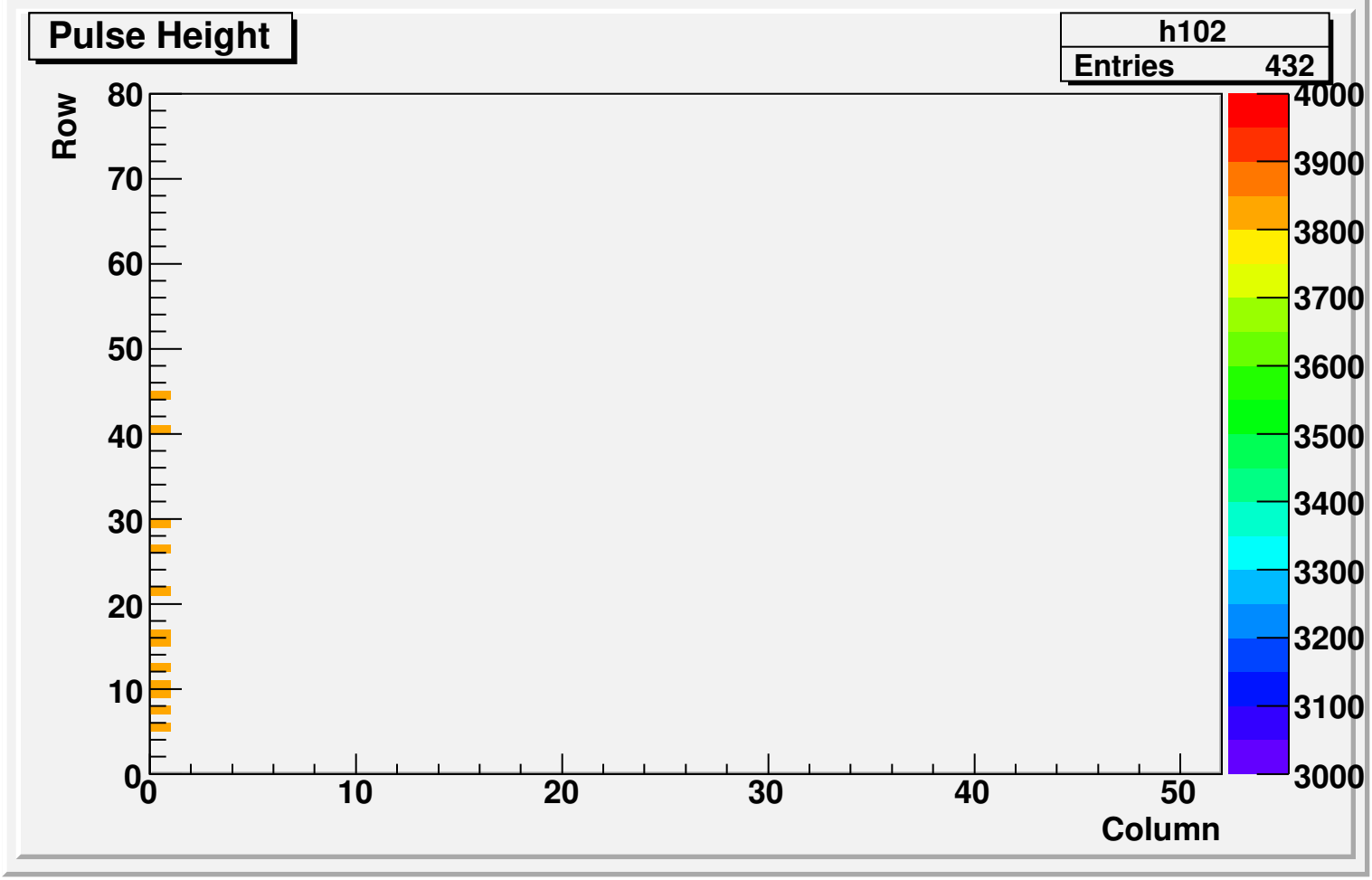
20
25
30
35


Pulse Height
h102
Entries 432
Row
Column
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20
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80
3000
3100
3200
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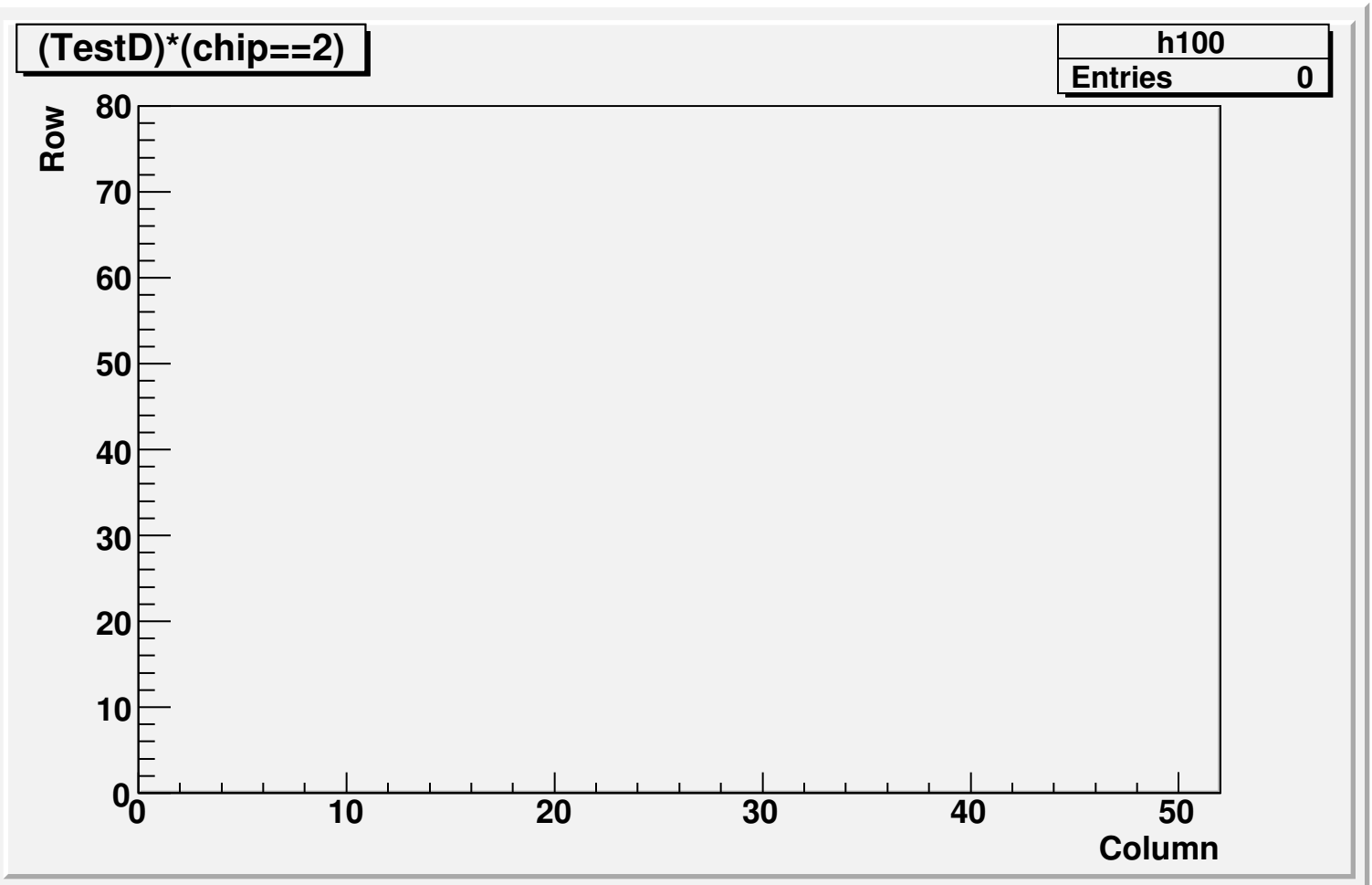
3600
3700
3800
3900
4000


(TestD)*(chip==2)
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Entries 0
Row
Column
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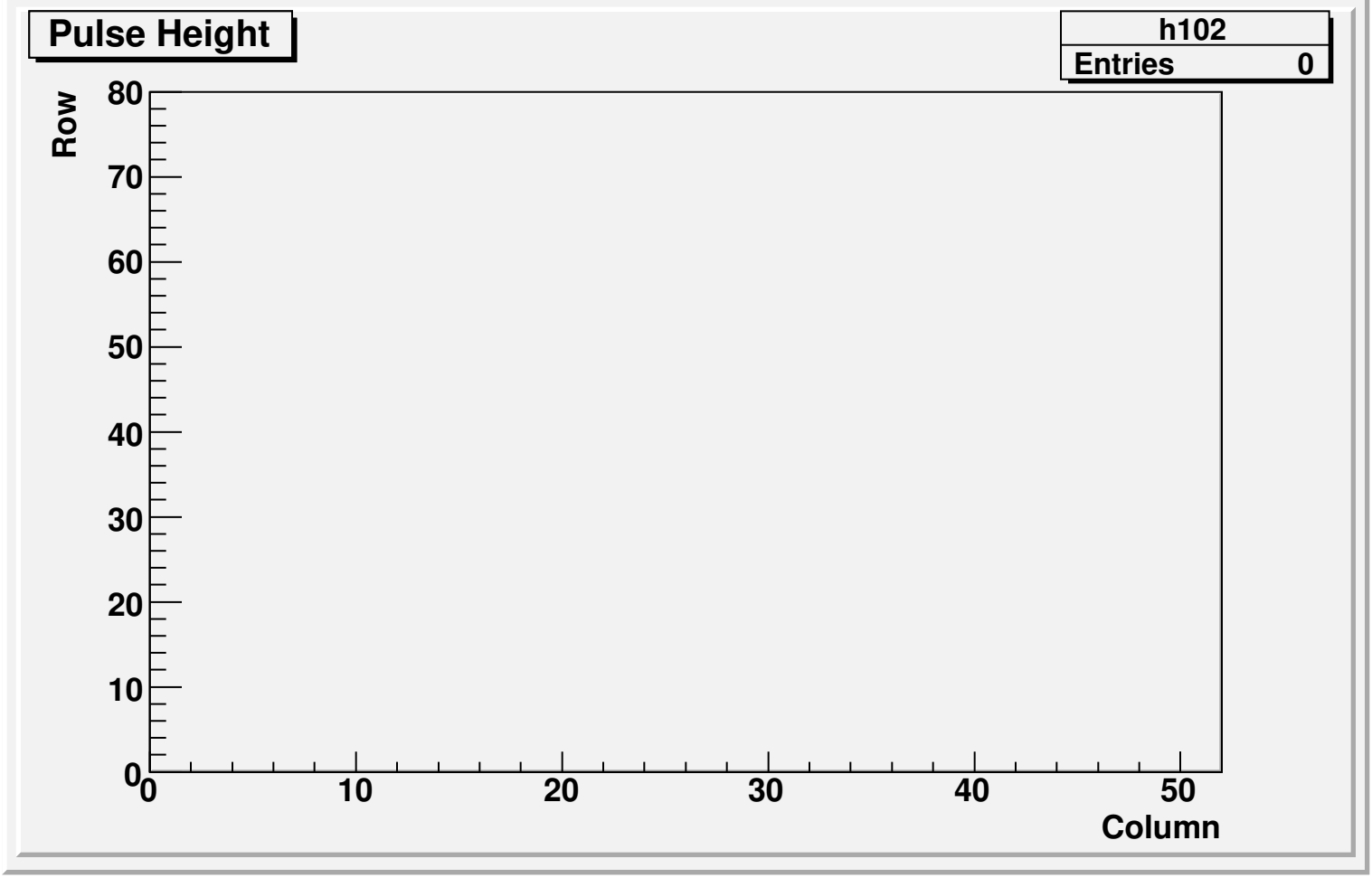
50
60
70
80


Pulse Height
h102
Entries 0
Row
Column
0
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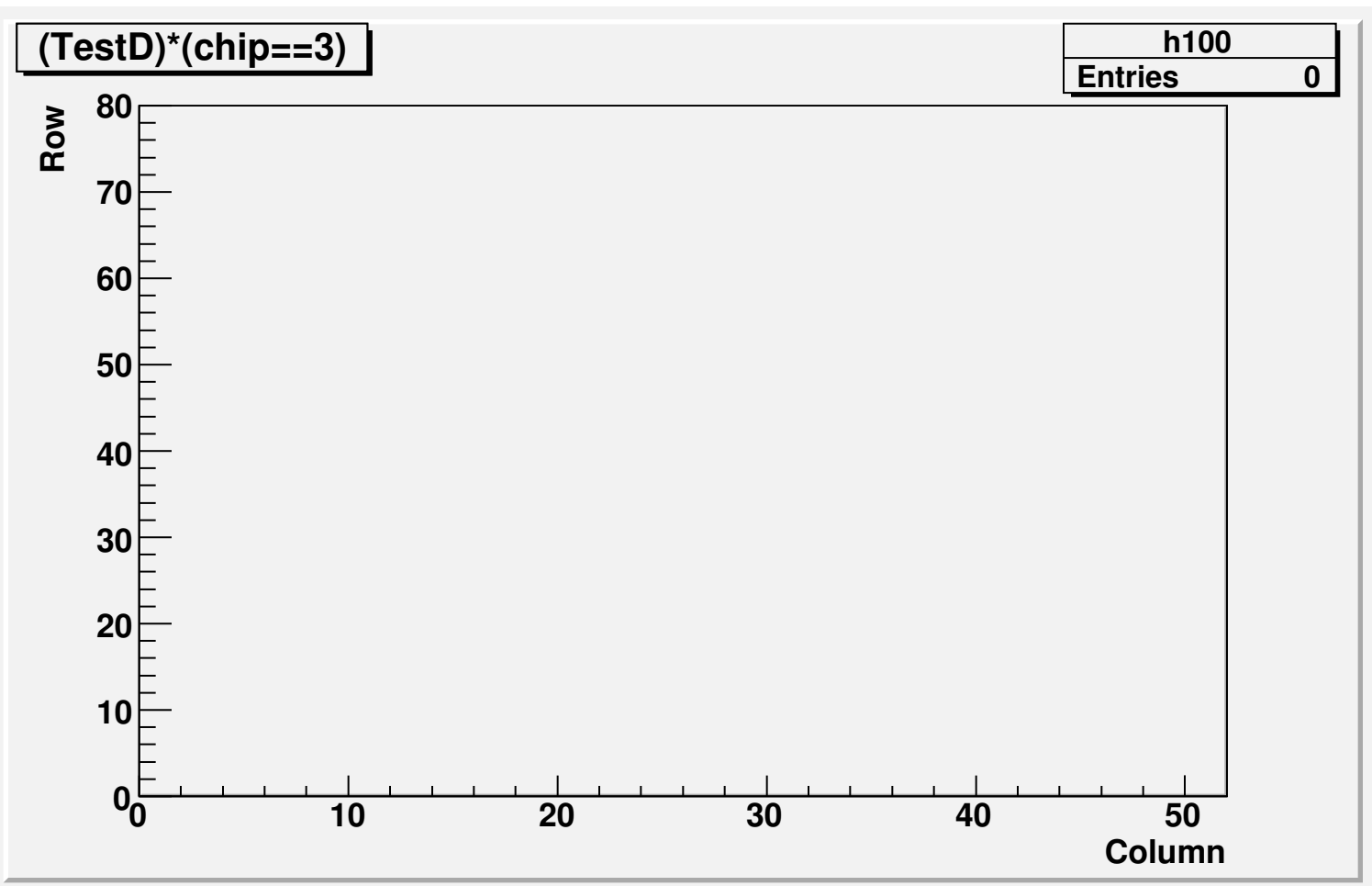
50
60
70
80


(TestD)*(chip==3)
h100
Entries 0
Row
Column
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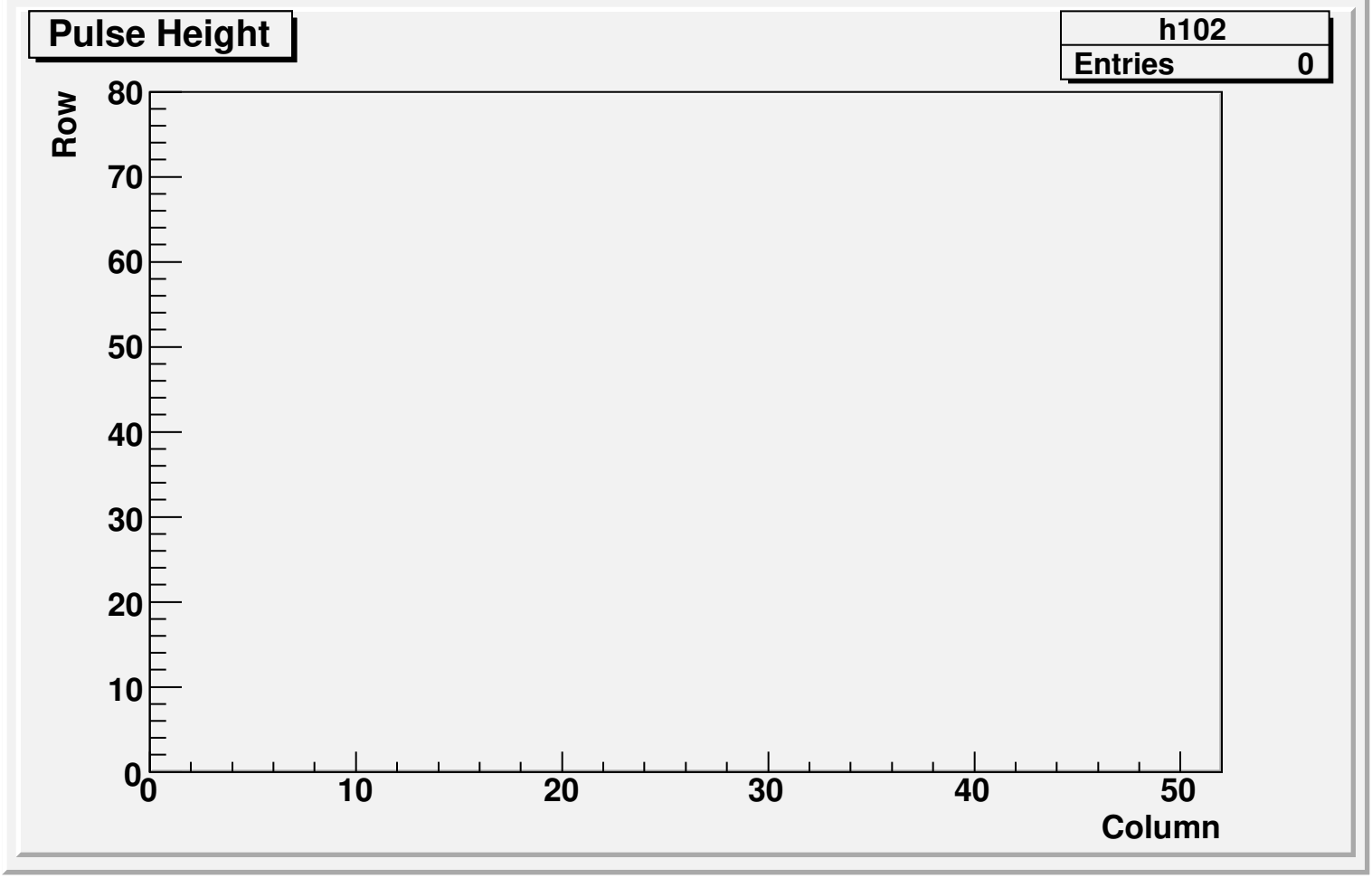
50
60
70
80


Pulse Height
h102
Entries 0
Row
Column
0
10
20
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50
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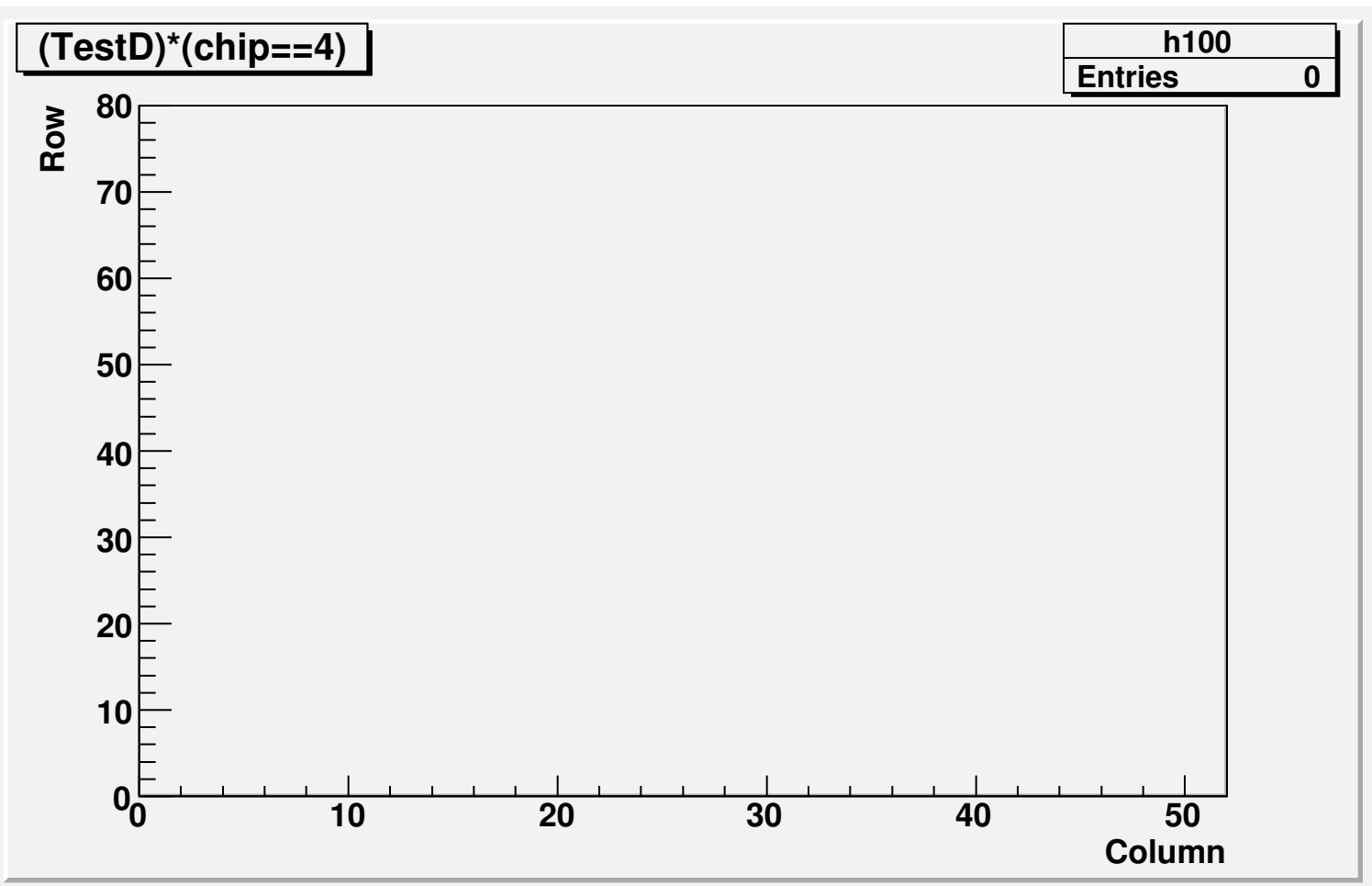
70
80


(TestD)*(chip==4)
h100
Entries 0
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70
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30
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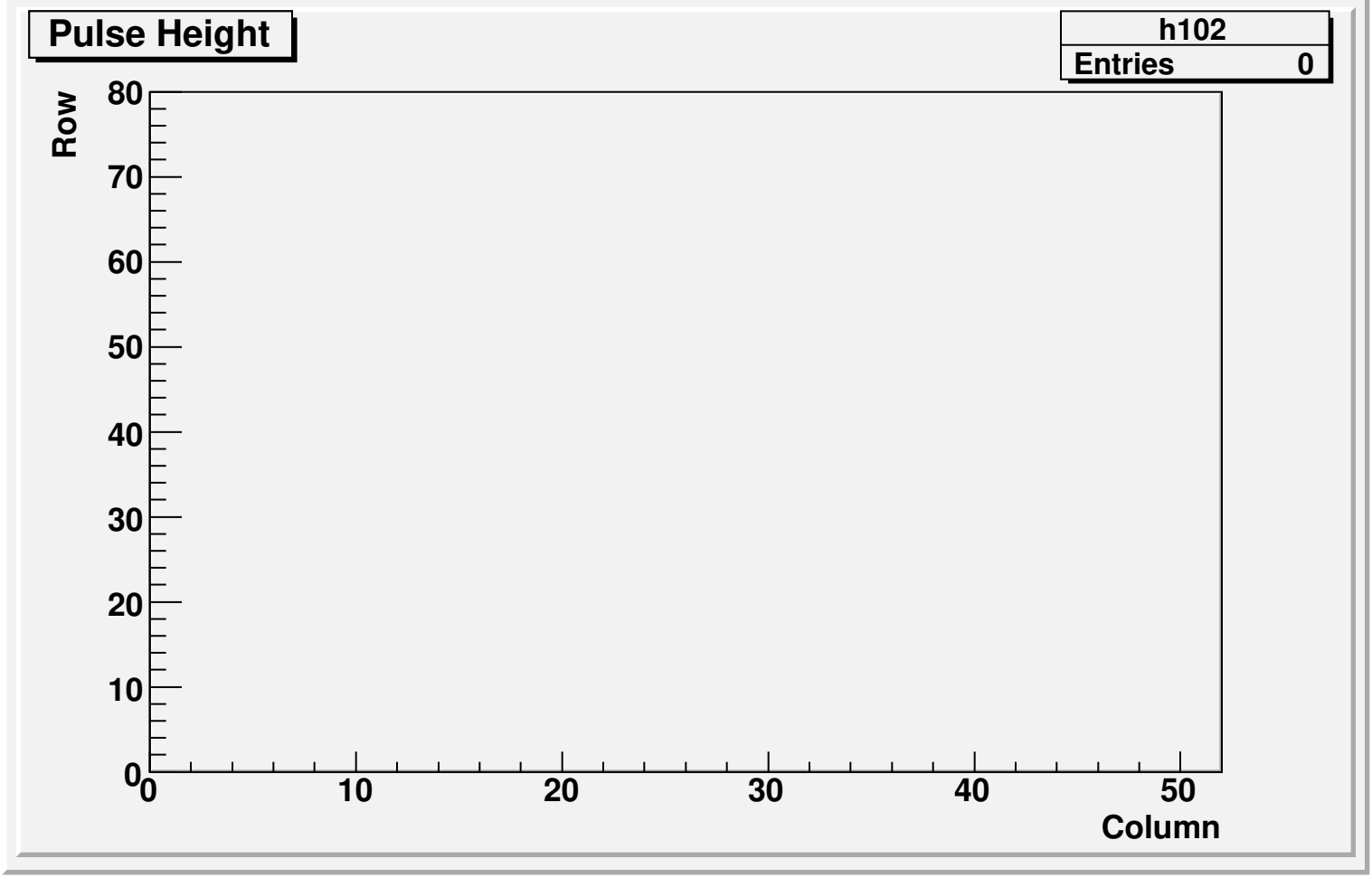
10
0
0
10
20
30
40
50
Column


Pulse Height
h102
Entries 0
Row
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50
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0
0
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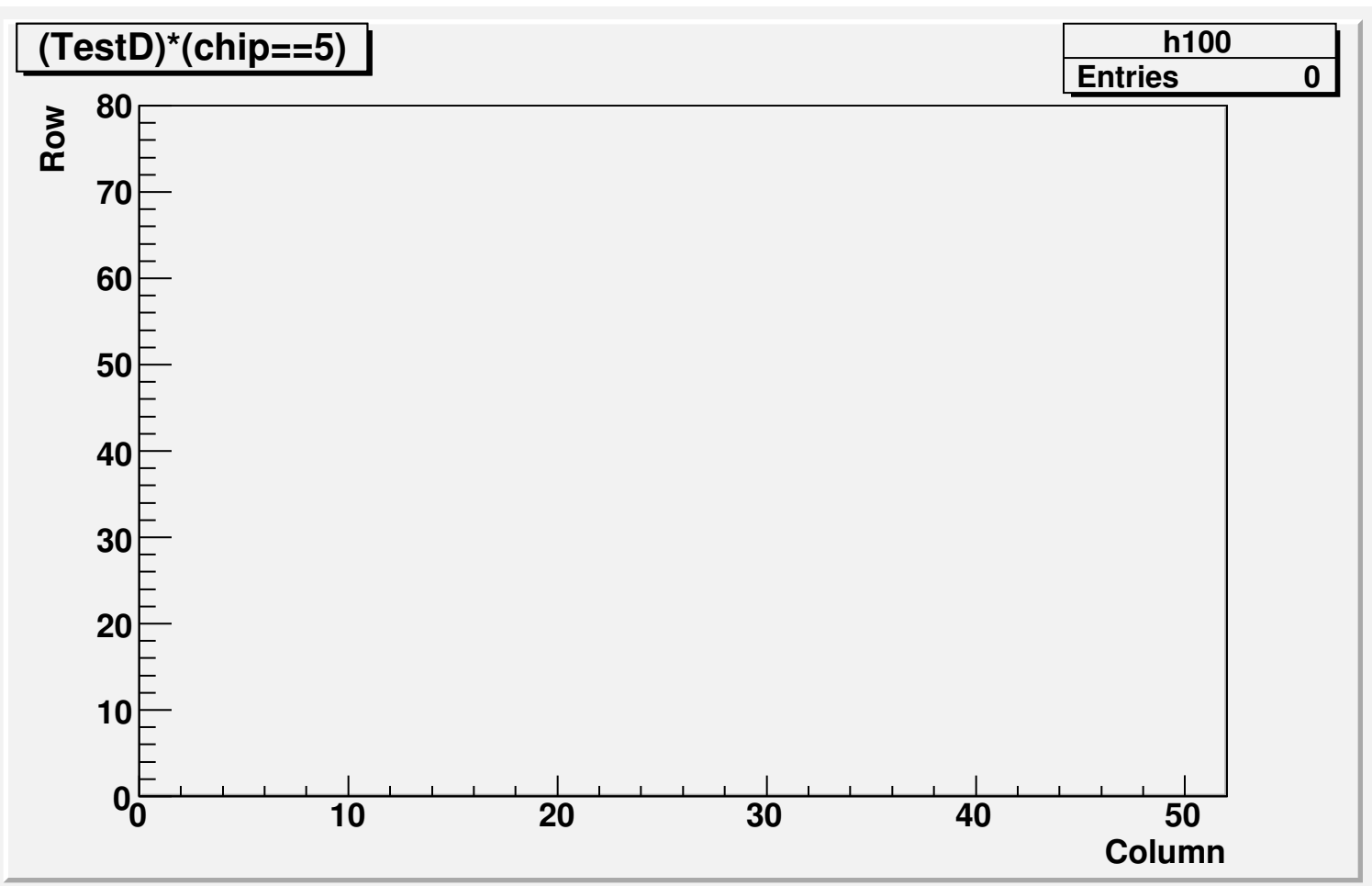
30
40
50
Column


(TestD)*(chip==5)
h100
Entries 0
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0
0
10
20
30
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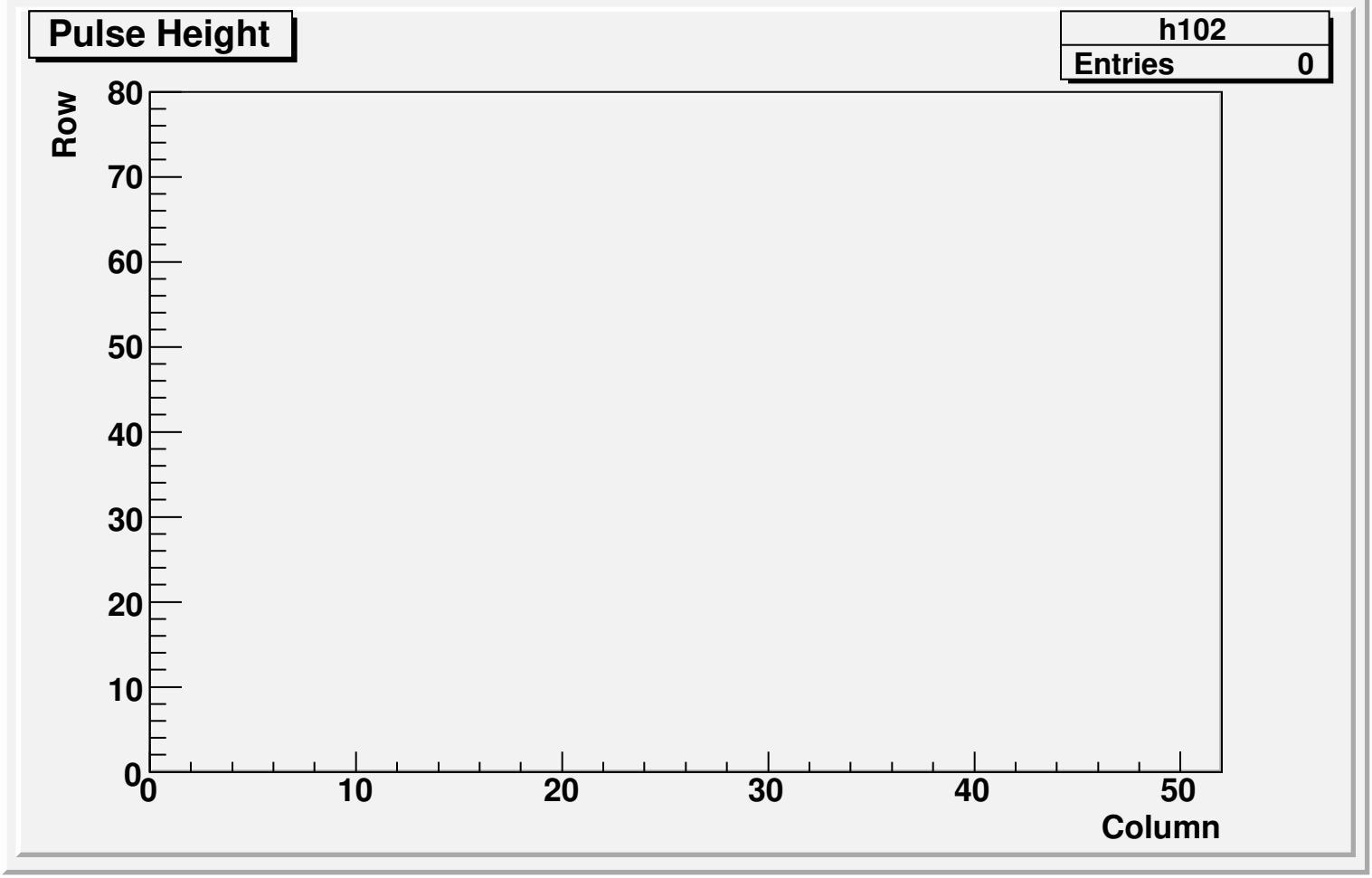
50
Column


Pulse Height
h102
Entries 0
Row
80
70
60
50
40
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0
0
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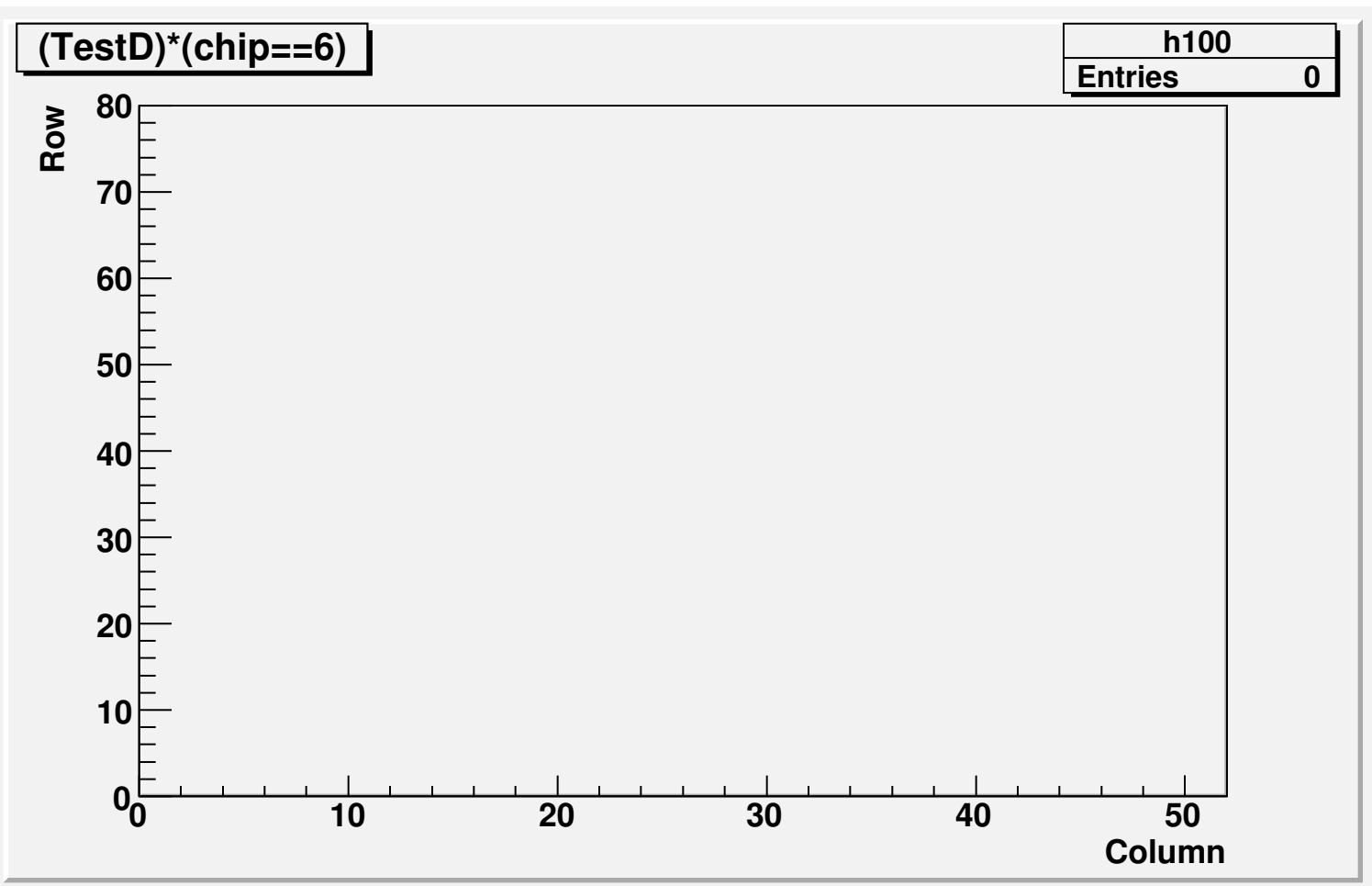
30
40
50
Column


(TestD)*(chip==6)
h100
Entries 0
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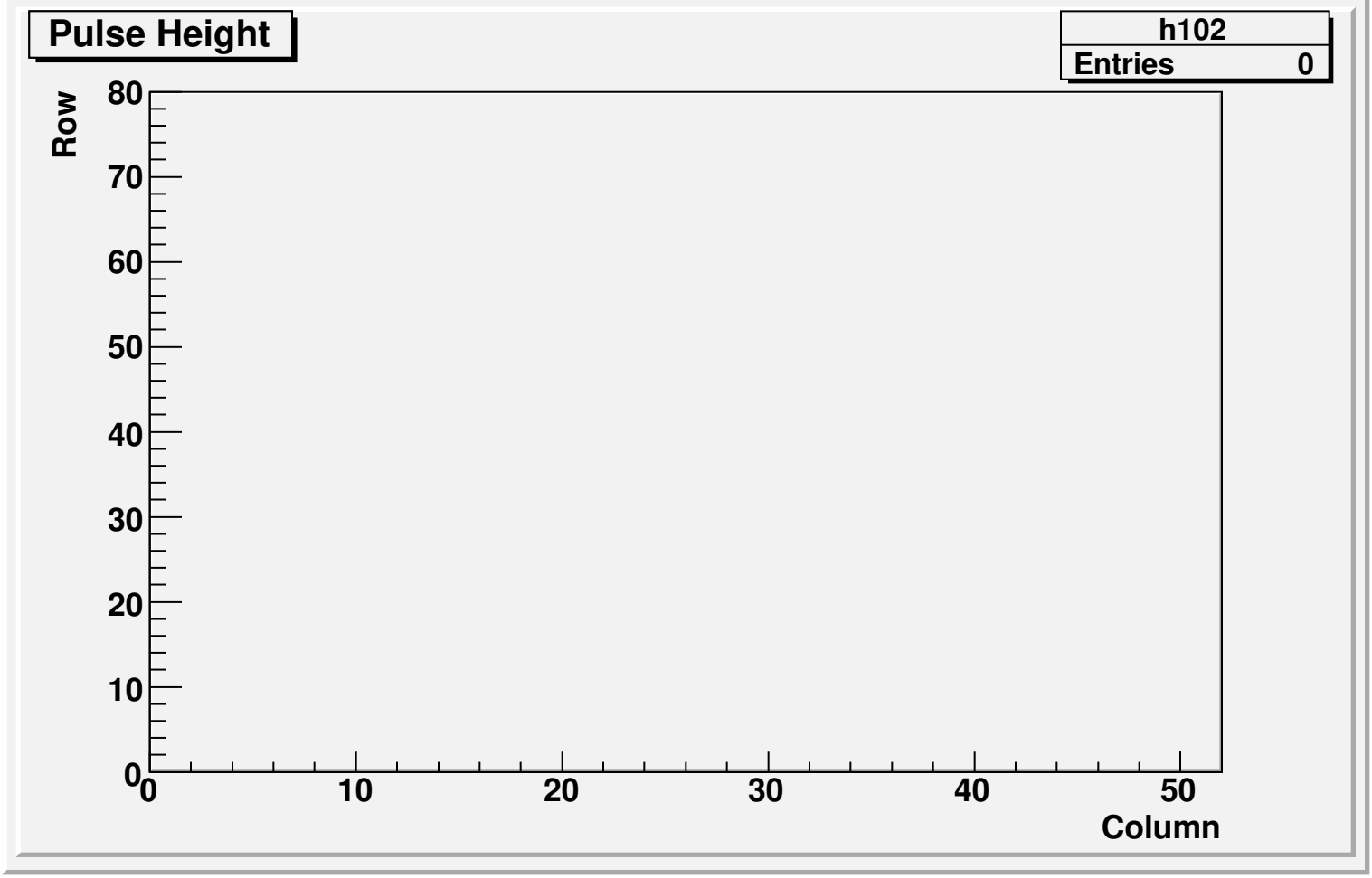
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column


Pulse Height
h102
Entries 0
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80
70
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50
40
30
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30
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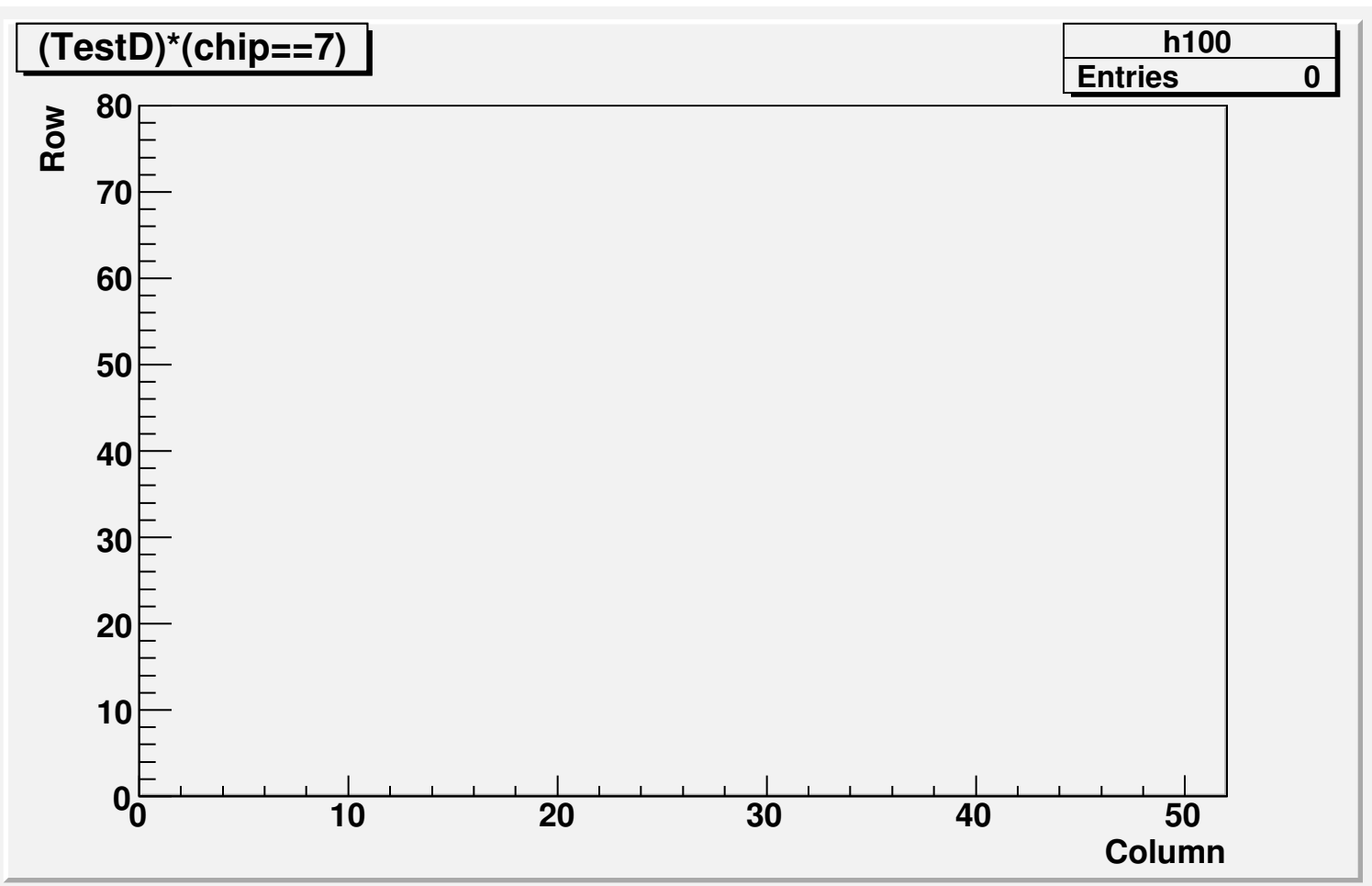
50
Column


(TestD)*(chip==7)
h100
Entries 0
Row
Column
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10
20
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60
70
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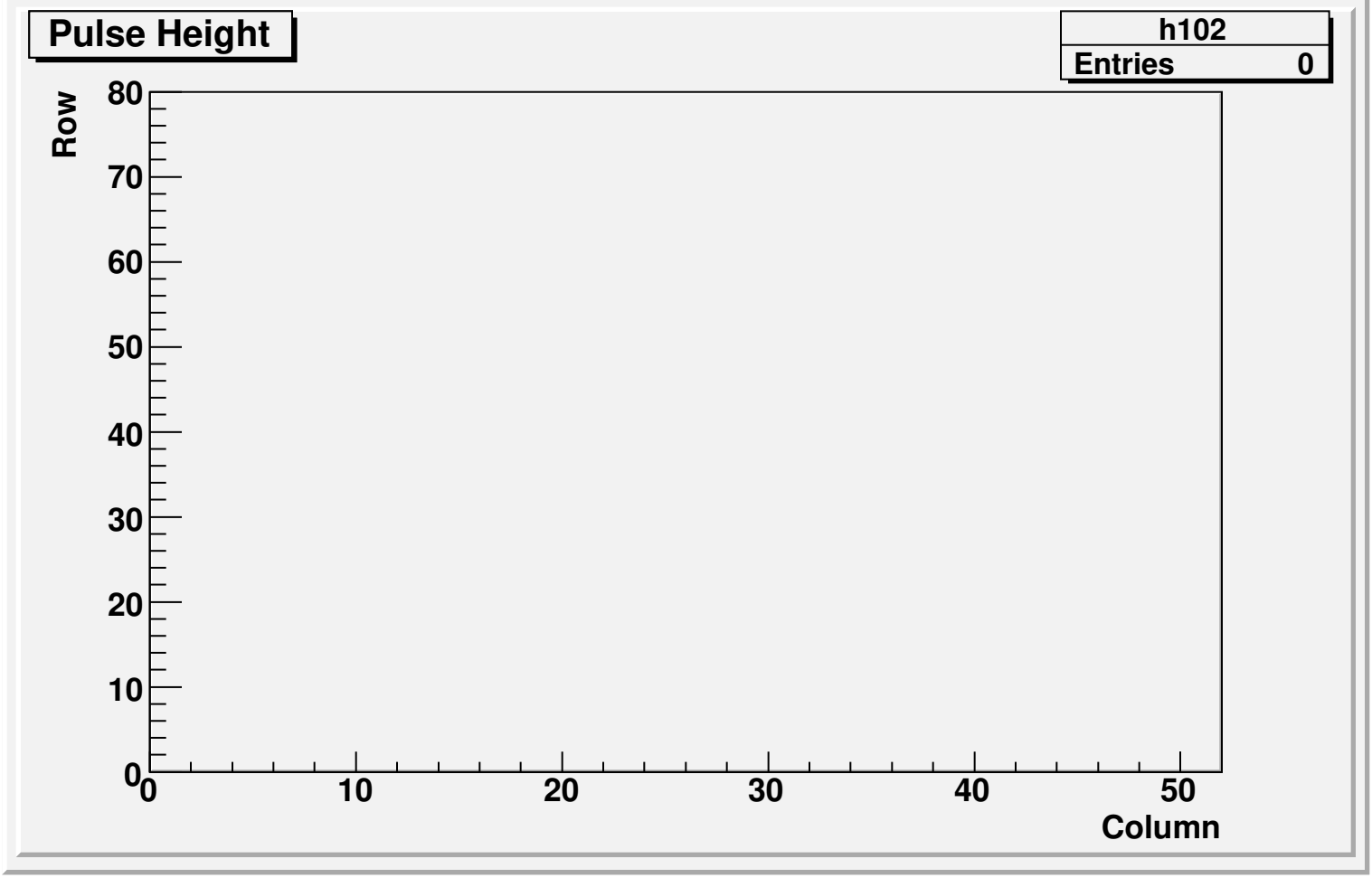
Pulse Height
h102
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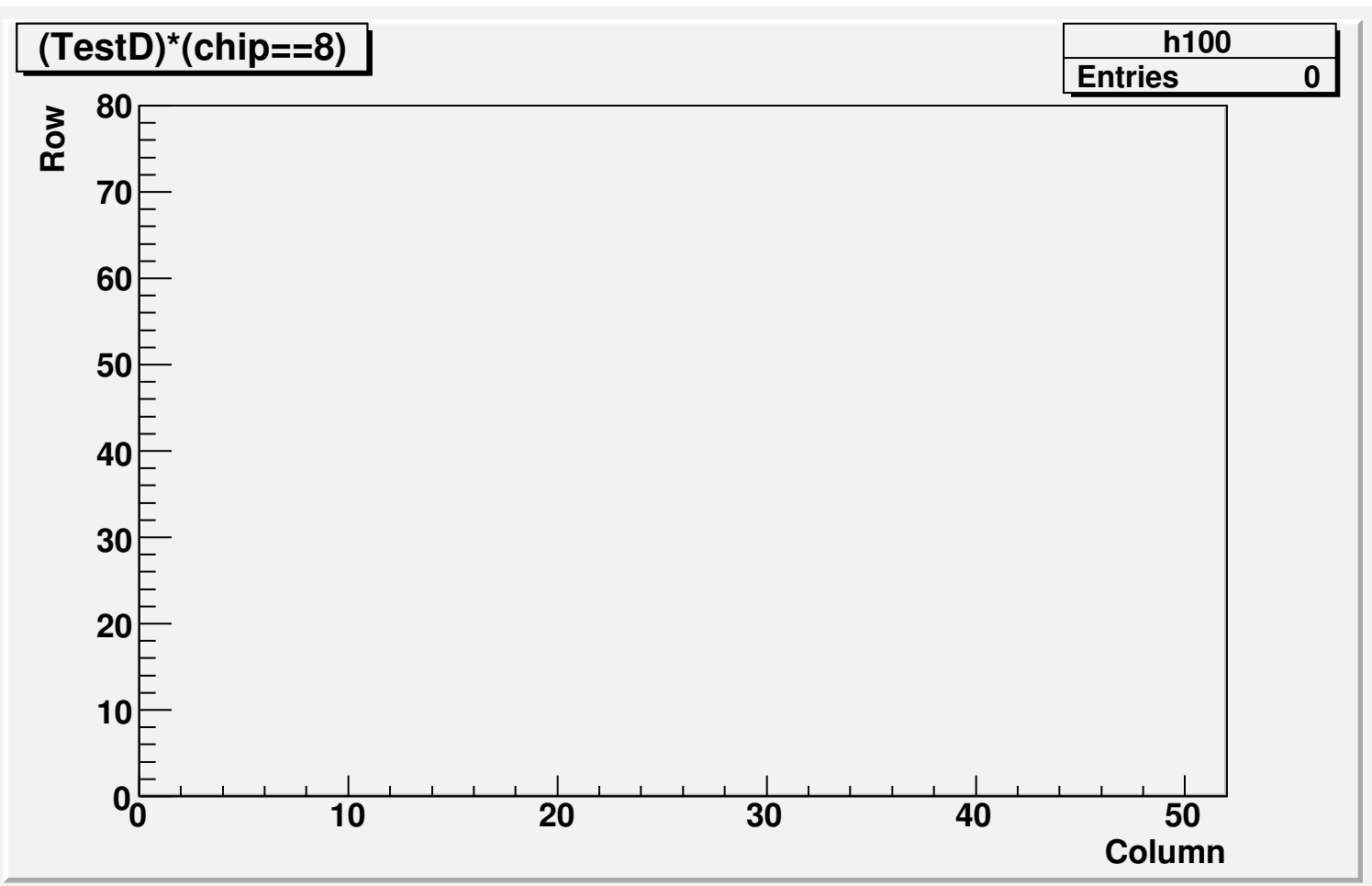
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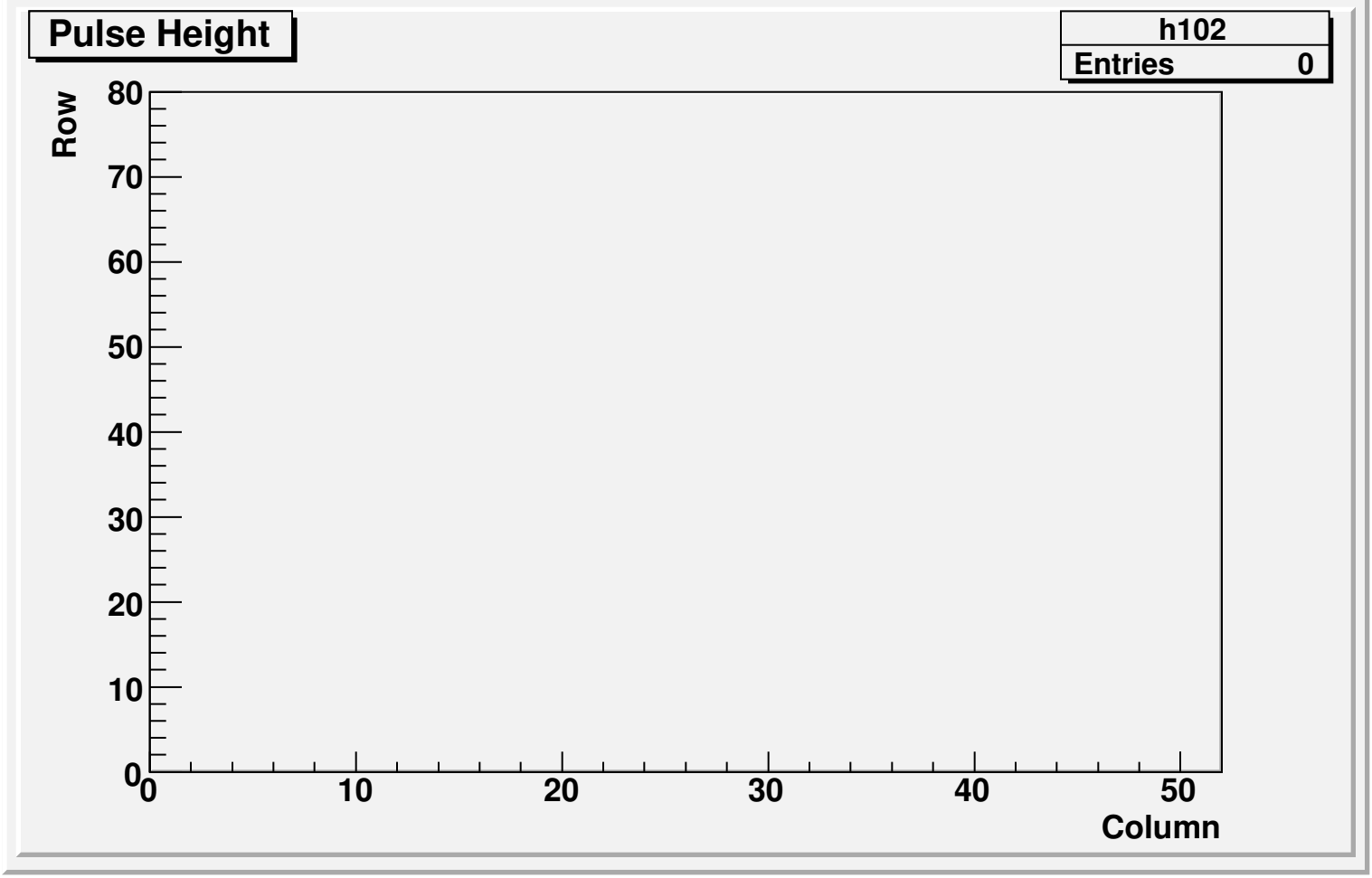
50
Column


Pulse Height
h102
Entries 0
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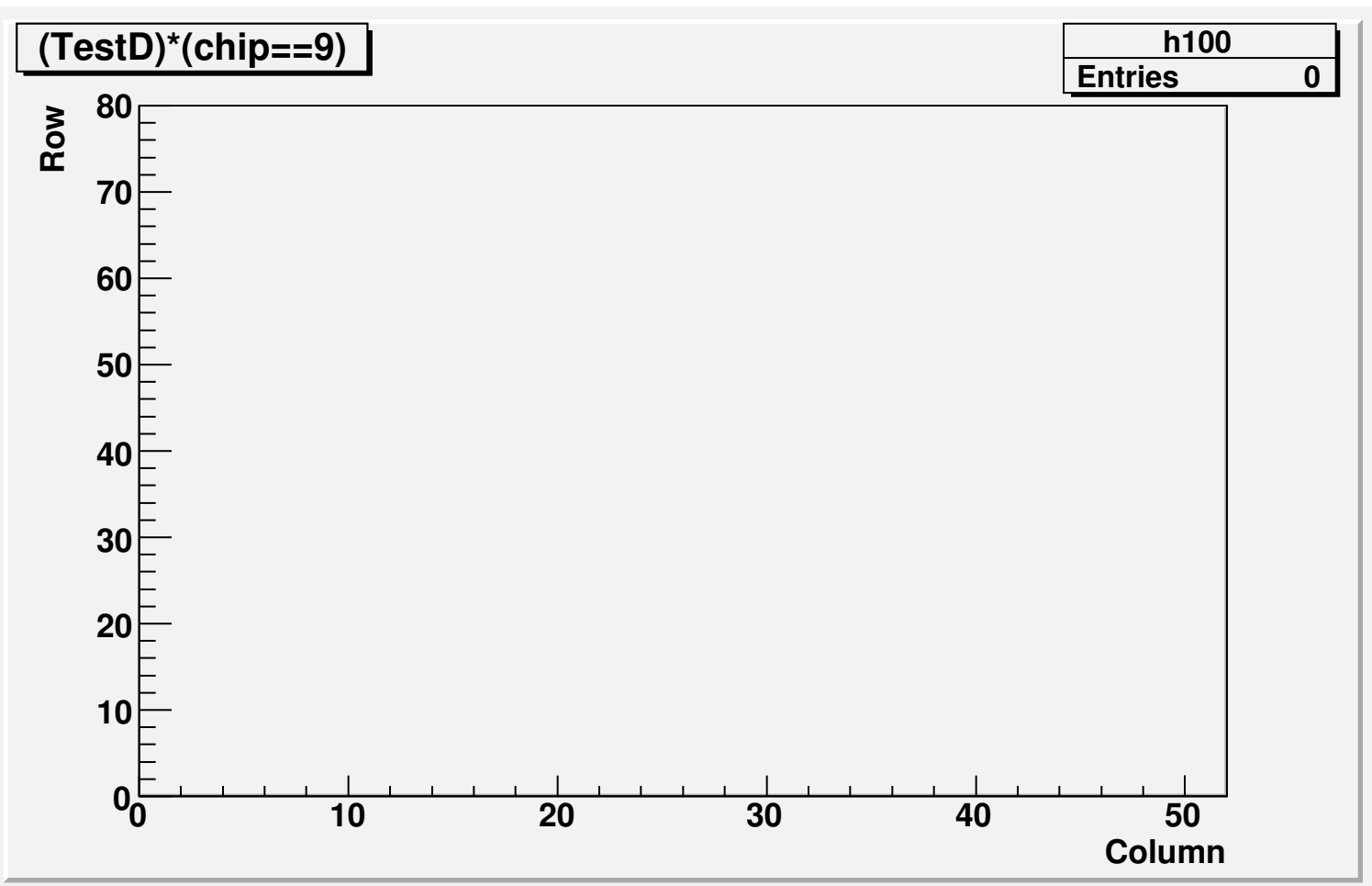
Column


(TestD)*(chip==9)
h100
Entries 0
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Column
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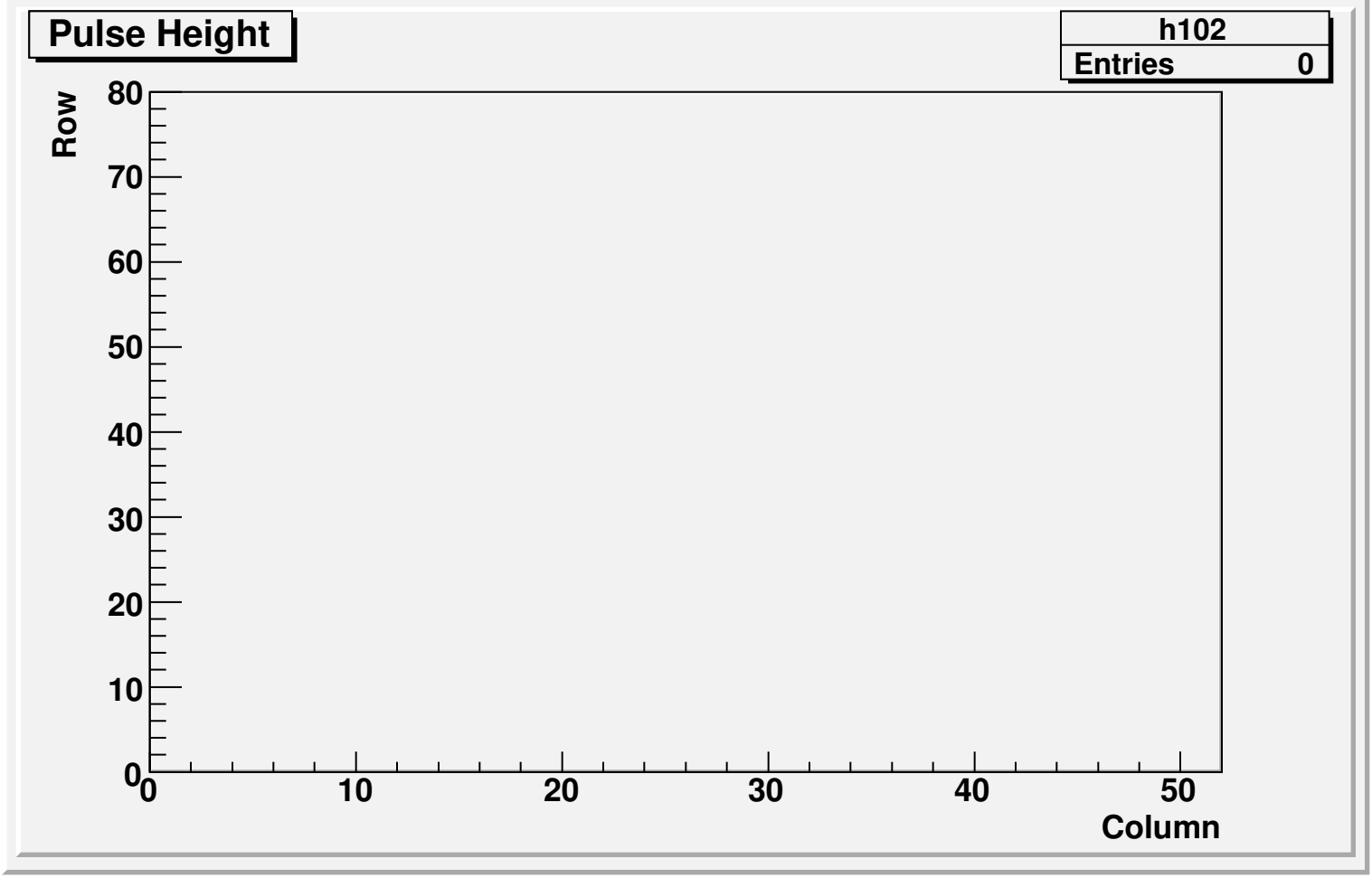
Pulse Height
h102
Entries 0
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Column
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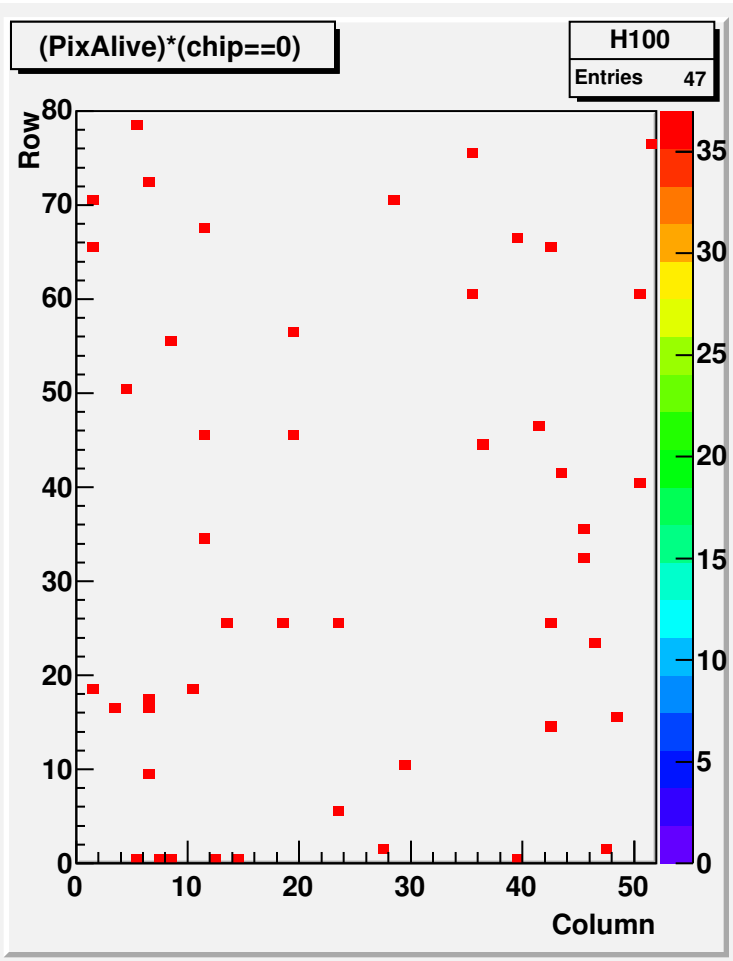
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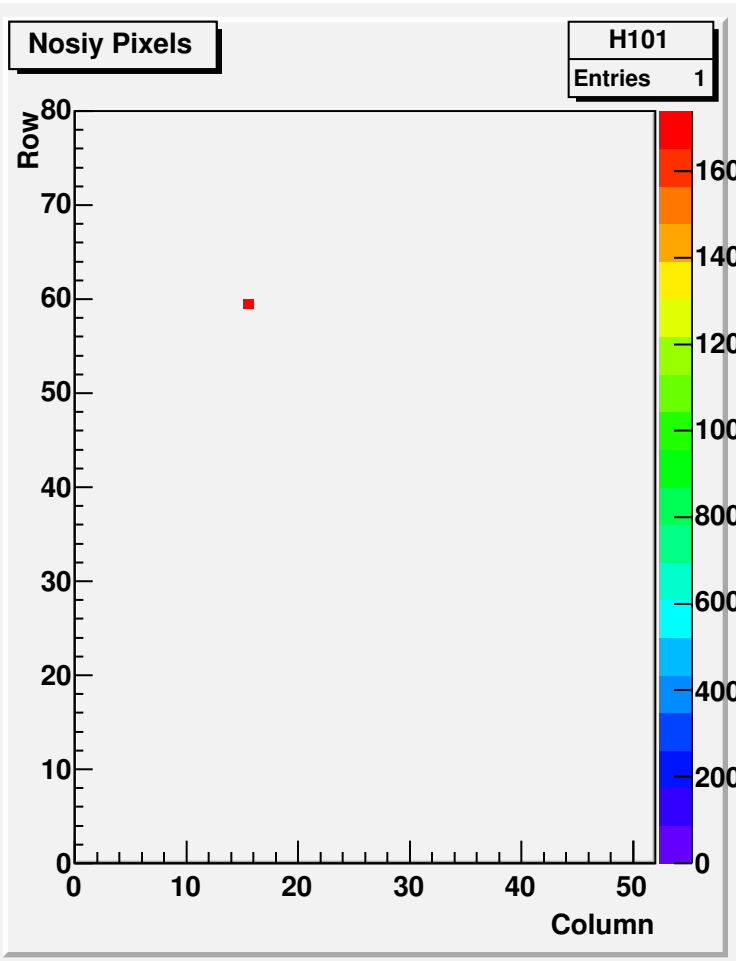
H100
Entries 47
Row
Column


Nosiy Pixels
H101
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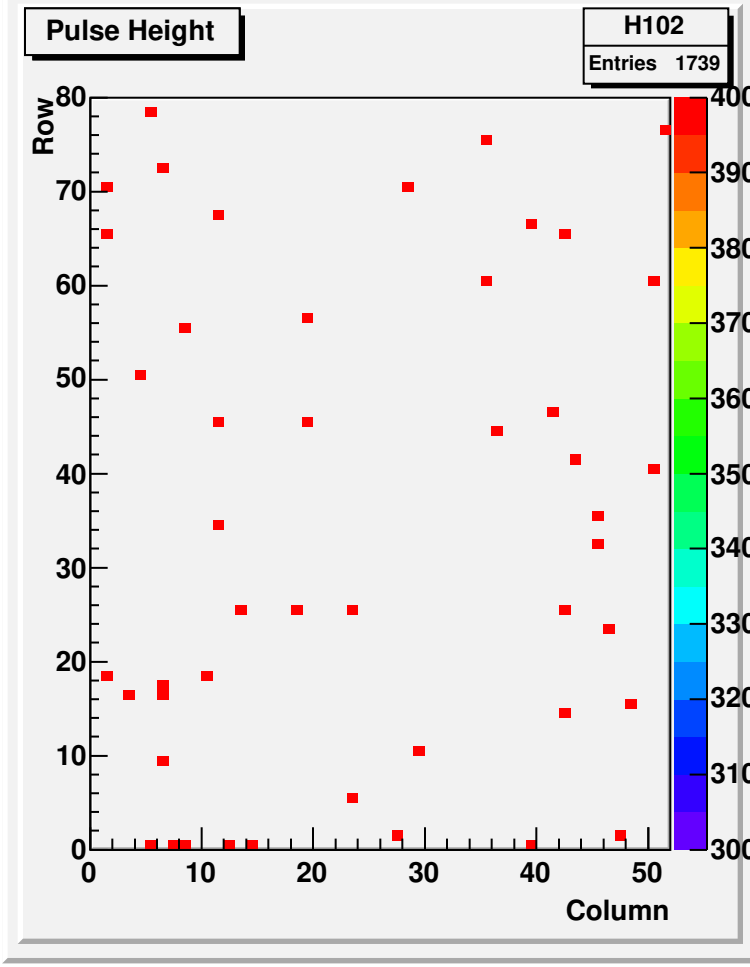
Row
Column


Pulse Height
H102
Entries 1739
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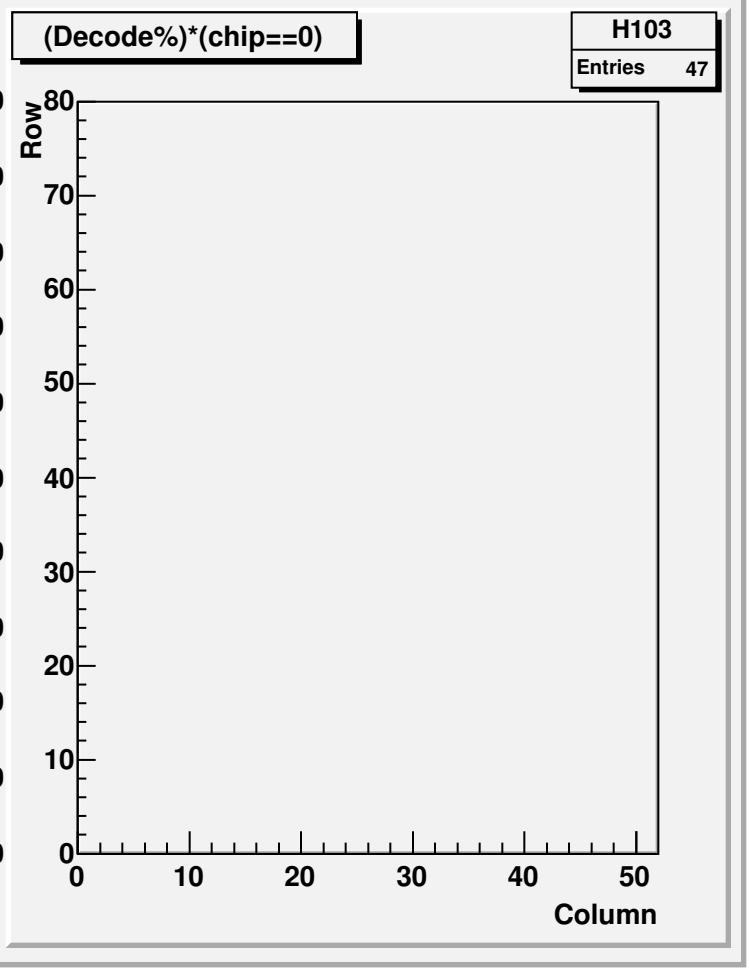
Column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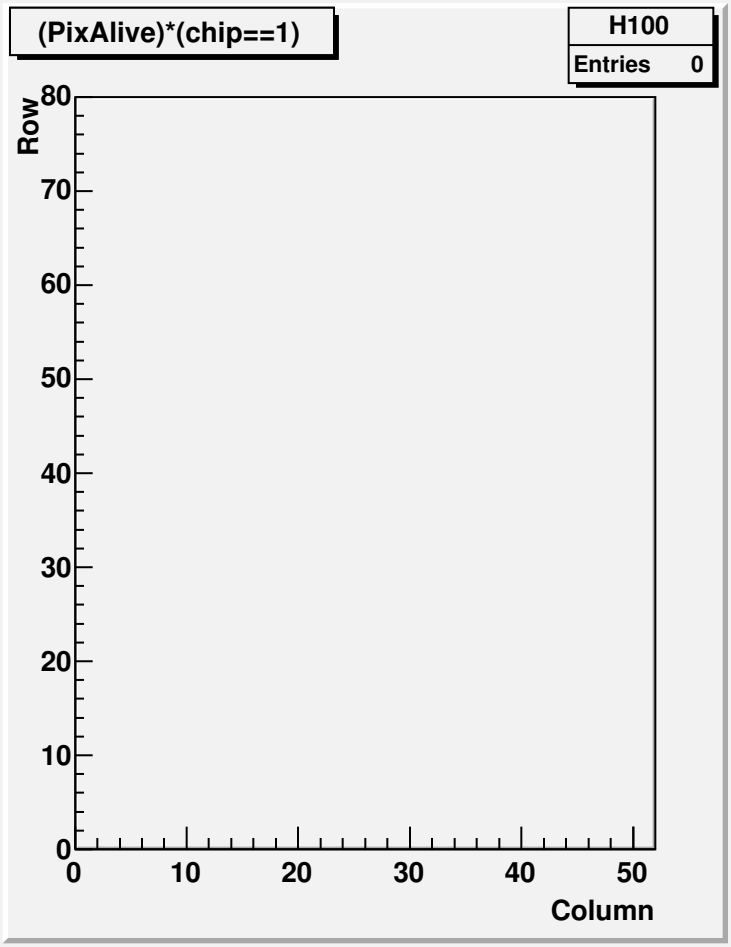
H103
Entries 47
Row
Column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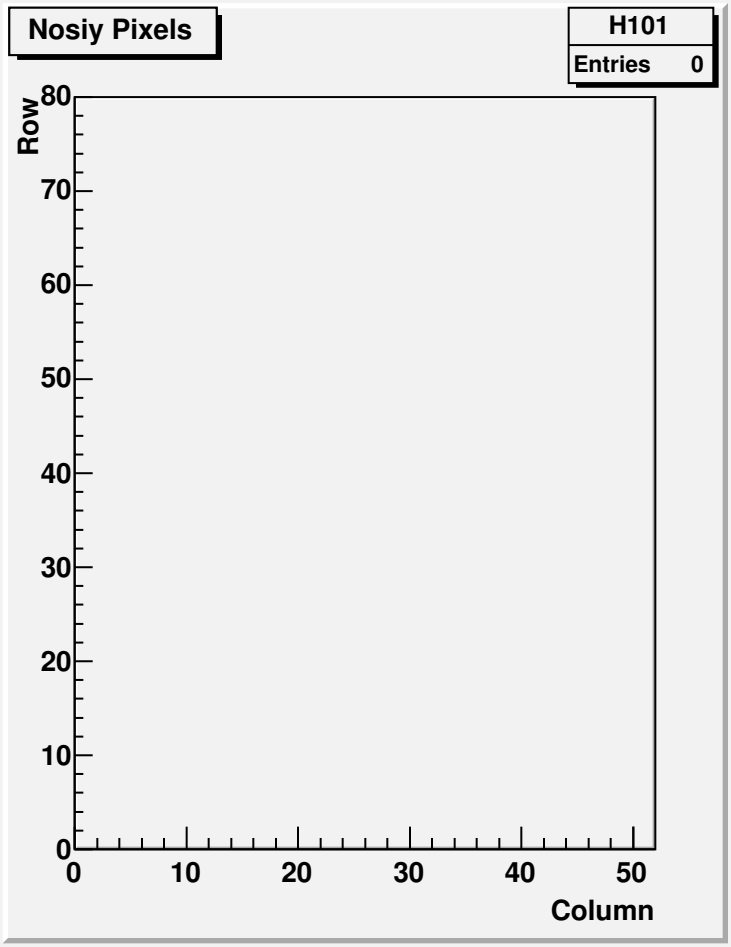
Row
Column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H101
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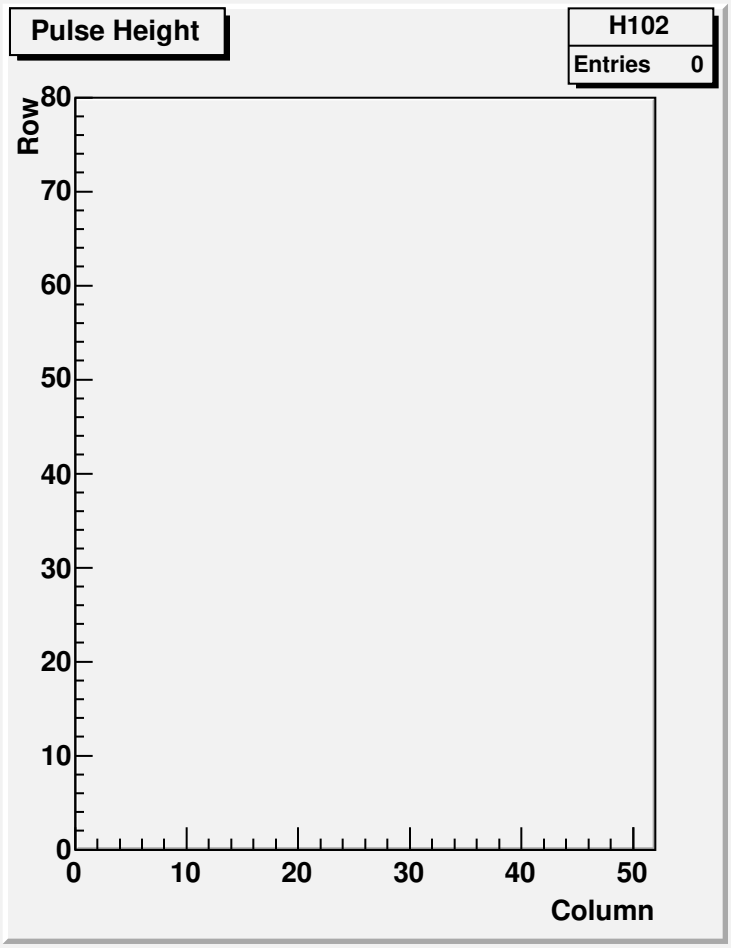
Column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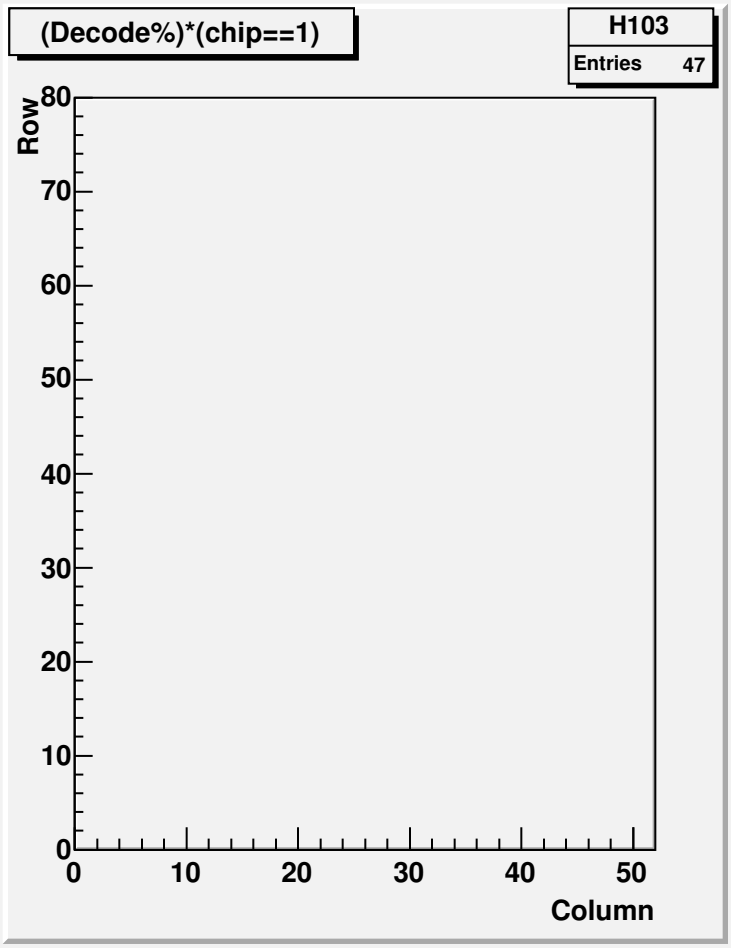
H102
Entries 0
Row
Column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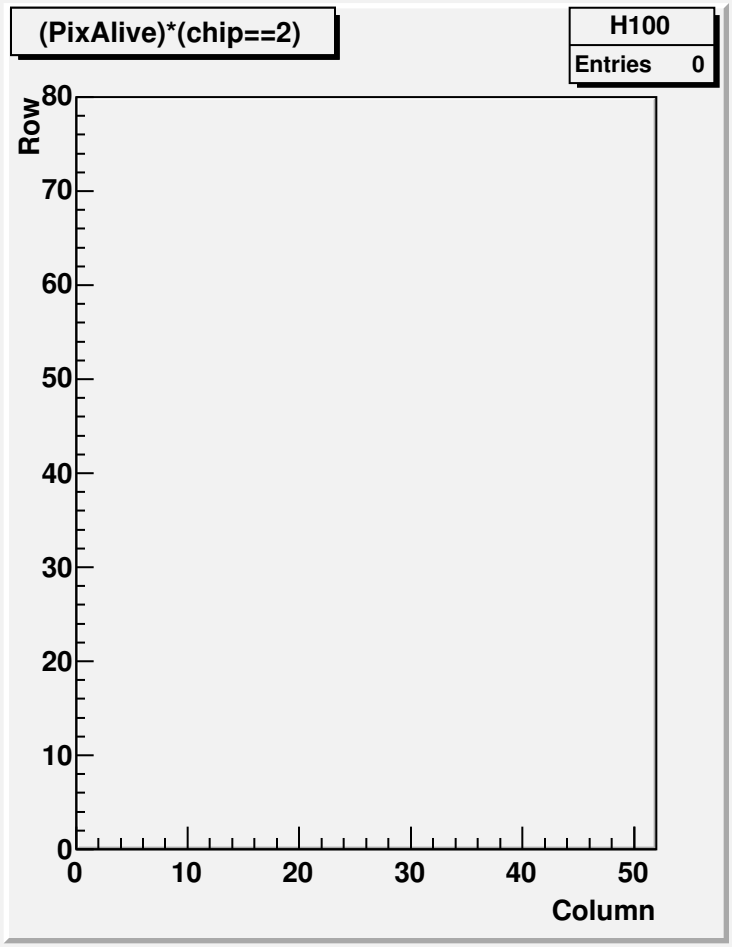
H103
Entries 47
Row
Column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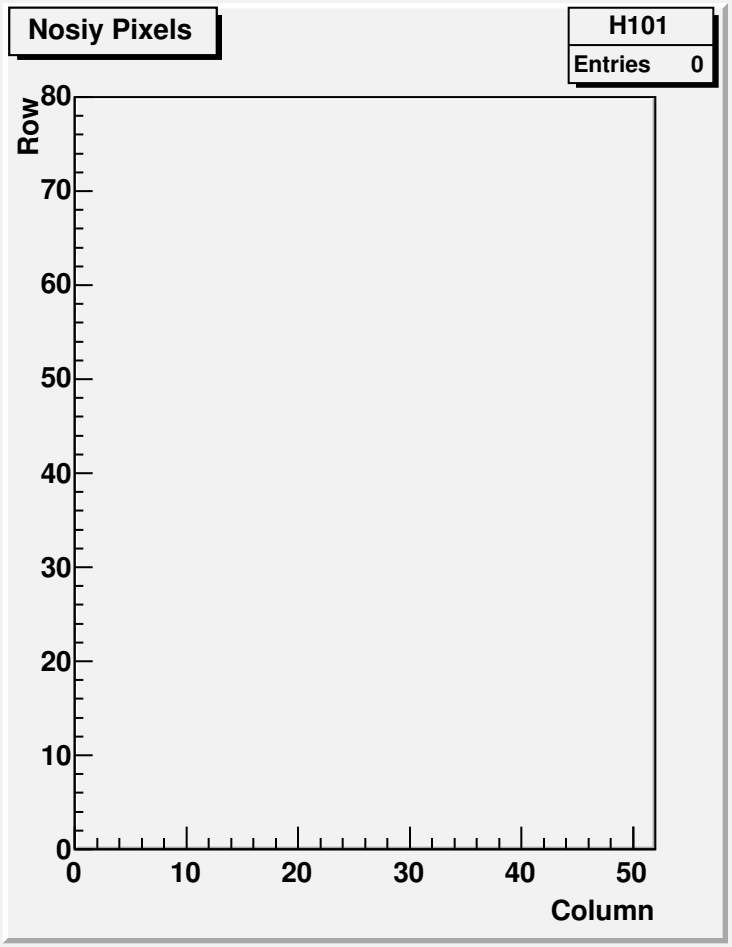
H100
Entries 0
Row
Column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H101
Entries 0
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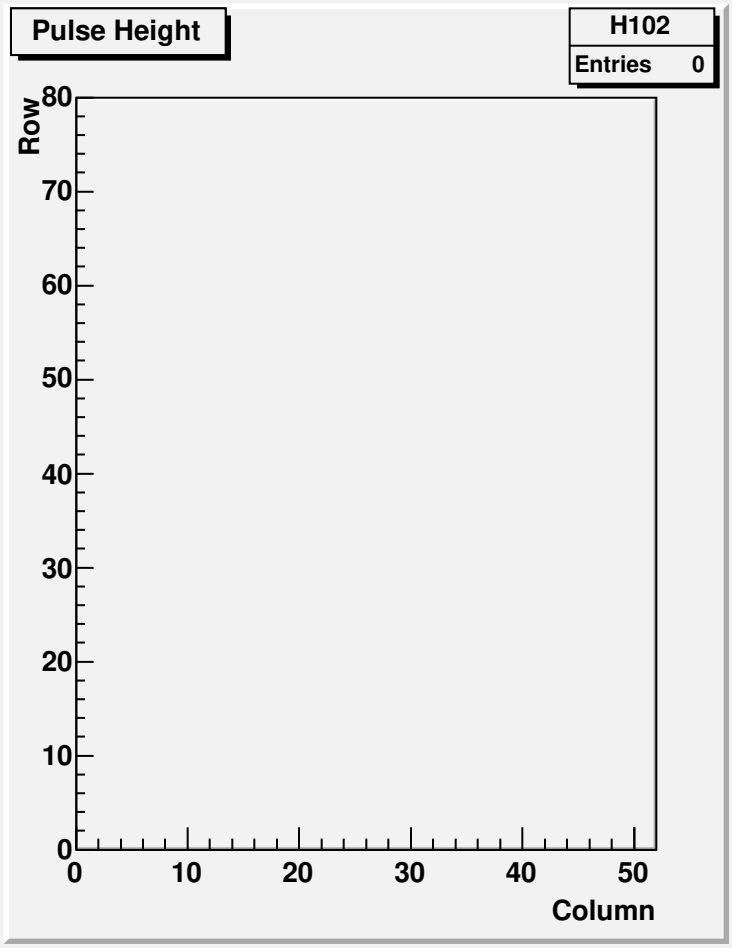
Column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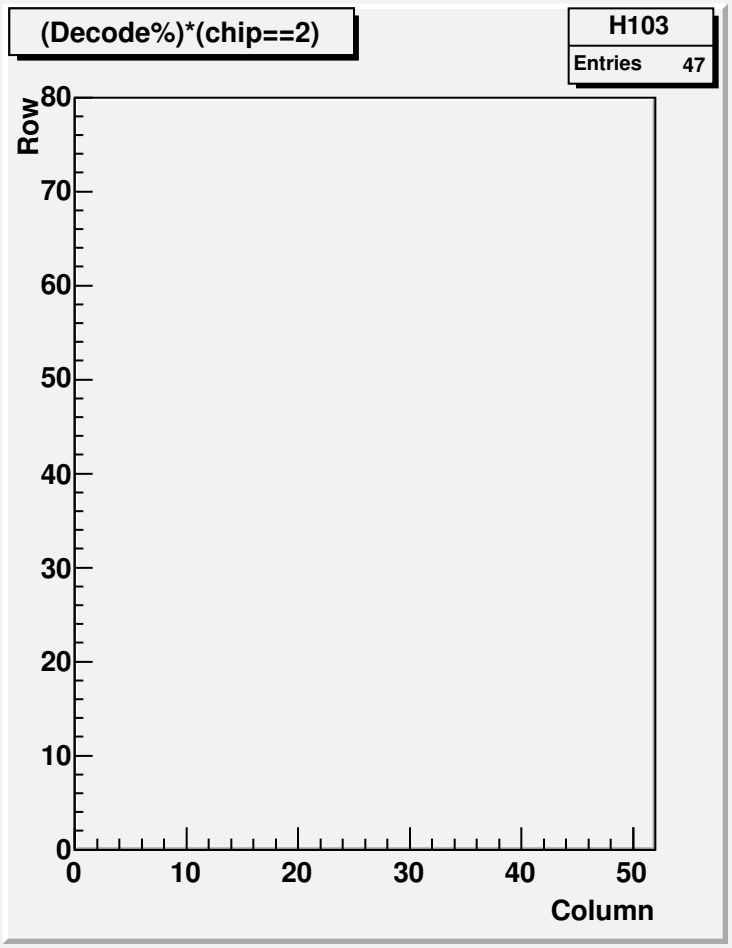
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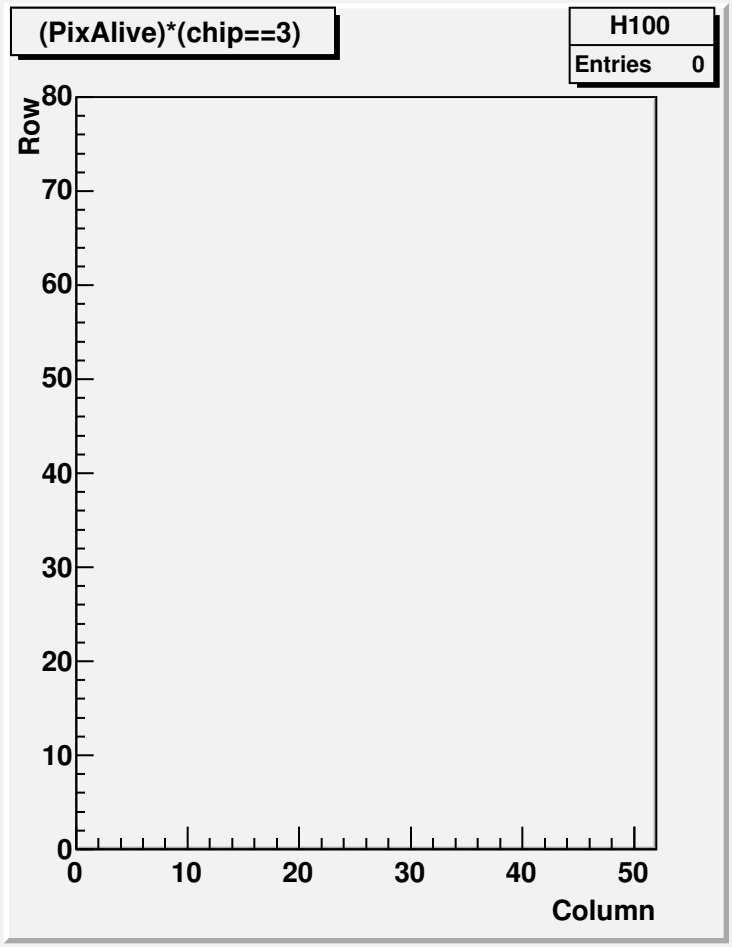
H103
Entries 47
Row
Column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H100
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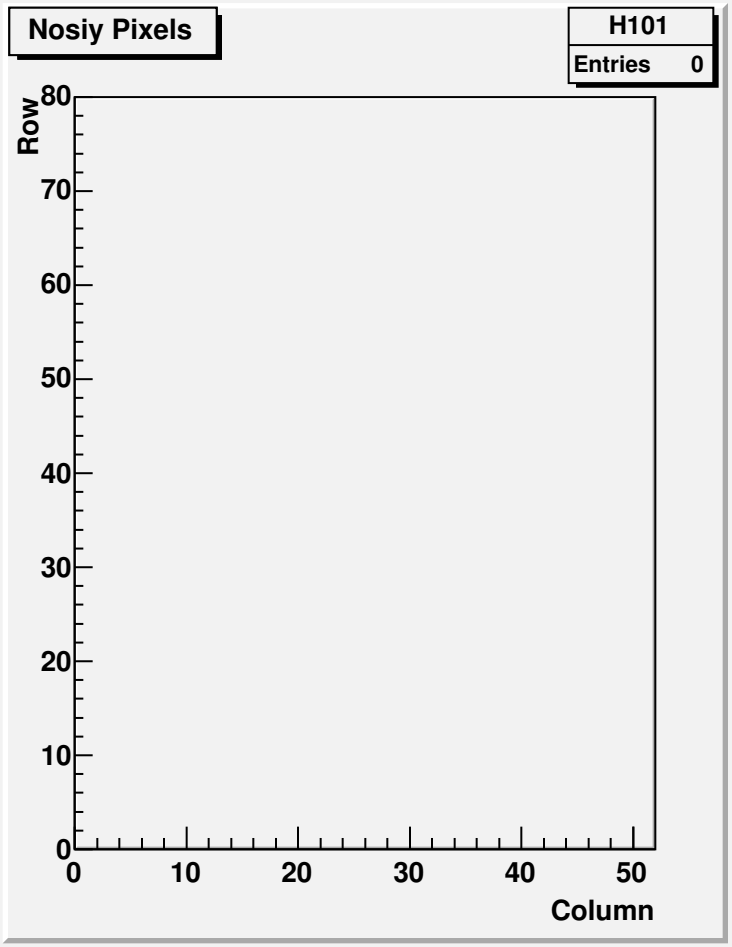
Column


Nosiy Pixels
H101
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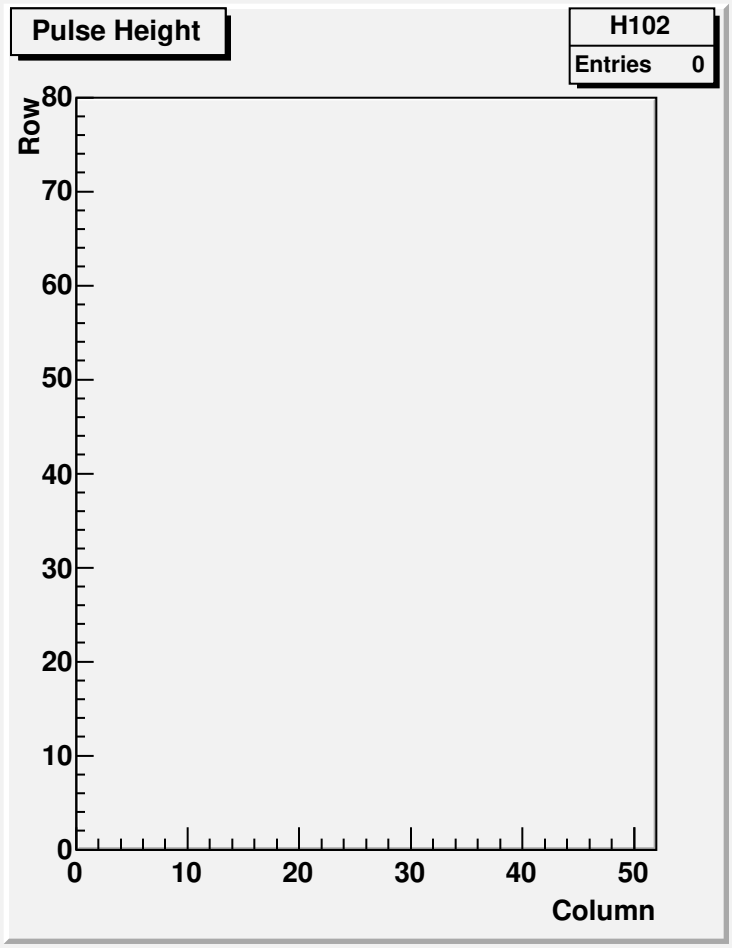
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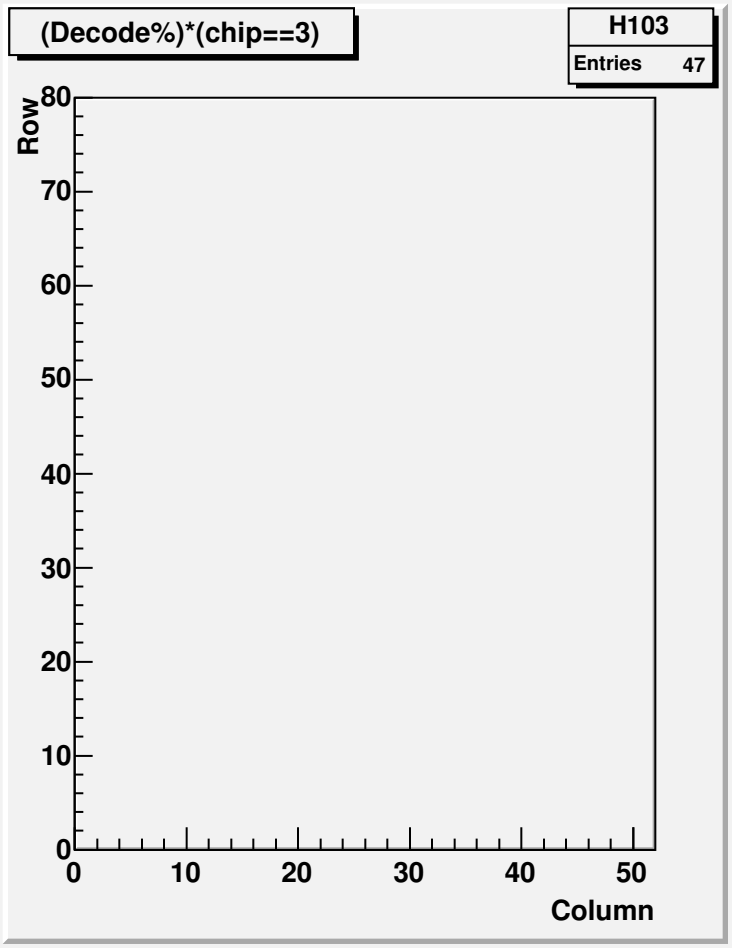
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H103
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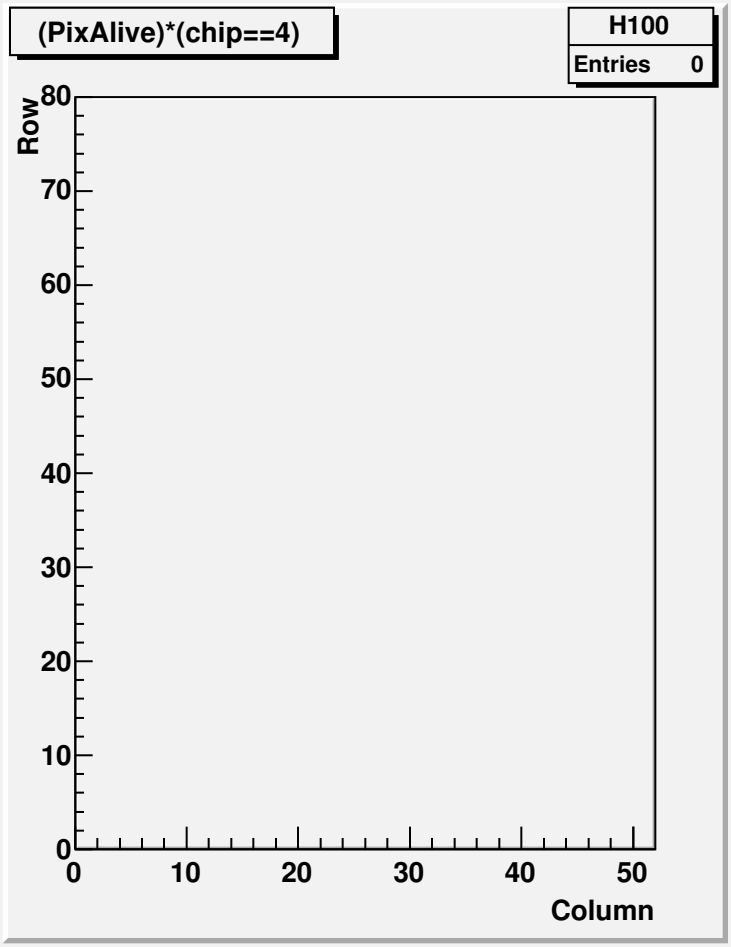
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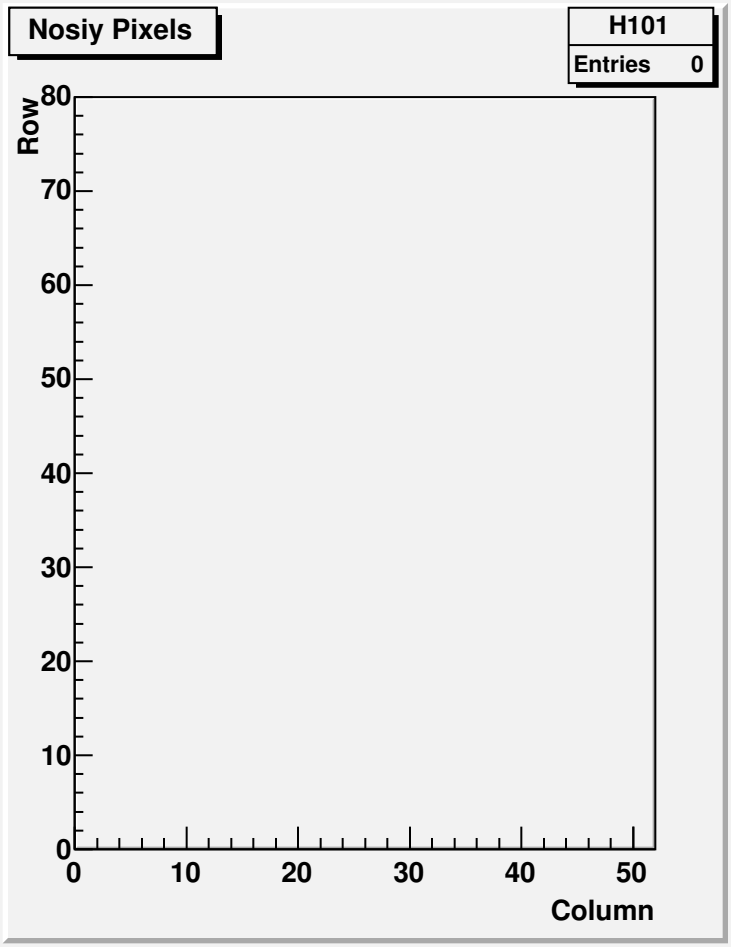
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H101
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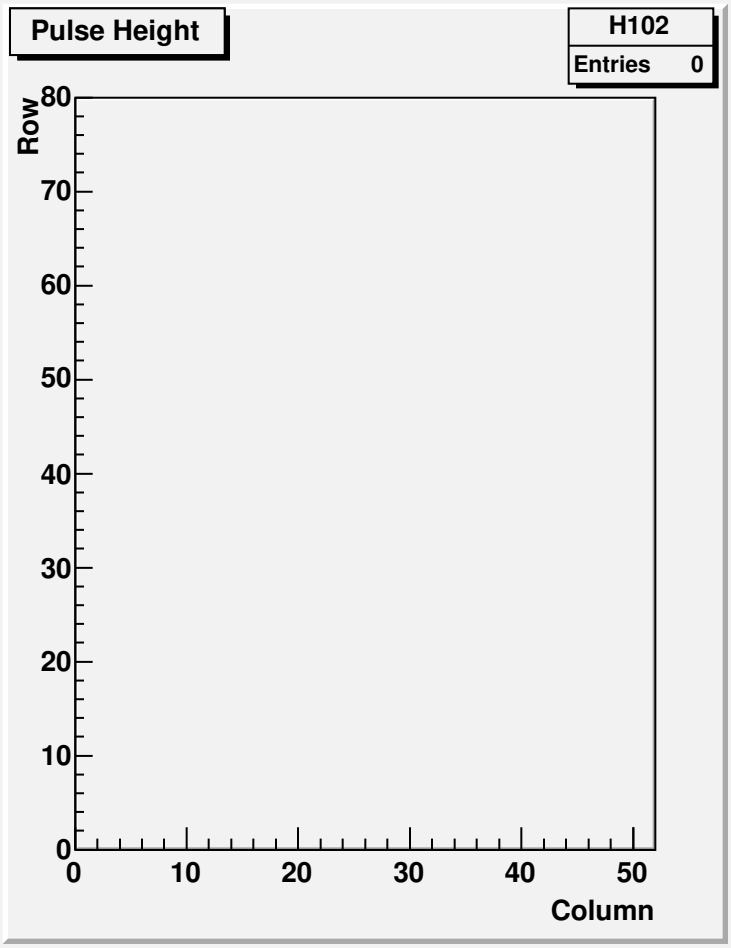
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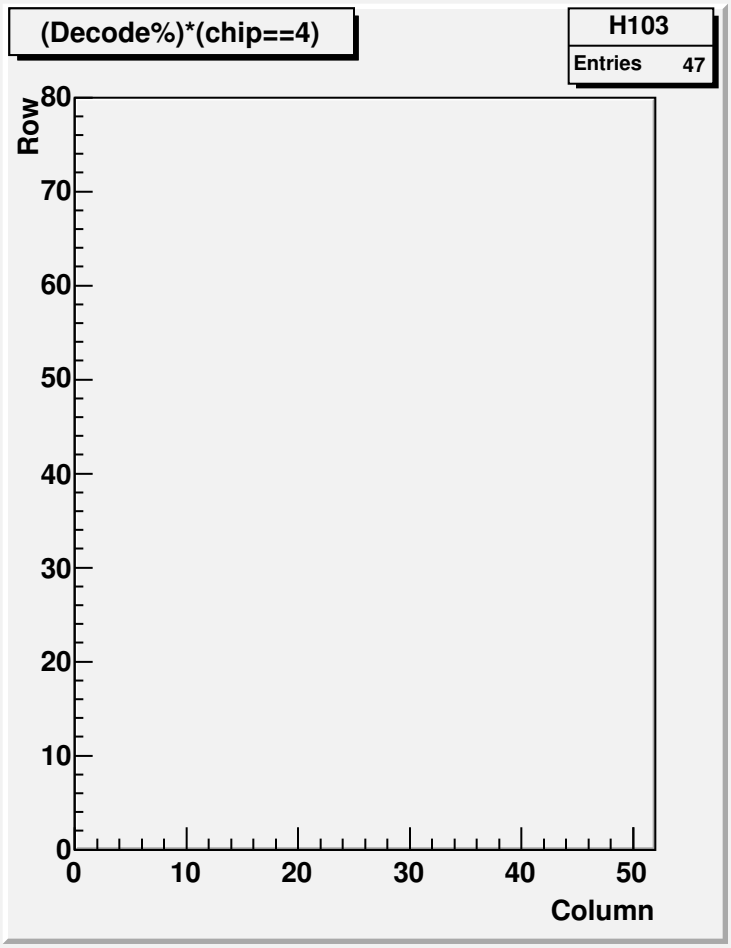
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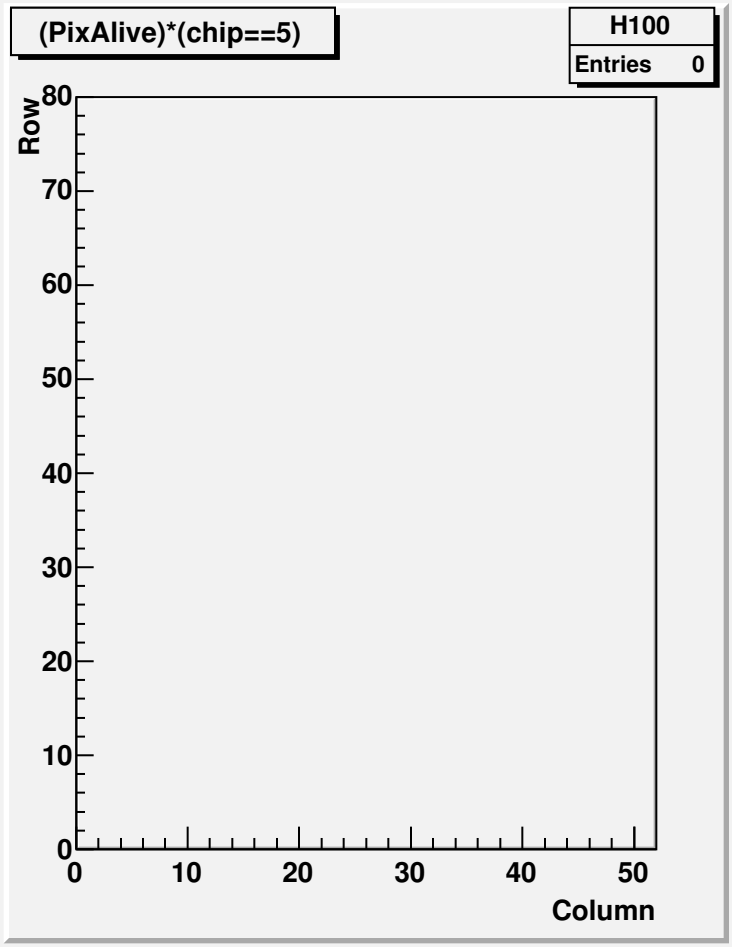
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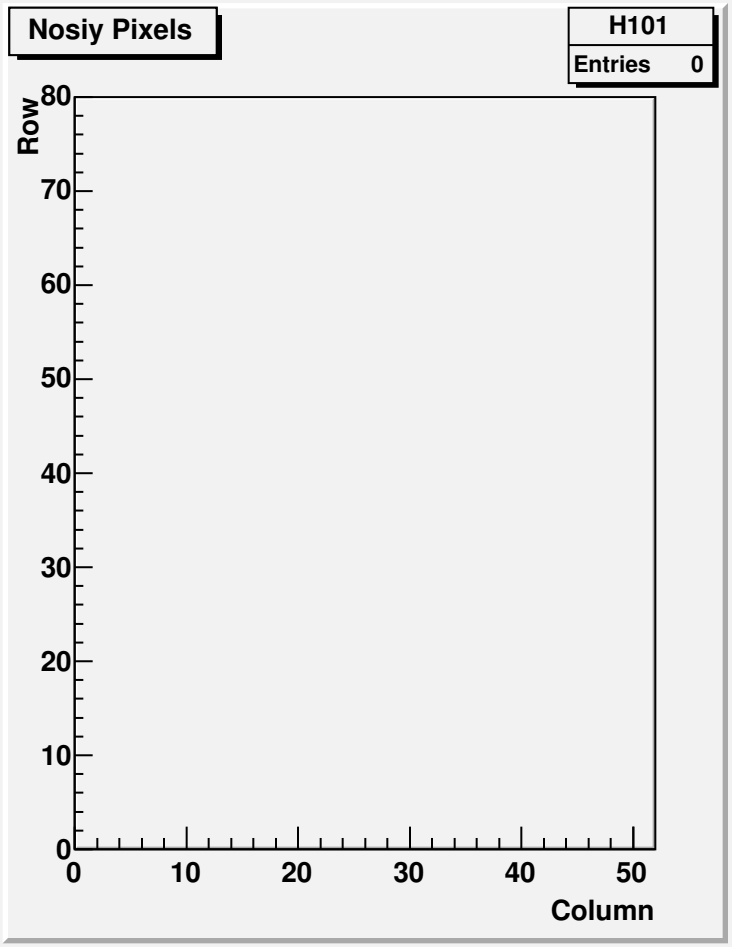
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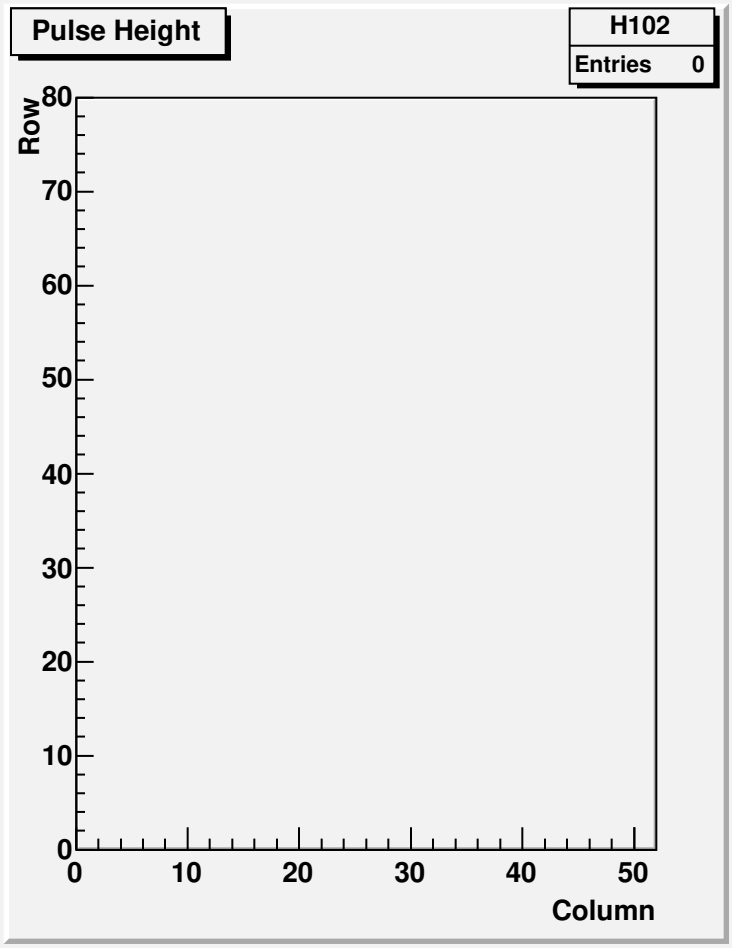
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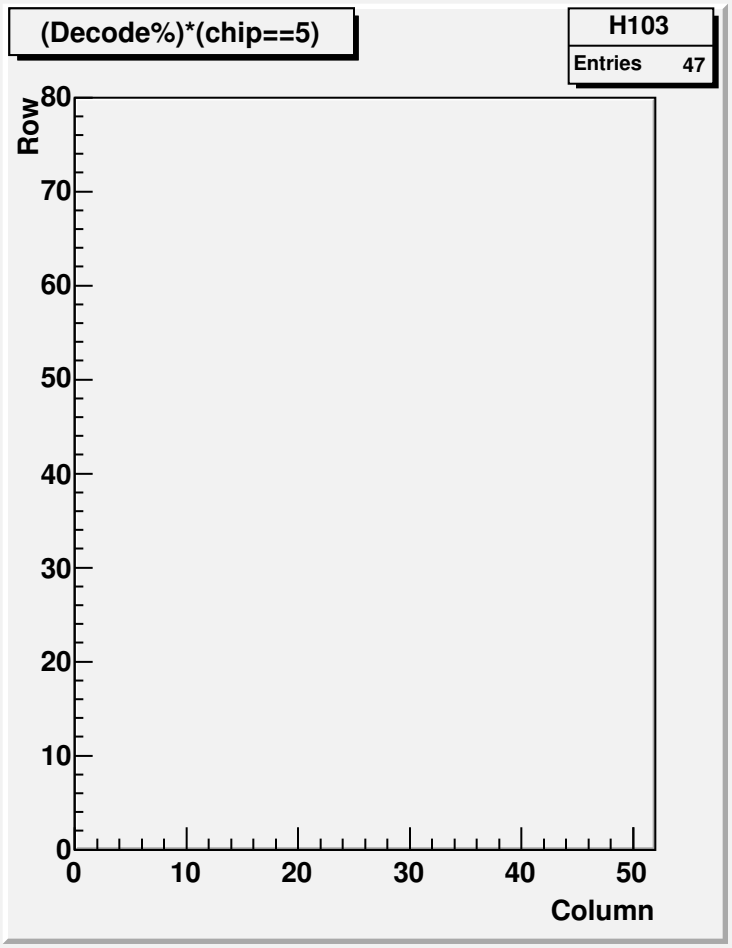
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H103
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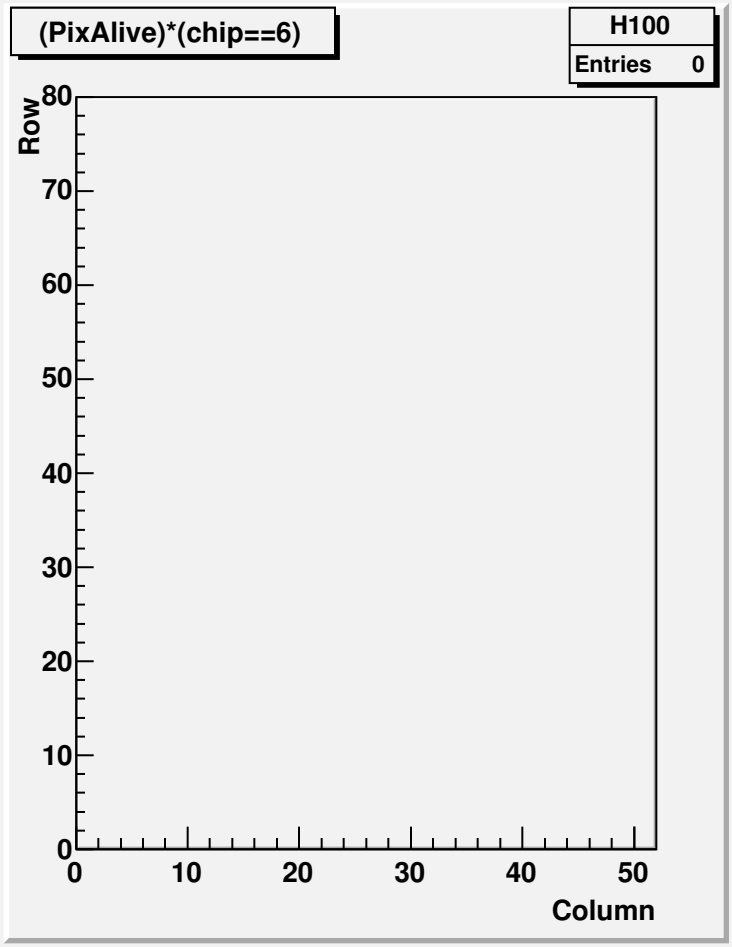
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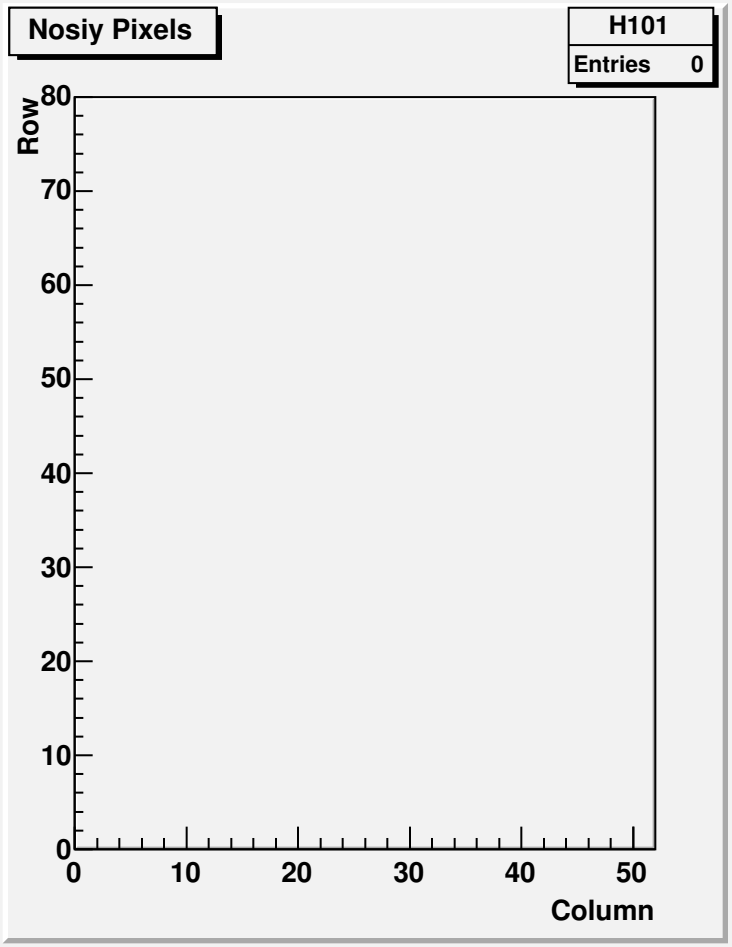
Nosiy Pixels
H101
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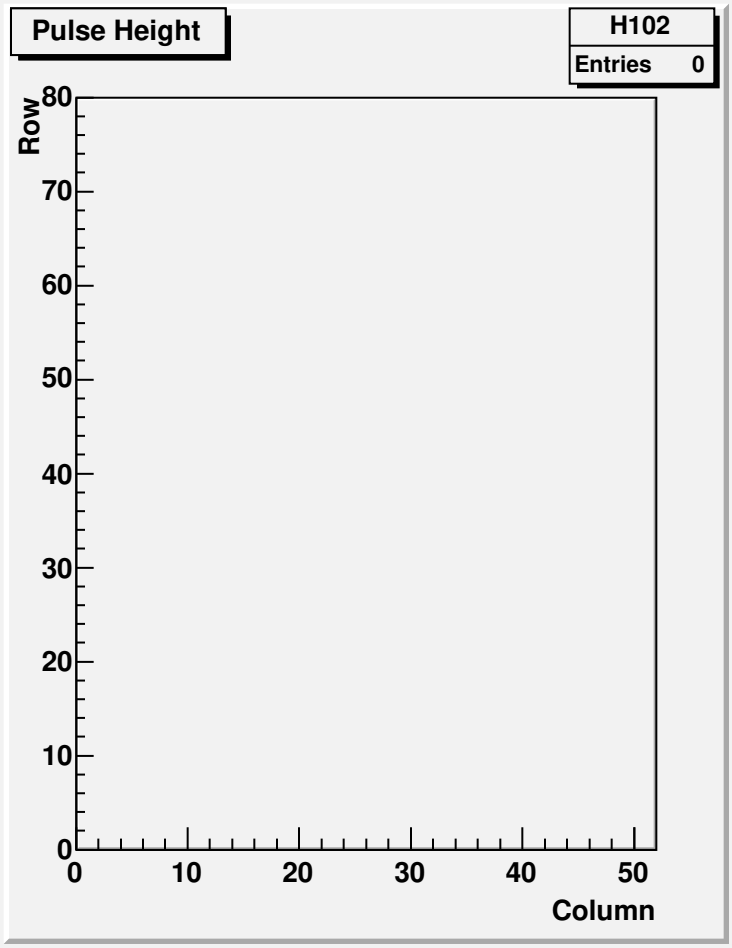
Column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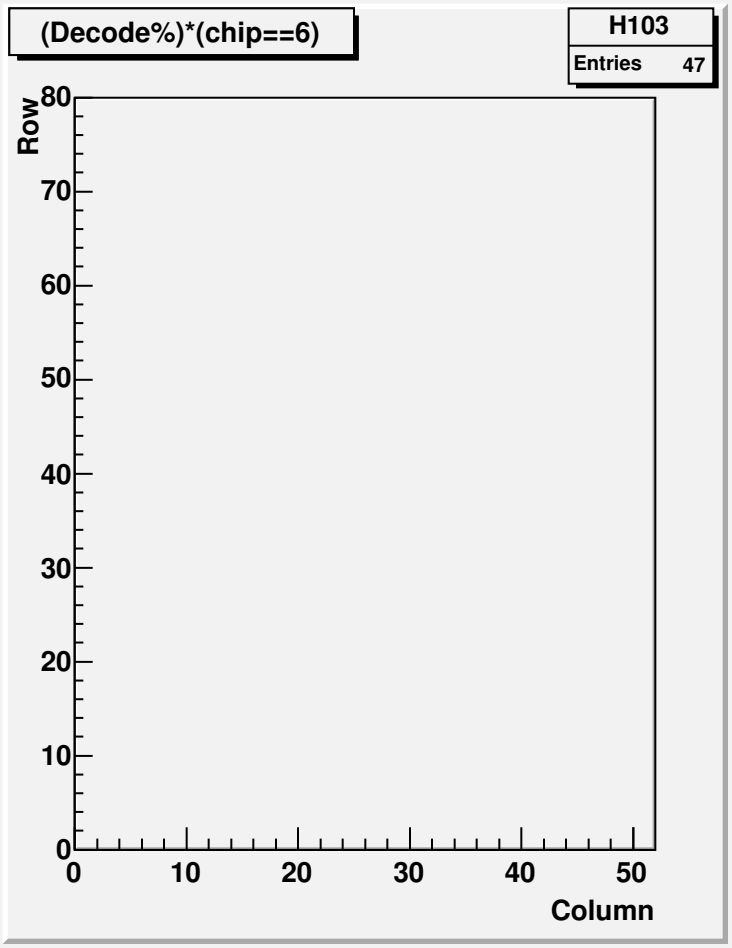
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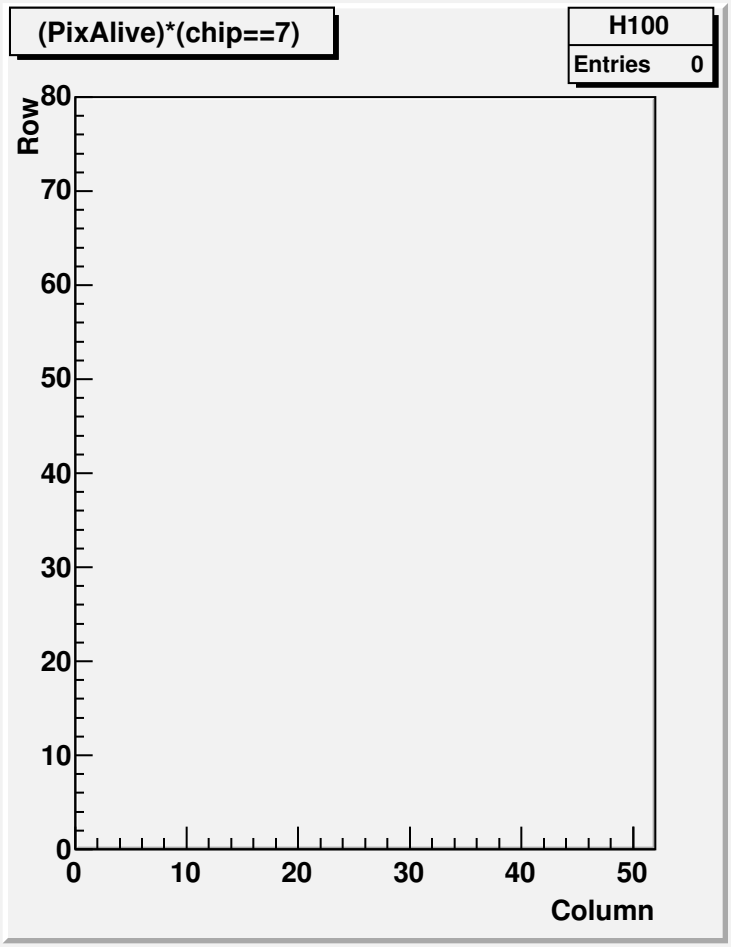
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H100
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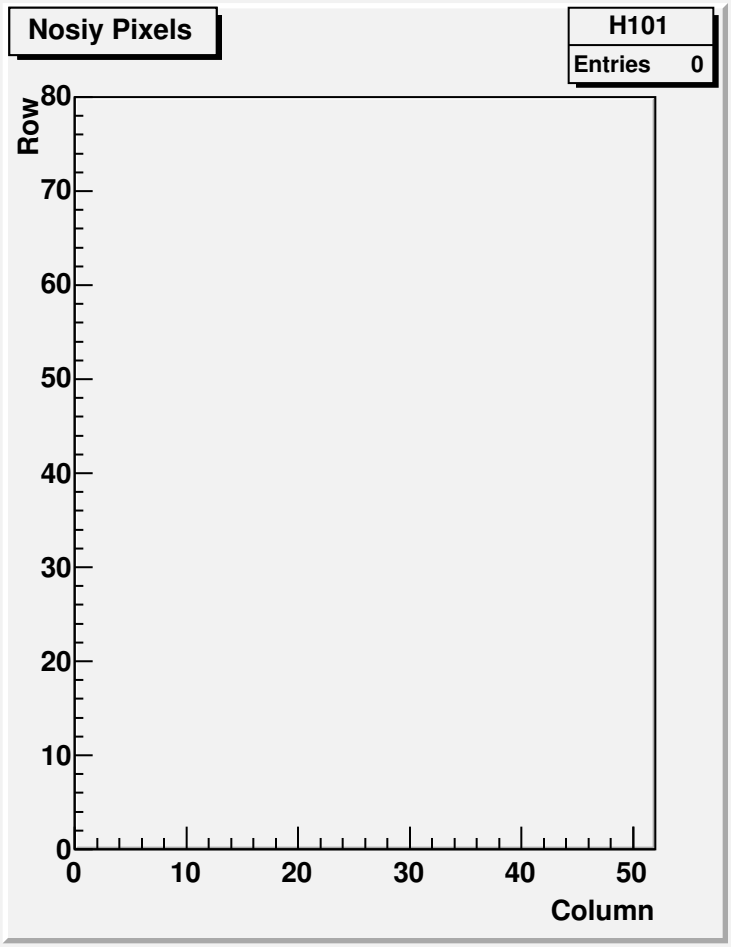
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H101
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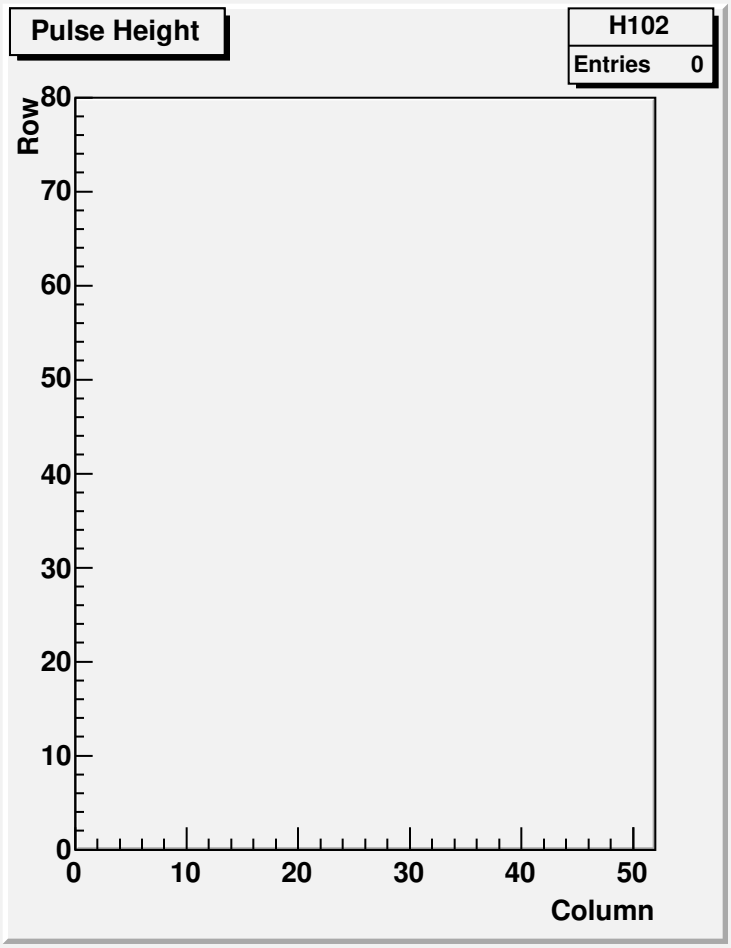
Pulse Height
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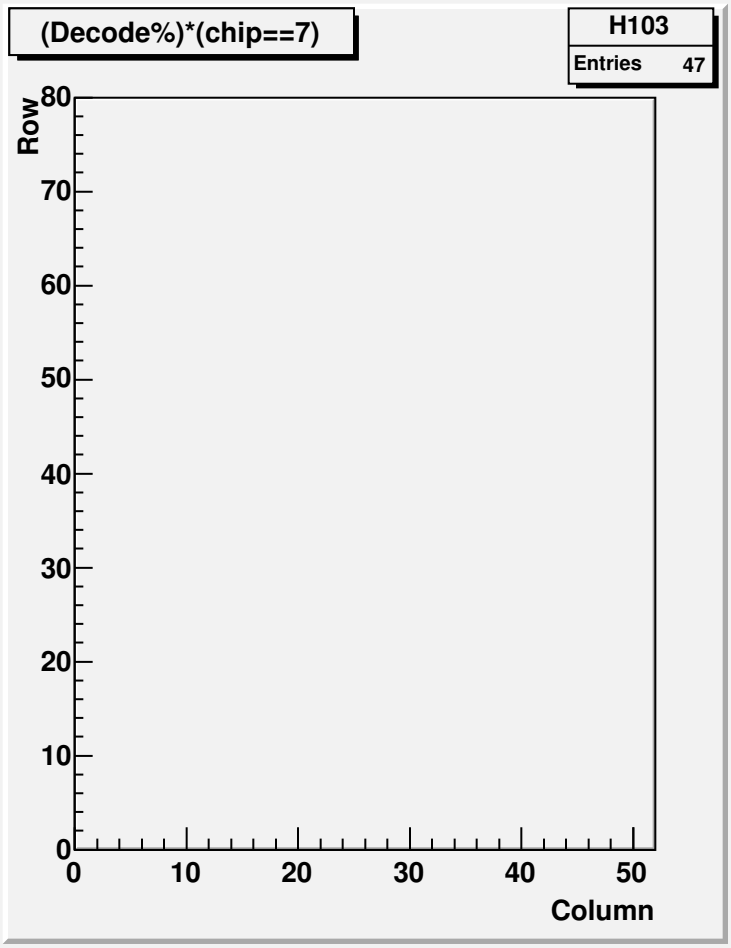
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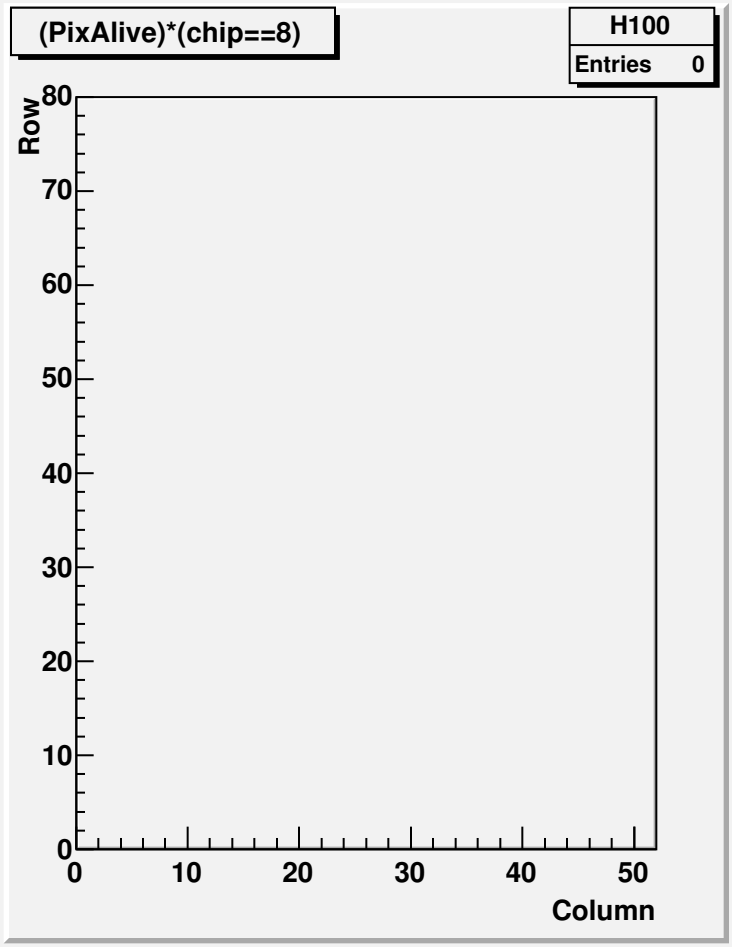
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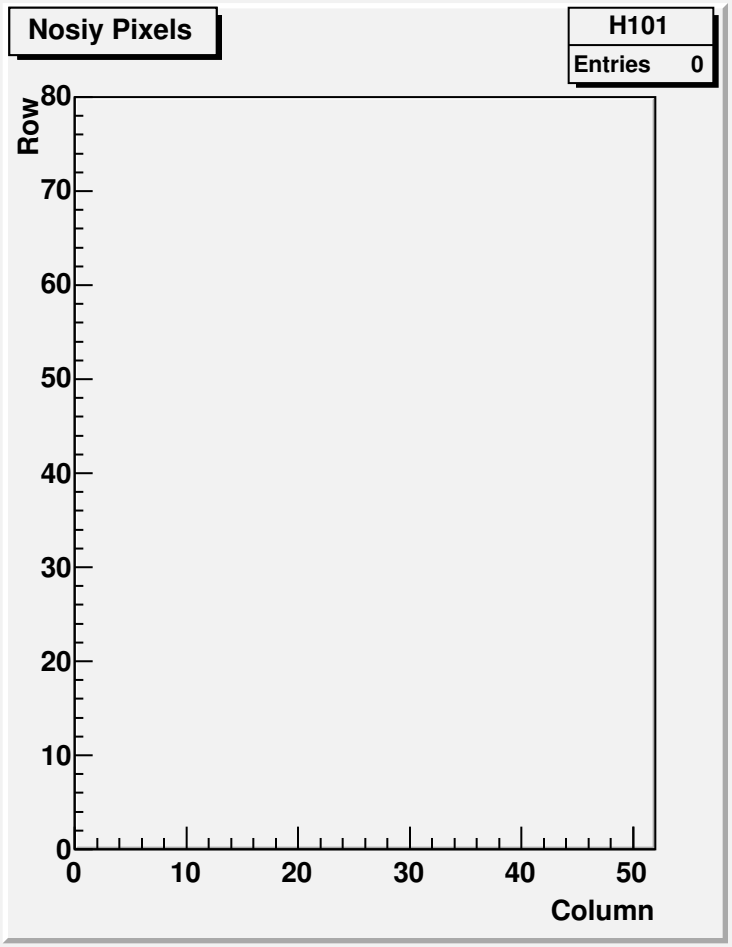
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H101
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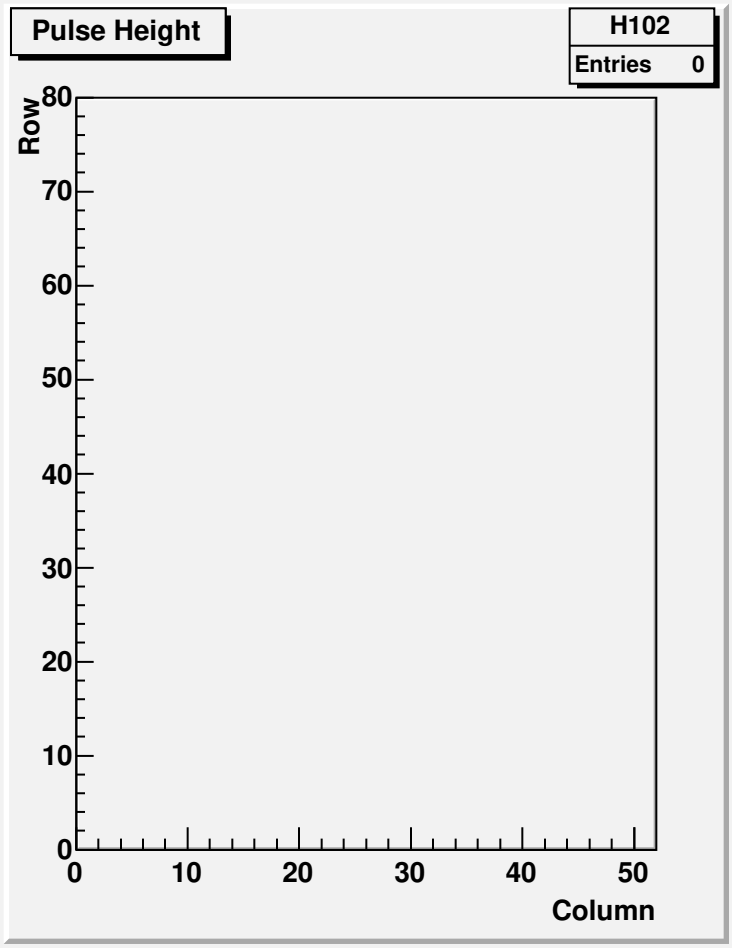
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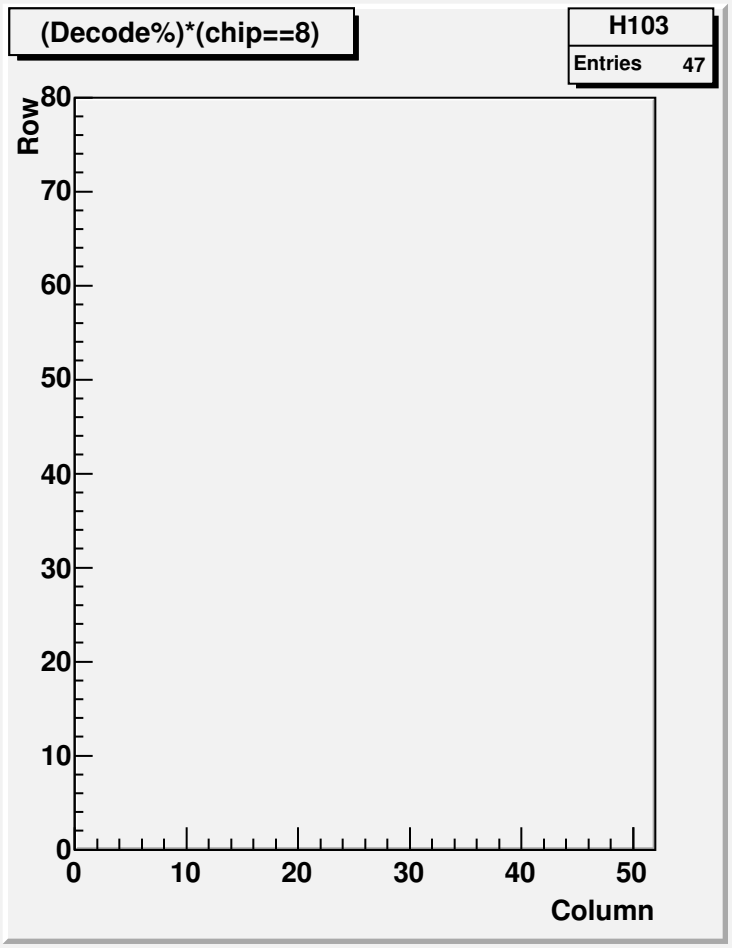
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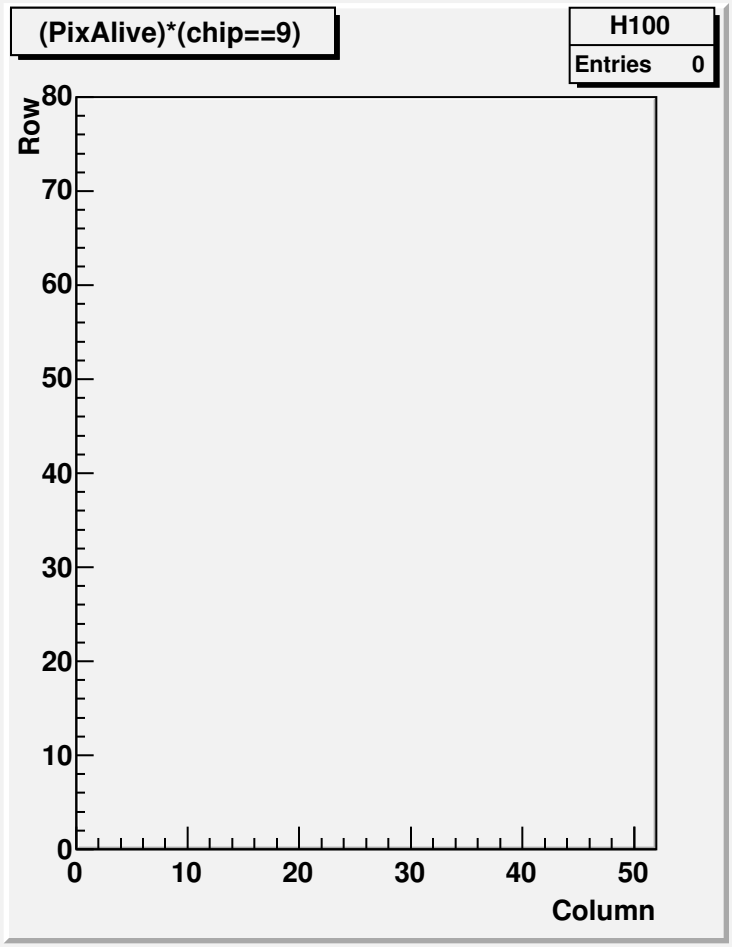
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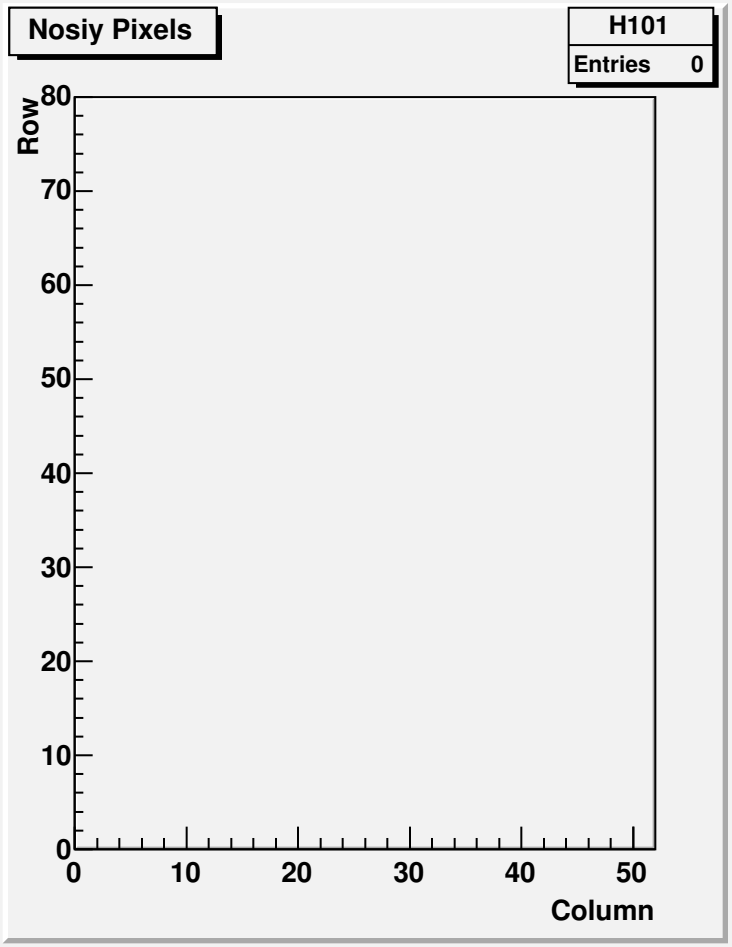
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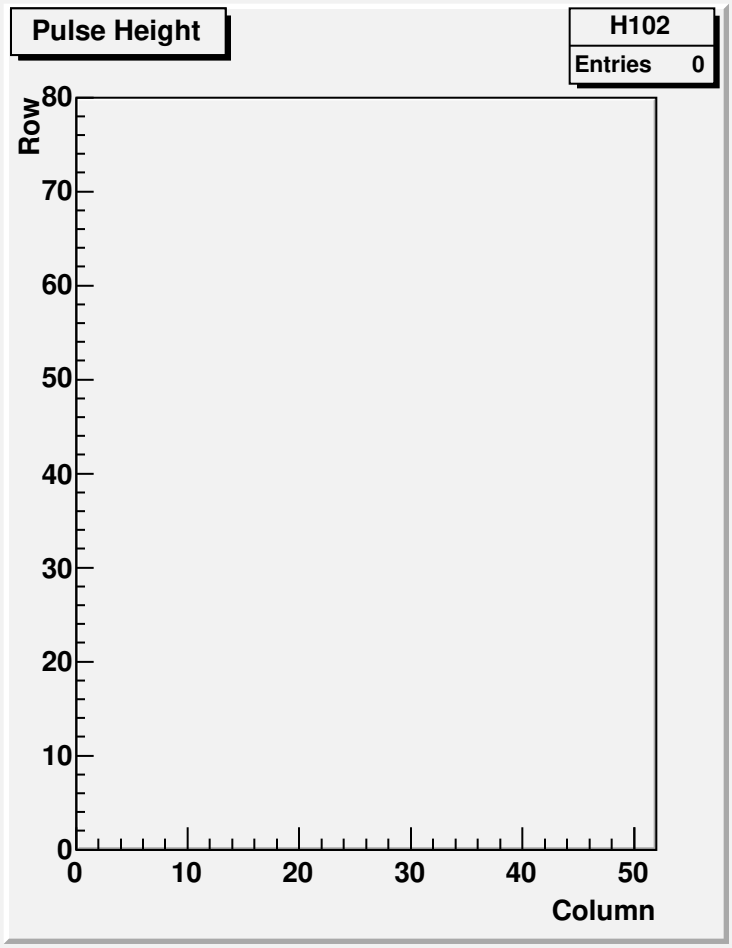
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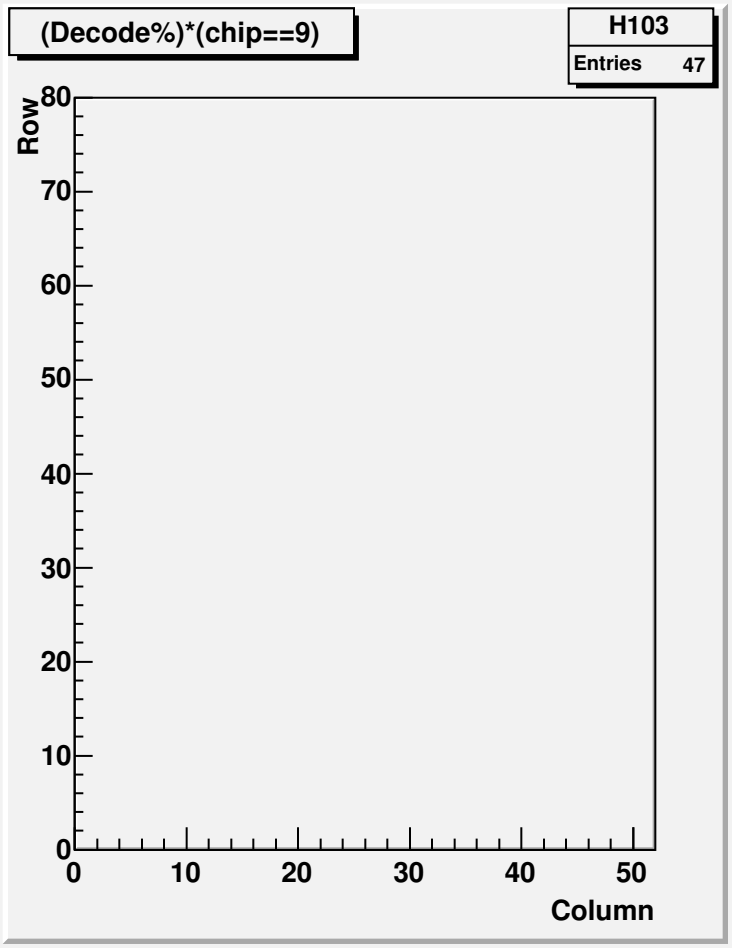
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H103
Entries 47
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