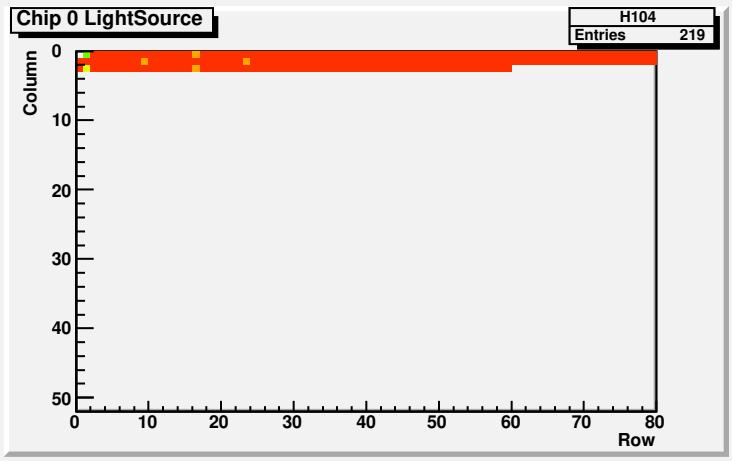
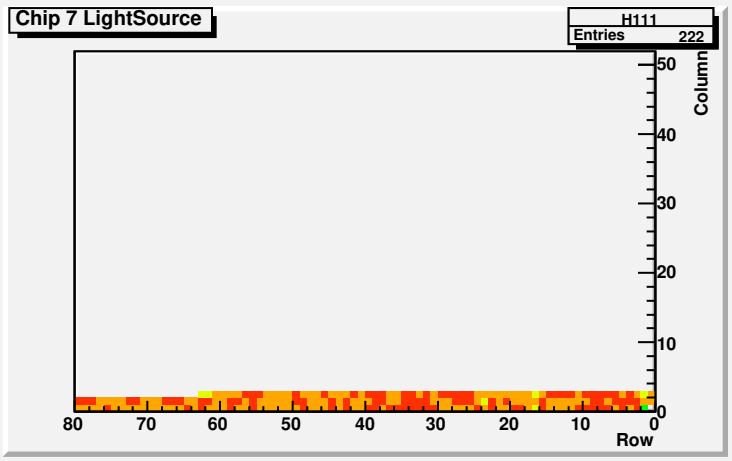
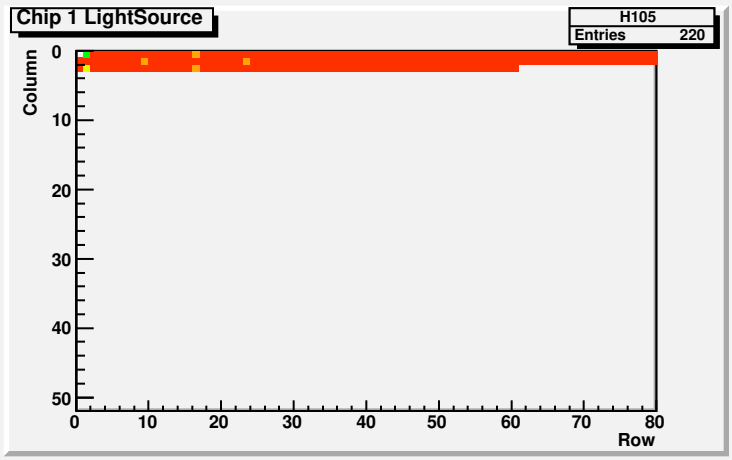
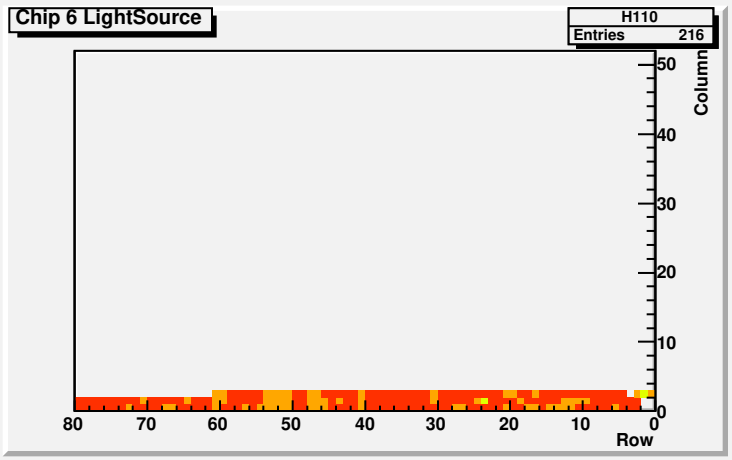
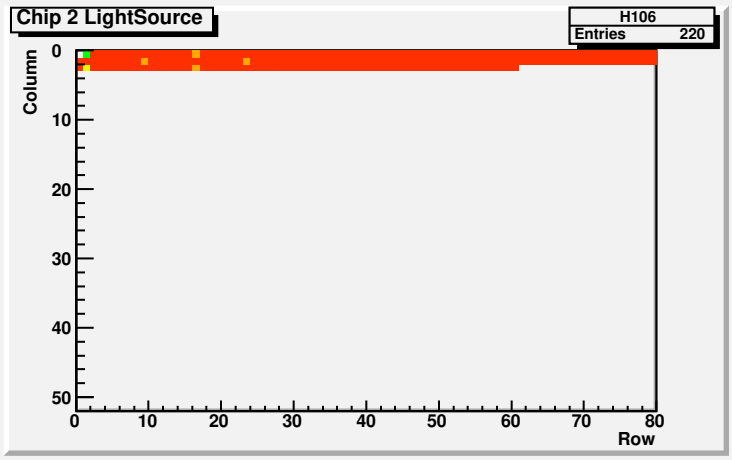
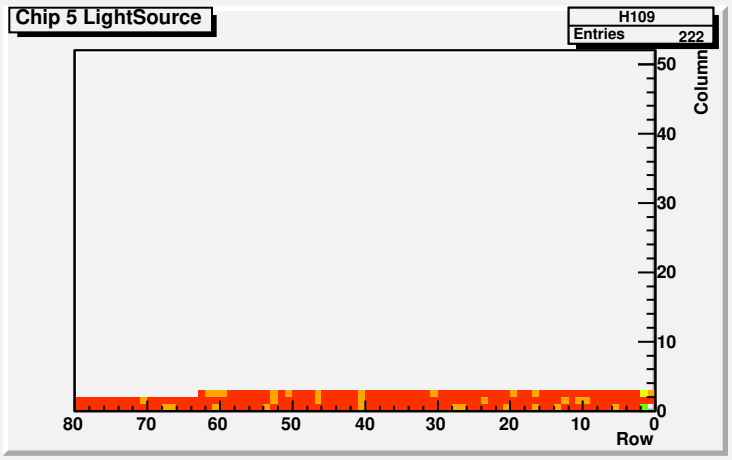

Chip 0 LightSource
H104
Entries 219
Column
Row

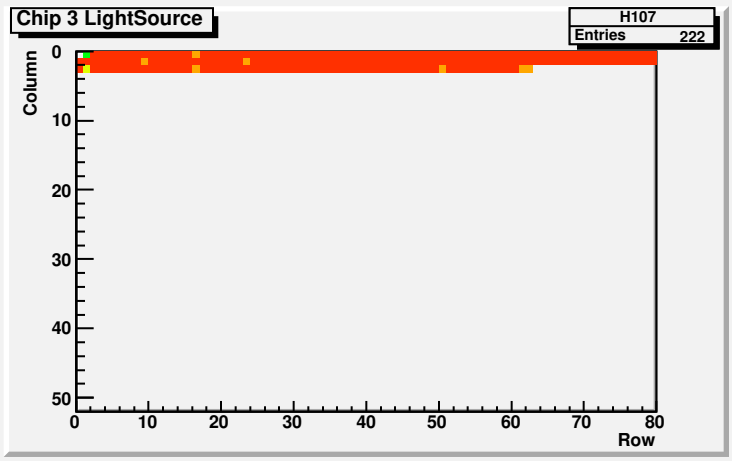

Chip 7 LightSource
H111
Entries 222
Column
Row

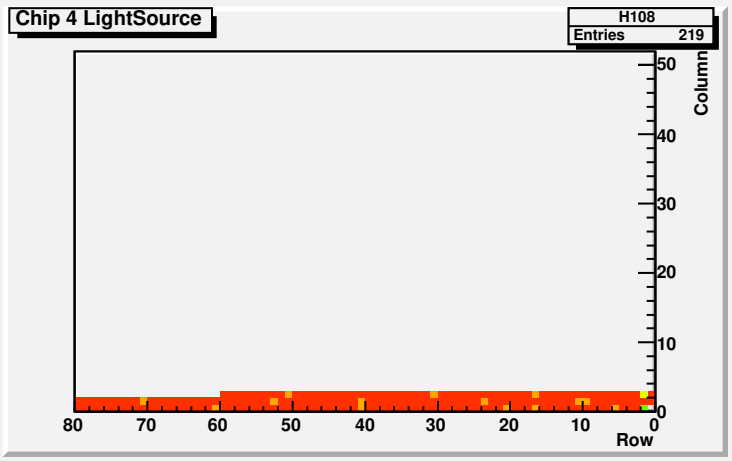

Chip 1 LightSource
H105
Entries 220
Column
Row

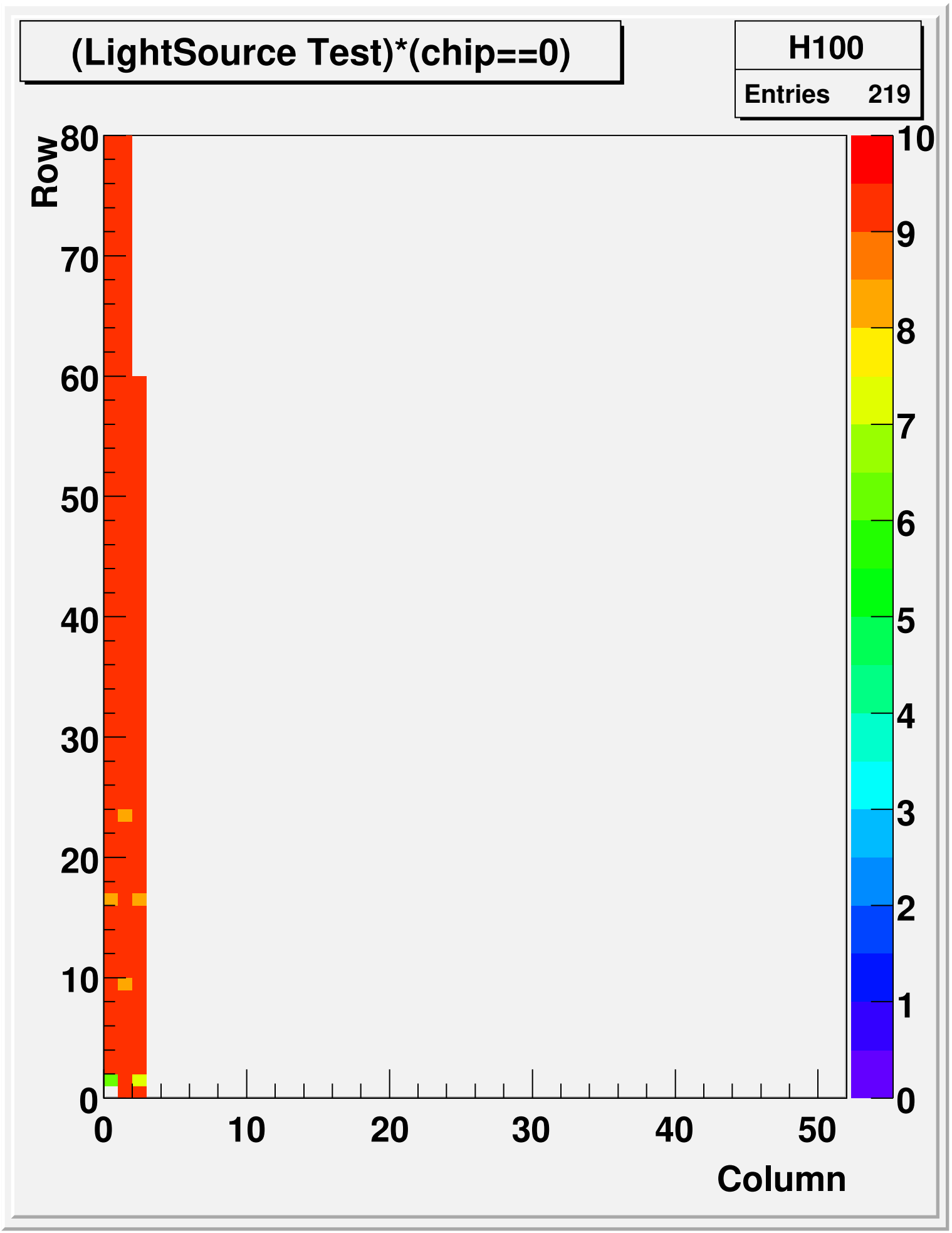

Chip 6 LightSource
H110
Entries 216
Column
Row

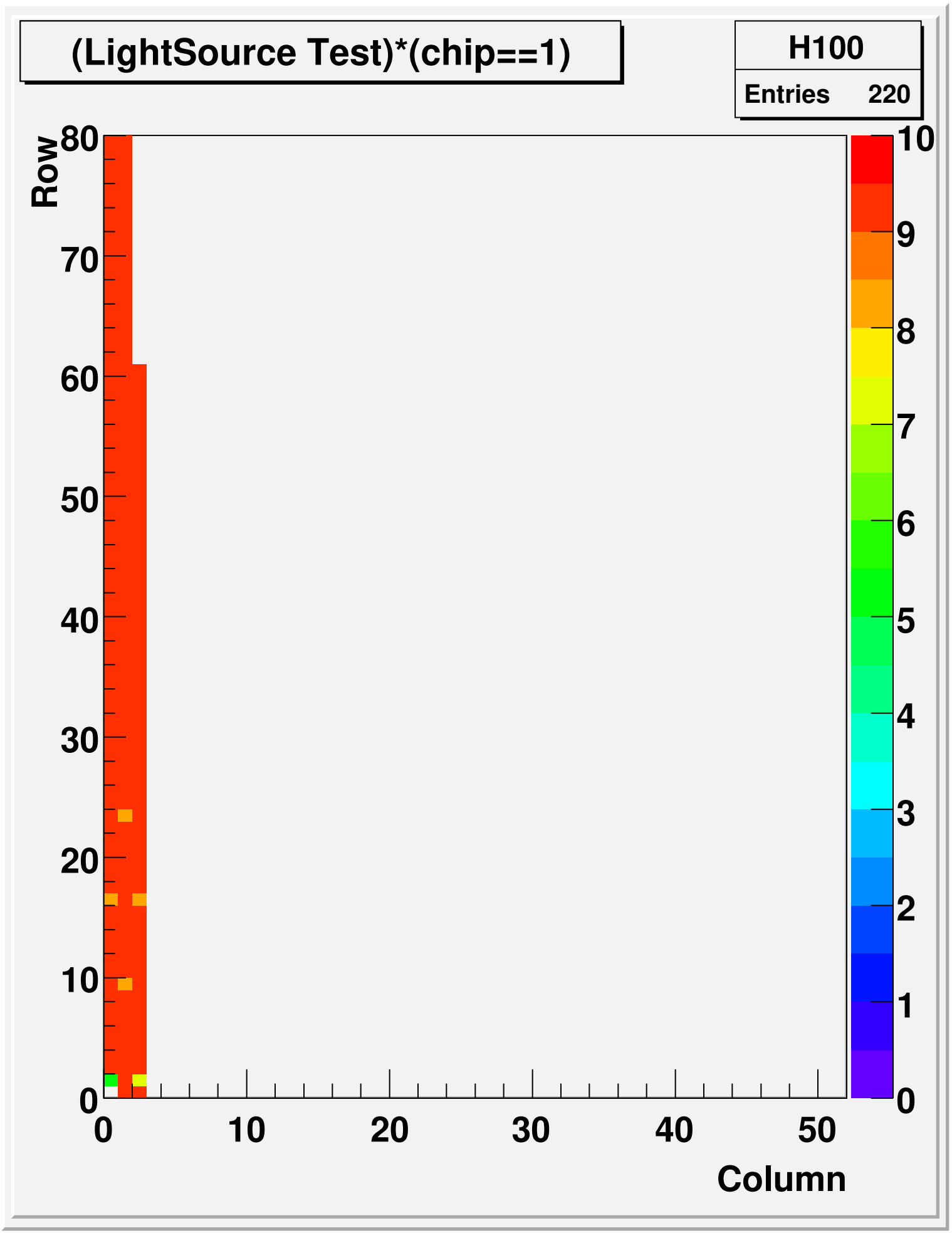

Chip 2 LightSource
H106
Entries 220
Column
Row

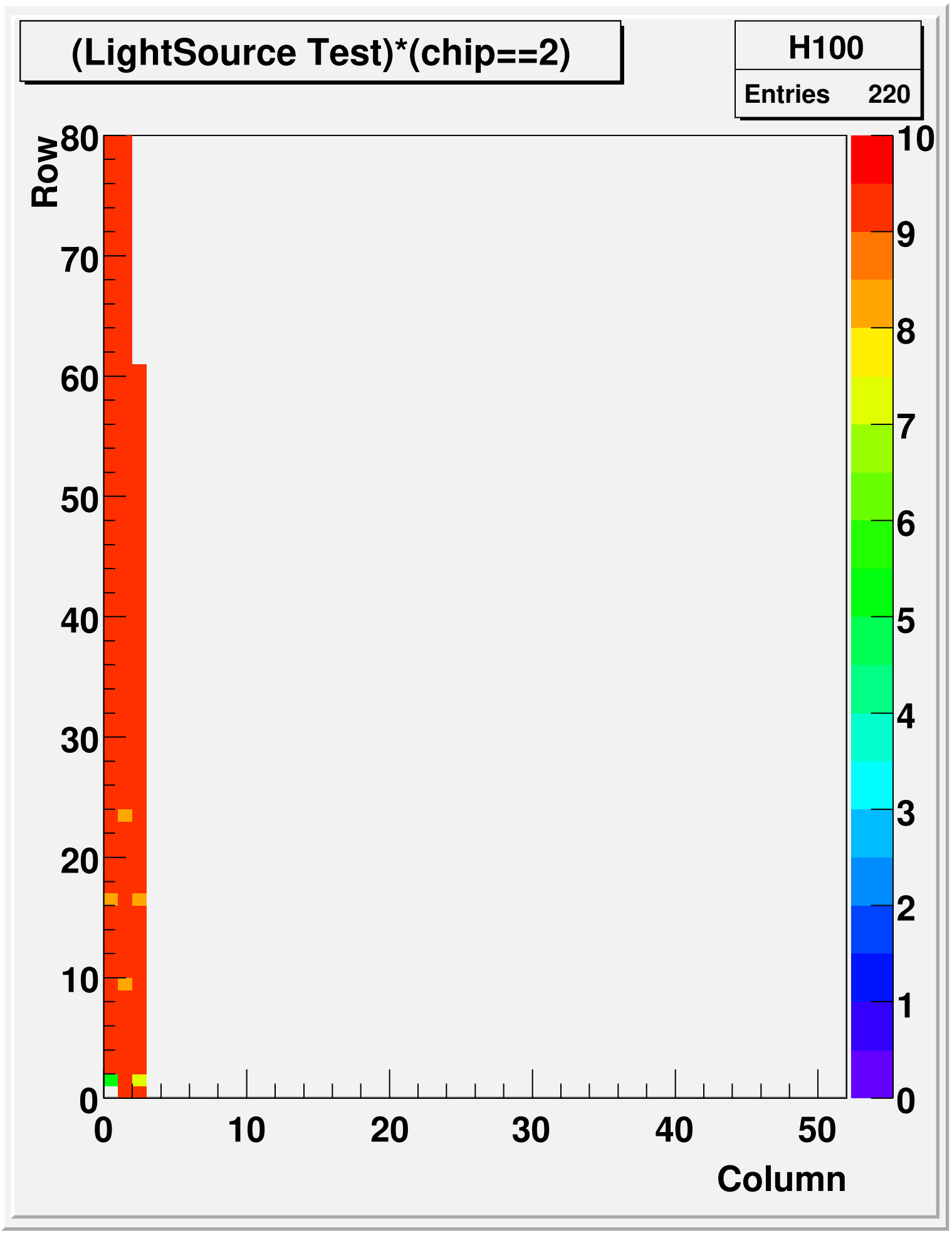

Chip 5 LightSource
H109
Entries 222
Column
Row

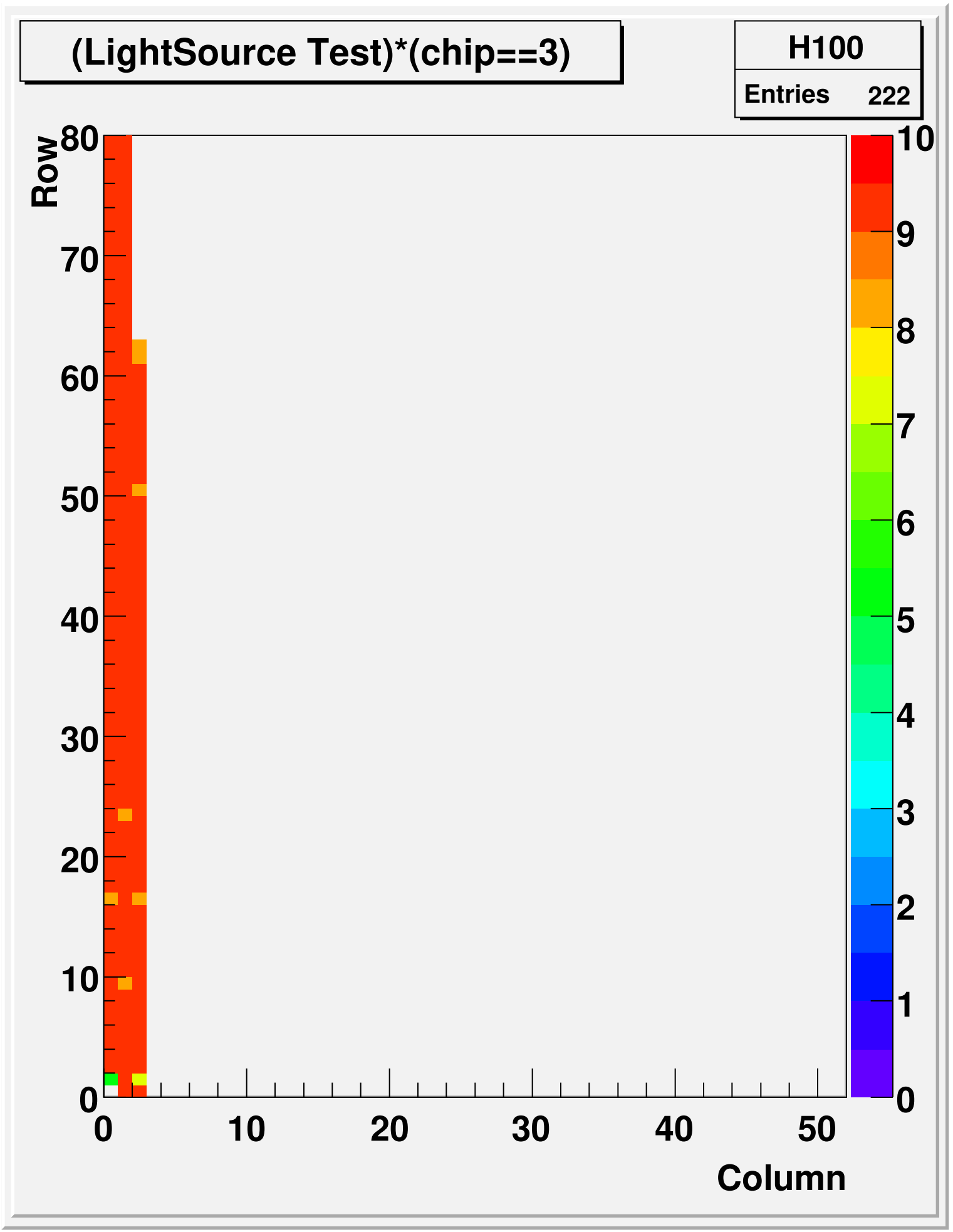

Chip 3 LightSource
H107
Entries 222
Column
Row

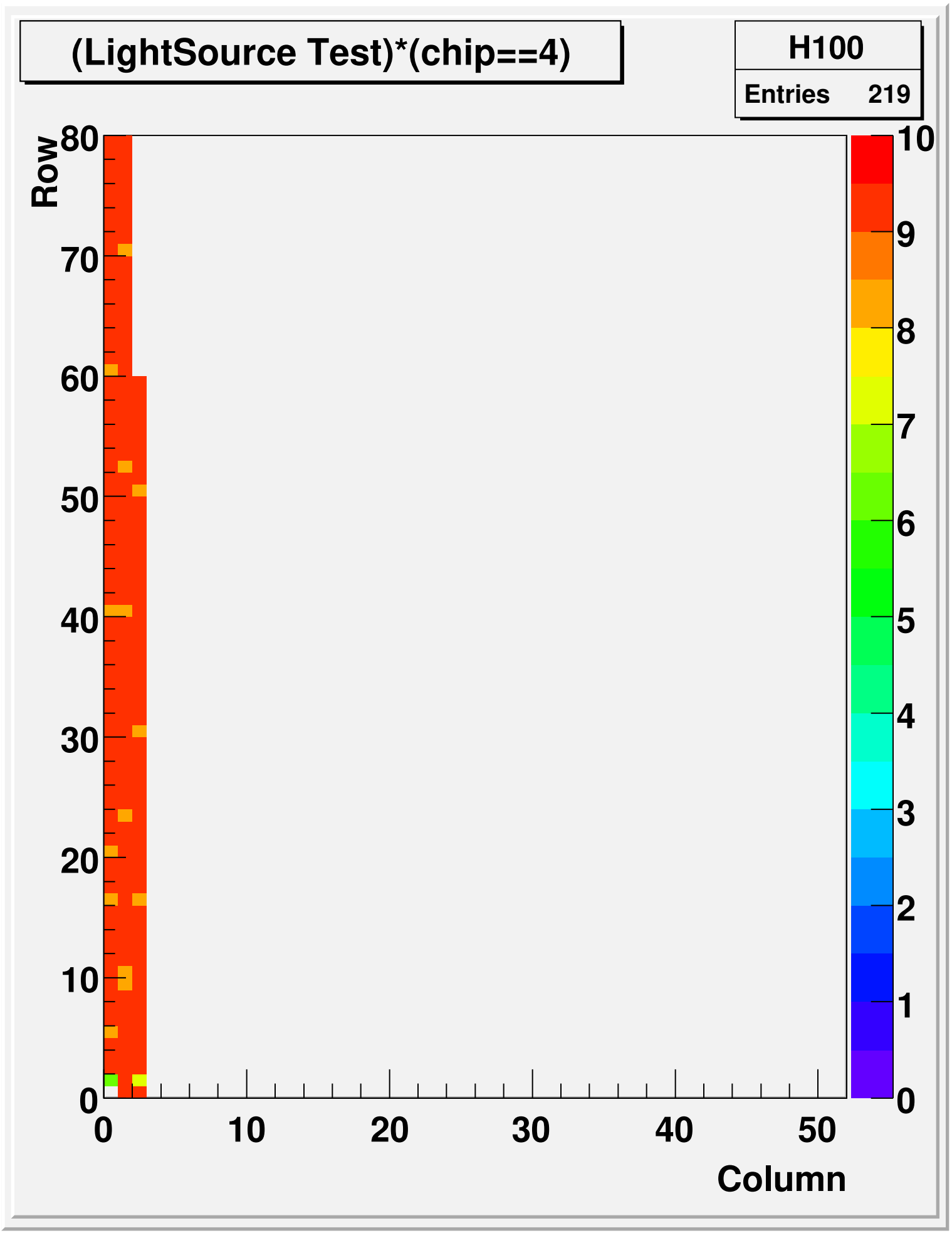

Chip 4 LightSource
H108
Entries 219
Column
Row

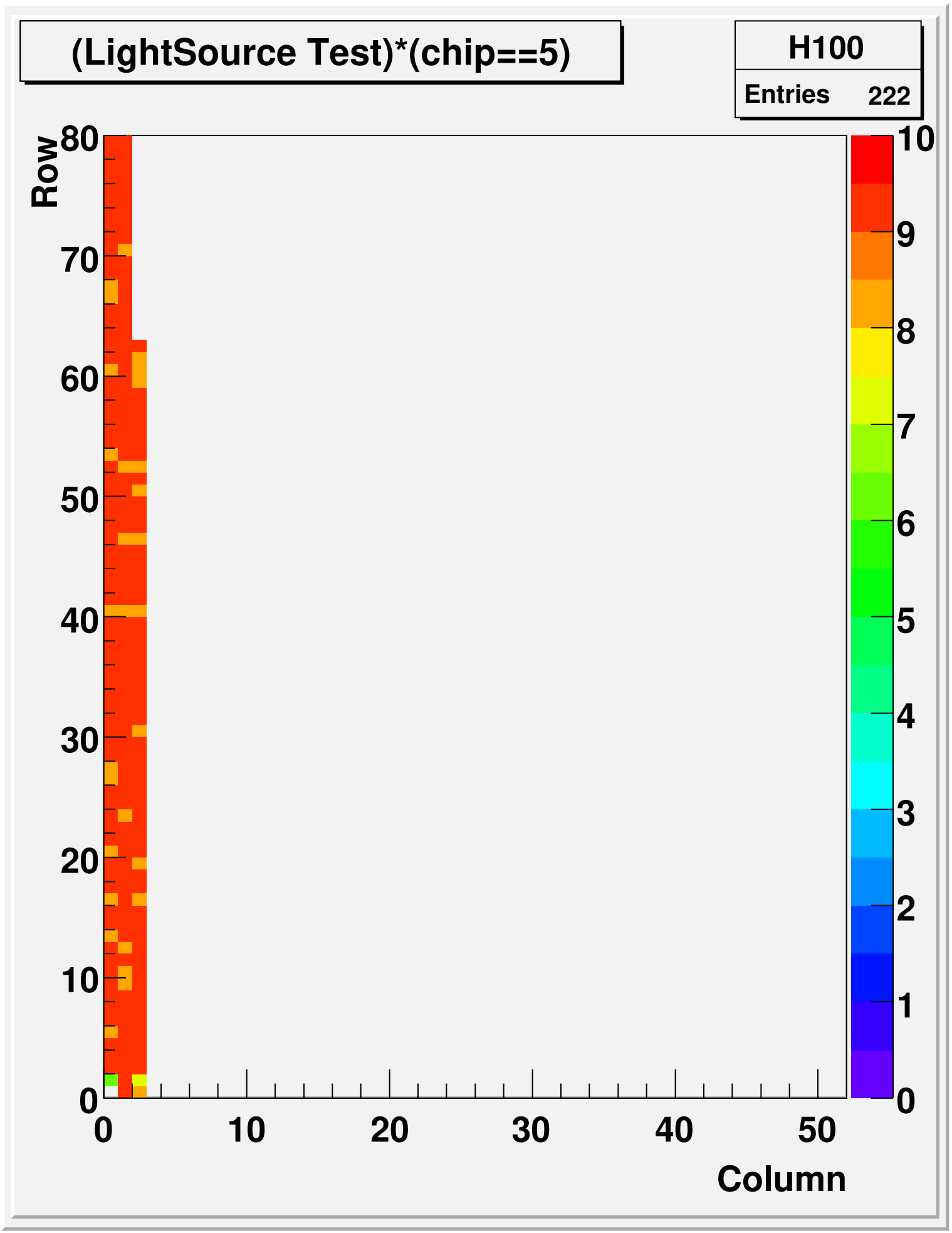

(LightSource Test)*(chip==0)
H100
Entries 219
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0

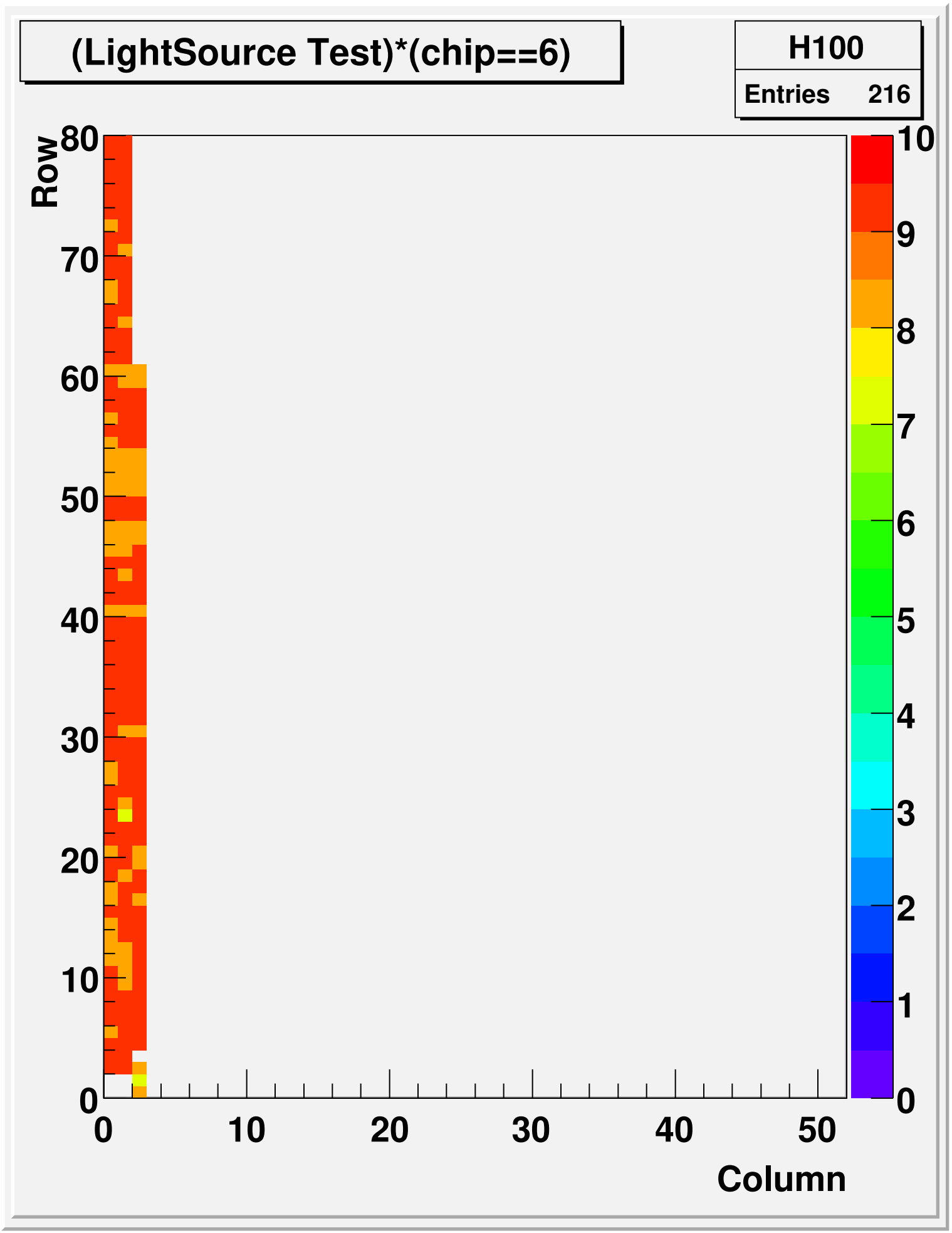

(LightSource Test)*(chip==1)
H100
Entries 220
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0

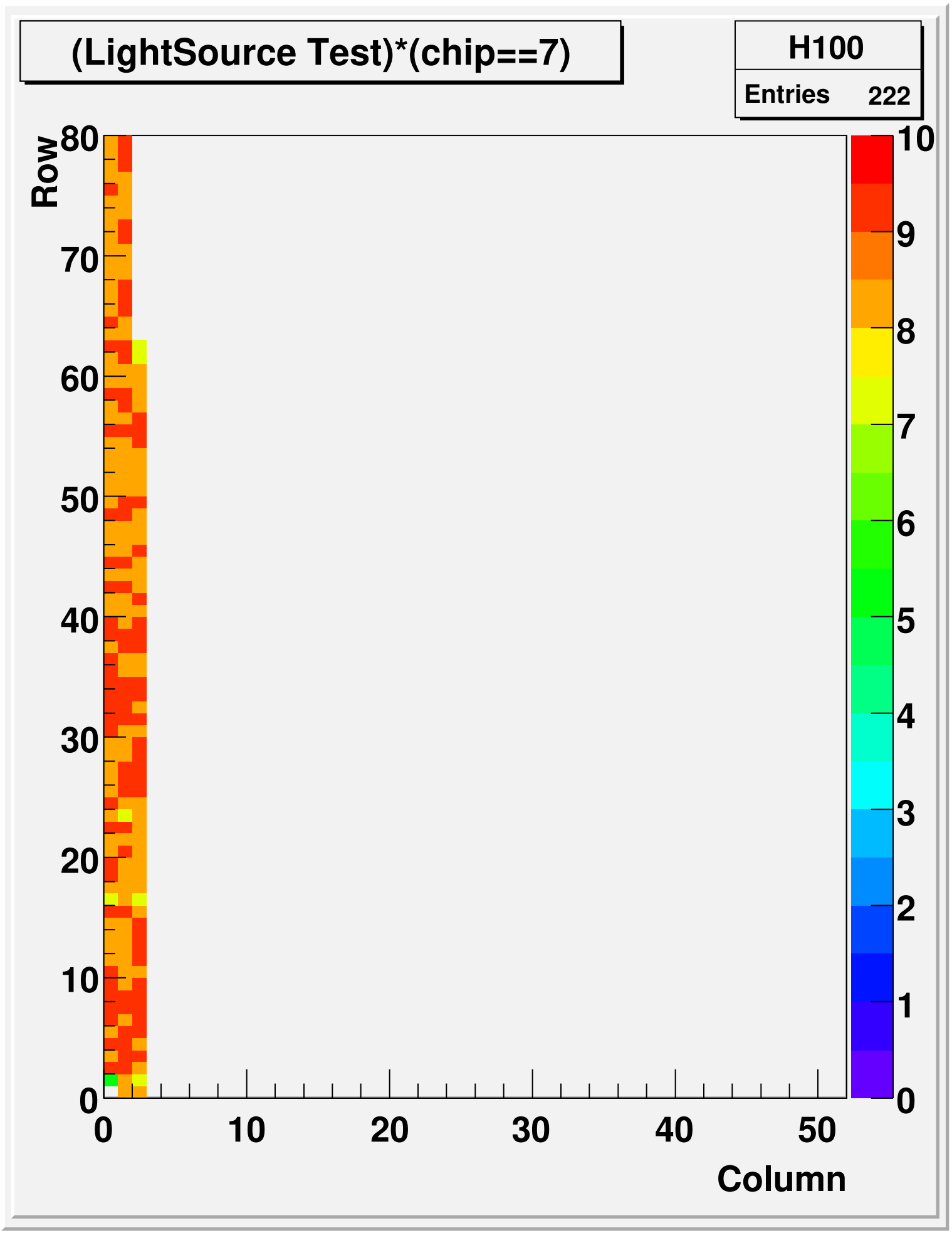

(LightSource Test)*(chip==2)
H100
Entries 220
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==3)
H100
Entries 222
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==4)
H100
Entries 219
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==5)
H100
Entries 222
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==6)
H100
Entries 216
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0


(LightSource Test)*(chip==7)
H100
Entries 222
Row
80
70
60
50
40
30
20
10
0
0
10
20
30
40
50
Column
10
9
8
7
6
5
4
3
2
1
0