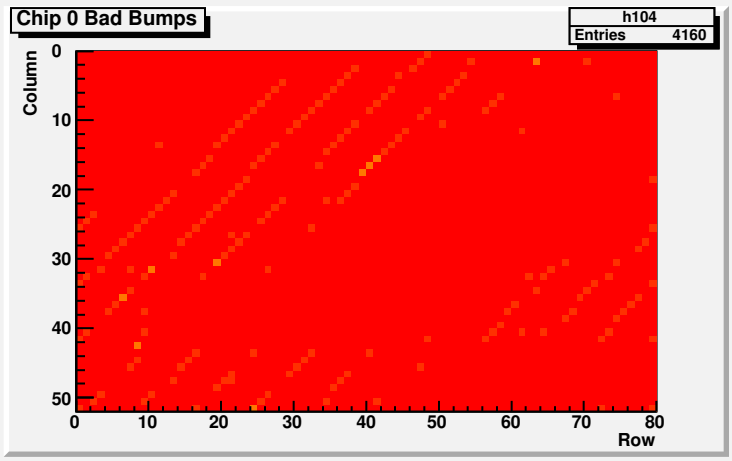
Chip 0 Bad Bumps
h104
Entries 4160
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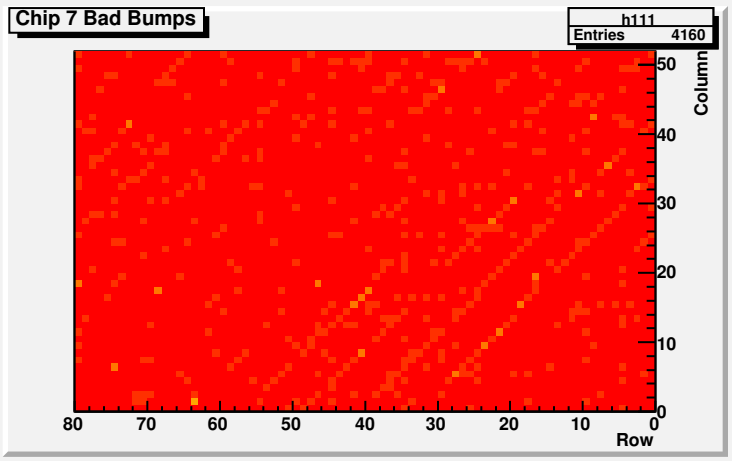
Chip 7 Bad Bumps
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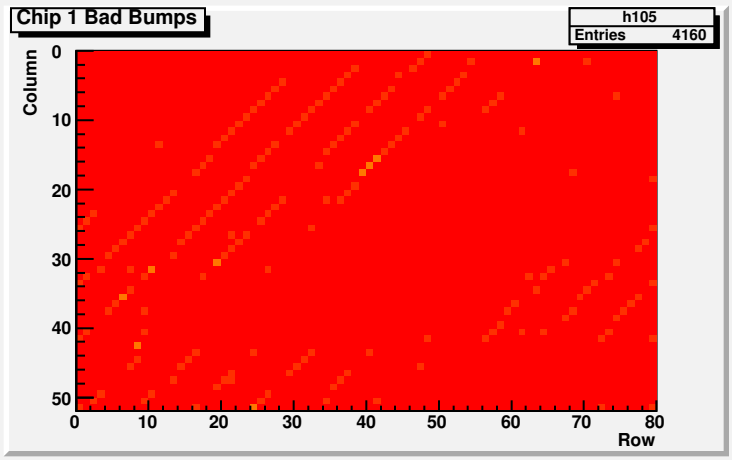
Chip 1 Bad Bumps
h105
Entries 4160
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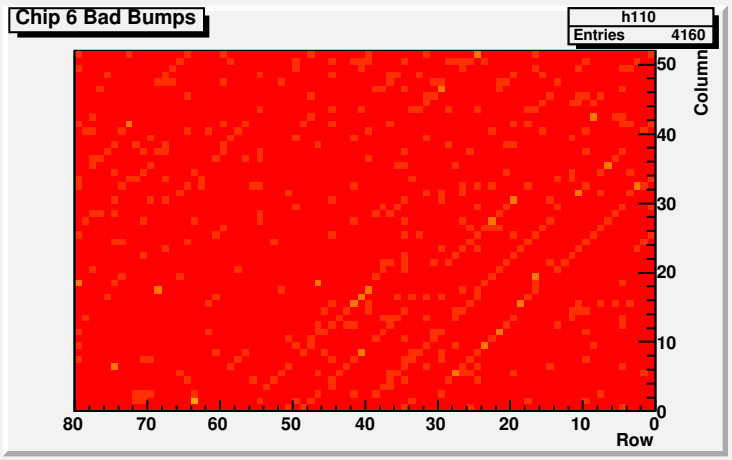
Chip 6 Bad Bumps
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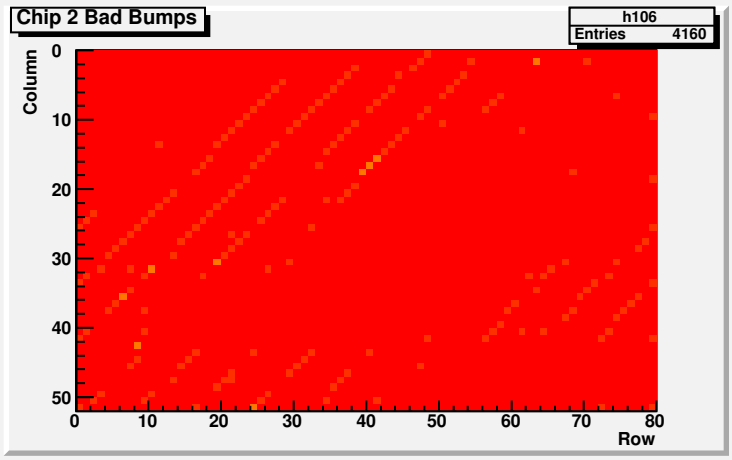
Chip 2 Bad Bumps
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Entries 4160
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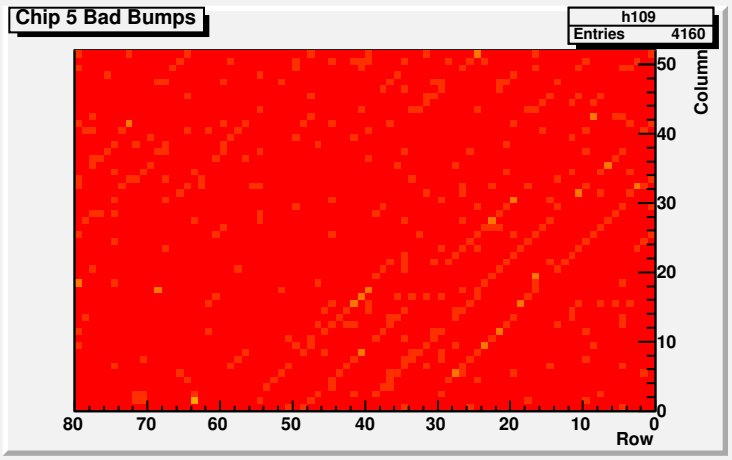
Chip 5 Bad Bumps
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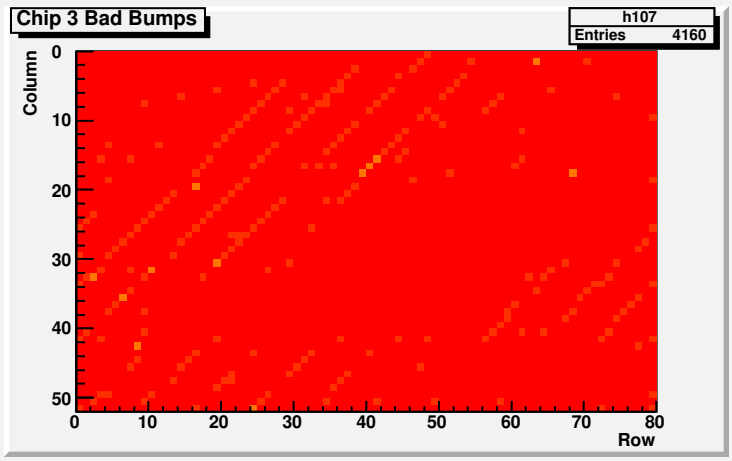
Chip 3 Bad Bumps
h107
Entries 4160
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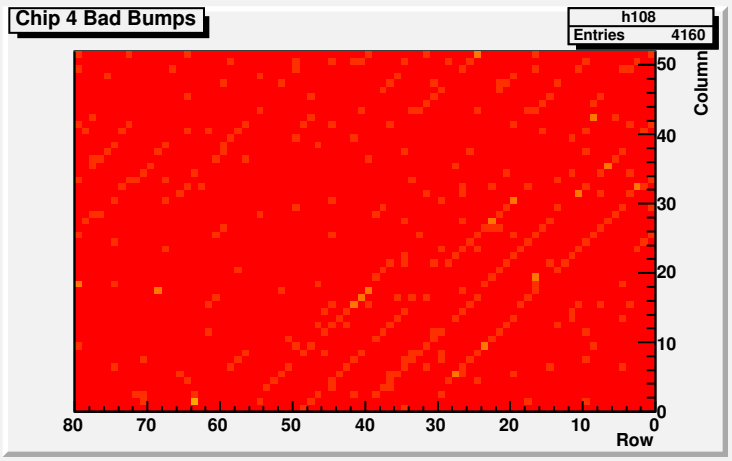
Chip 4 Bad Bumps
h108
Entries 4160
Column
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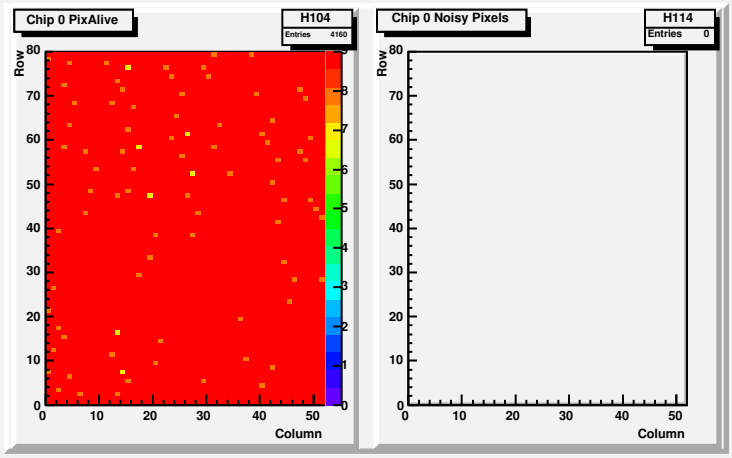
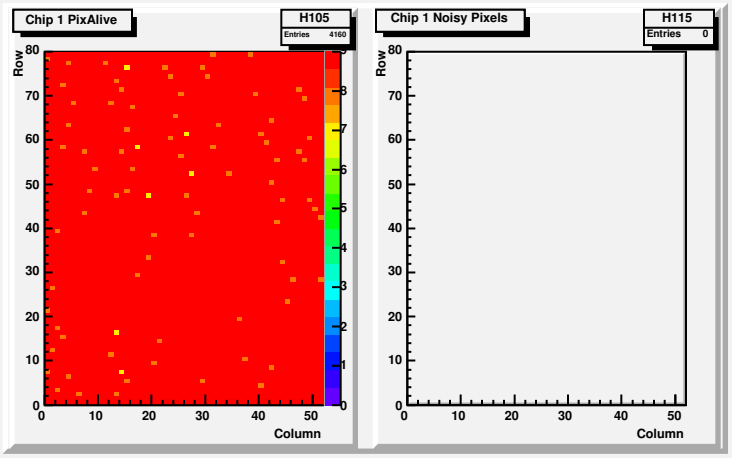

Chip 0 PixAlive
H104
Entries 4160
Row
Column

Chip 0 Noisy Pixels
H114
Entries 0
Row
Column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H105
Entries 4160
Chip 1 Noisy Pixels
H115
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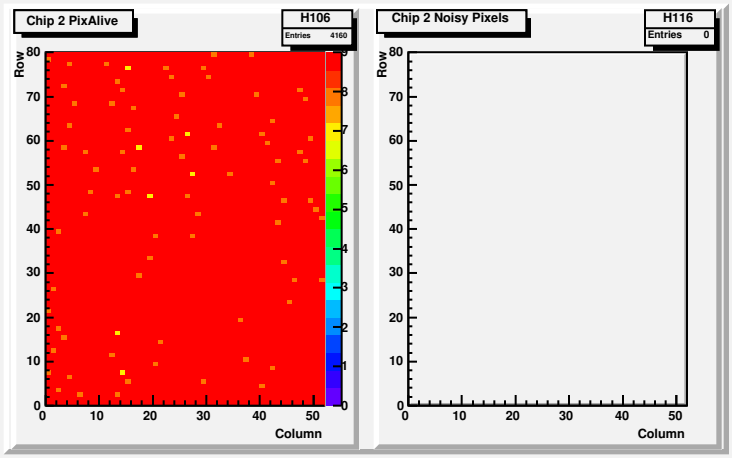
Row
Column


Chip 2 PixAlive
H106
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Chip 2 Noisy Pixels
H116
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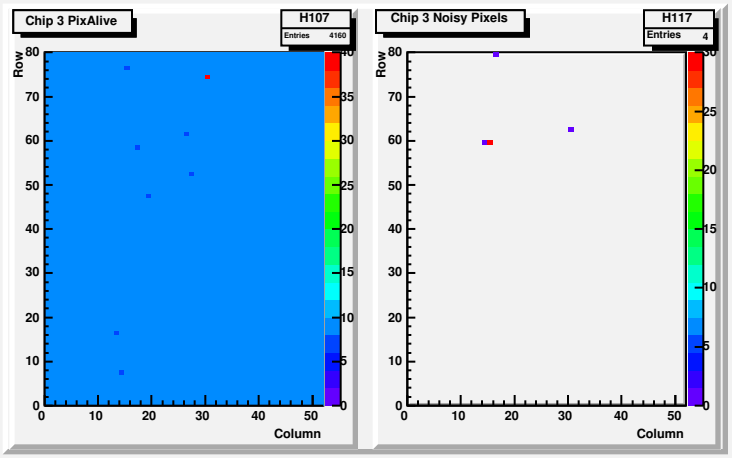
Column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H107
Entries 4160
Chip 3 Noisy Pixels
H117
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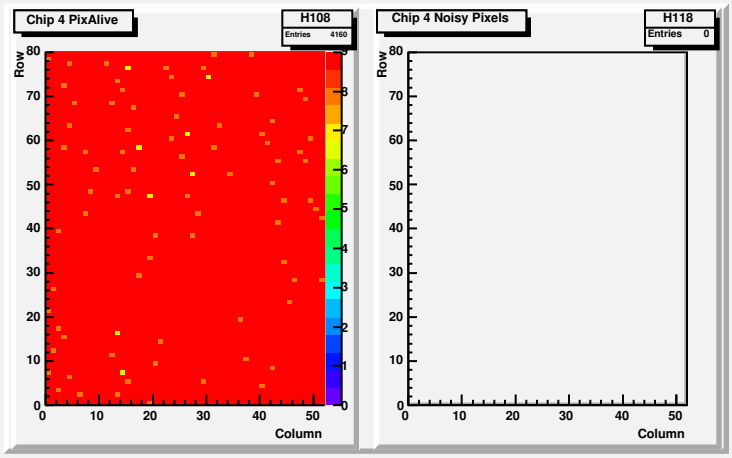
Column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Entries 4160
Chip 4 Noisy Pixels
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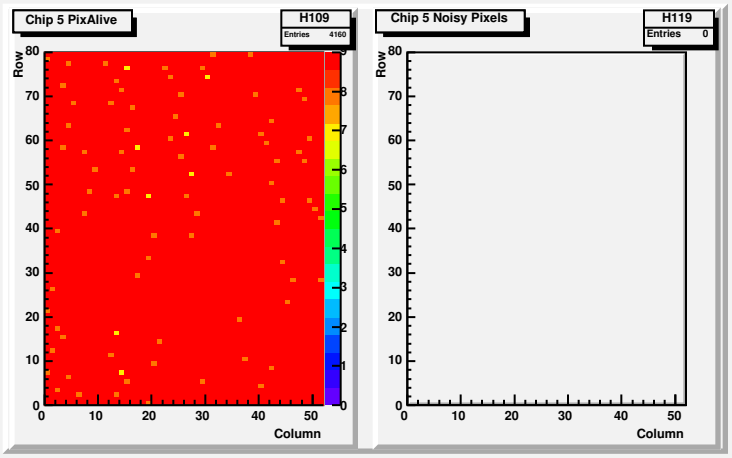
Chip 5 PixAlive
H109
Entries 4160
Chip 5 Noisy Pixels
H119
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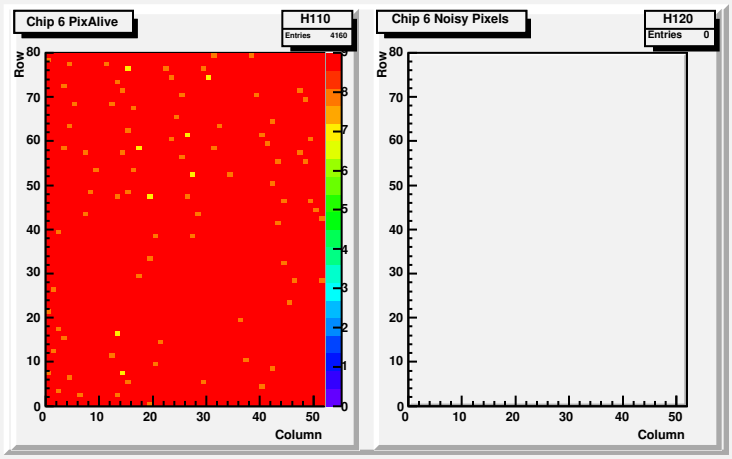
Column


Chip 6 PixAlive
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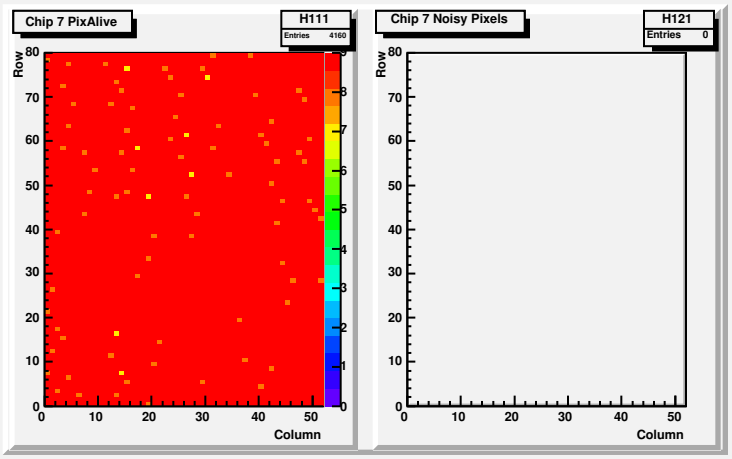
Entries 4160
Chip 6 Noisy Pixels
H120
Entries 0
Row
Column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Entries 4160
Chip 7 Noisy Pixels
H121
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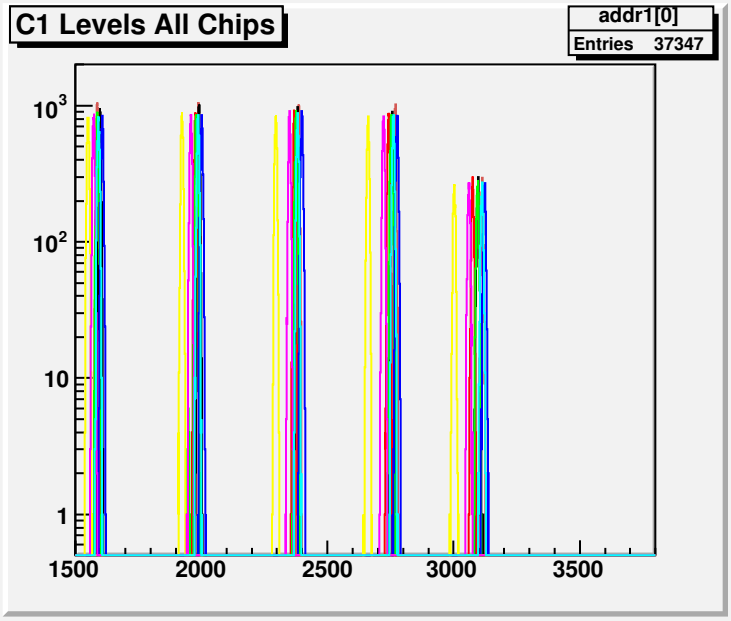
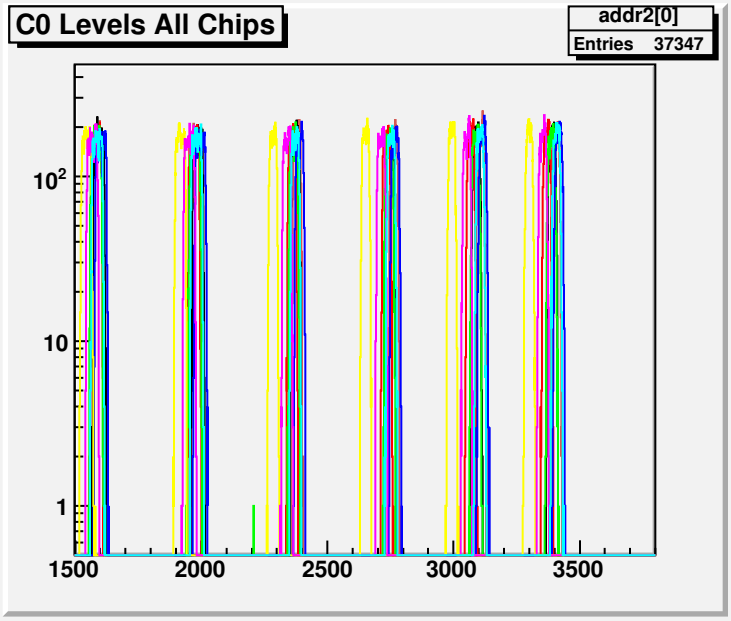
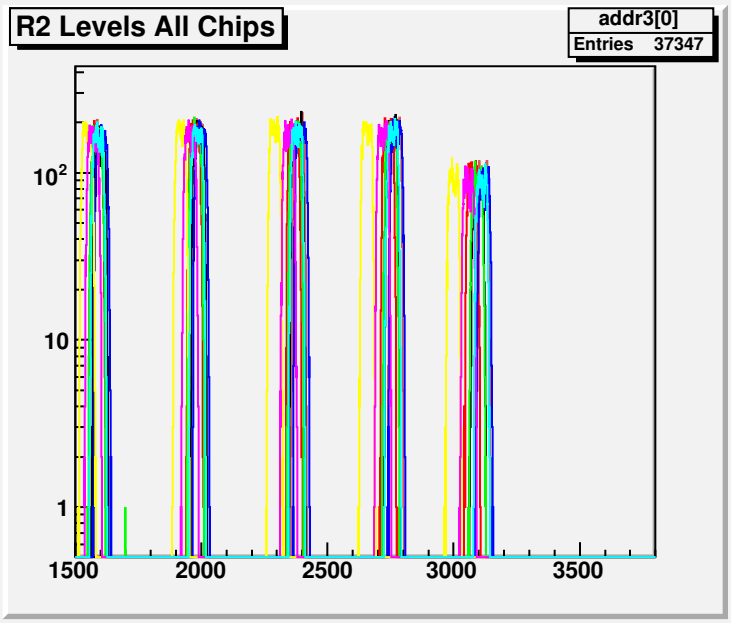
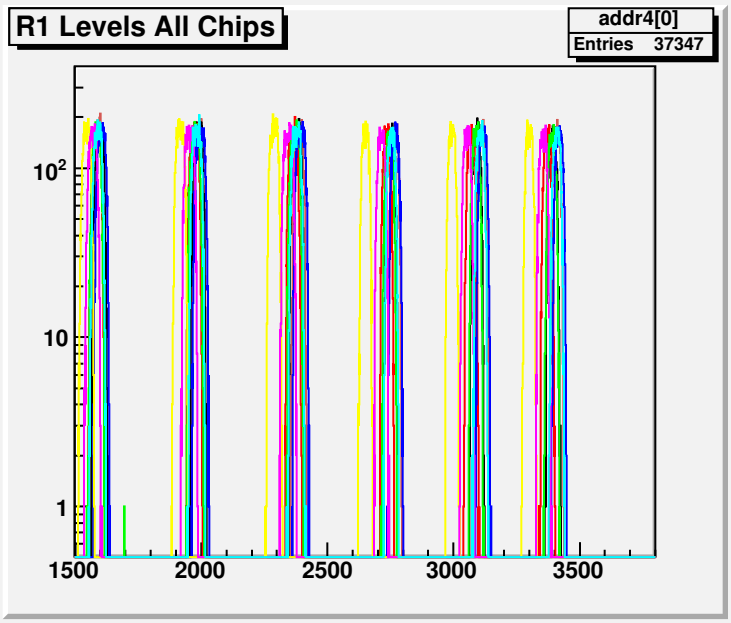
Column


C1 Levels All Chips
addr1[0]
Entries 37347
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37347
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37347
10^2
10
1
1500
2000
2500
3000
3500


R1 Levels All Chips
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Entries 37347
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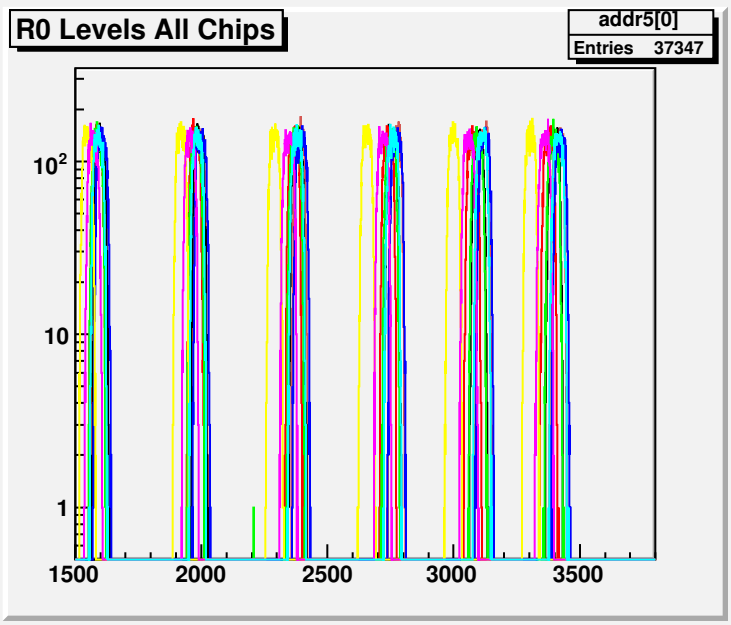
1
1500
2000
2500
3000
3500


R0 Levels All Chips
addr5[0]
Entries 37347
10^2
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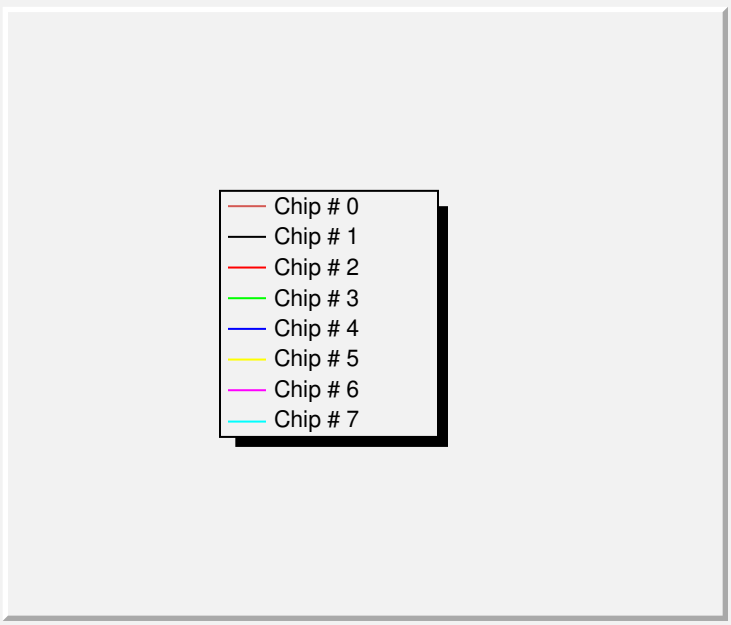
1
1500
2000
2500
3000
3500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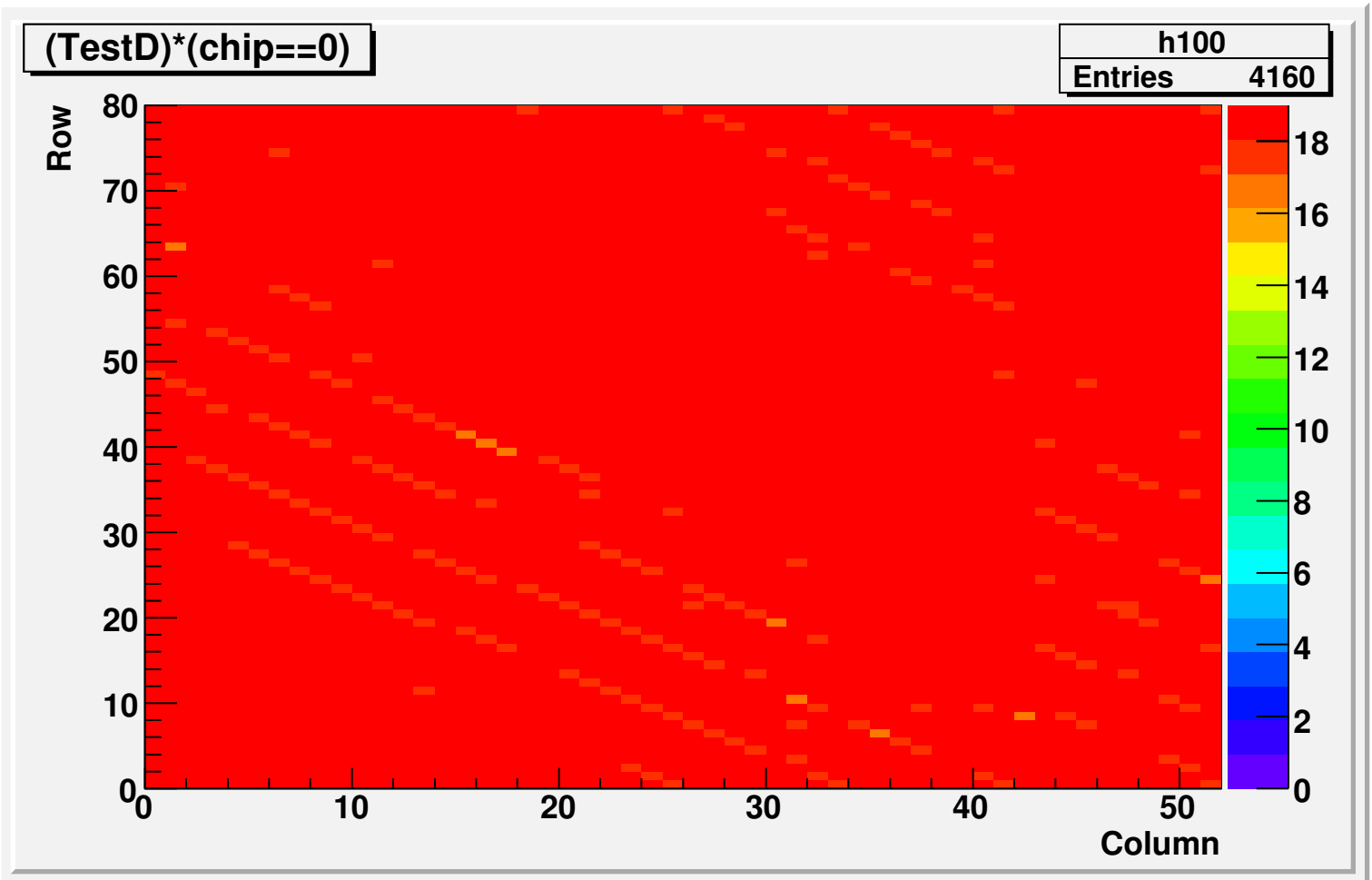
Chip # 1
Chip # 2
Chip # 3
Chip # 4
Chip # 5
Chip # 6
Chip # 7


(TestD)*(chip==0)
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Entries 4160
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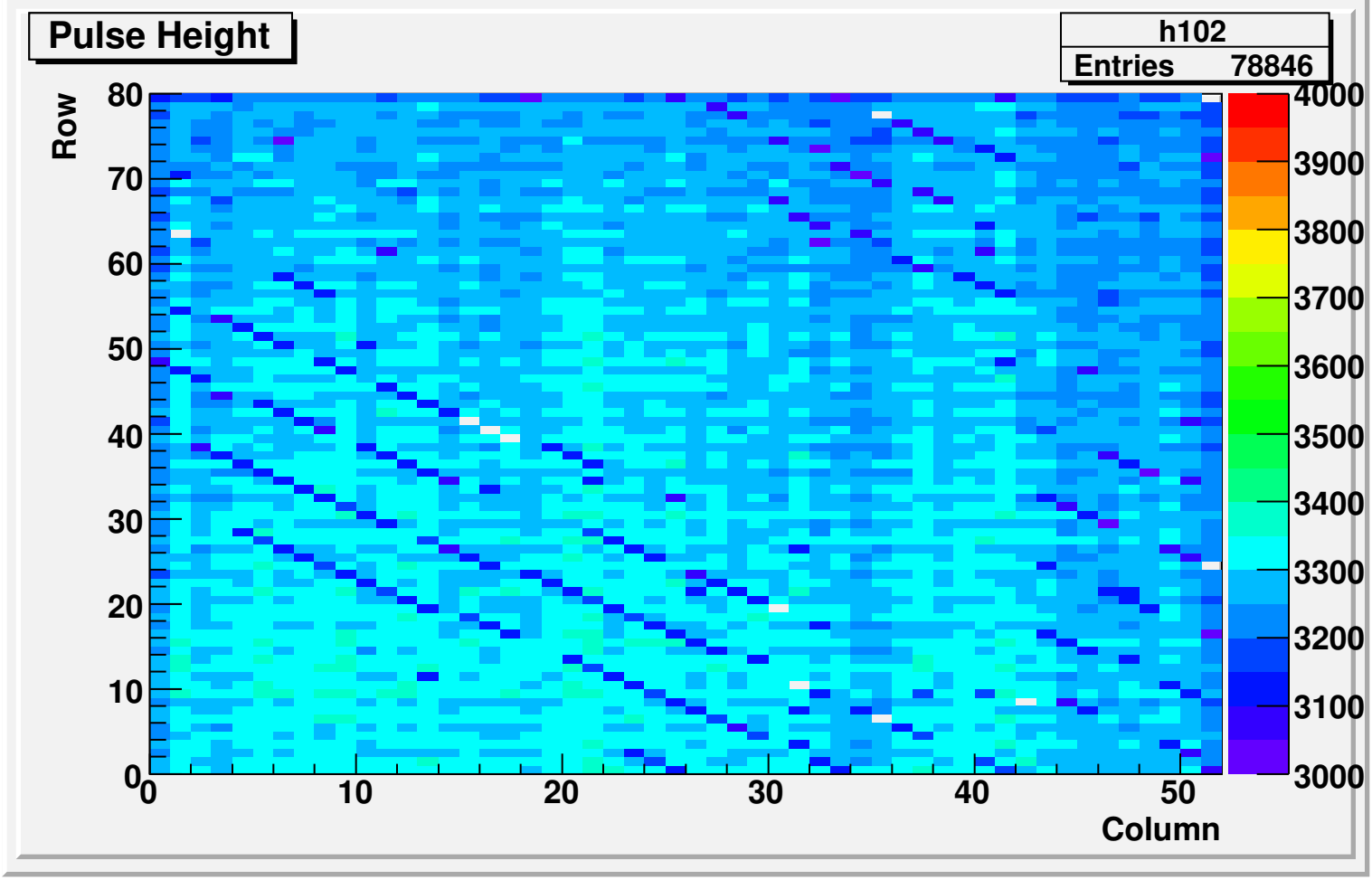
Pulse Height
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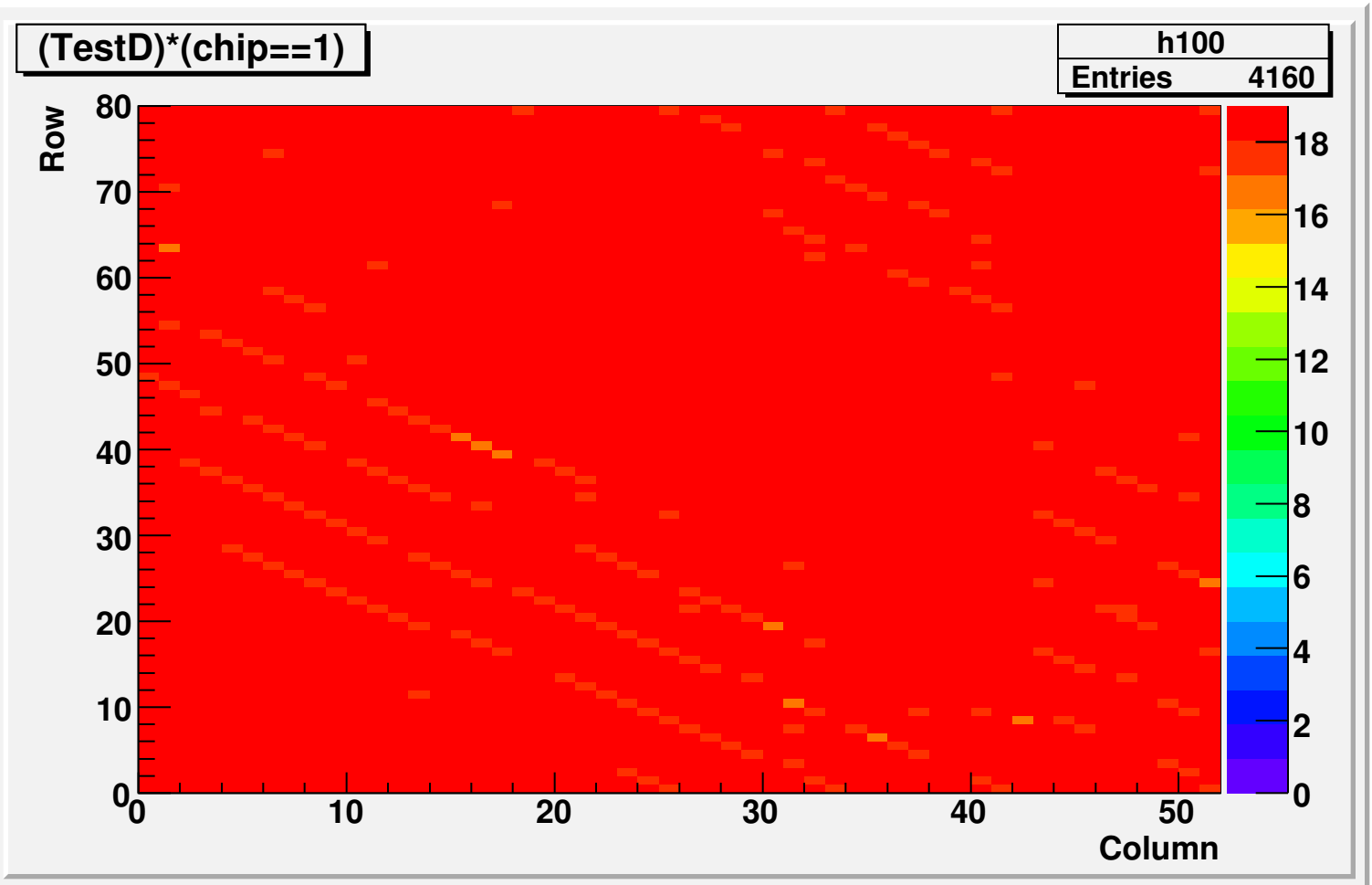
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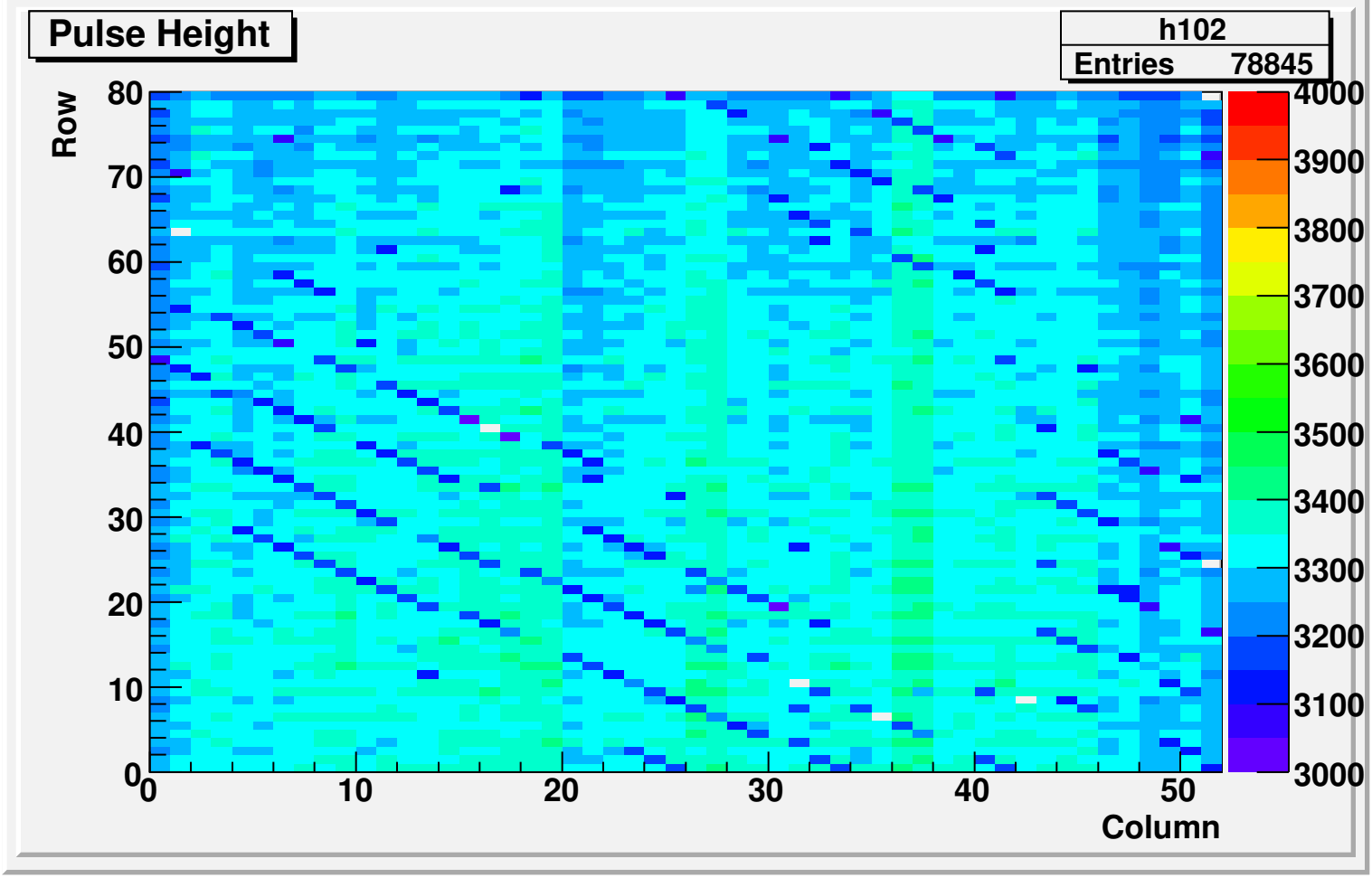
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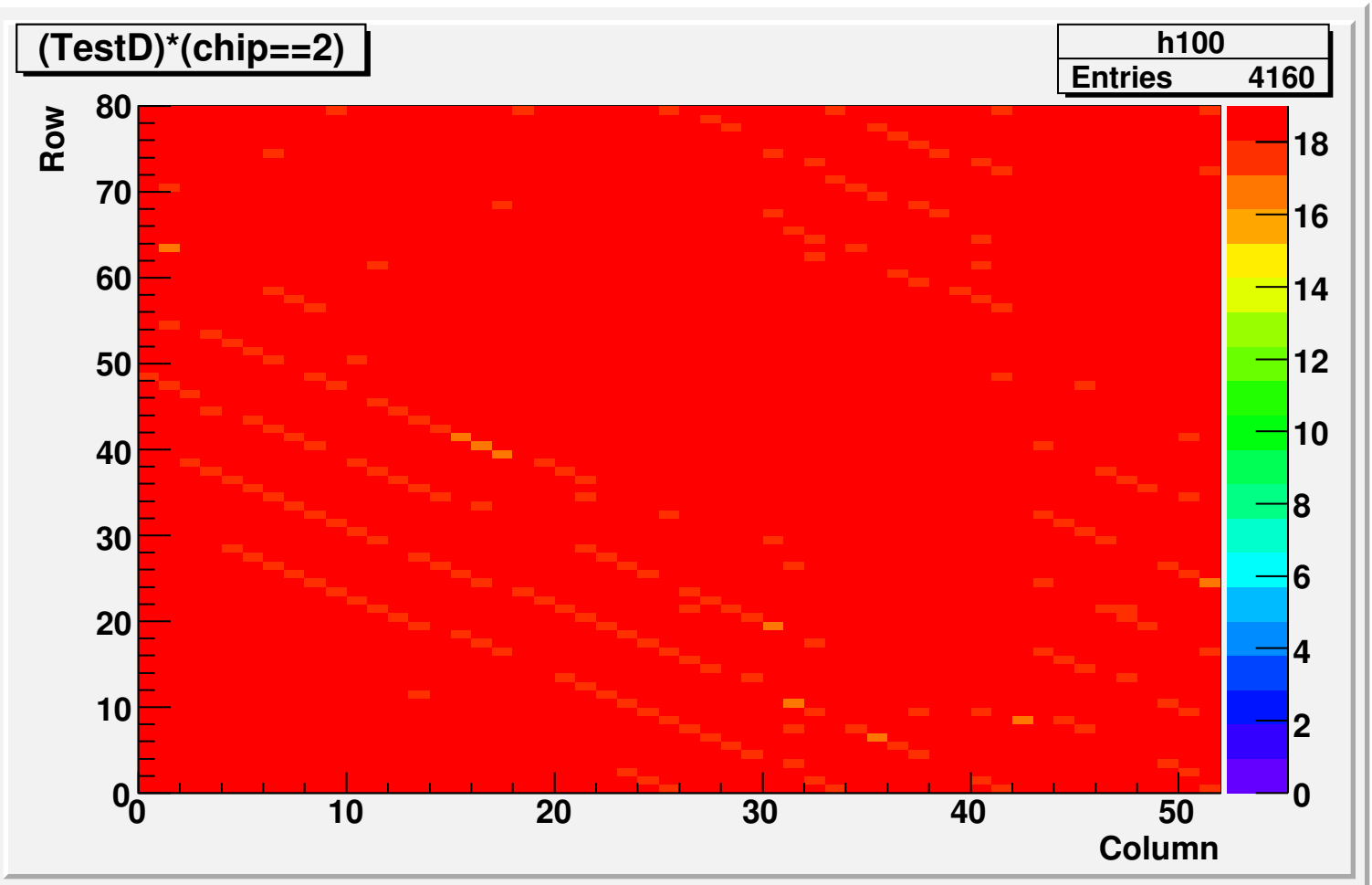
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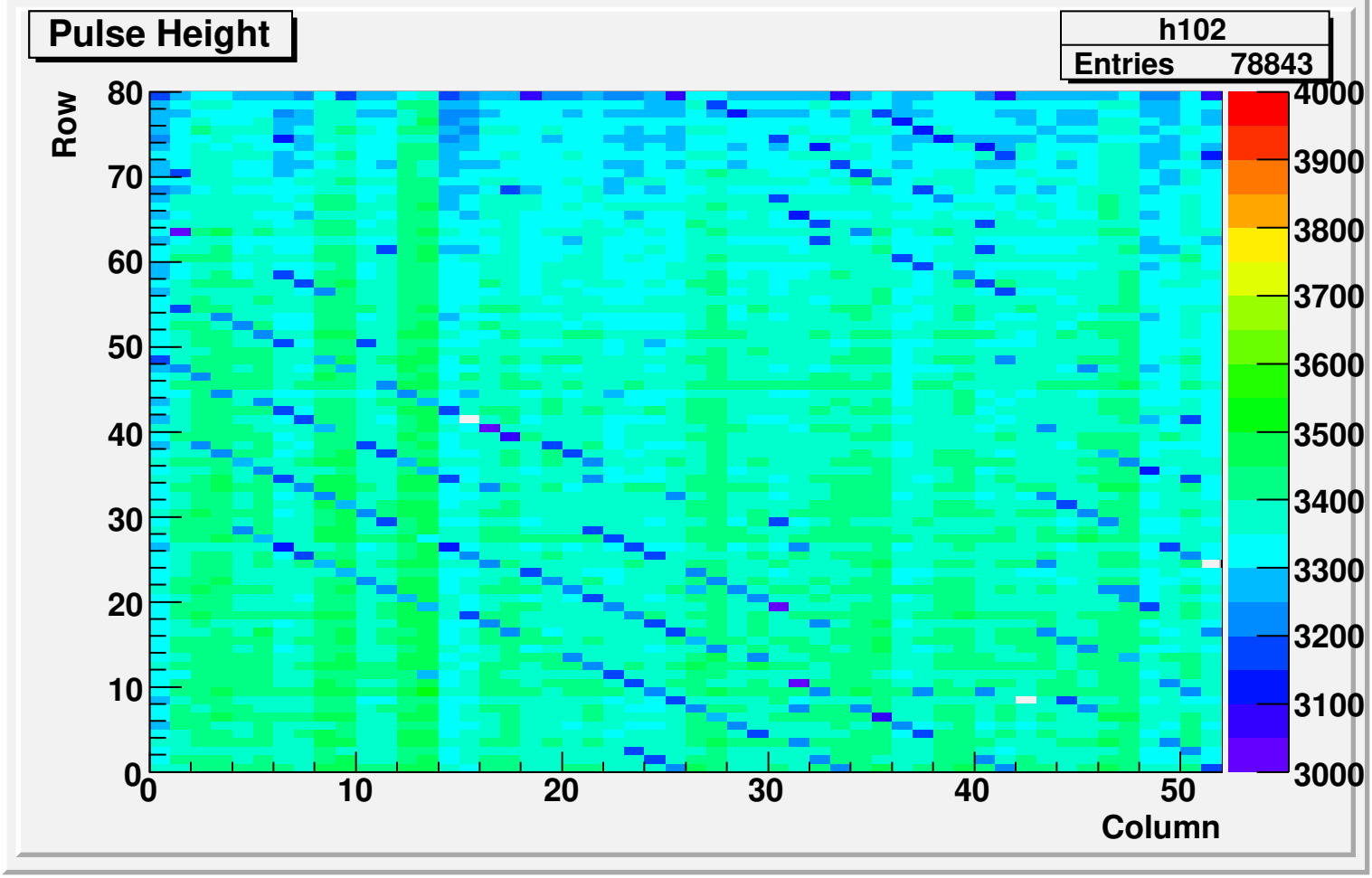
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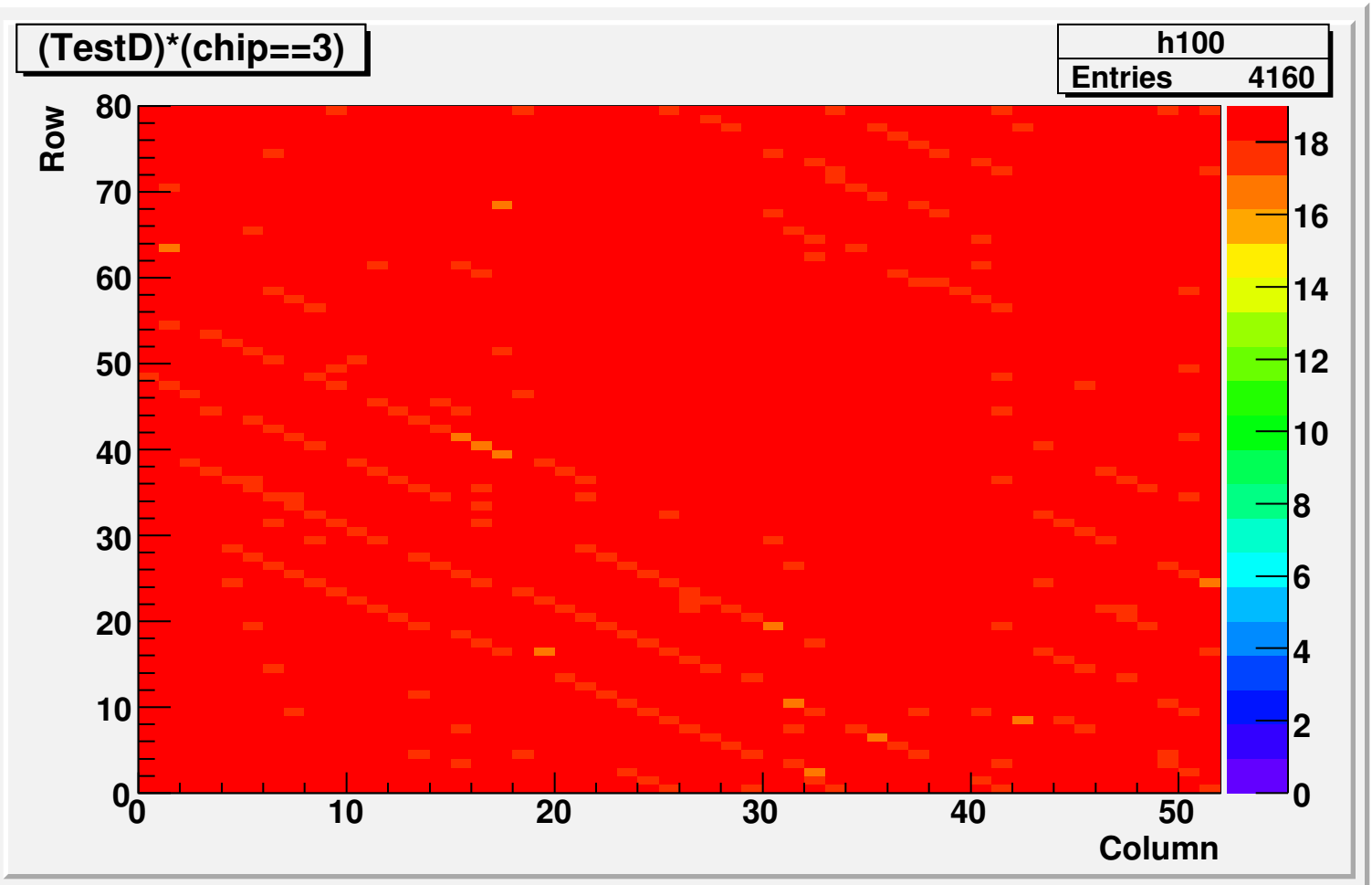
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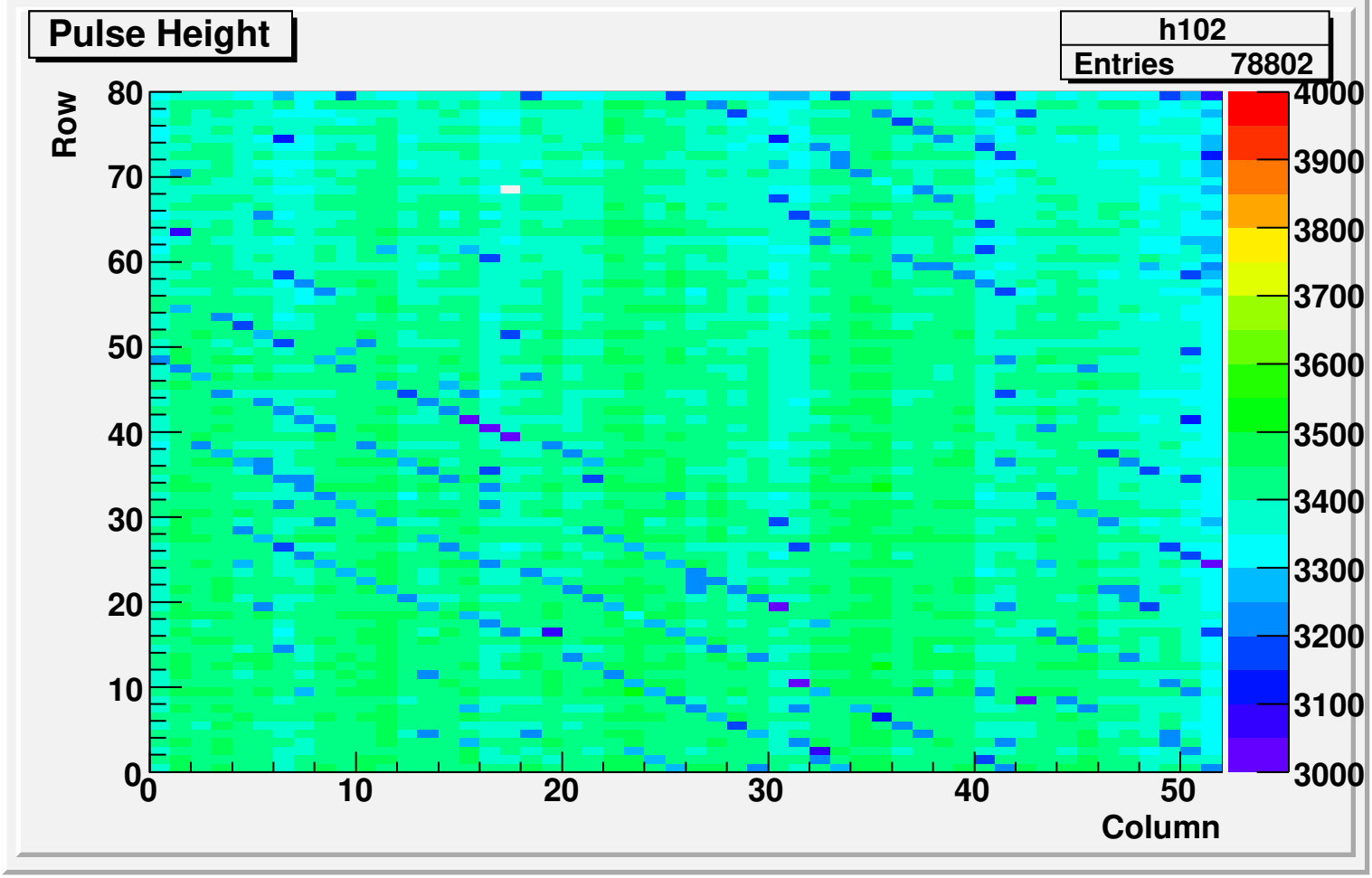
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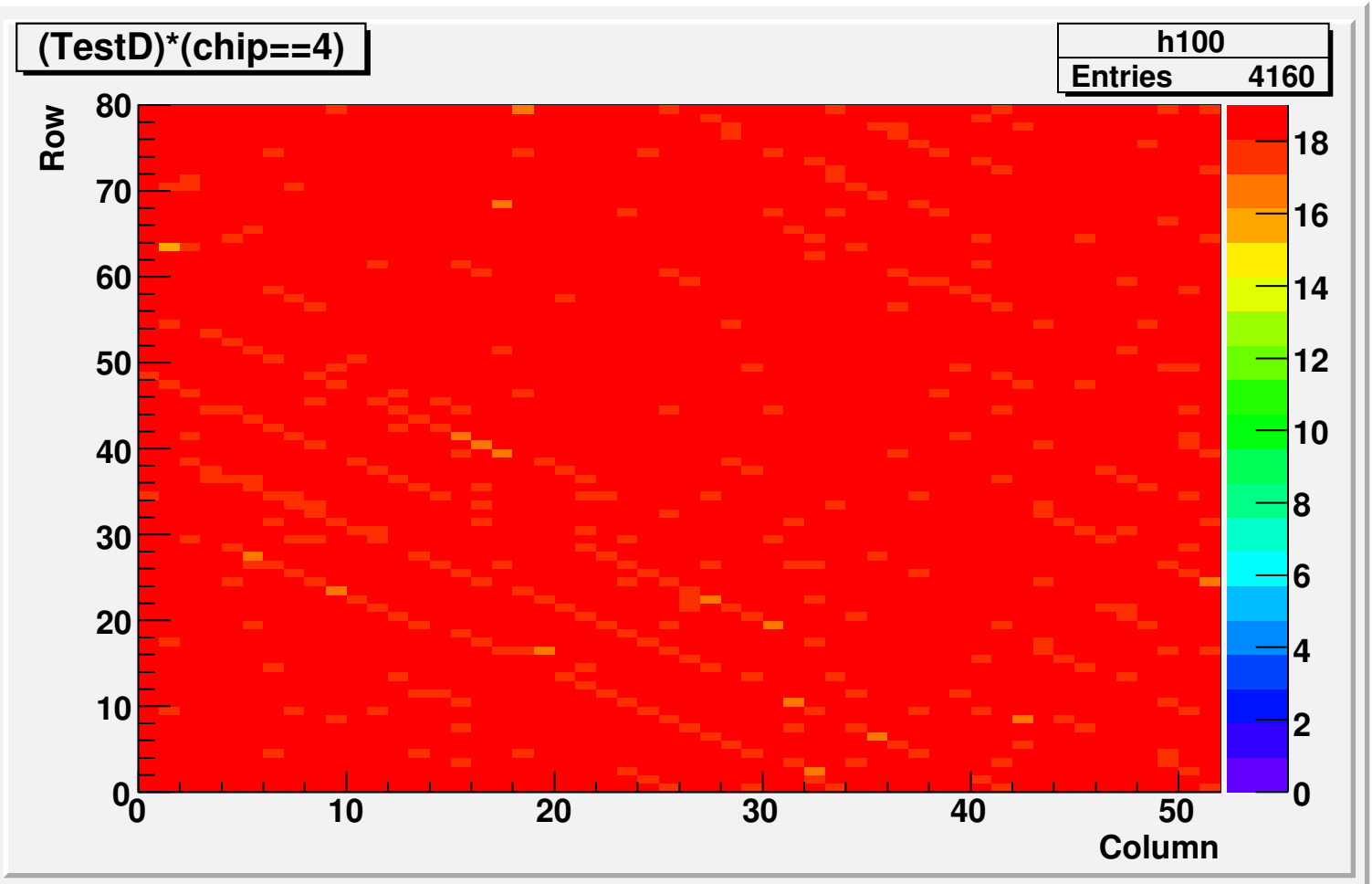
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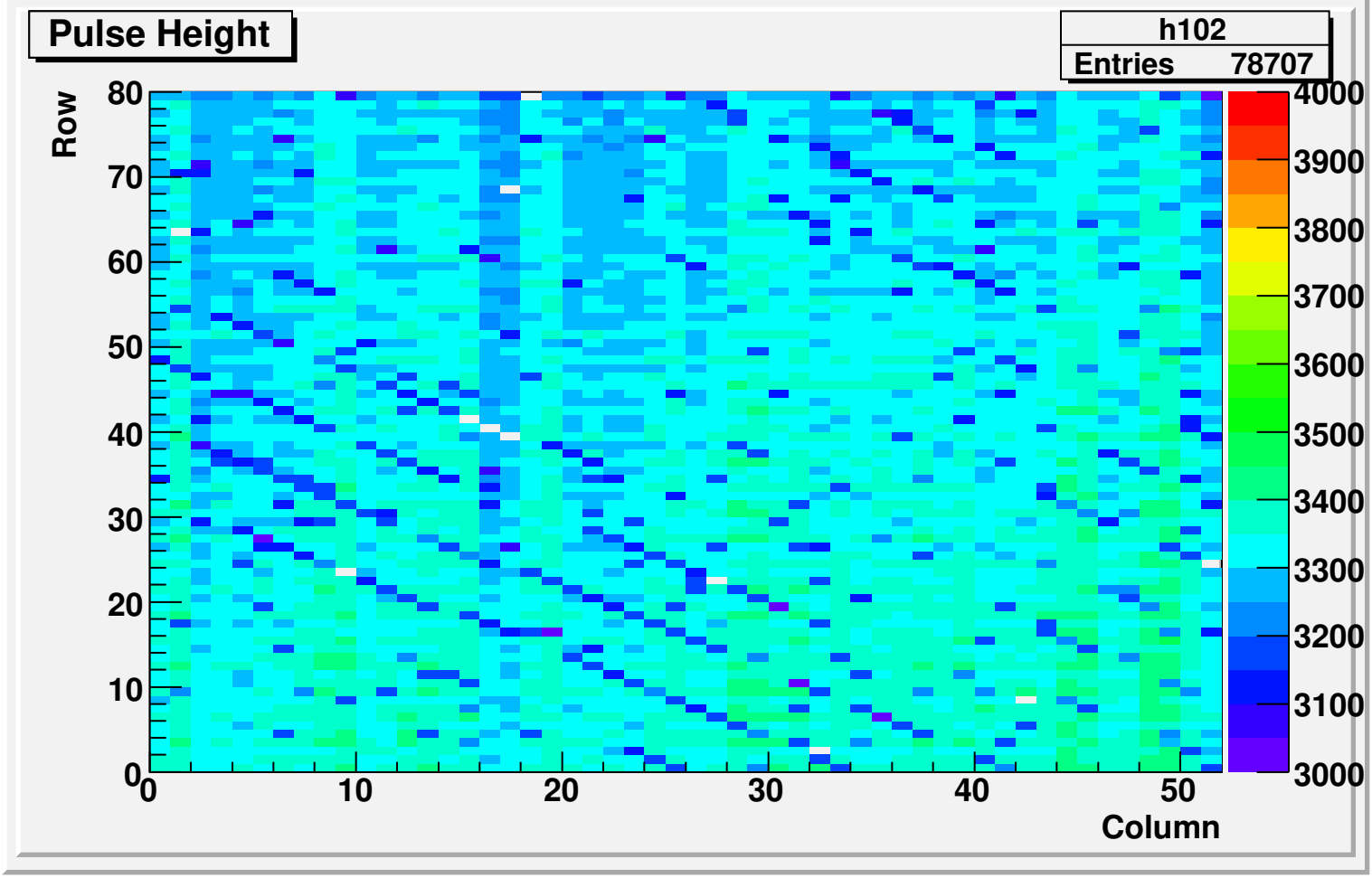
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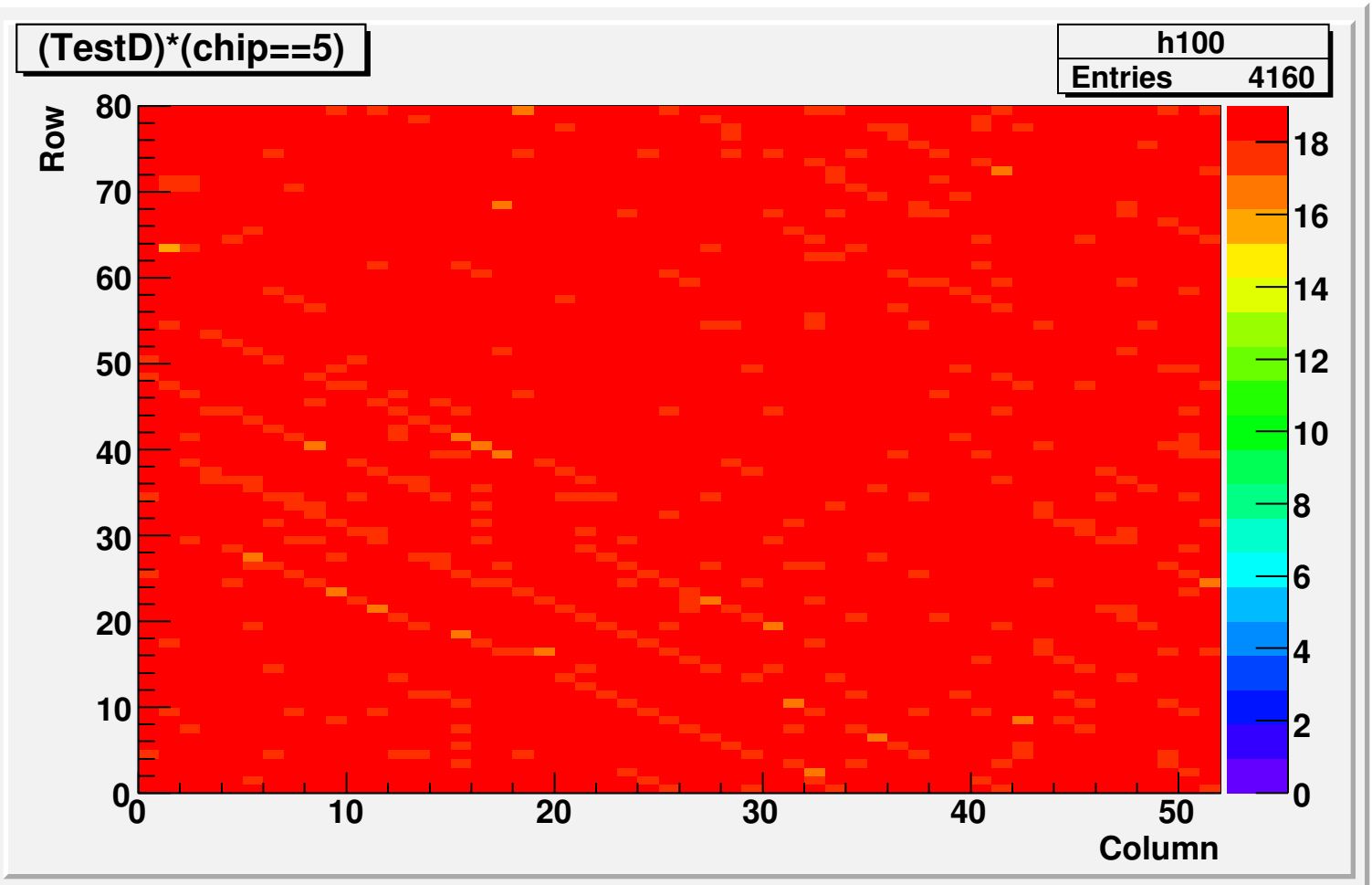
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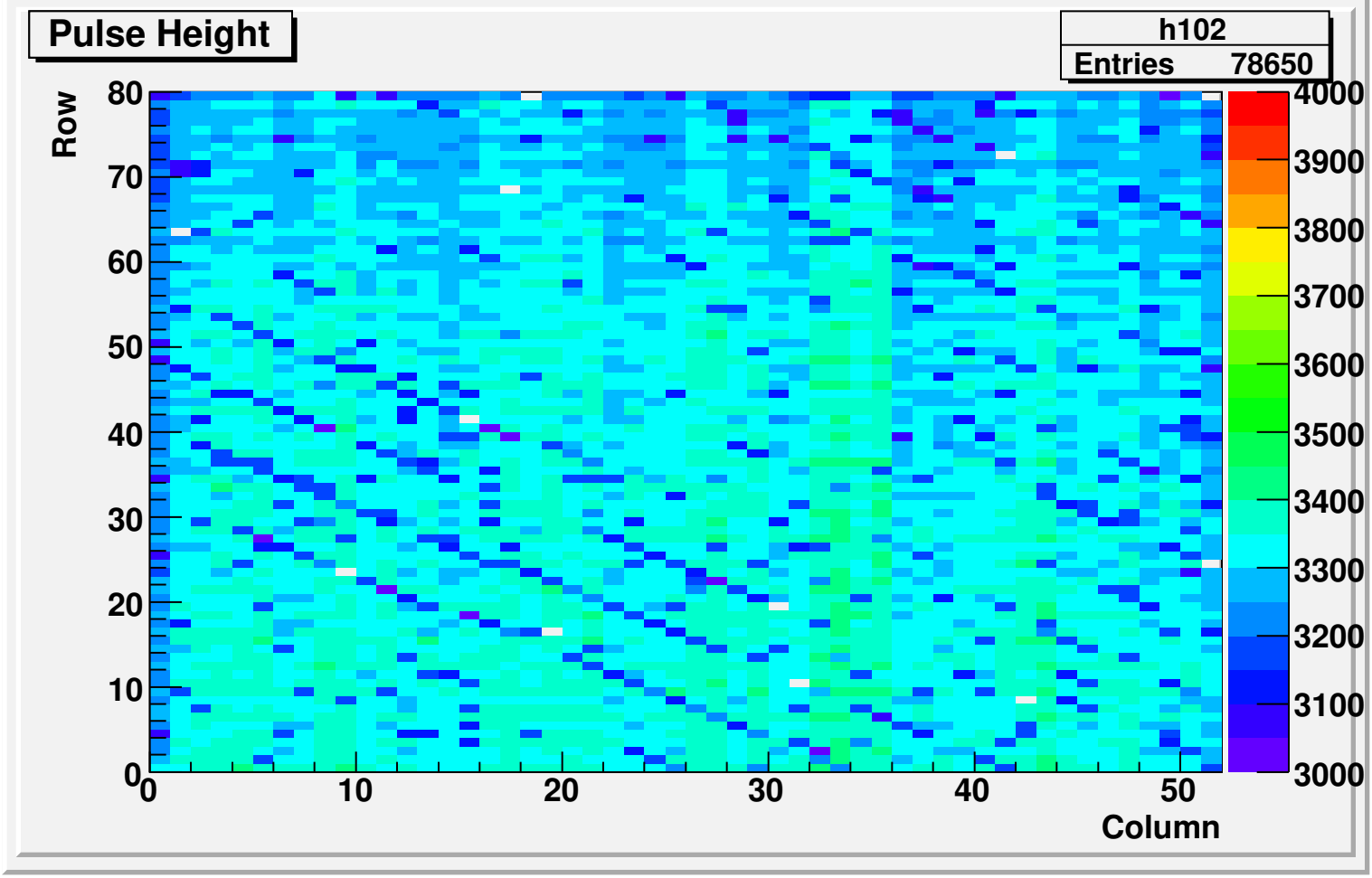
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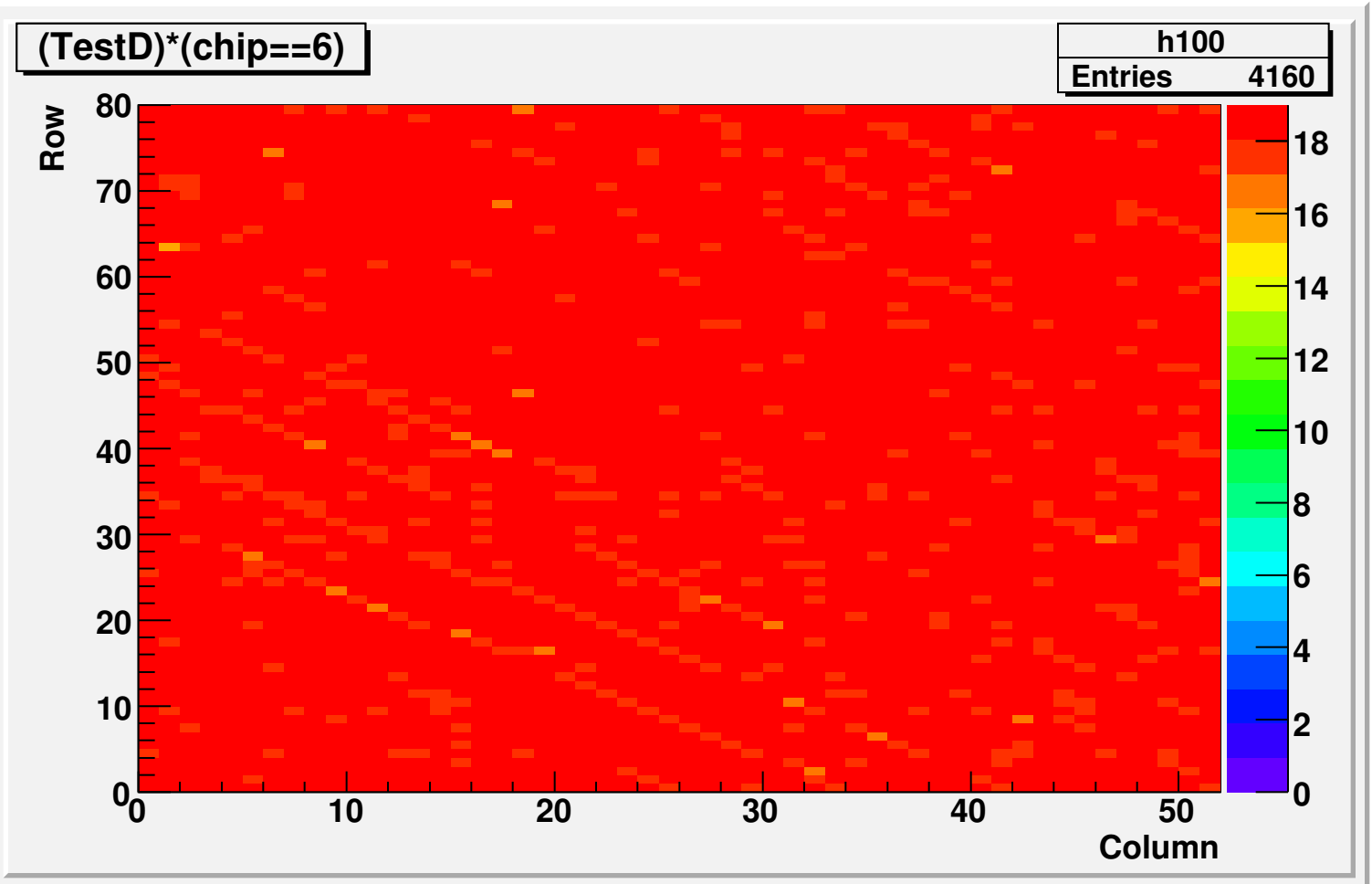
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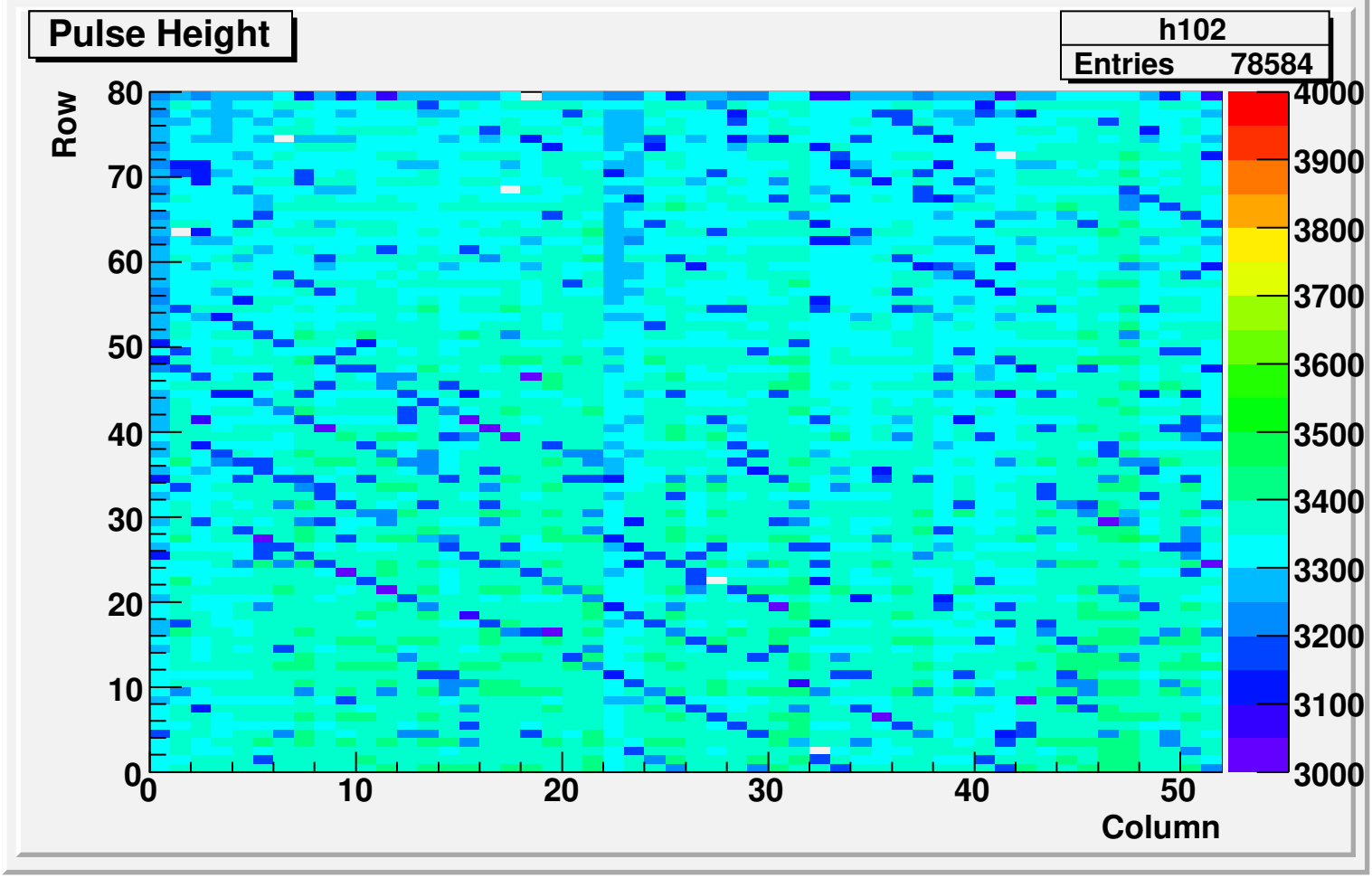
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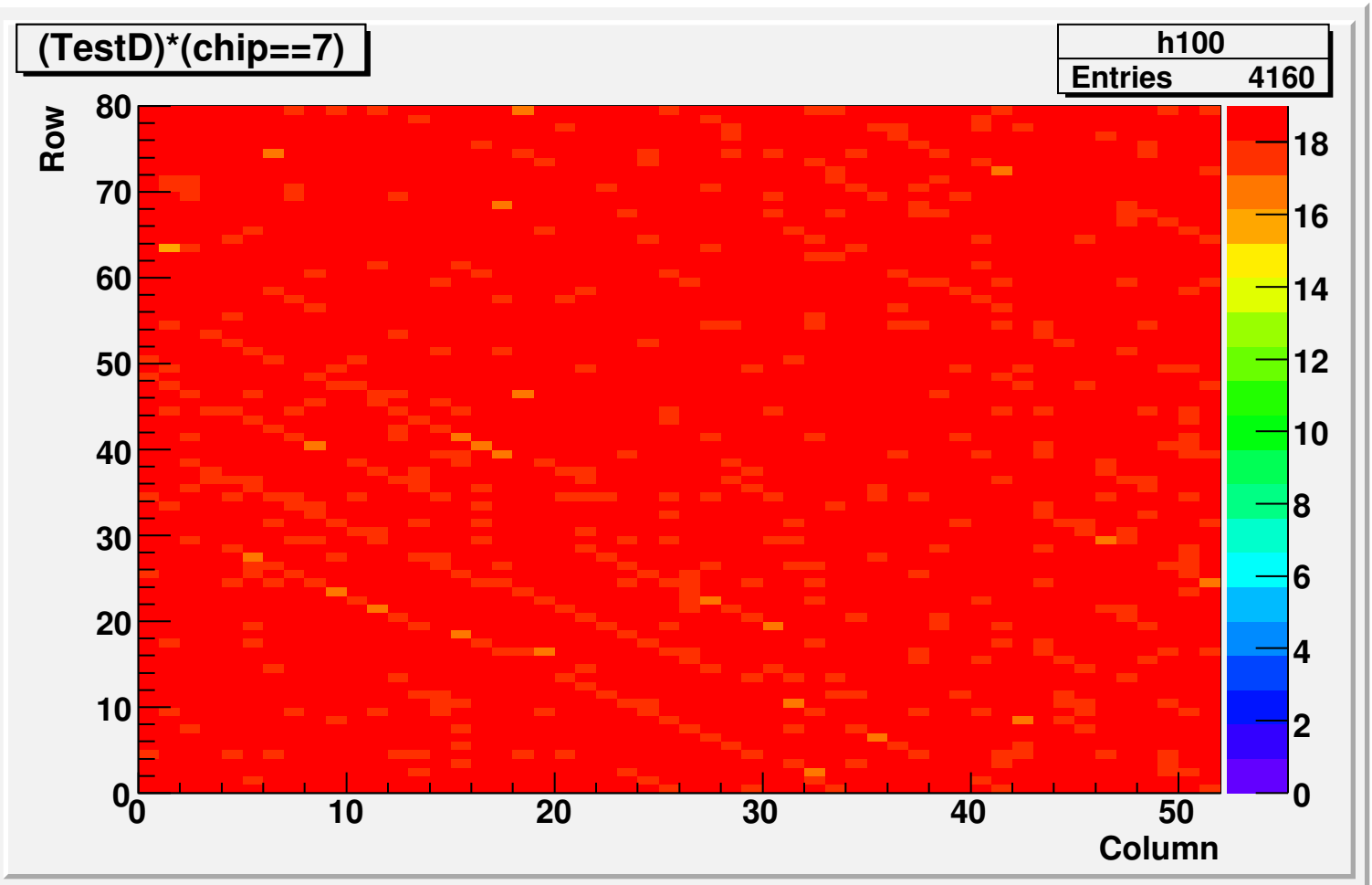
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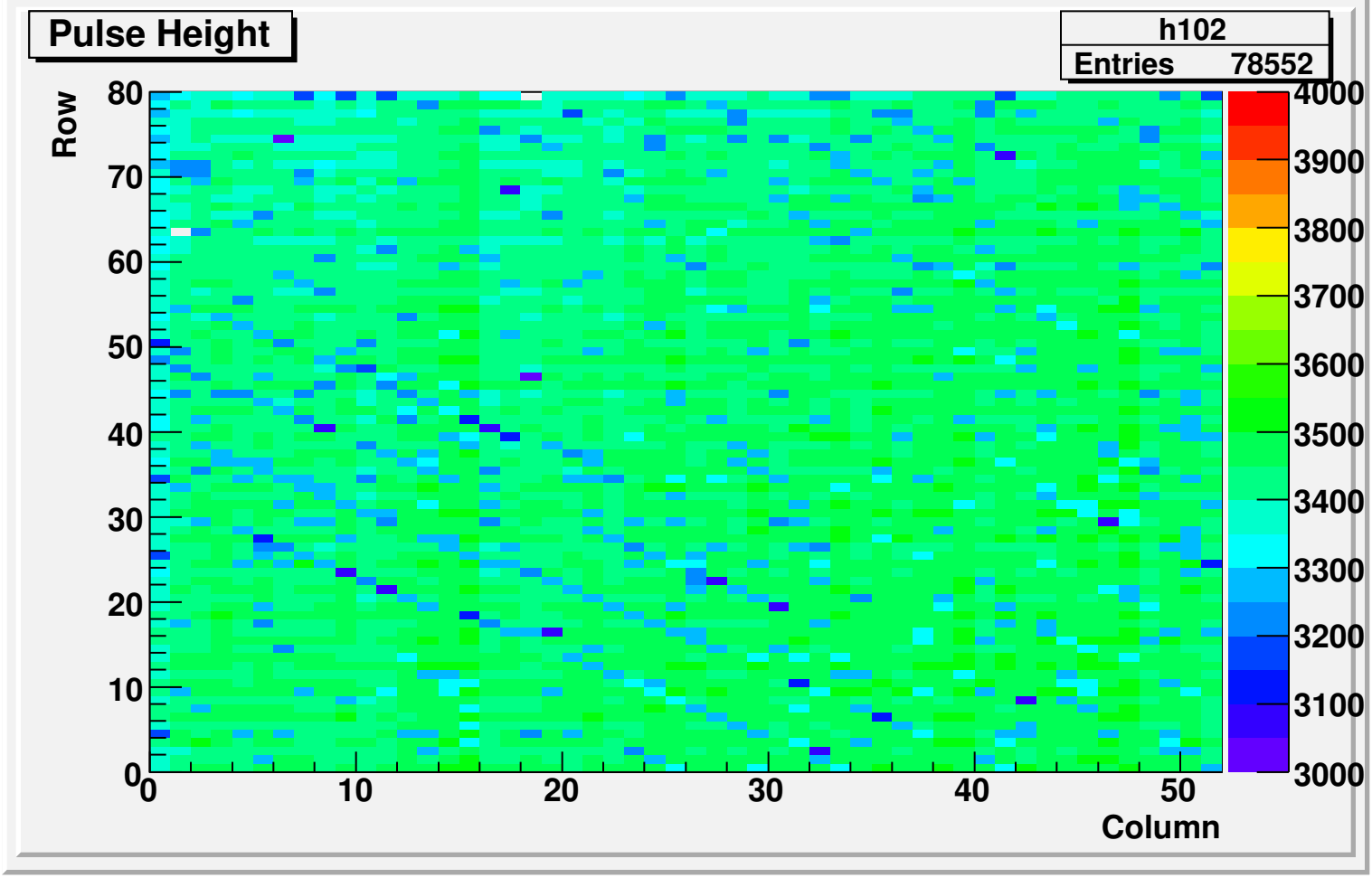
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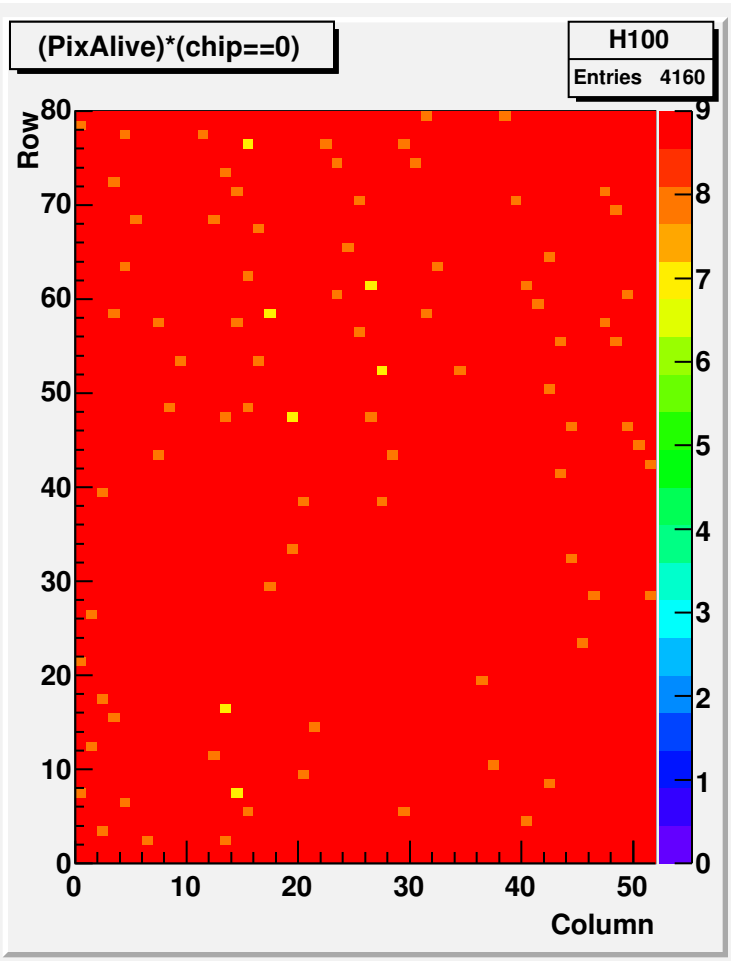
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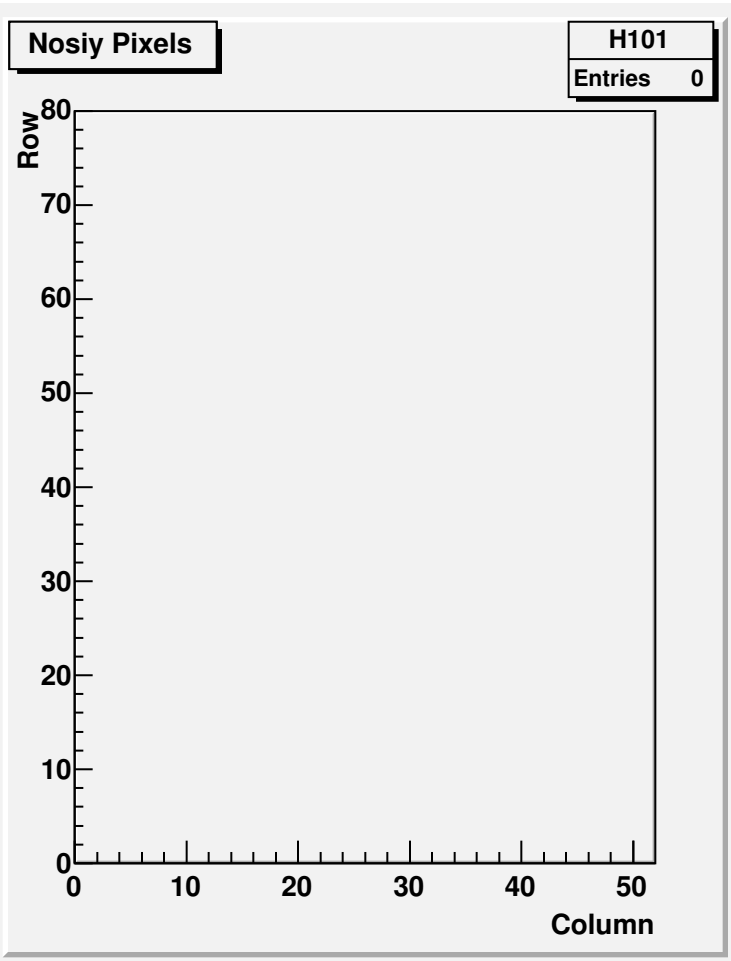
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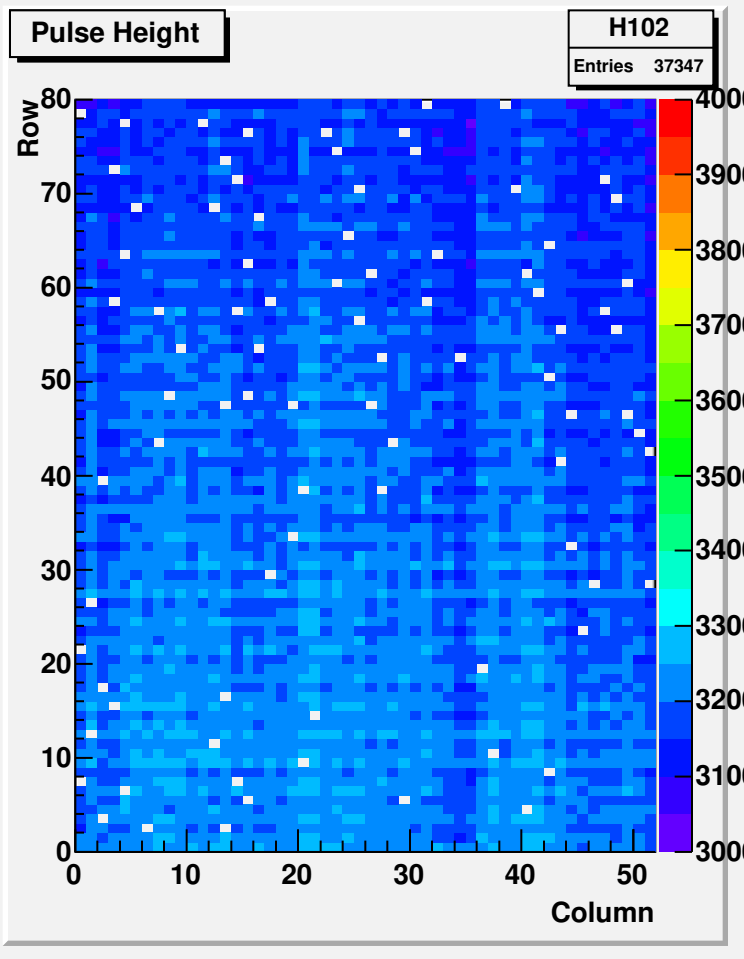
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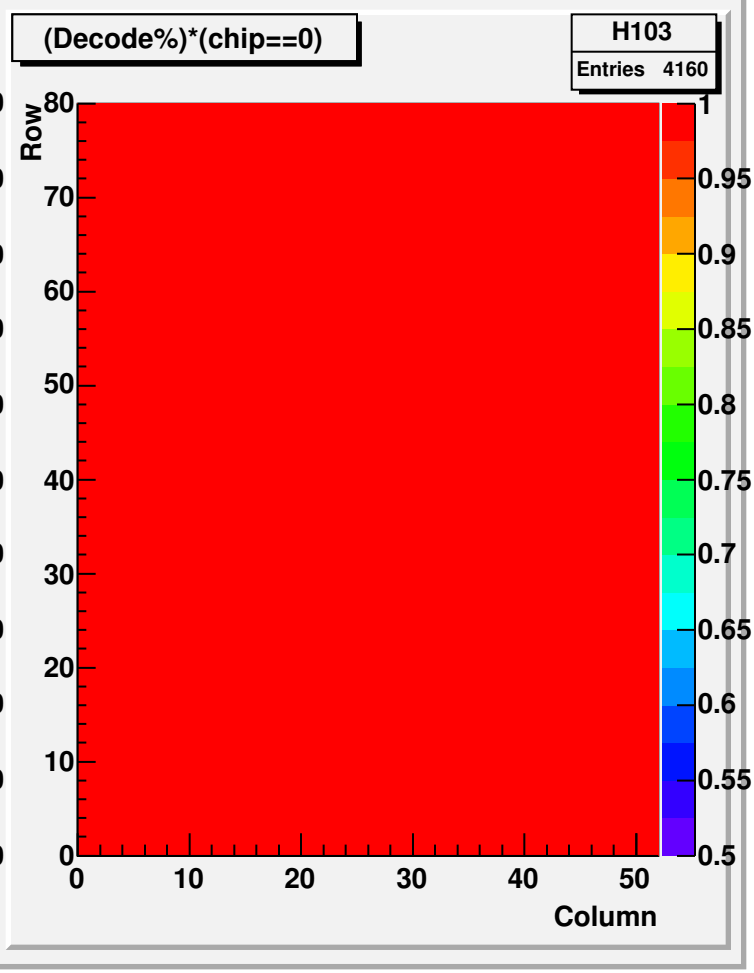
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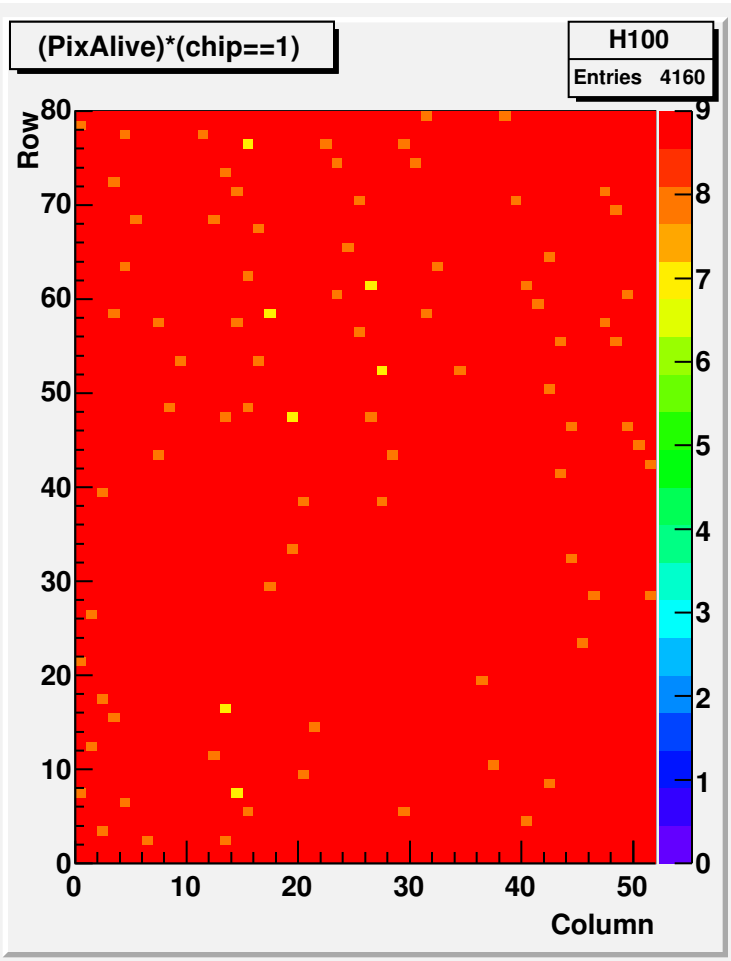
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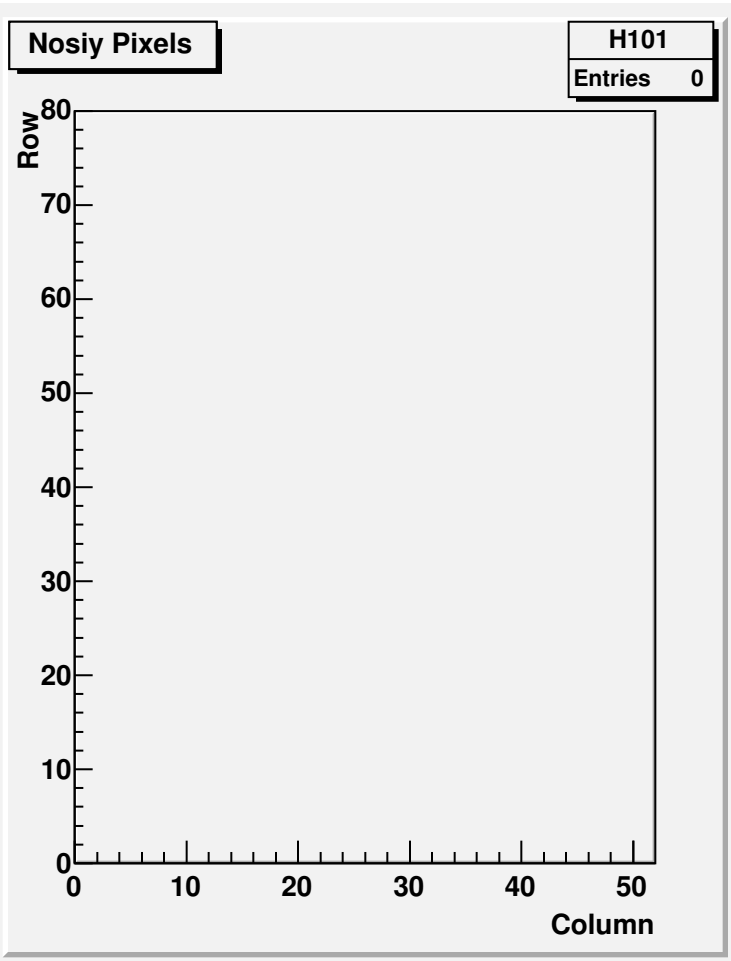
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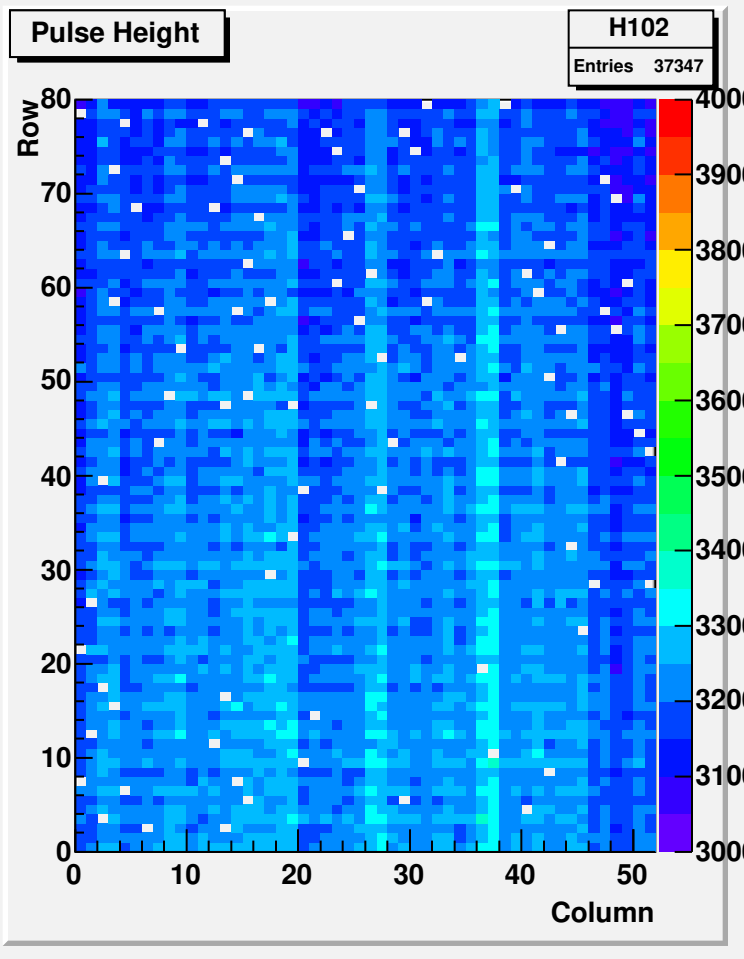
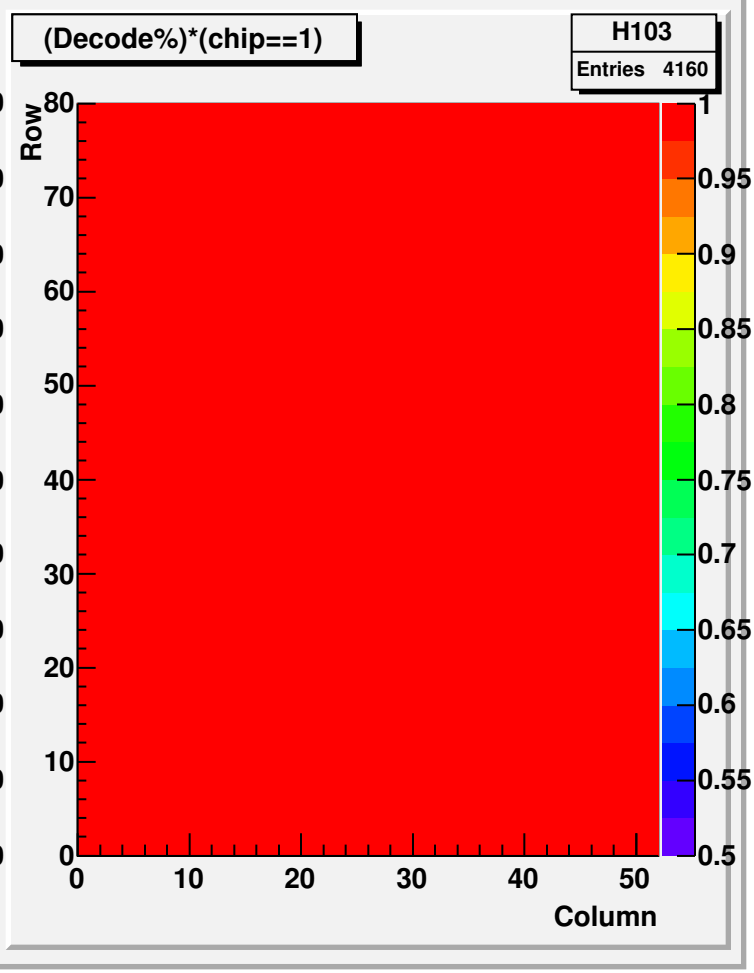
Row
Column


Pulse Height
H102
Entries 37347
Row
Column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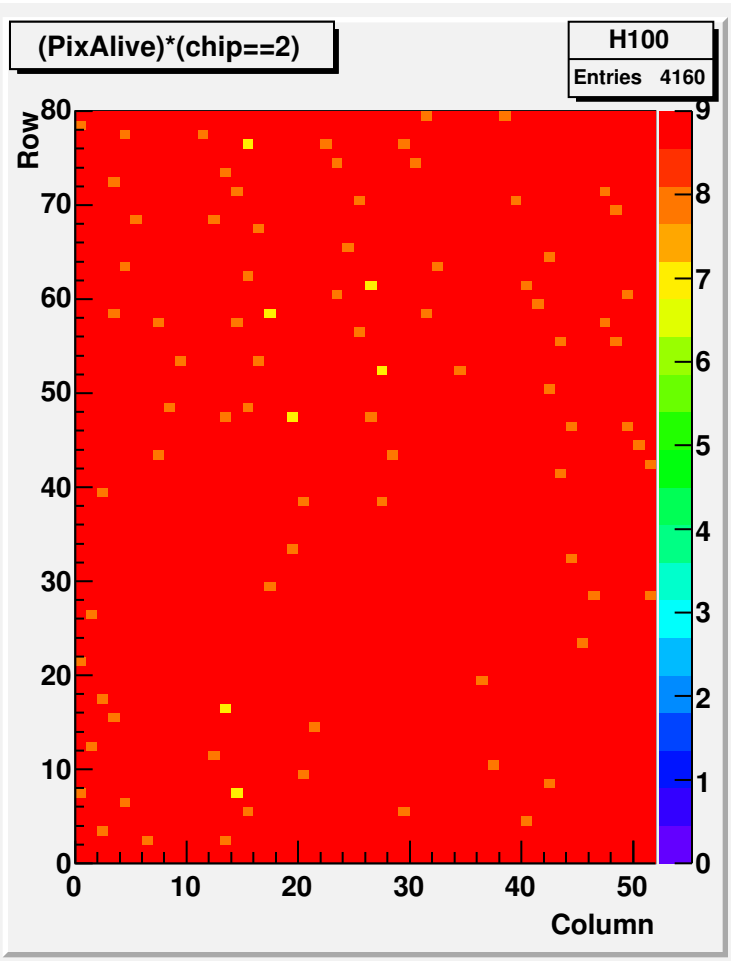
H103
Entries 4160
Row
Column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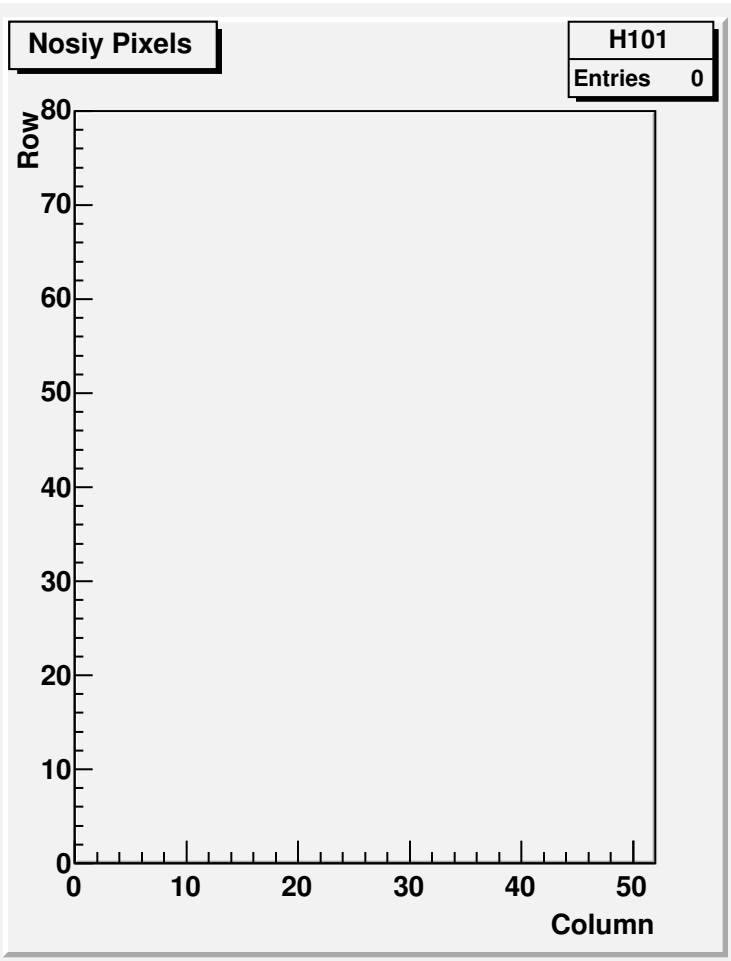
H100
Entries 4160
Row
Column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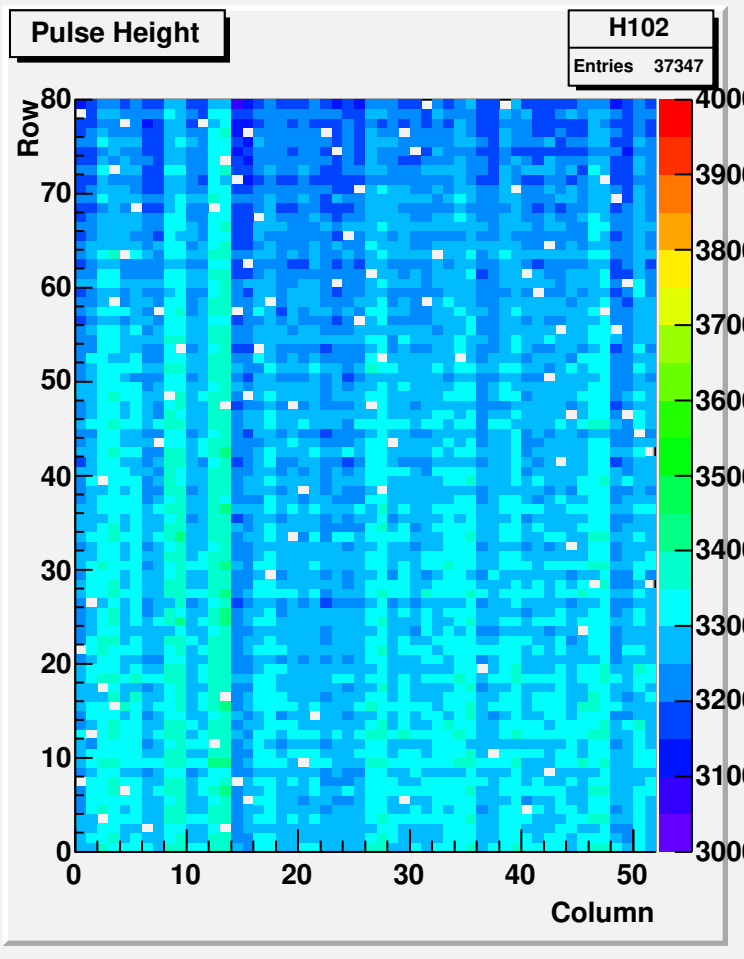
H101
Entries 0
Row
Column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H102
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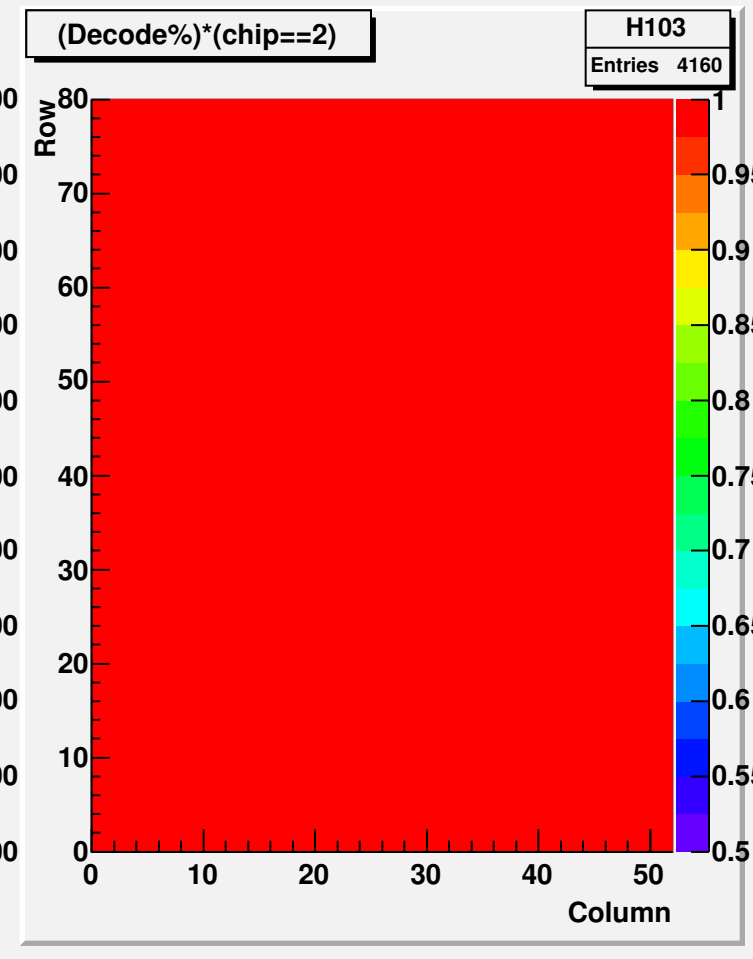
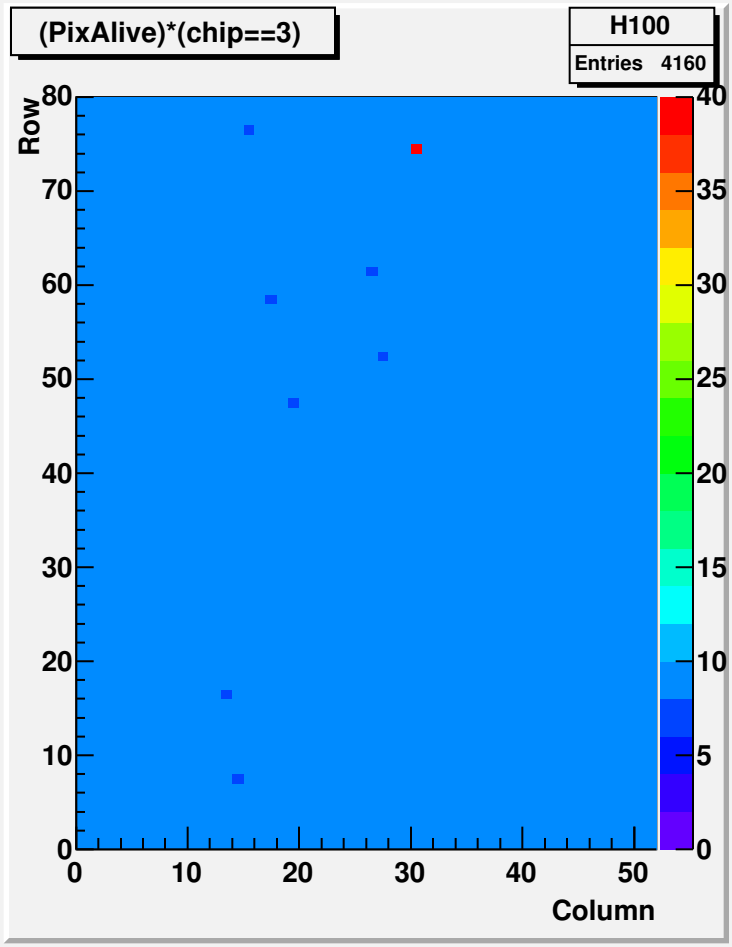

(Decode%)*(chip==2)
H103
Entries 4160
Row
Column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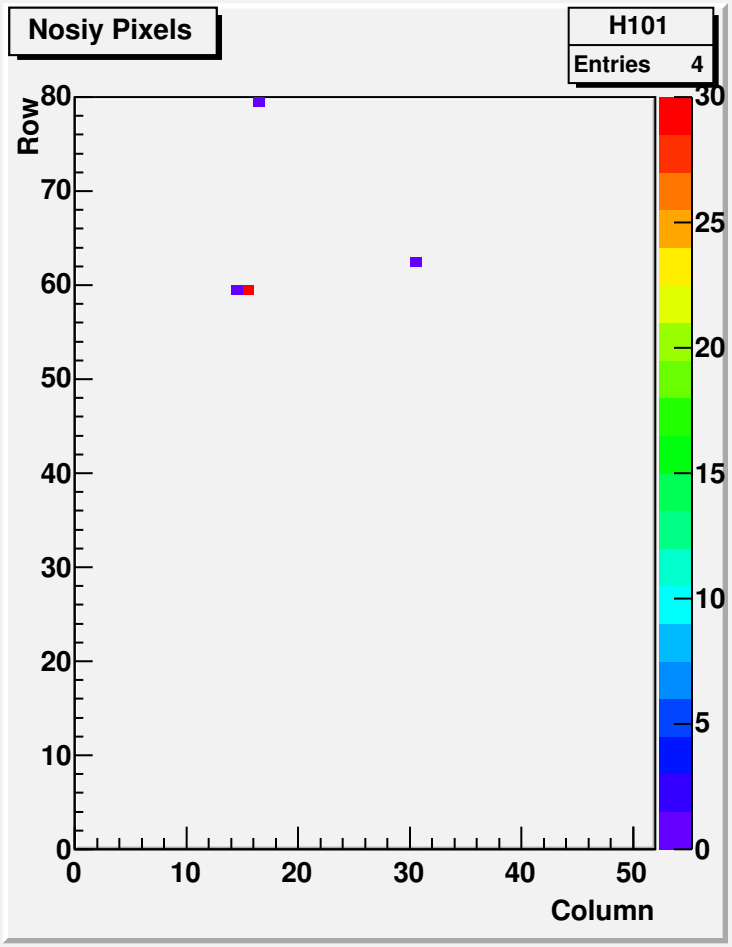
H100
Entries 4160
Row
Column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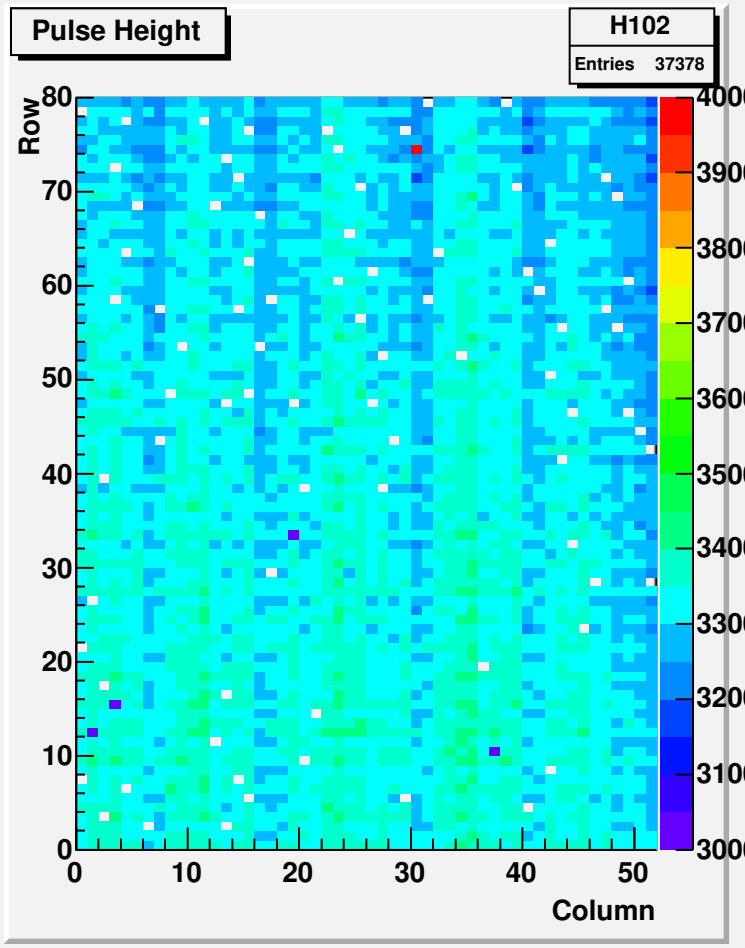
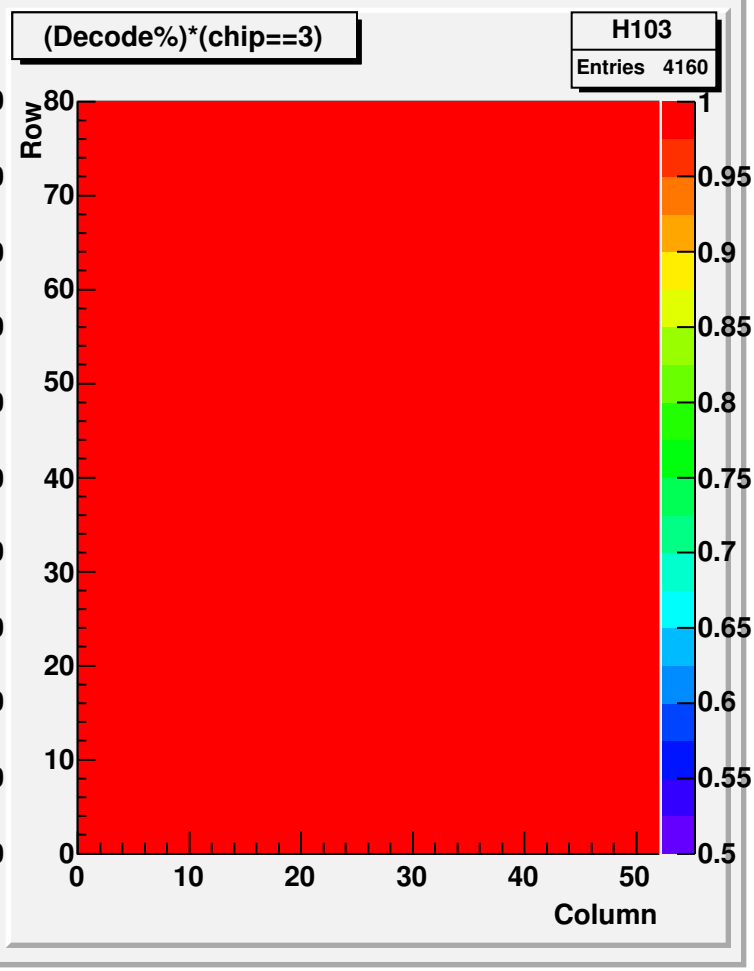
H101
Entries 4
Row
Column


Pulse Height
H102
Entries 37378
Row
Column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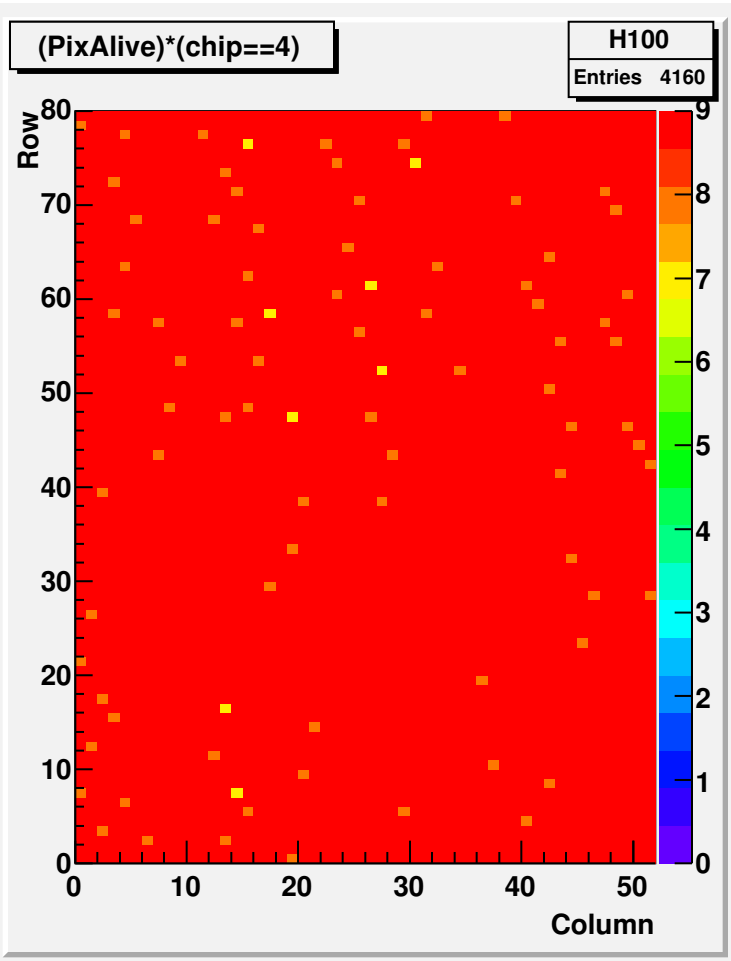
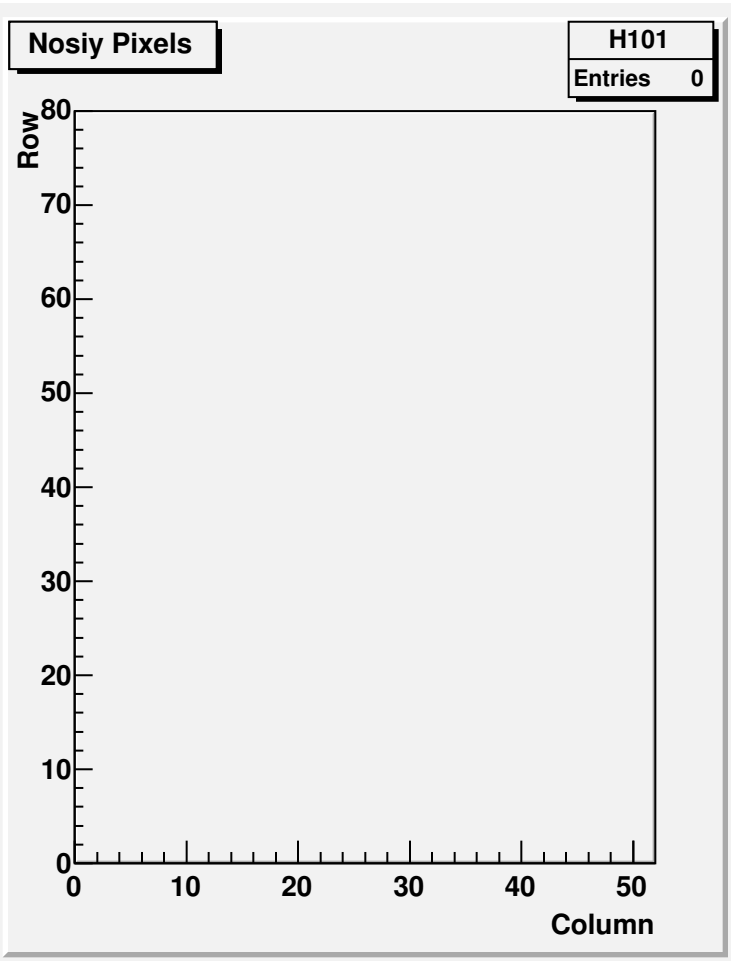
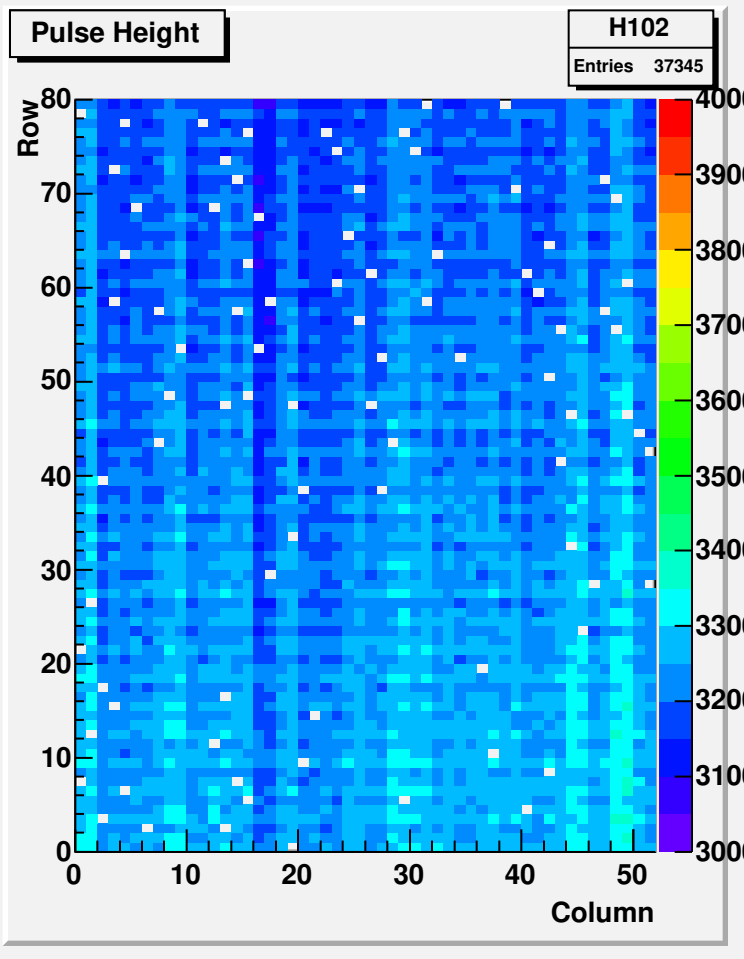
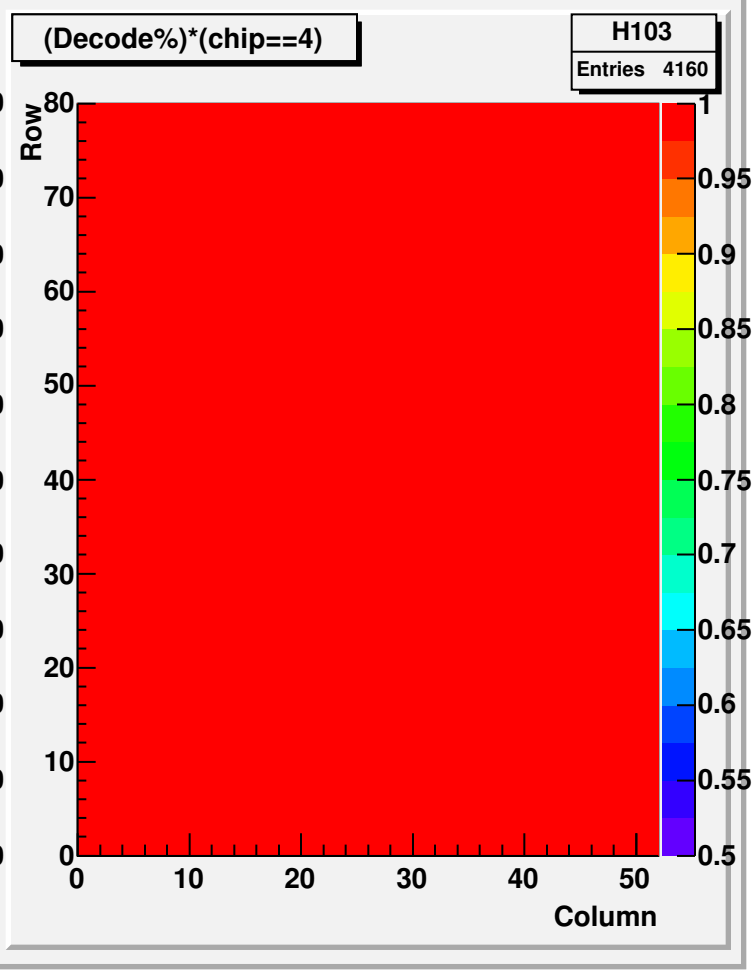
H103
Entries 4160
Row
Column


(PixAlive)*(chip==4)
H100
Entries 4160
Row
Column


Nosiy Pixels
H101
Entries 0
Row
Column


Pulse Height
H102
Entries 37345
Row
Column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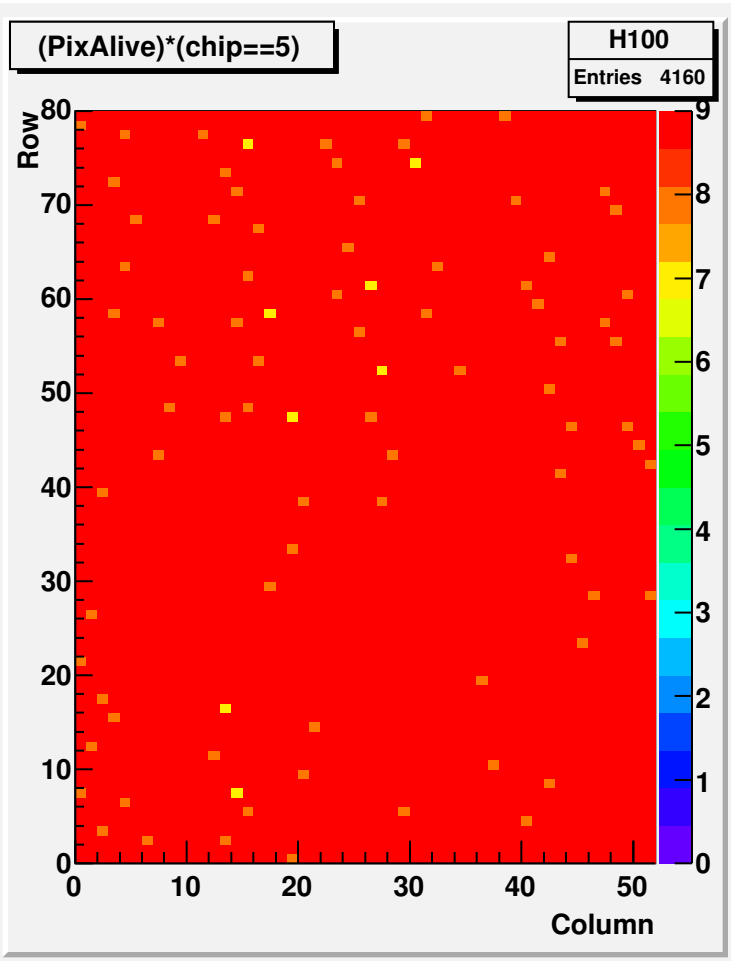
H103
Entries 4160
Row
Column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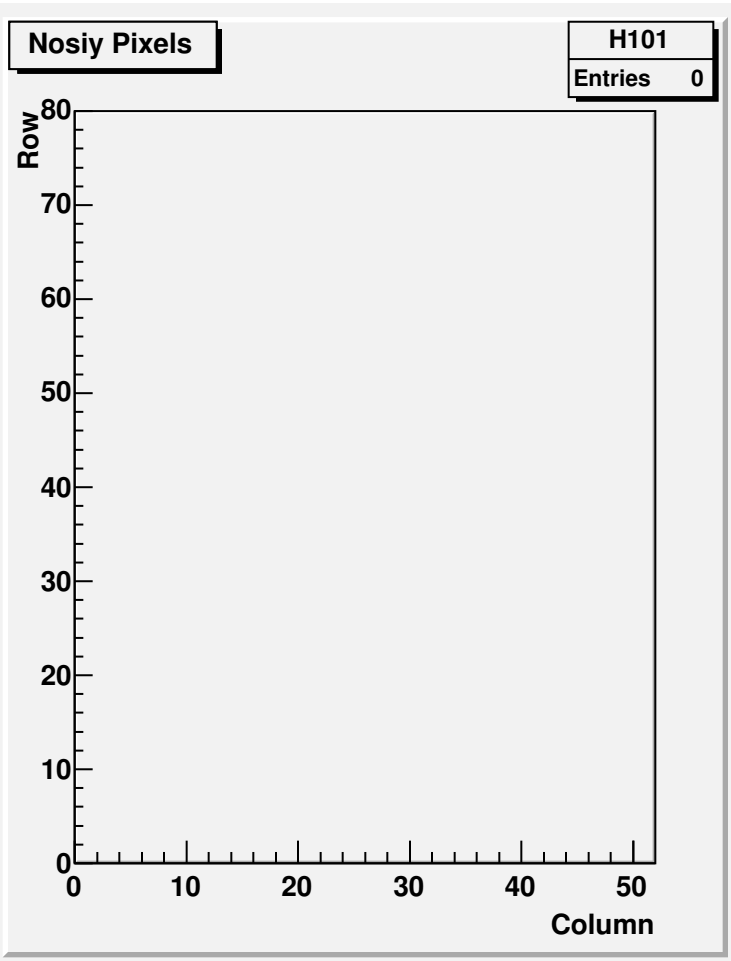
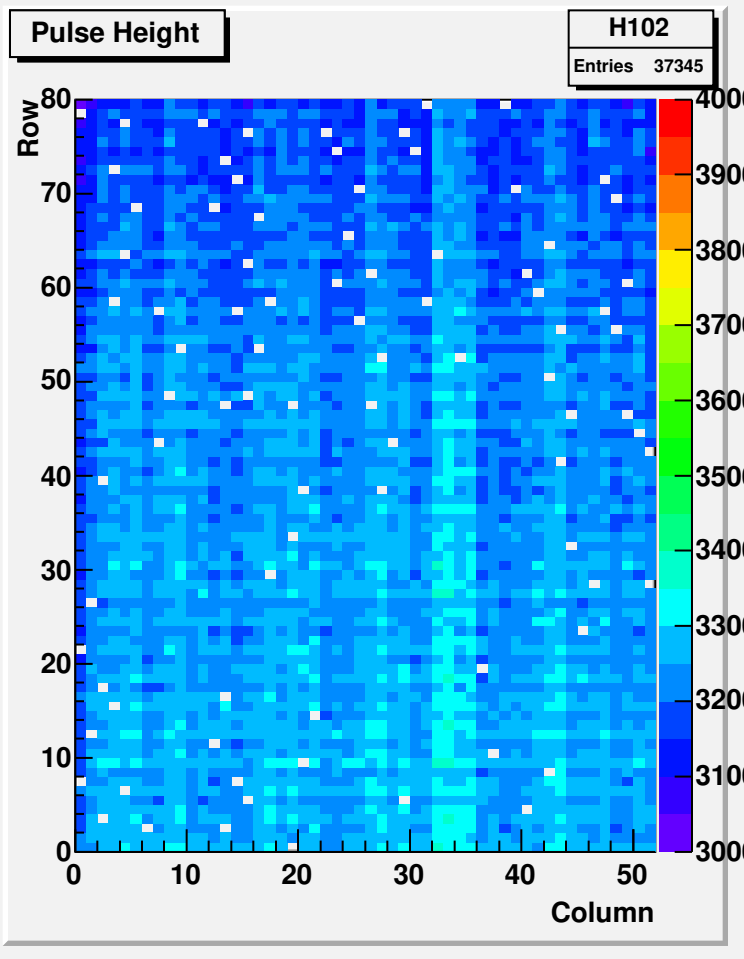
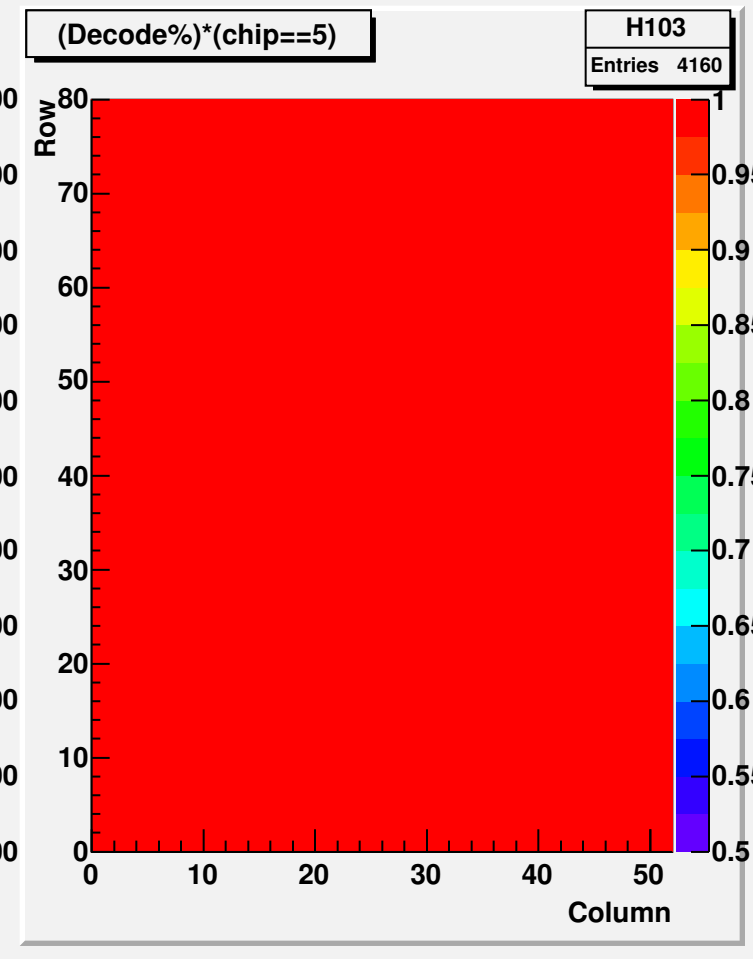
H100
Entries 4160
Row
Column


Nosiy Pixels
H101
Entries 0
Row
Column


Pulse Height
H102
Entries 37345
Row
Column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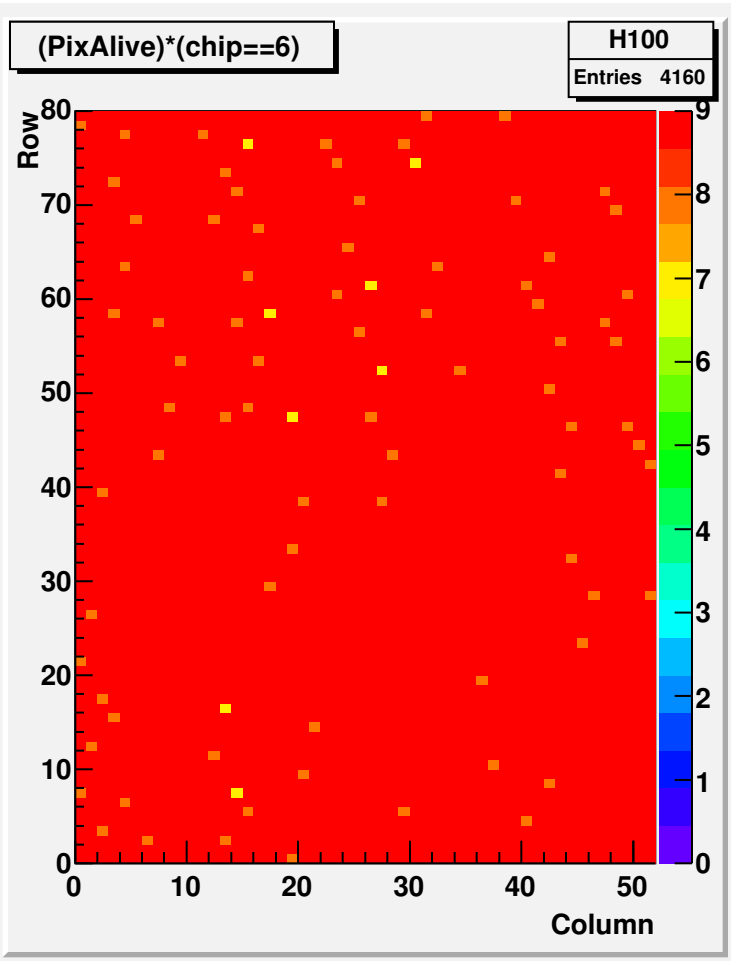
H103
Entries 4160
Row
Column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H100
Entries 4160
Row
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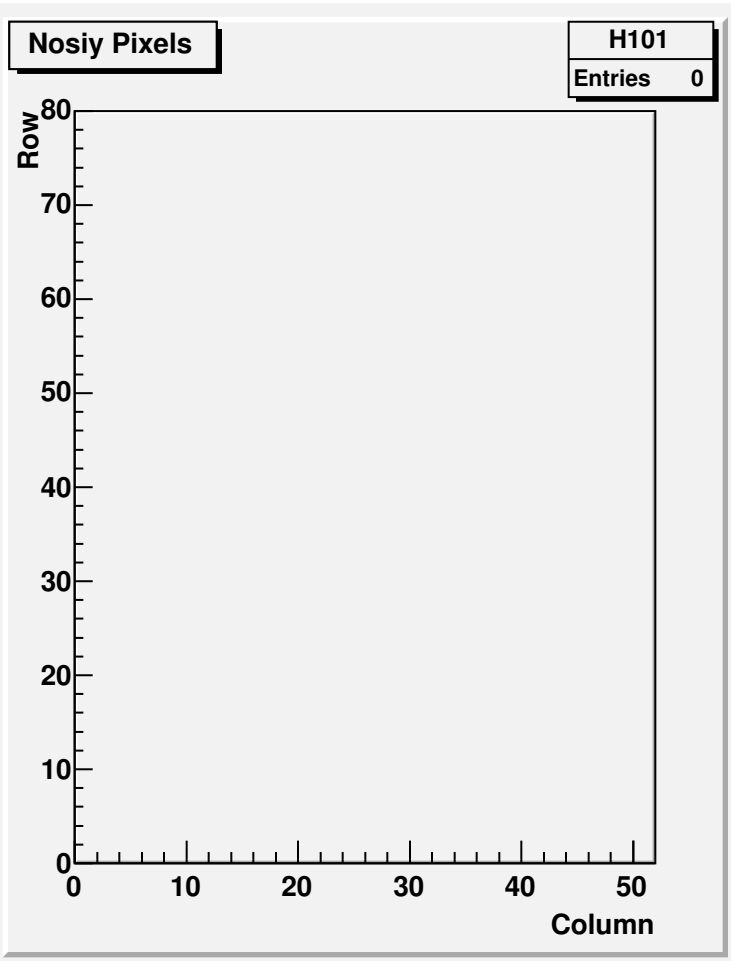
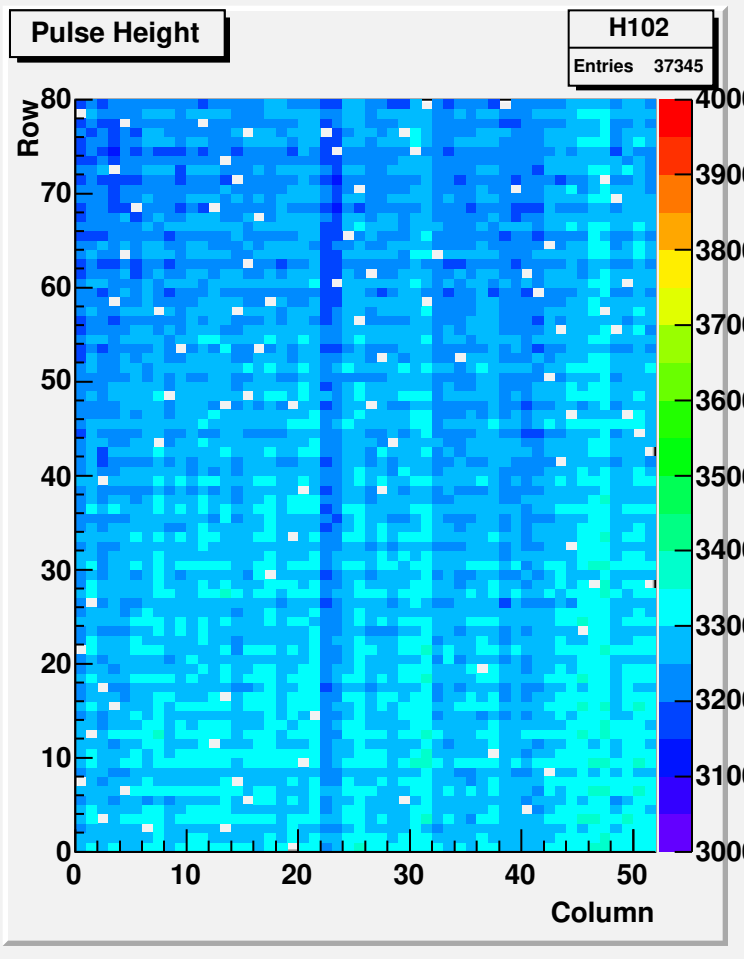
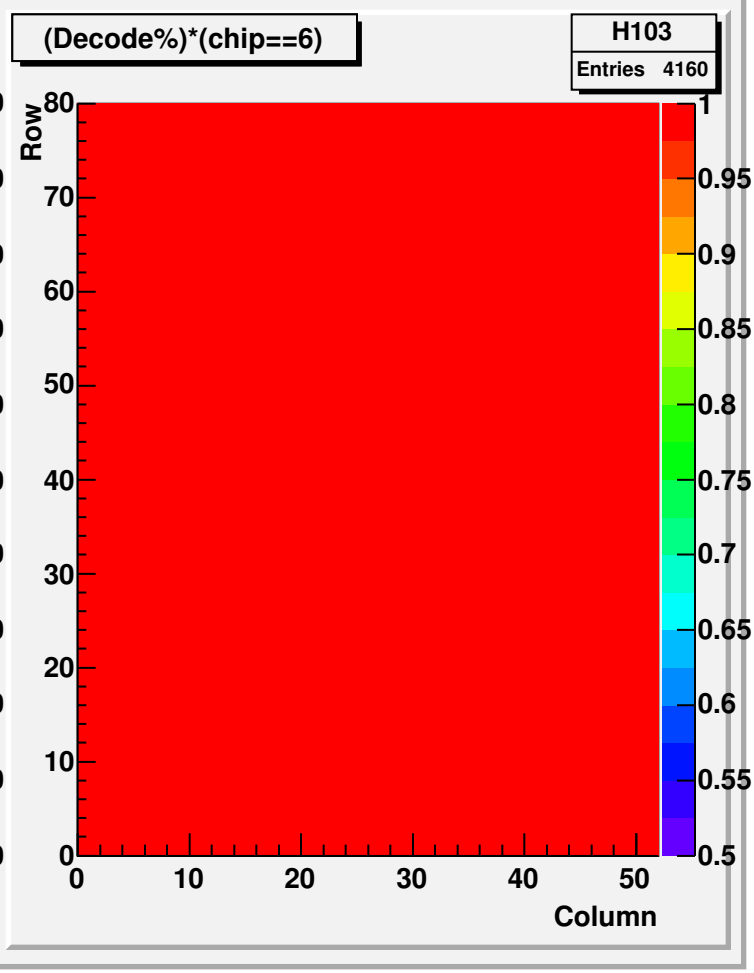

Nosiy Pixels
H101
Entries 0
Row
Column


Pulse Height
H102
Entries 37345
Row
Column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H103
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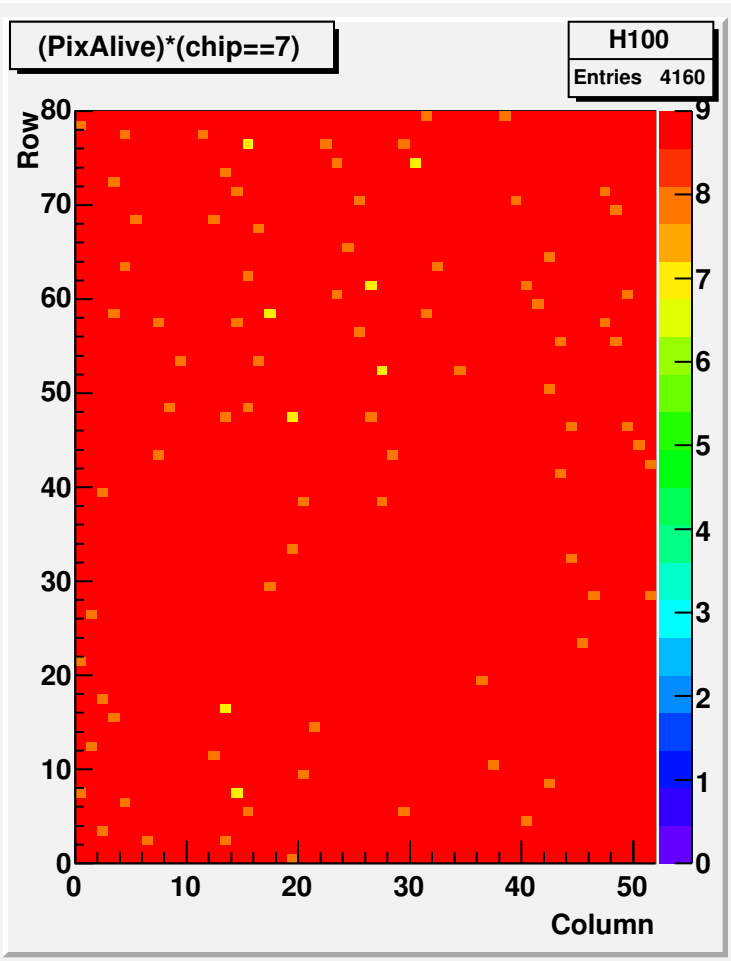
Row
Column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H100
Entries 4160
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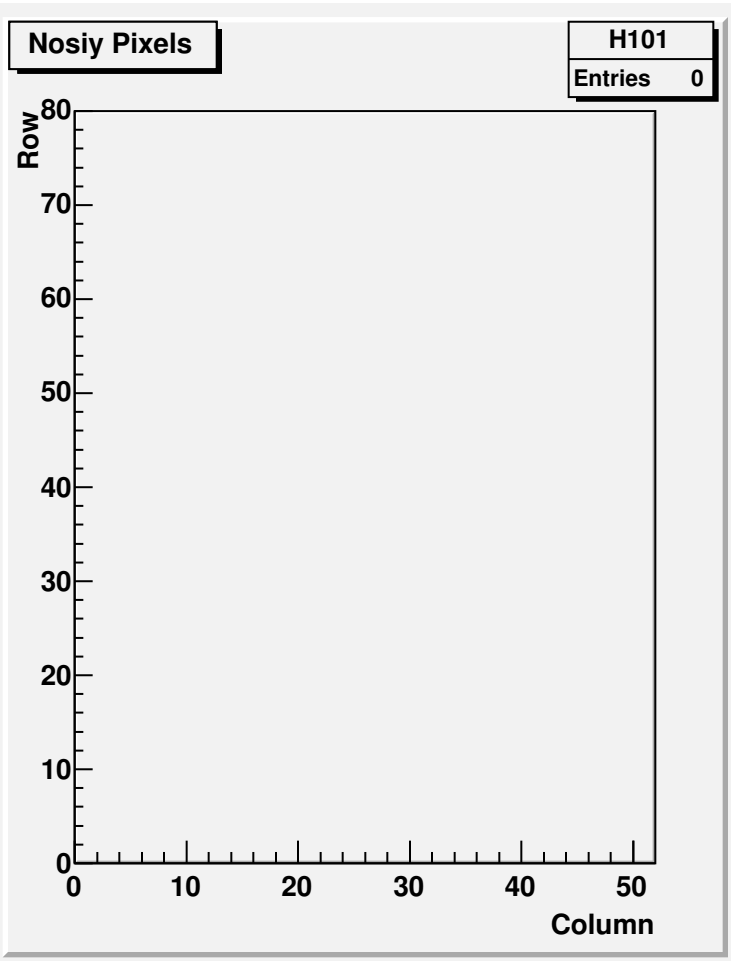
Nosiy Pixels
H101
Entries 0
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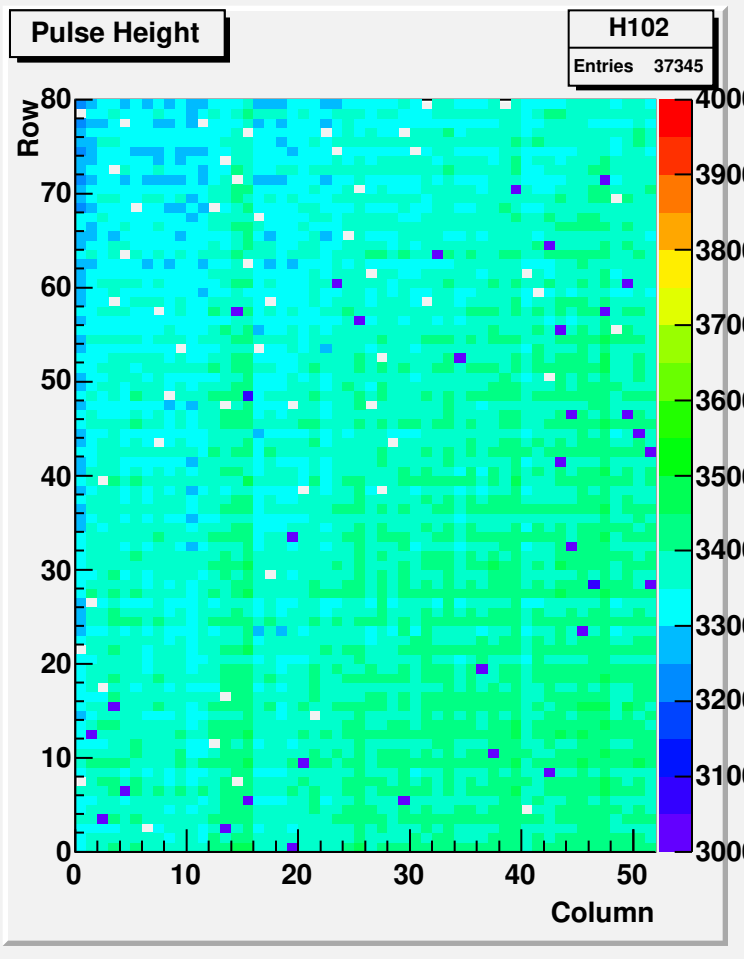
Column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H102
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Row
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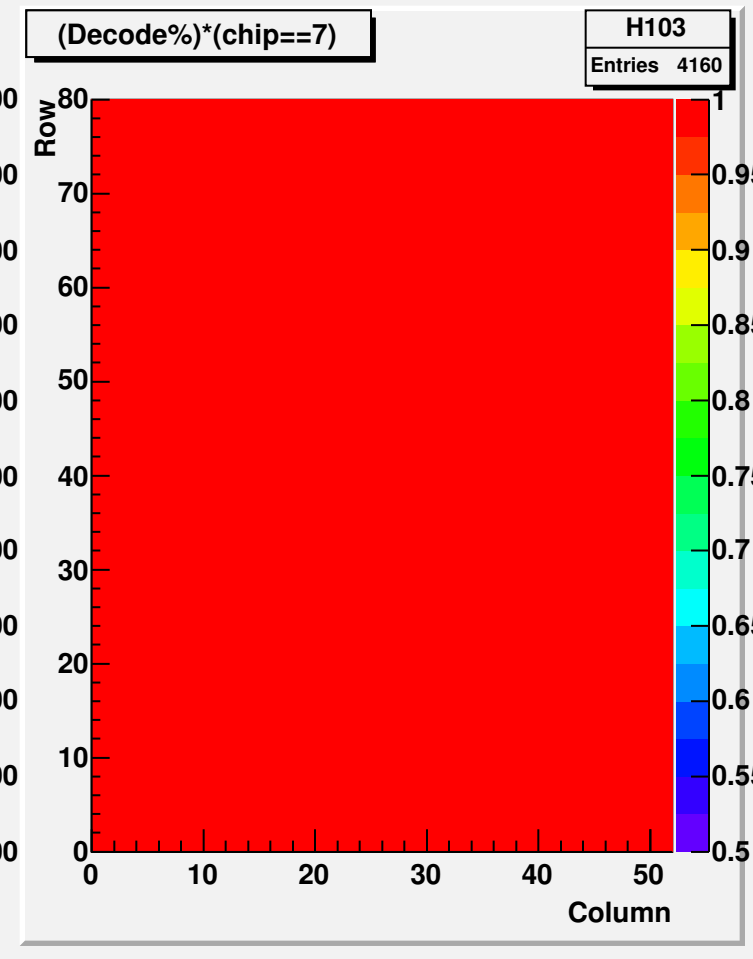
(Decode%)*(chip==7)
H103
Entries 4160
Row
Column