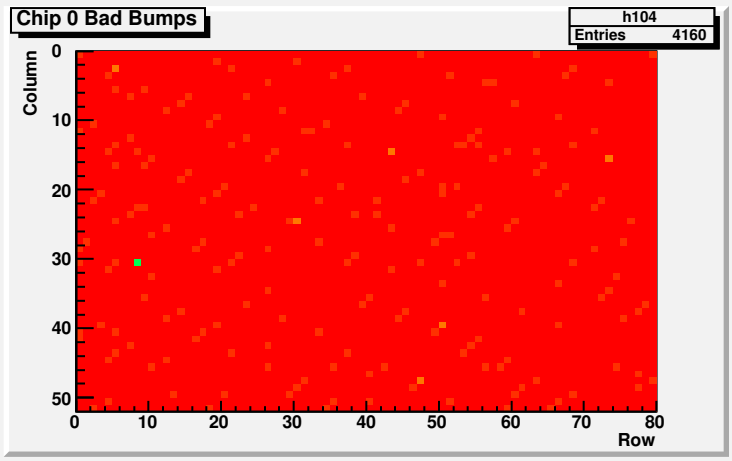
Chip 0 Bad Bumps
h104
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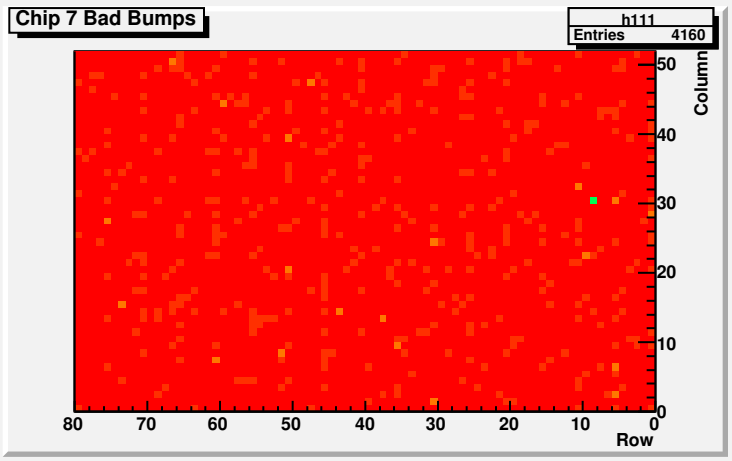
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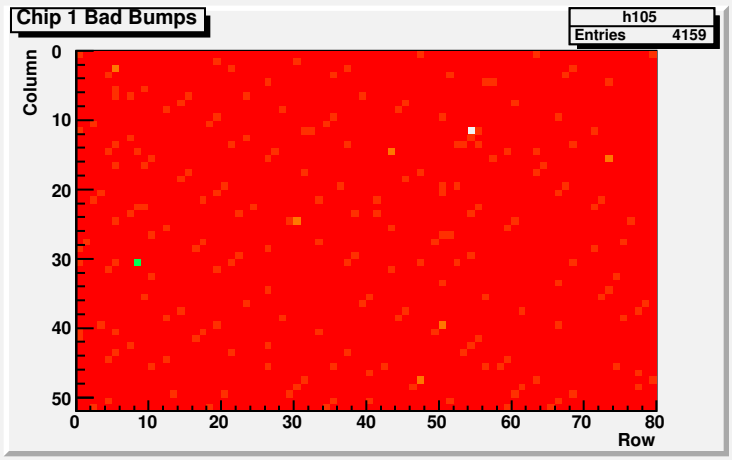
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Entries 4159
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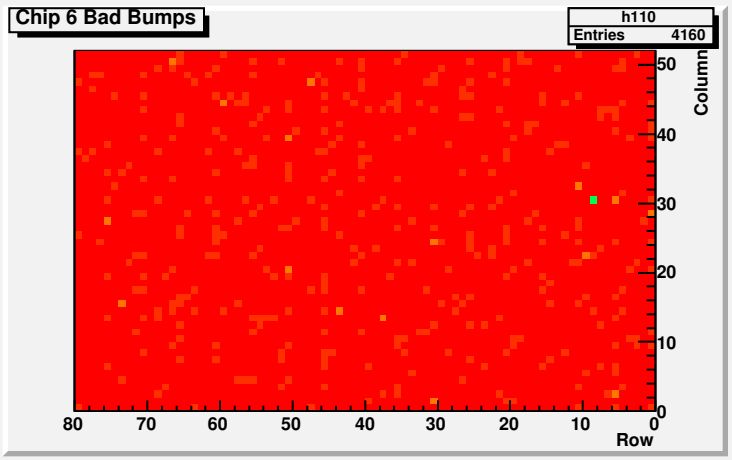
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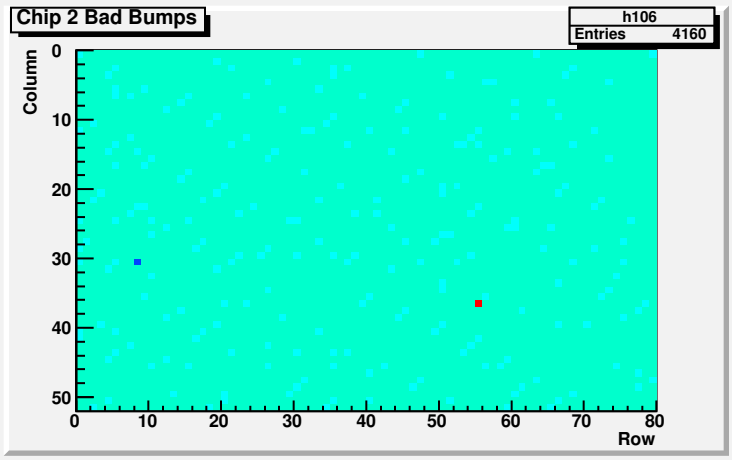
Chip 2 Bad Bumps
h106
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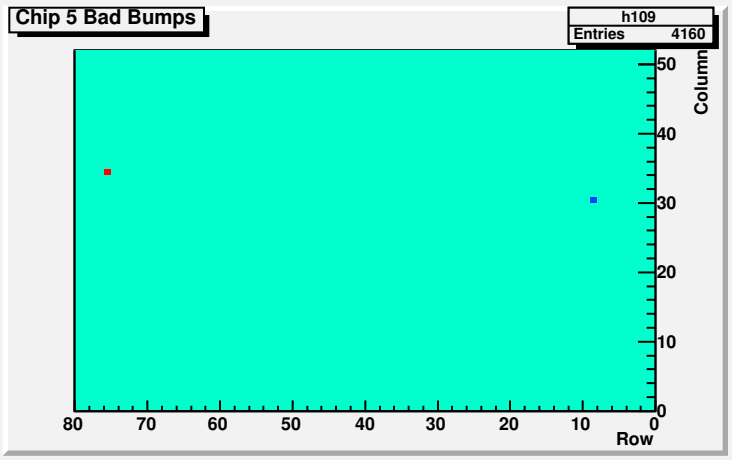
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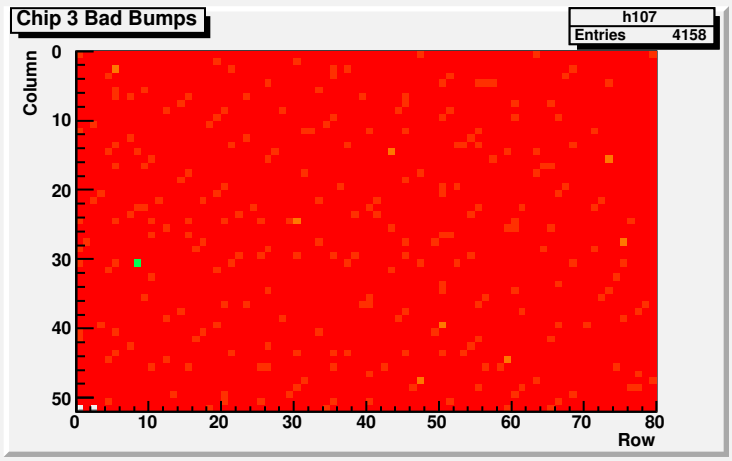
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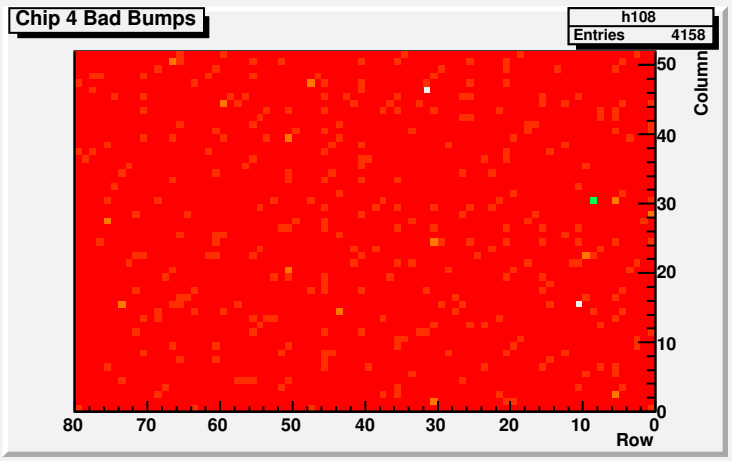
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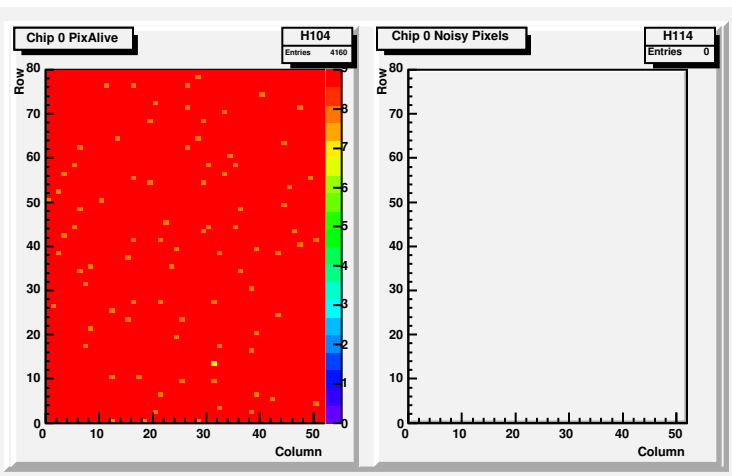
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Entries 4160
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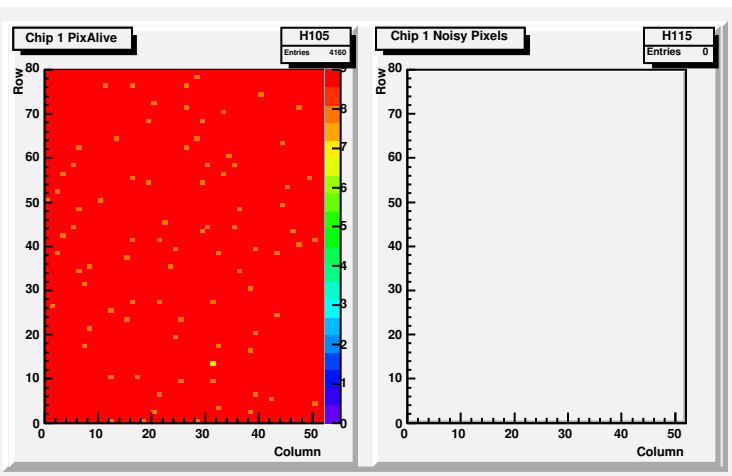
Chip 0 Noisy Pixels
H114
Entries 0
Row
Column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Entries 4160
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Column

Chip 1 Noisy Pixels
H115
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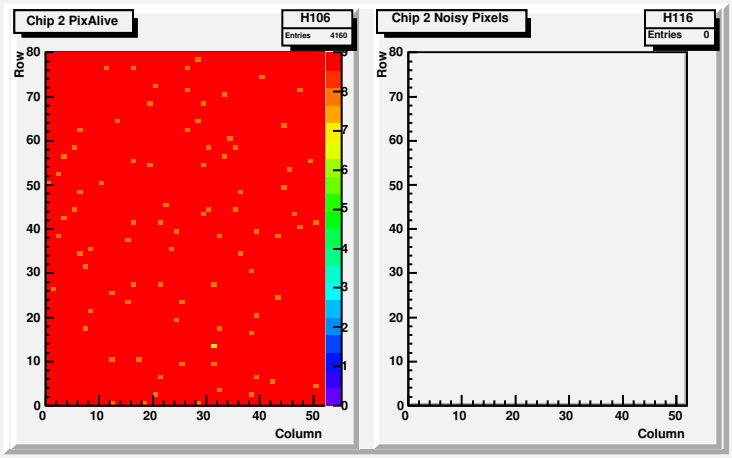
Row
Column


Chip 2 PixAlive
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Entries 4160
Row
Column

Chip 2 Noisy Pixels
H116
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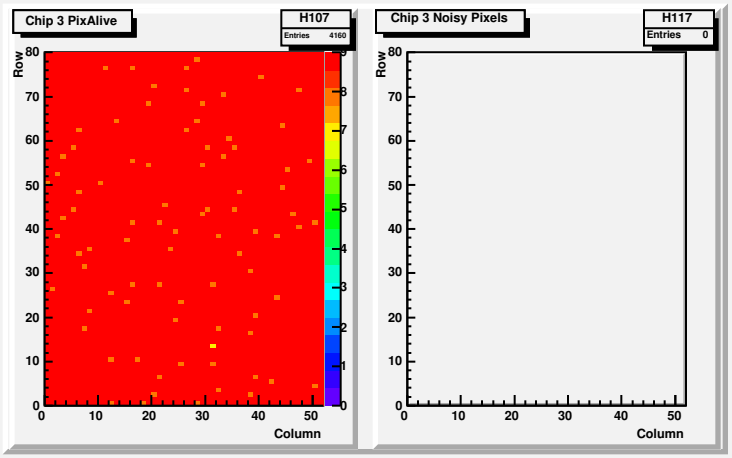
Row
Column


Chip 3 PixAlive
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Entries 4160
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Column

Chip 3 Noisy Pixels
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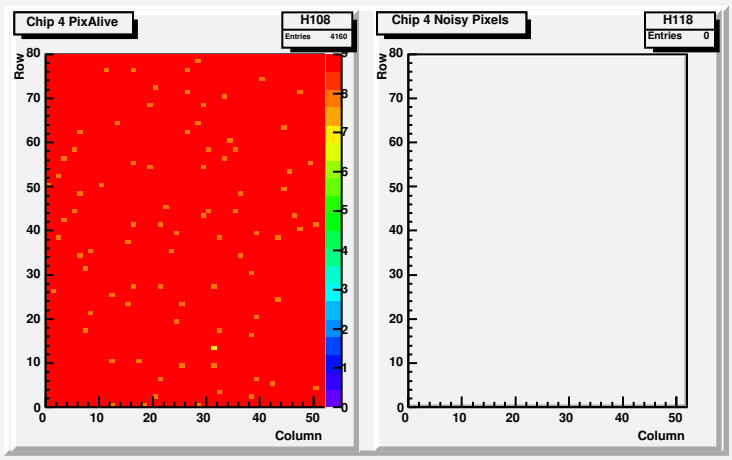
Column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Entries 4160
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Chip 4 Noisy Pixels
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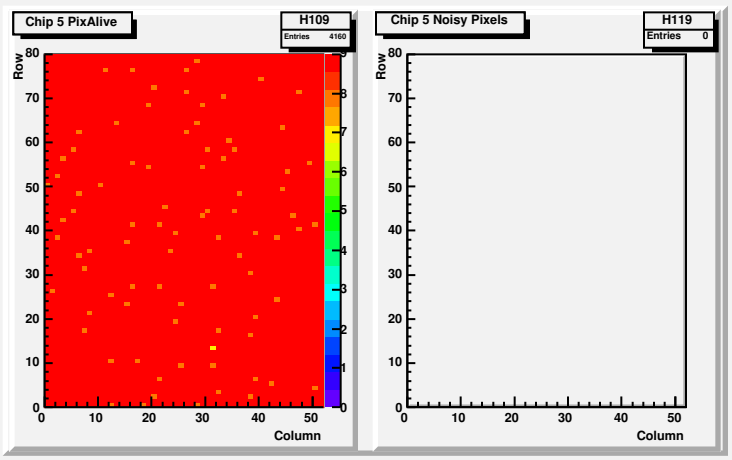
Column


Chip 5 PixAlive
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Entries 4160
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Chip 5 Noisy Pixels
H119
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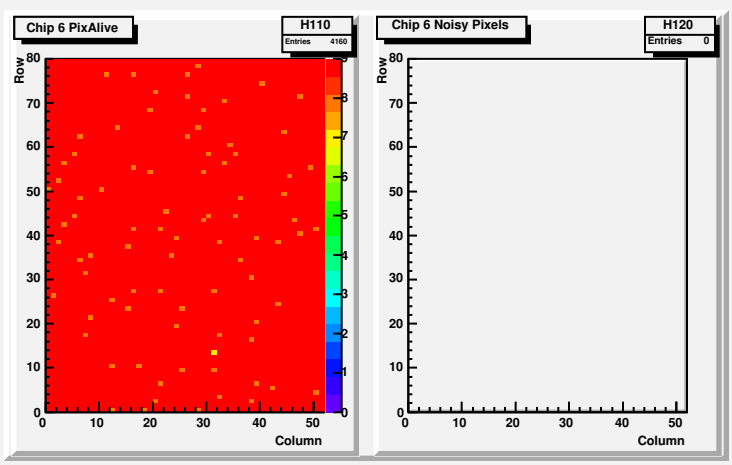
Row
Column


Chip 6 PixAlive
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Entries 4160
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Column

Chip 6 Noisy Pixels
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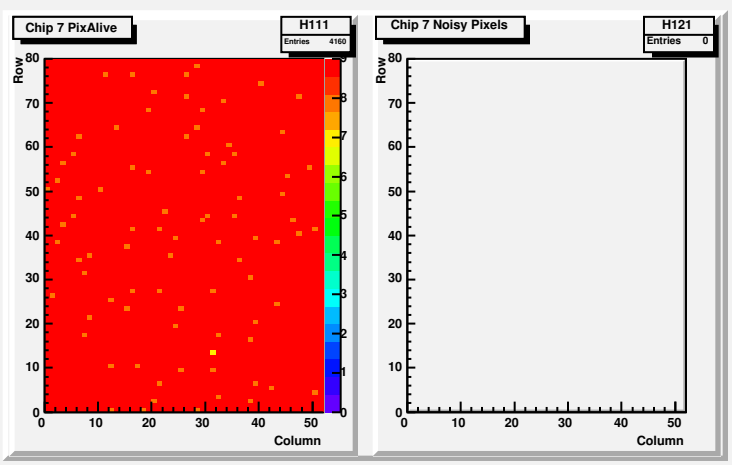
Entries 0
Row
Column


Chip 7 PixAlive
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Entries 4160
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Column

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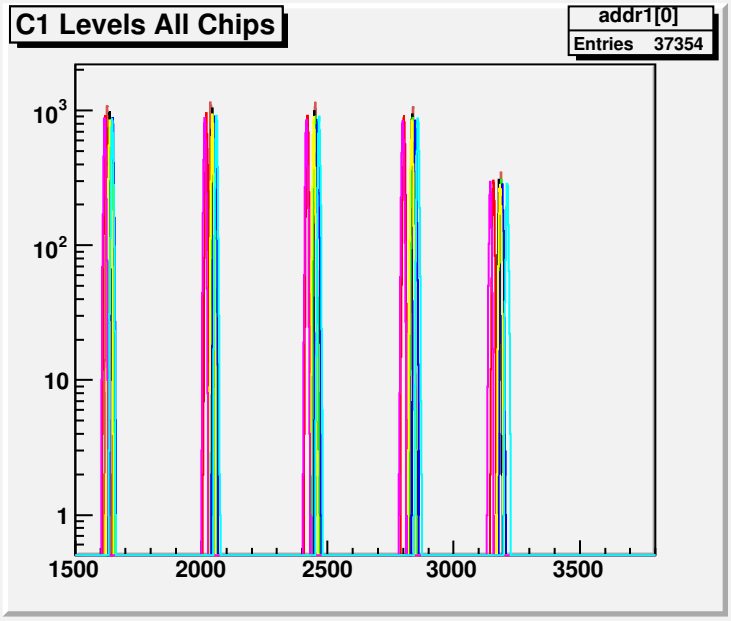
Entries 0
Row
Column


C1 Levels All Chips
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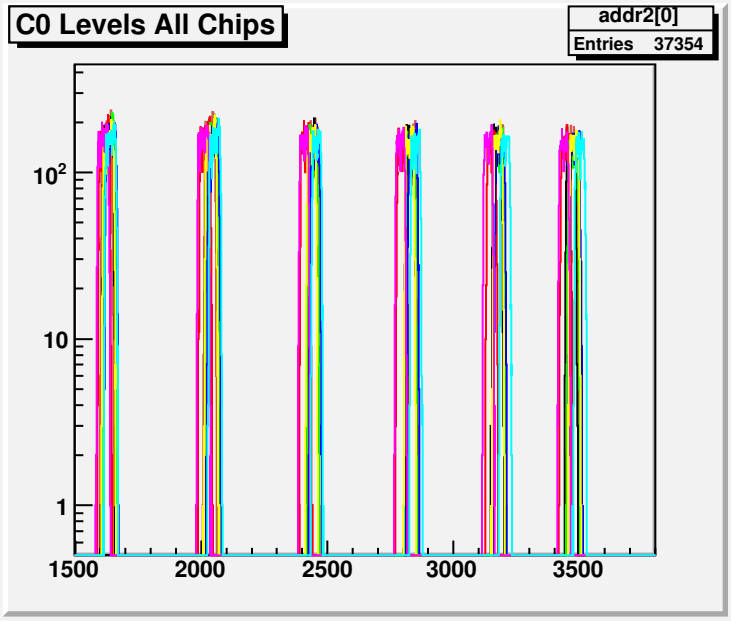
Entries 37354
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37354
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1
1500
2000
2500
3000
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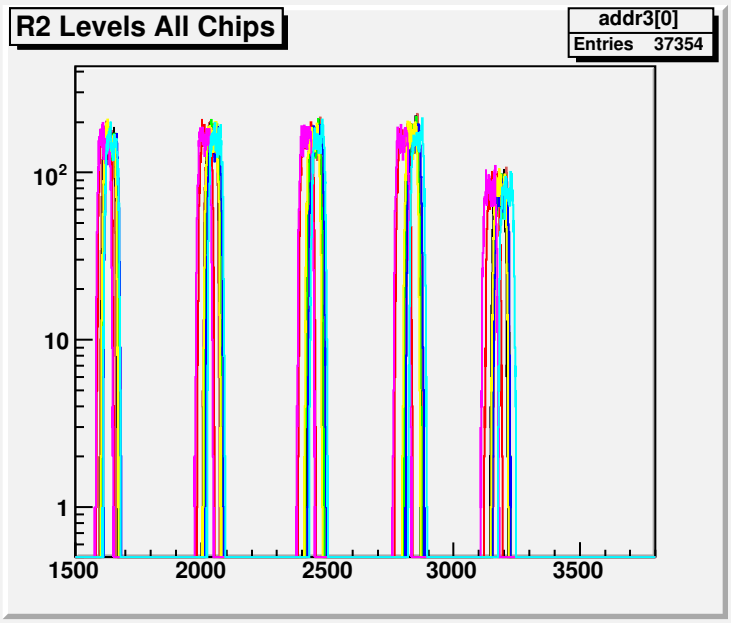
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Entries 37354
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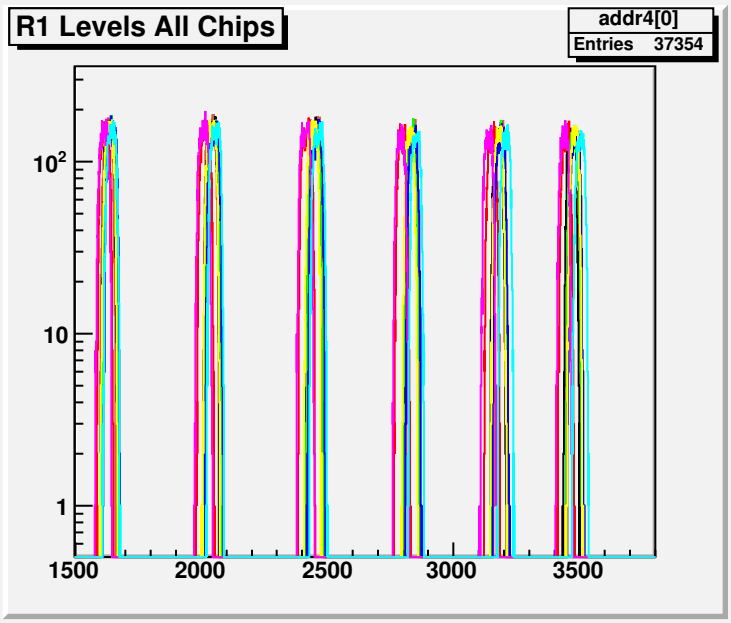
1
1500
2000
2500
3000
3500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addr4[0]
Entries 37354
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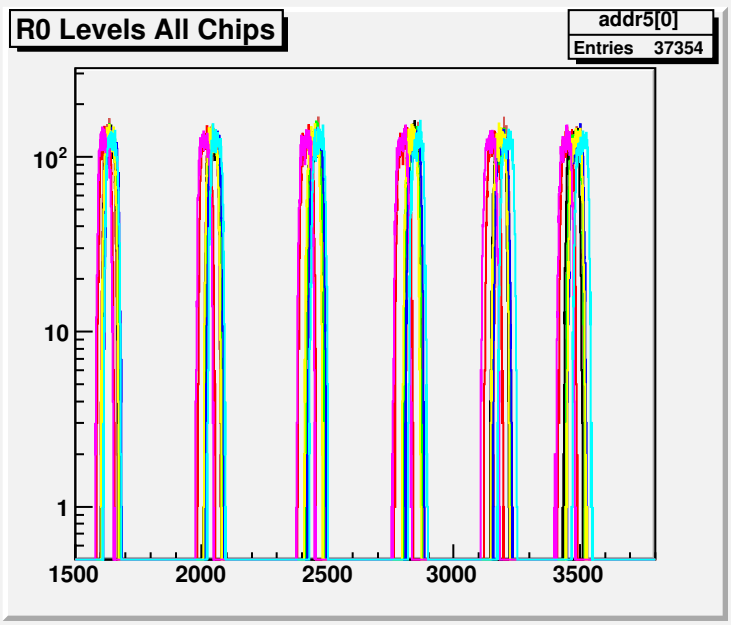
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addr5[0]
Entries 37354
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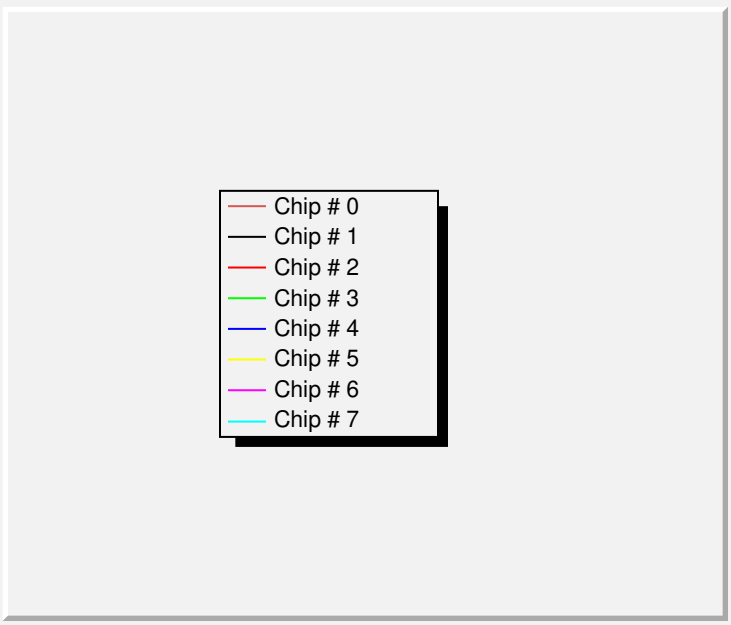
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Chip # 1
Chip # 2
Chip # 3
Chip # 4
Chip # 5
Chip # 6
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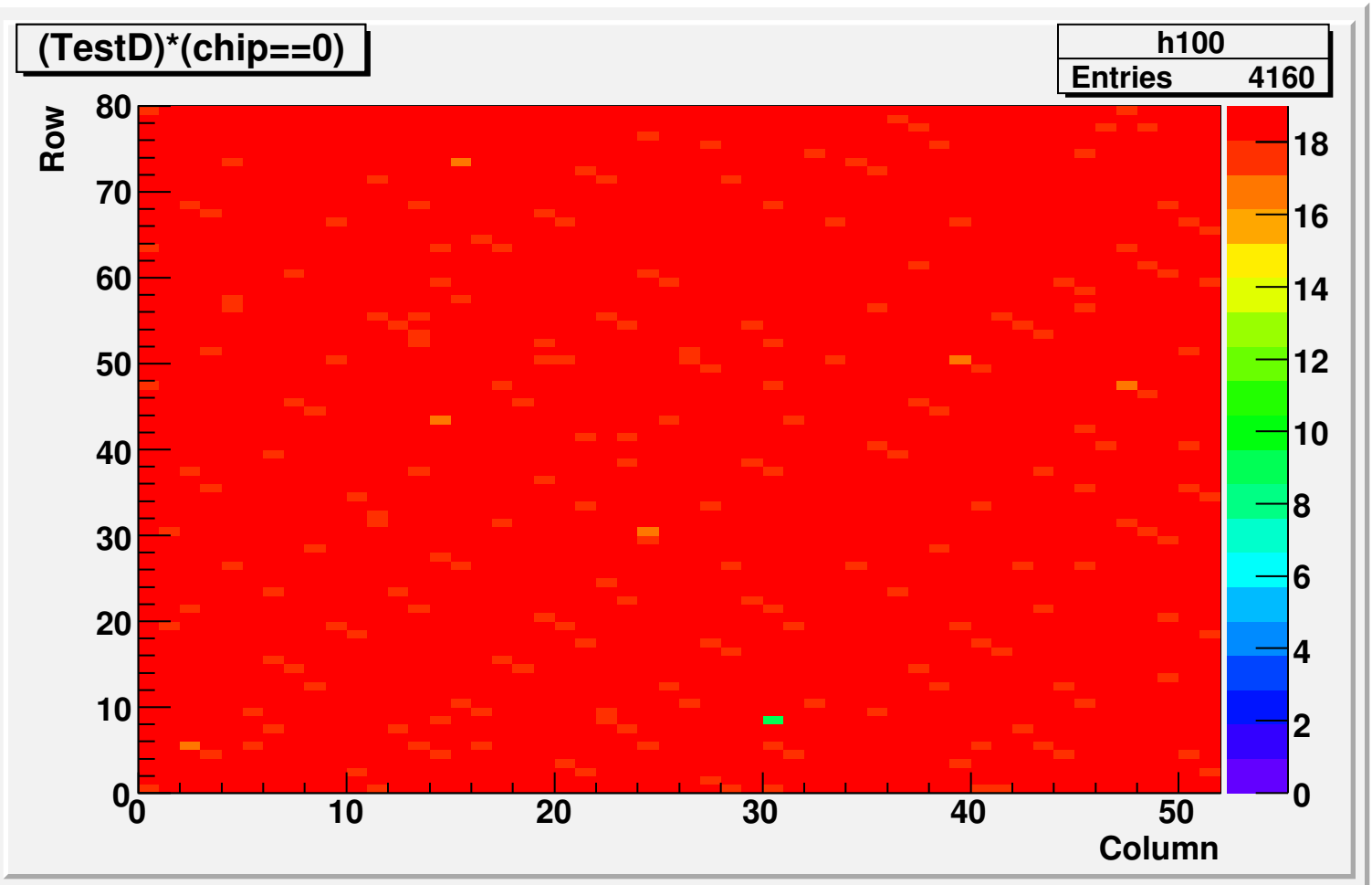
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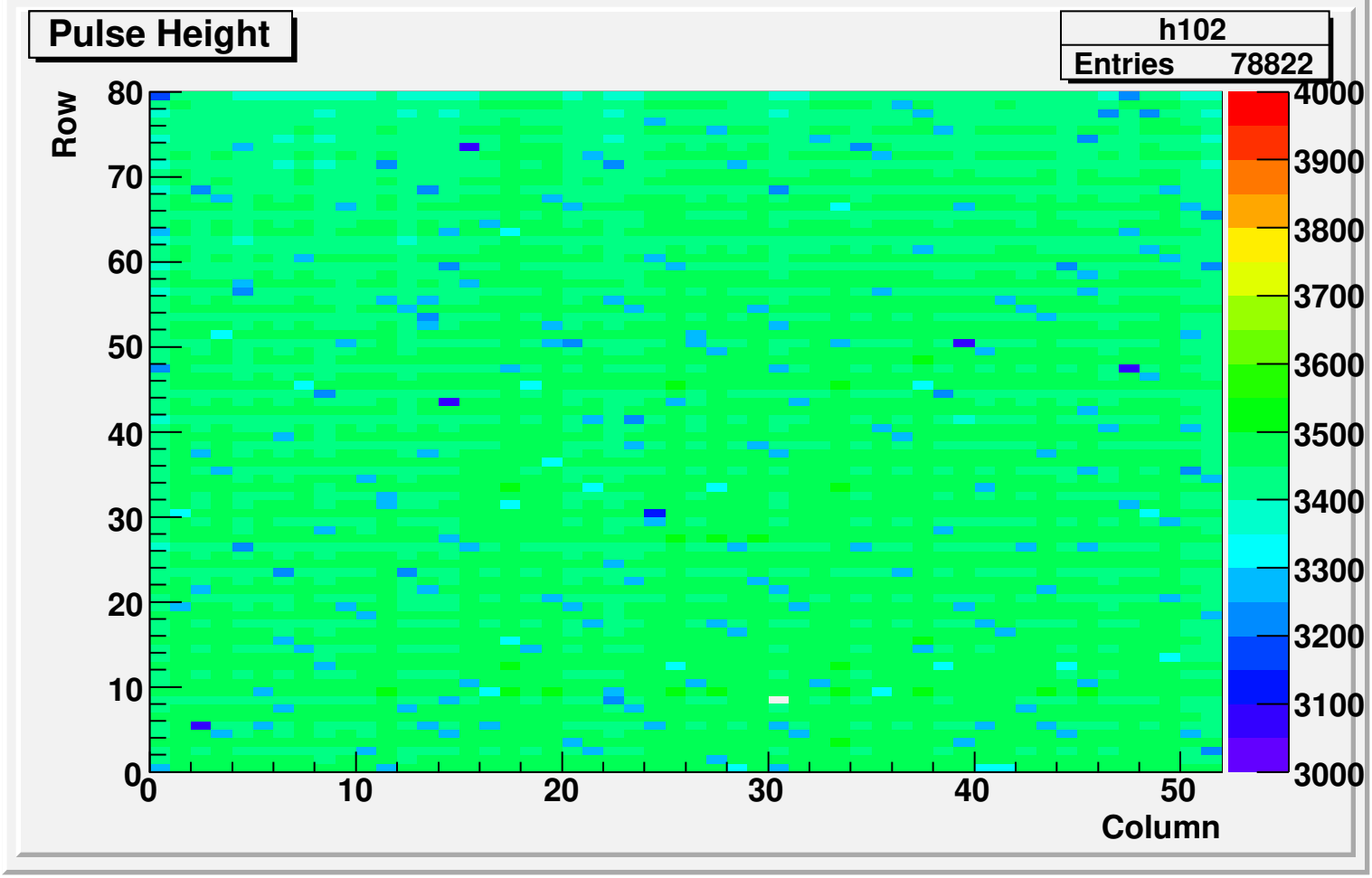
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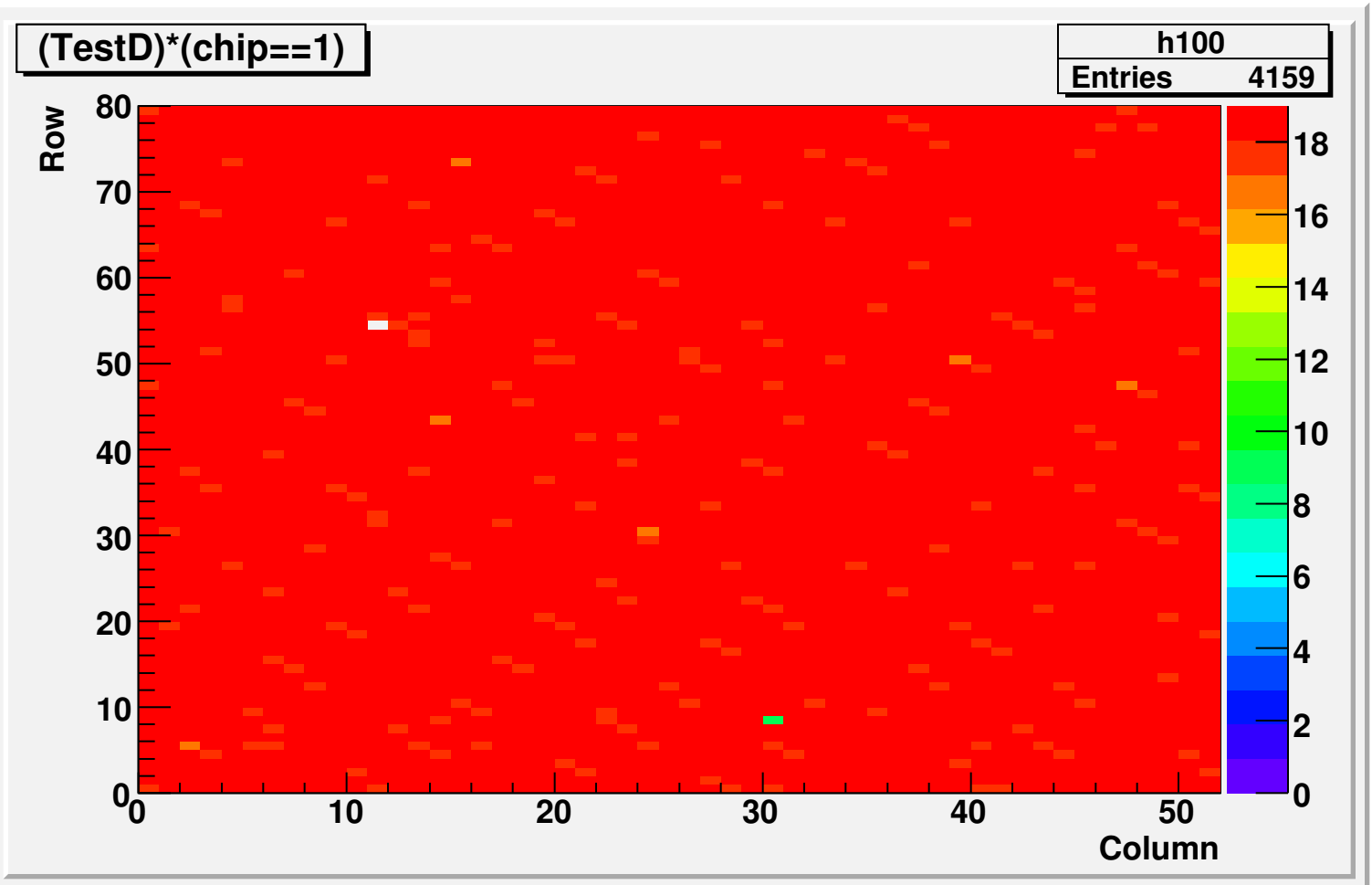
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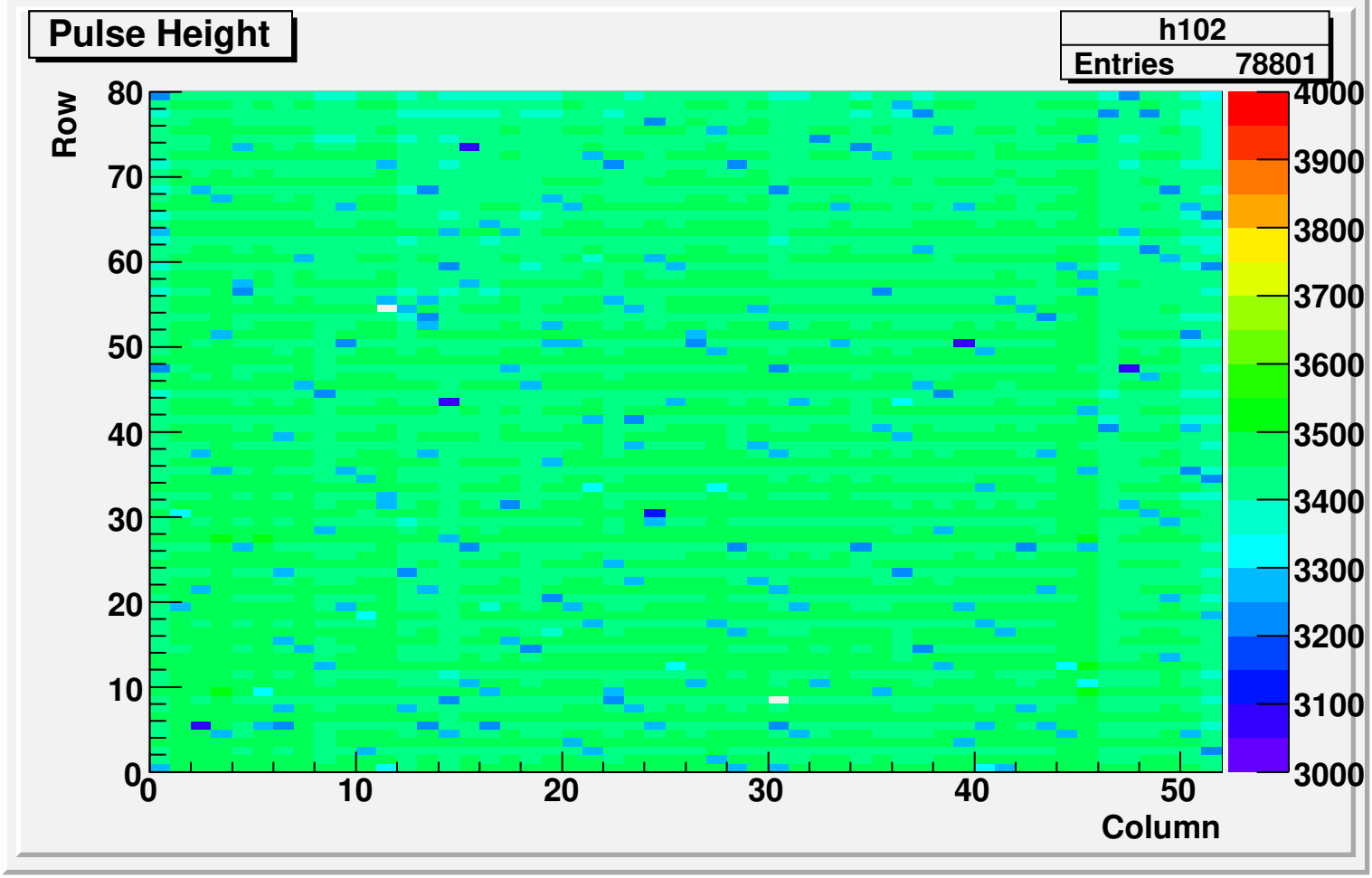
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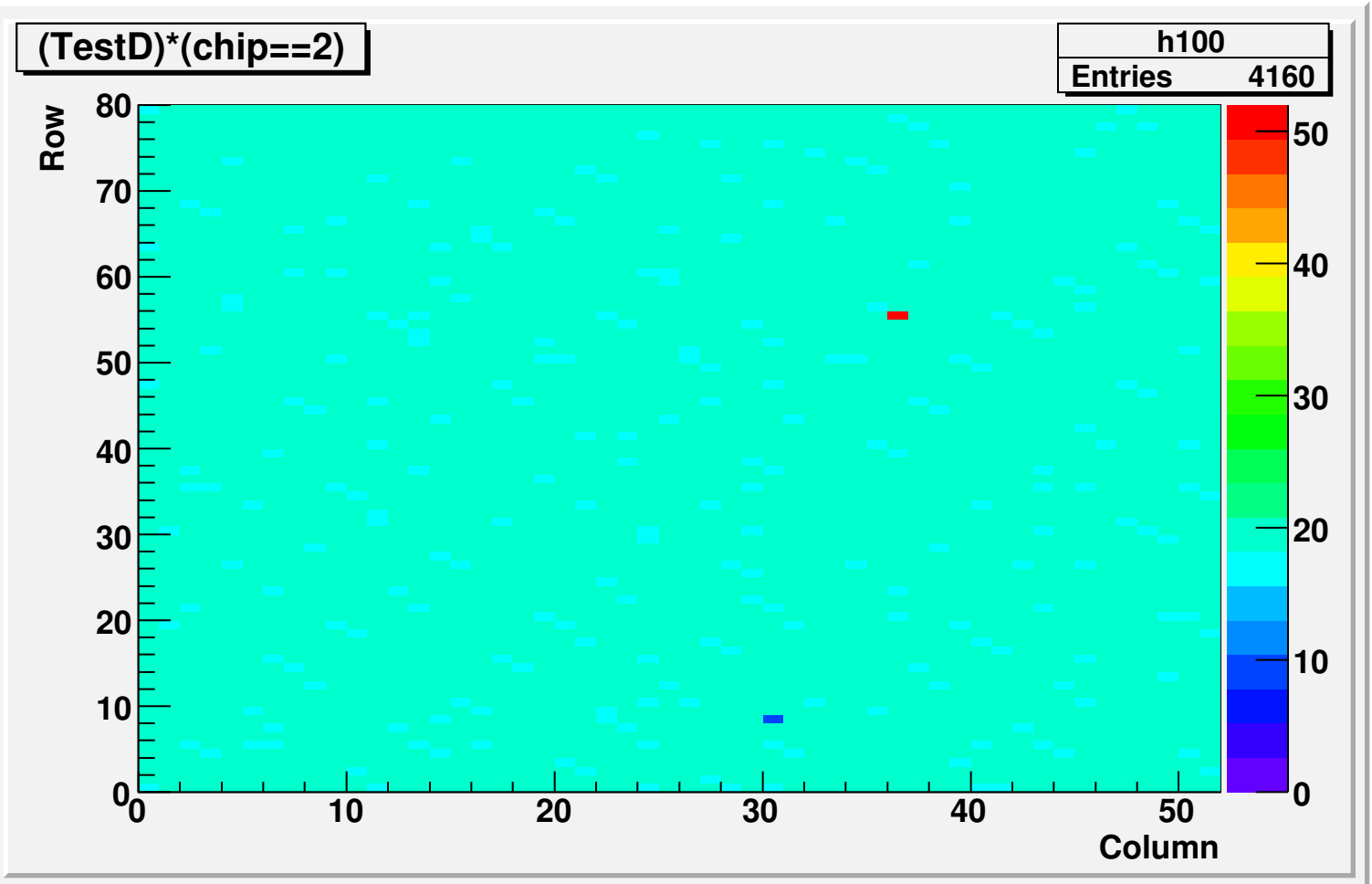
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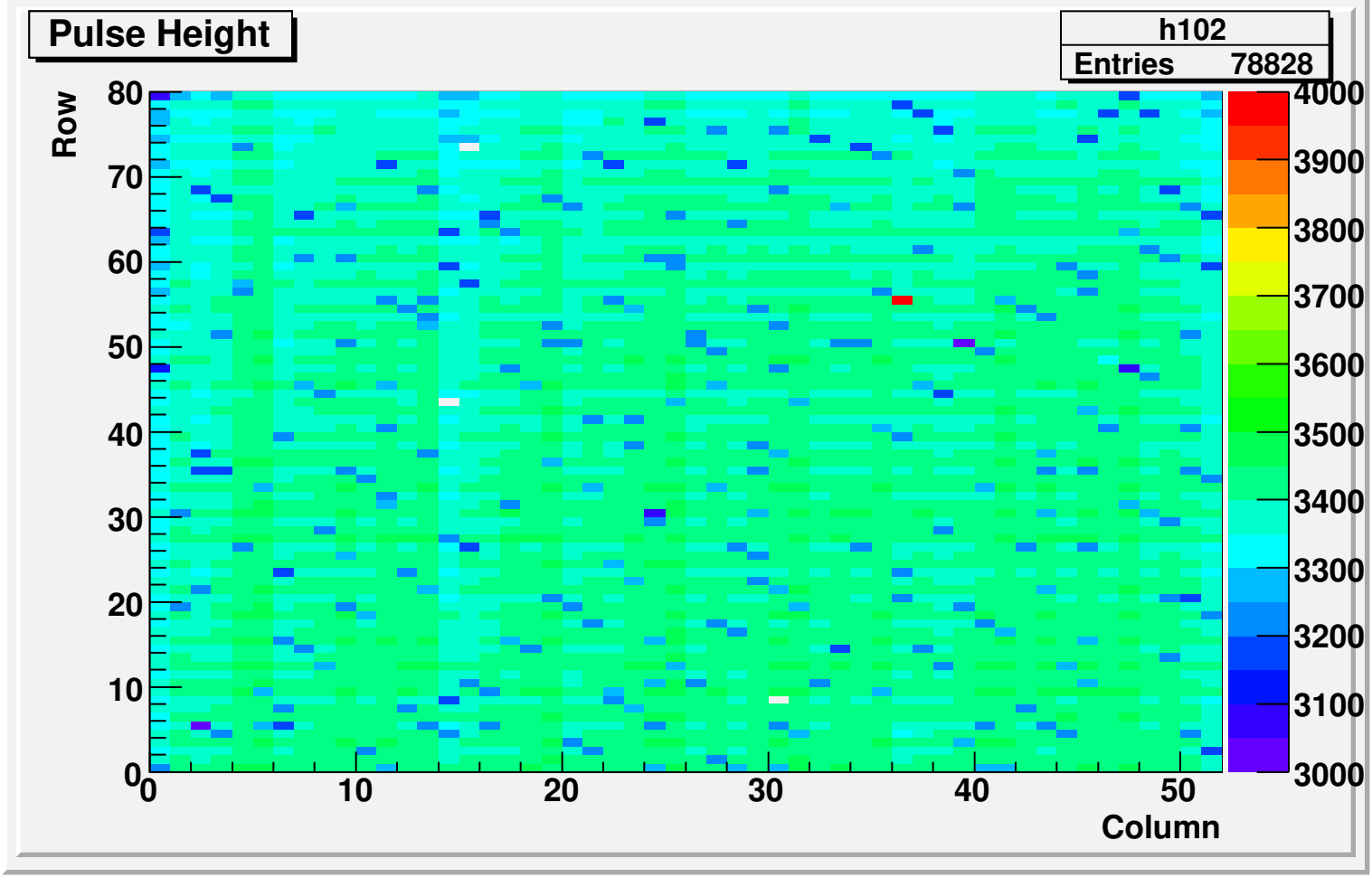
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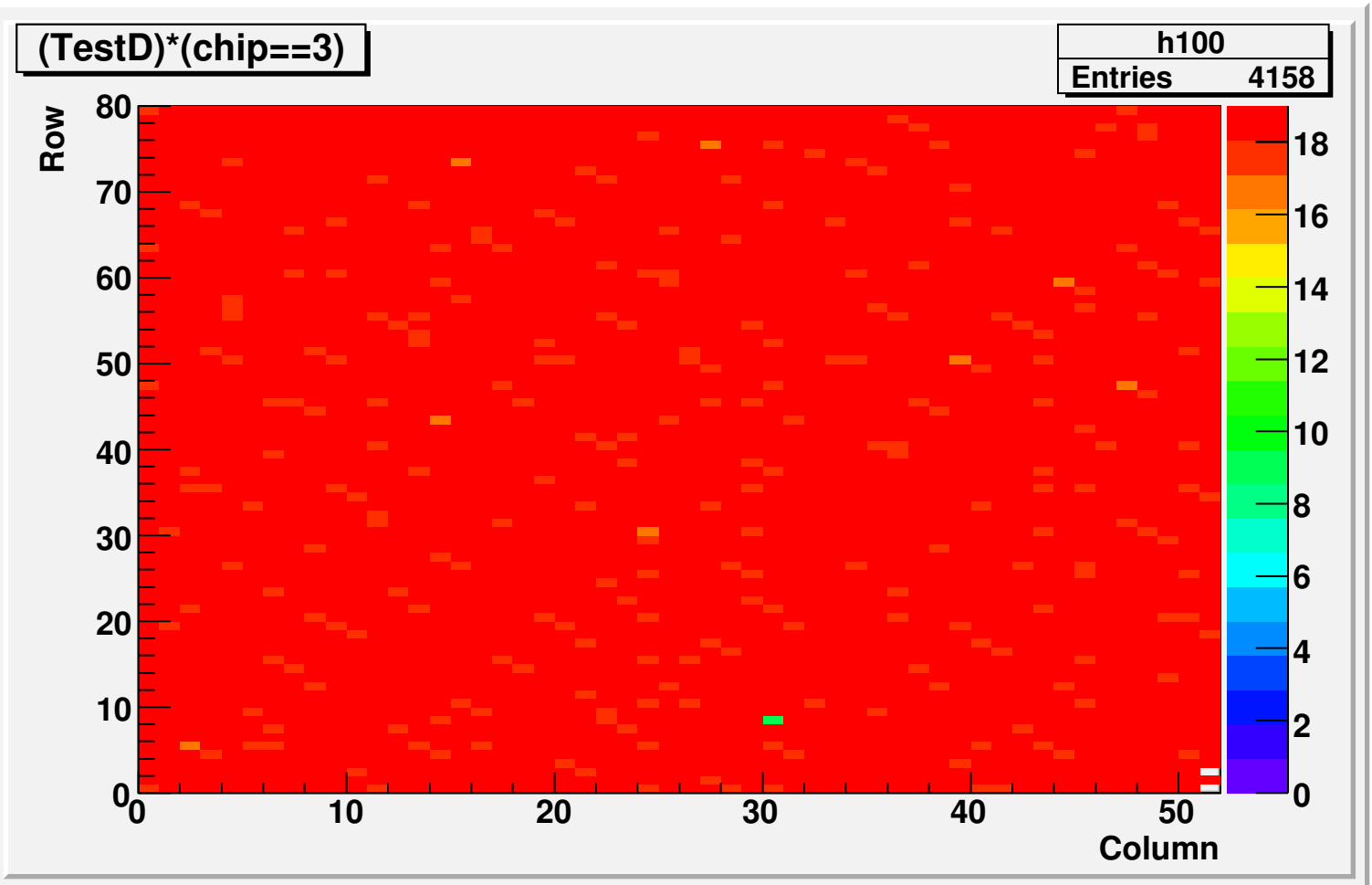
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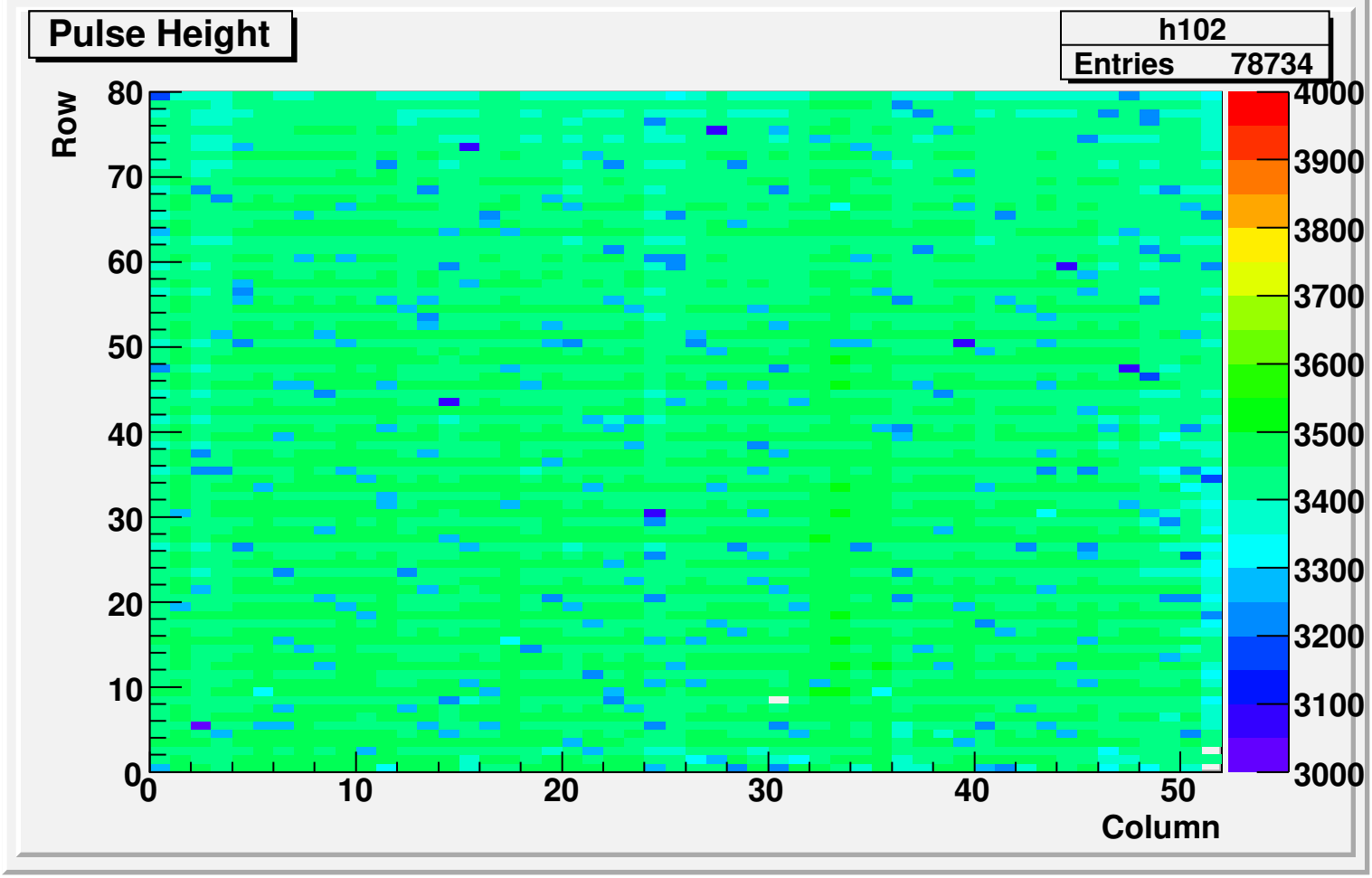
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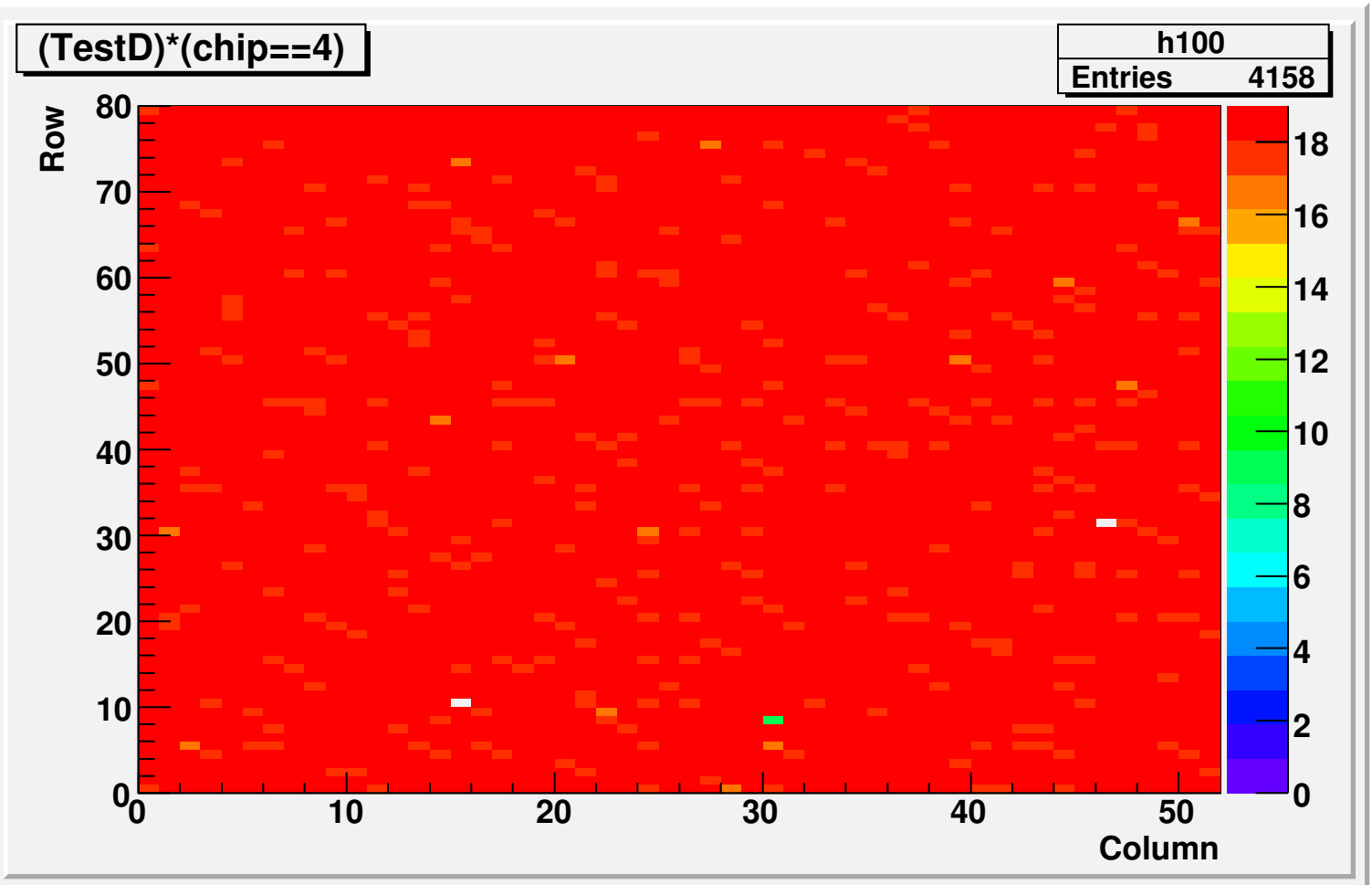
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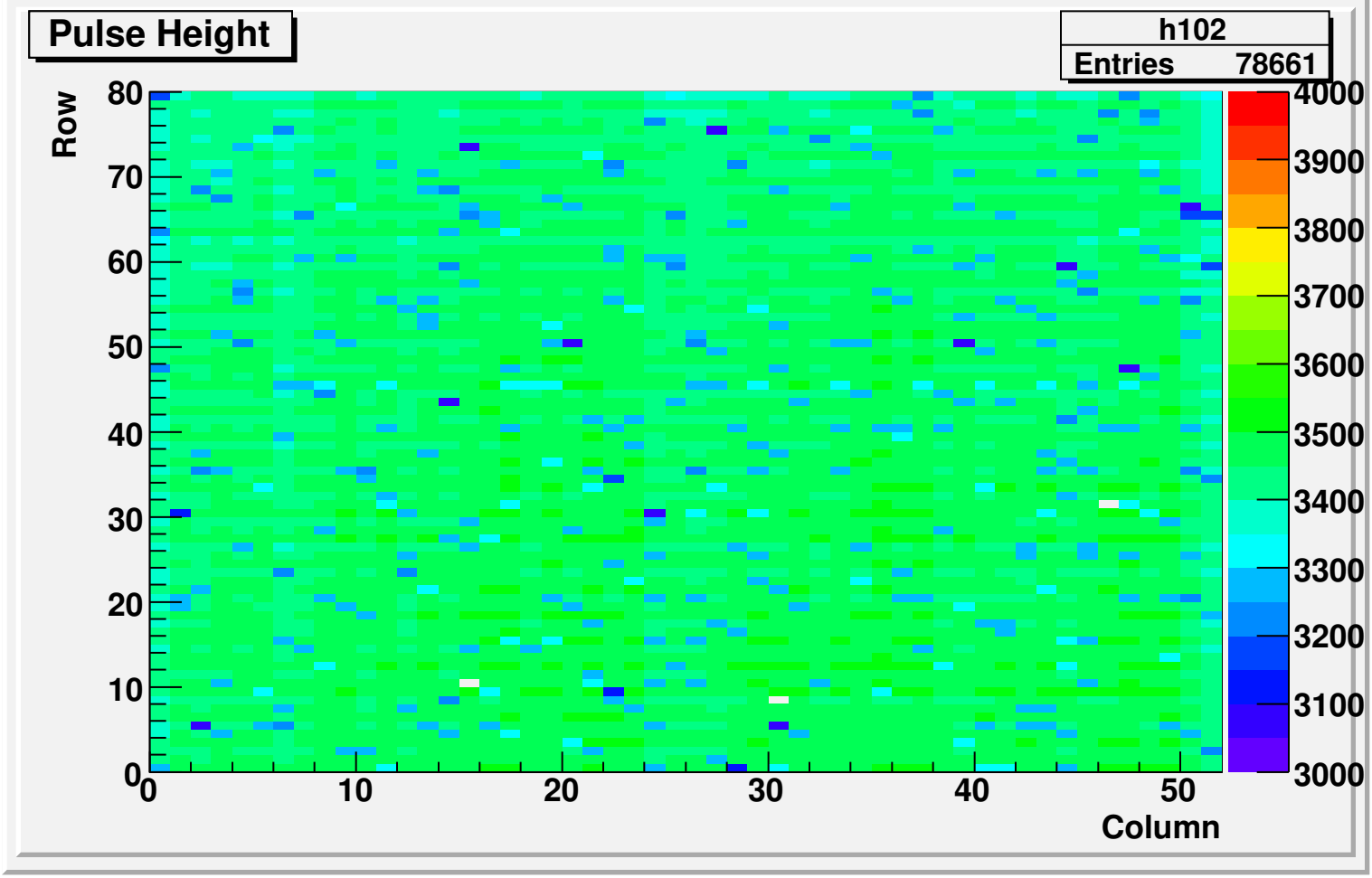
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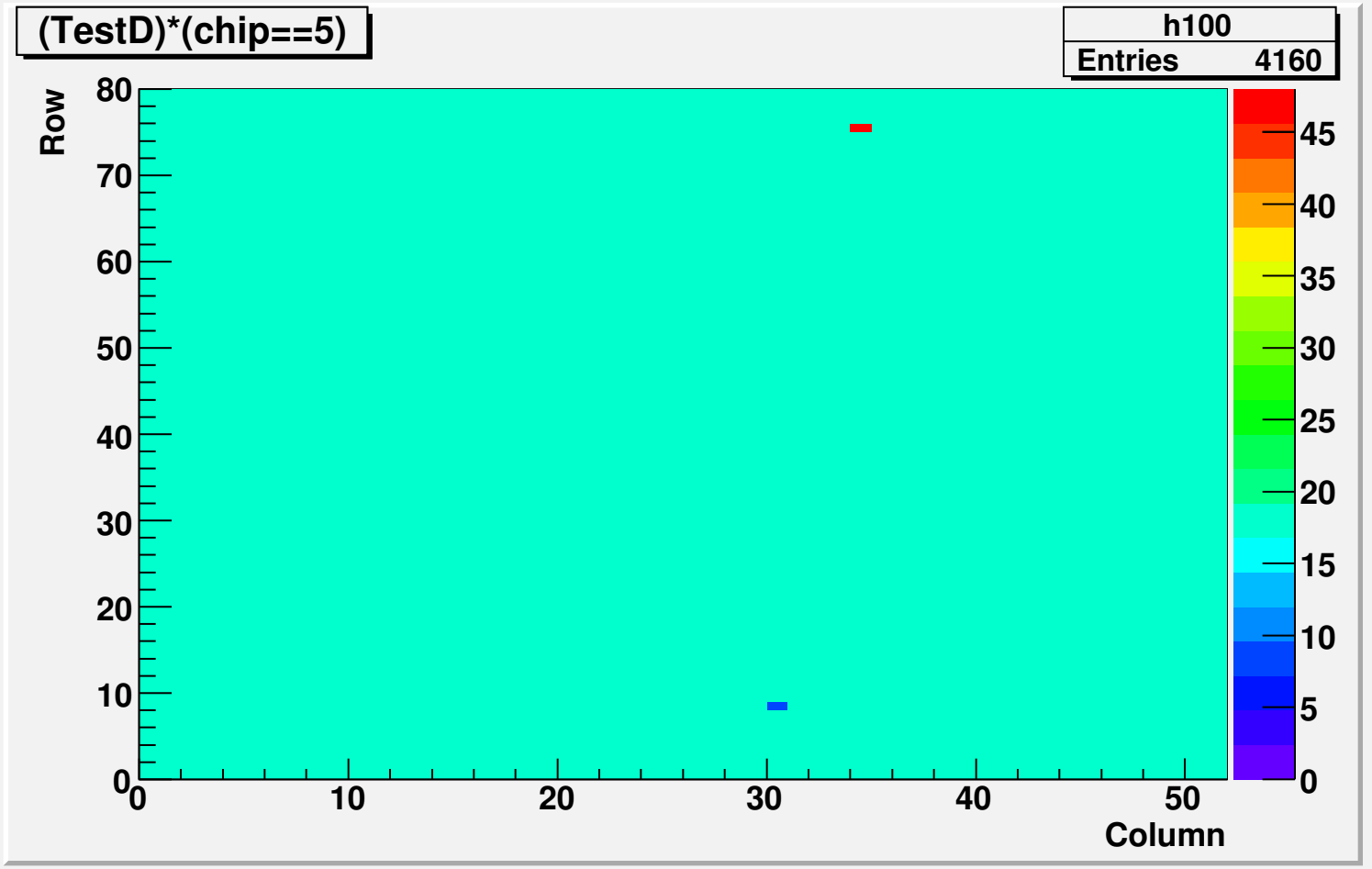
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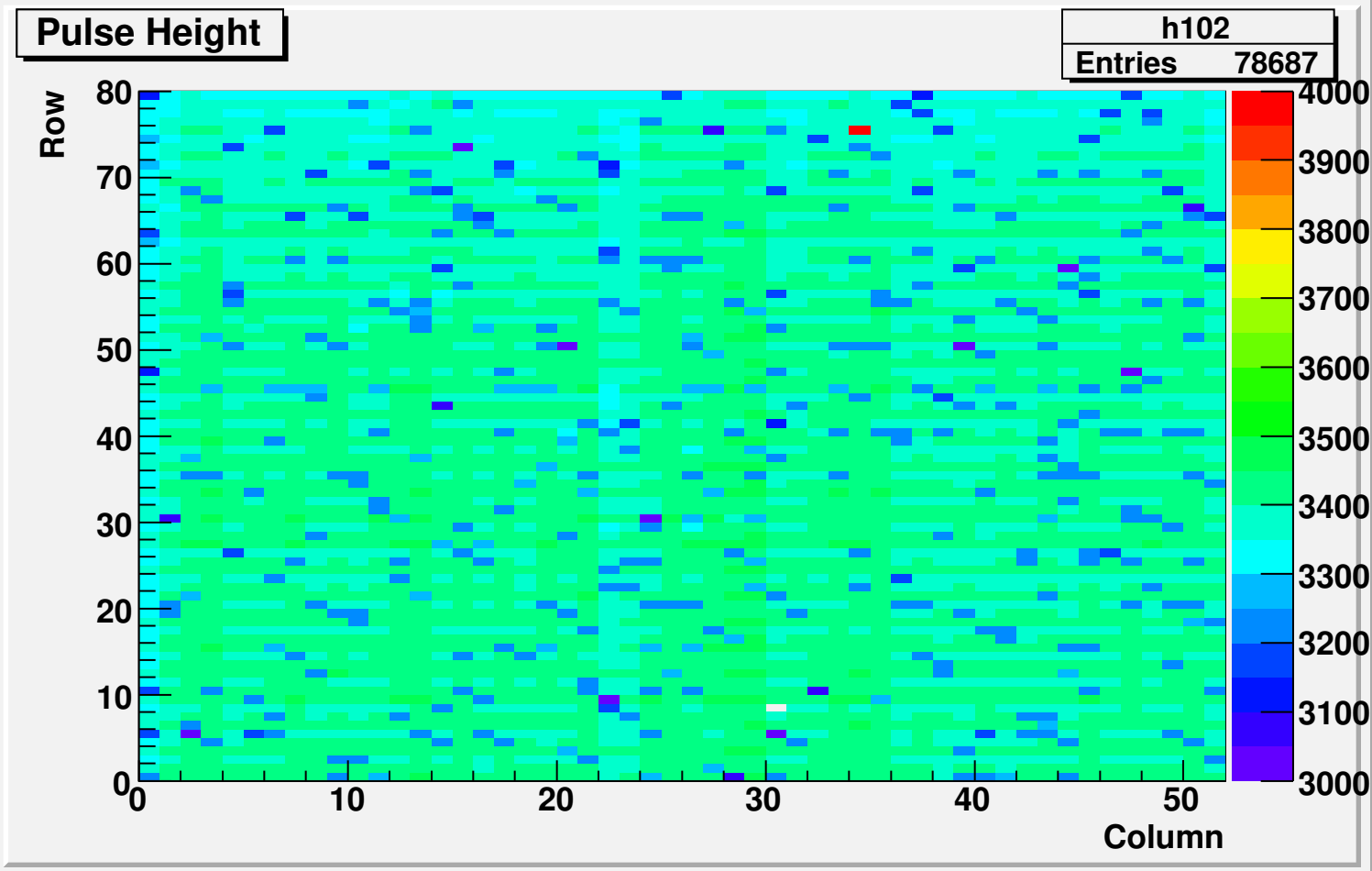
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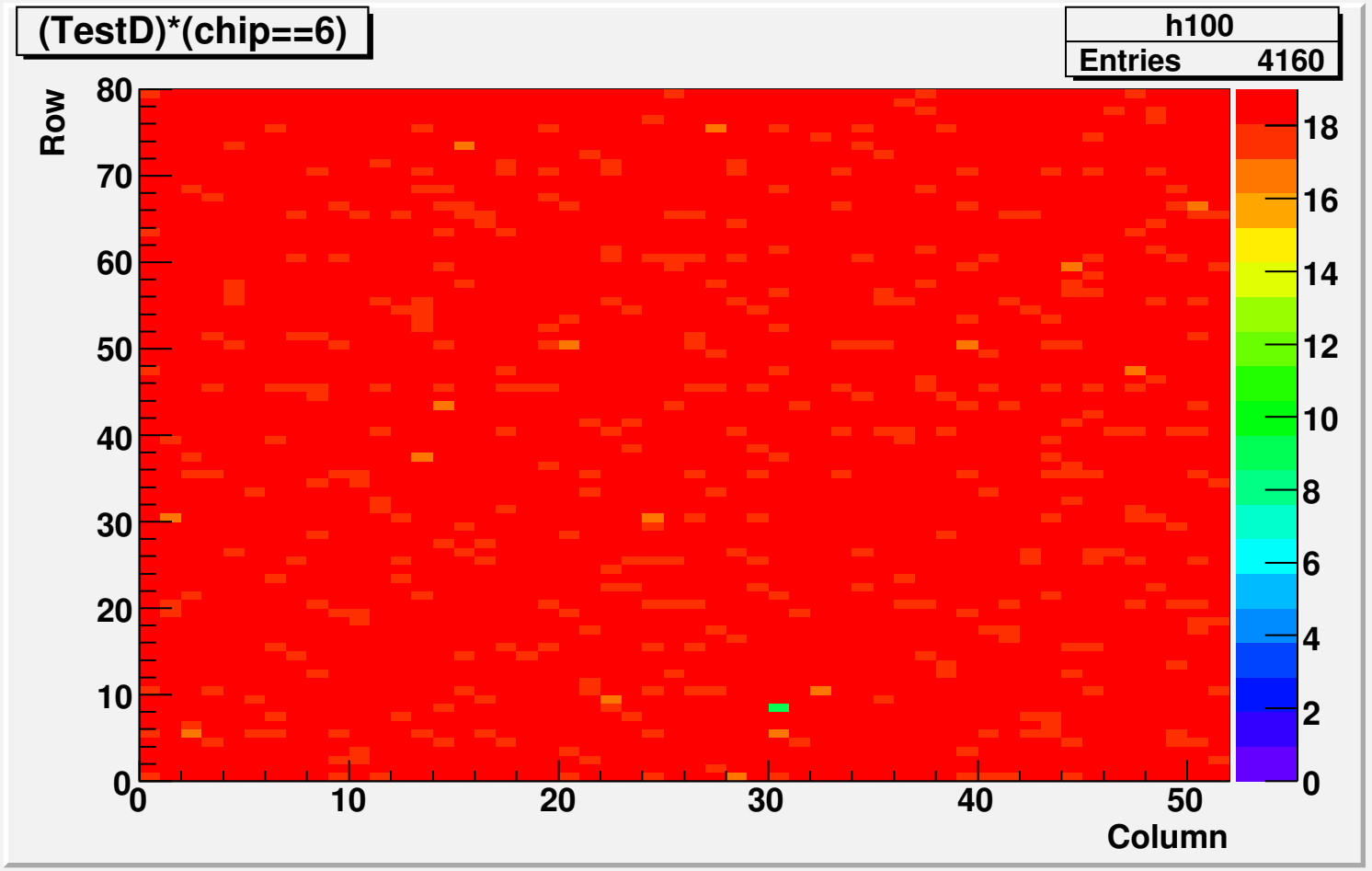
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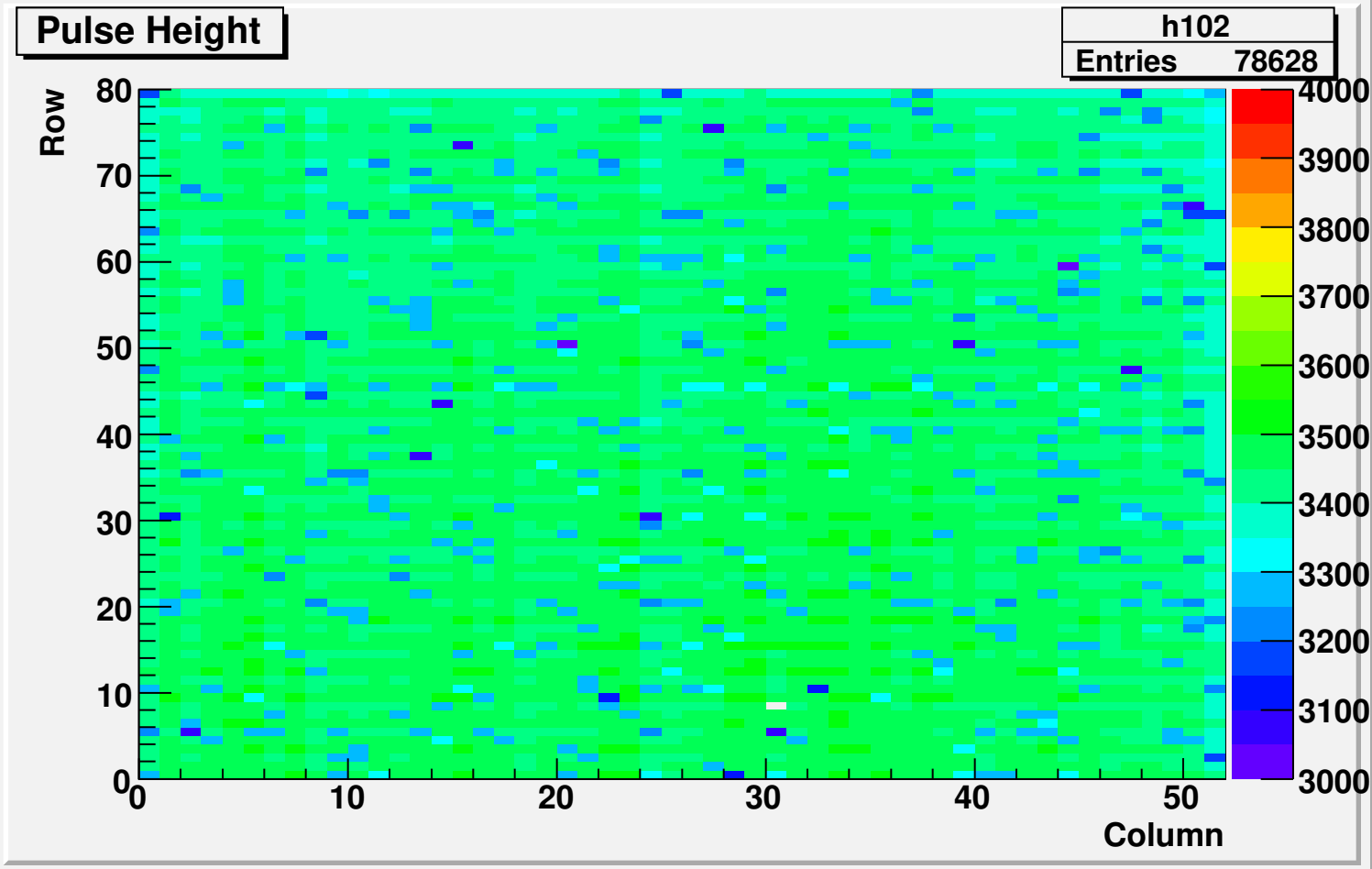
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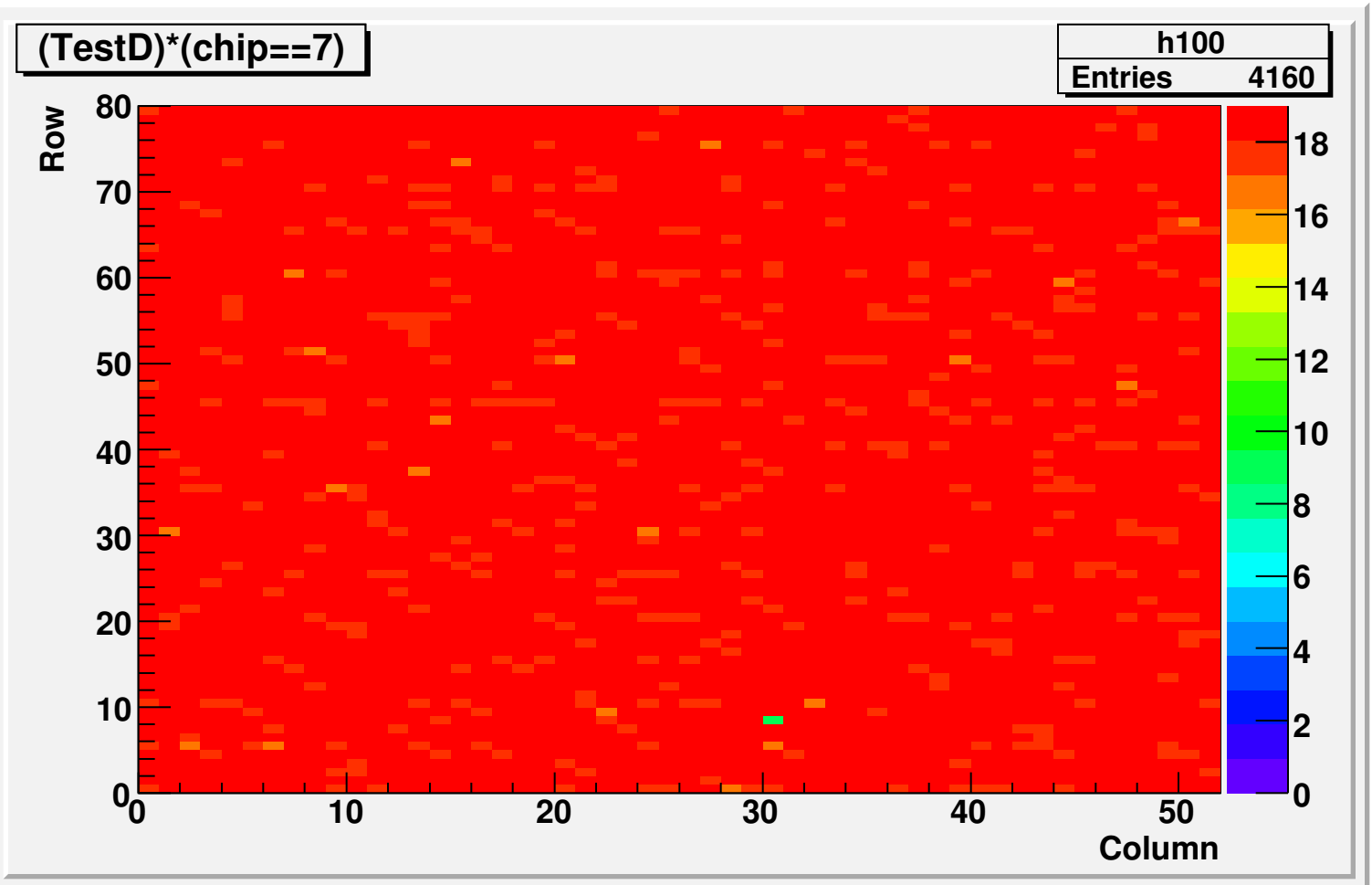
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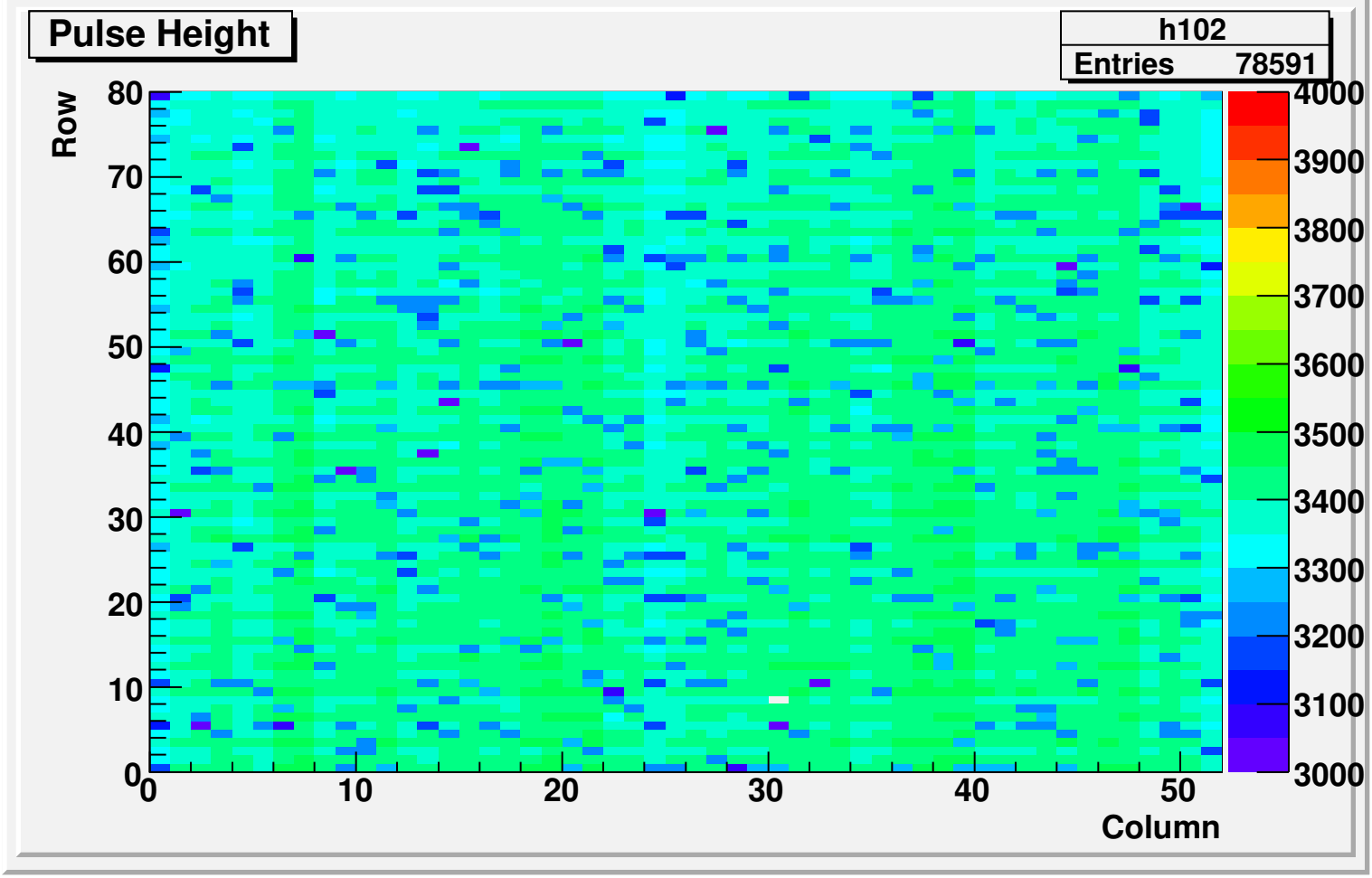
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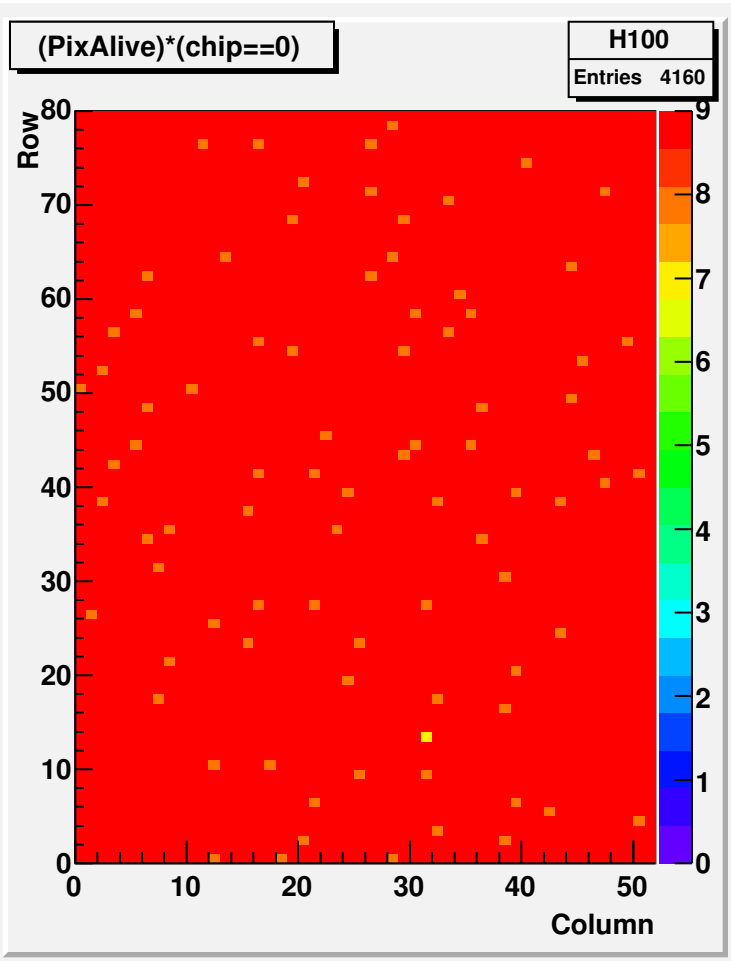
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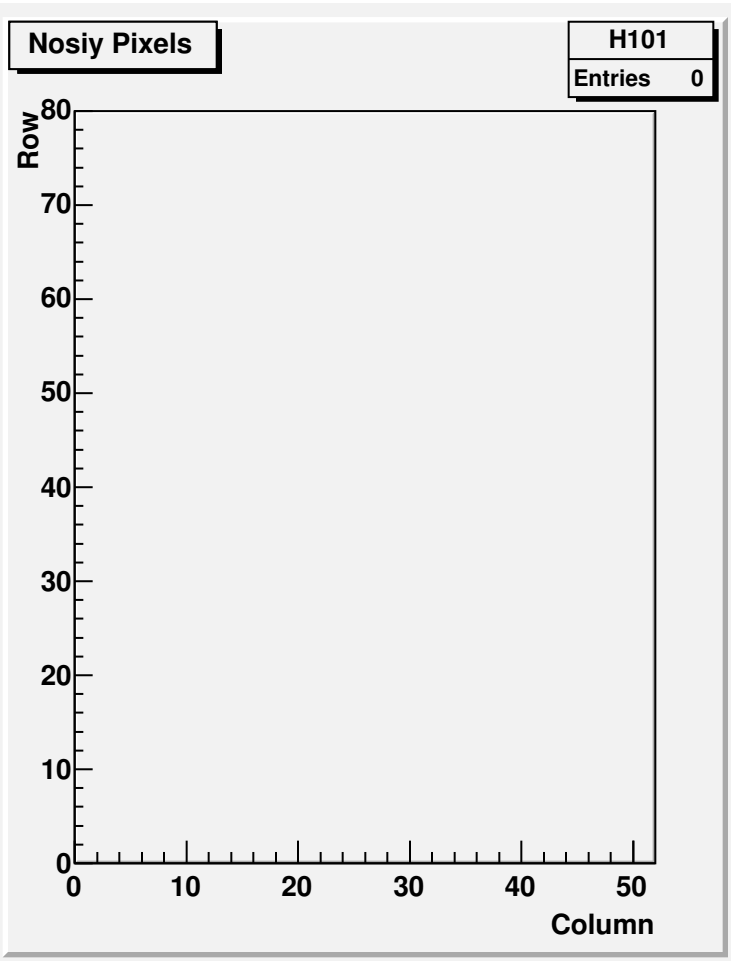
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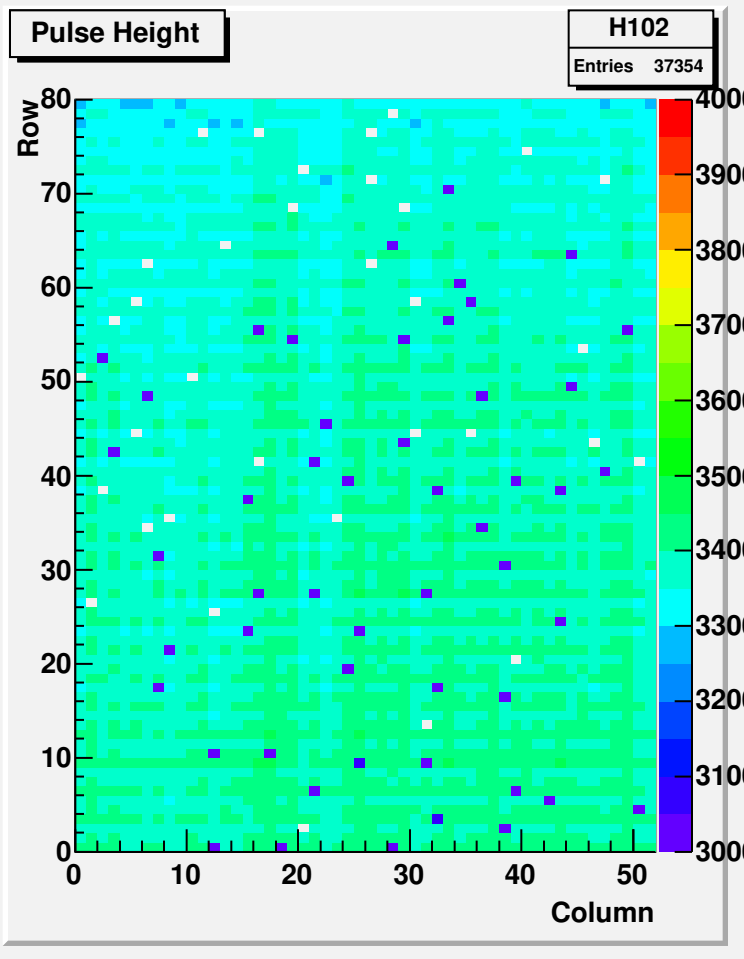
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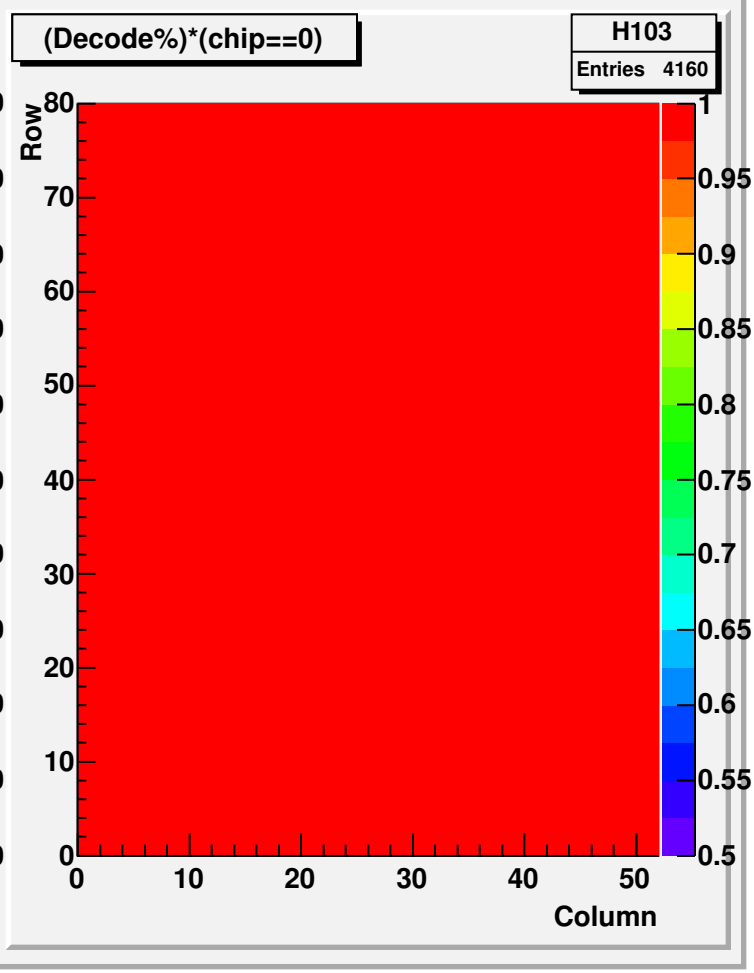
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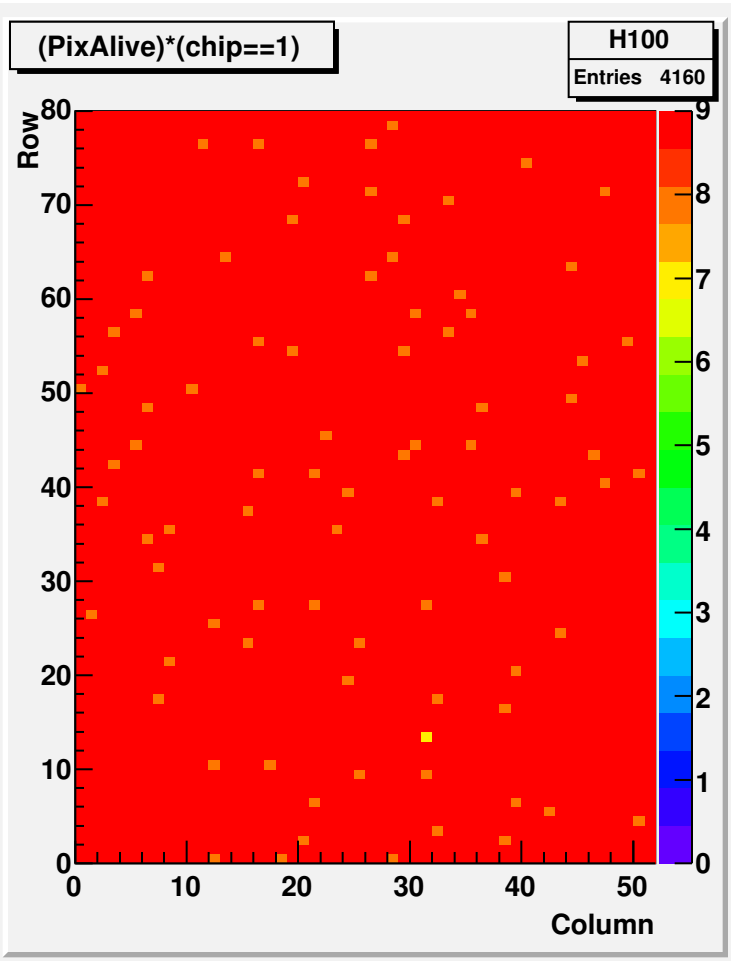
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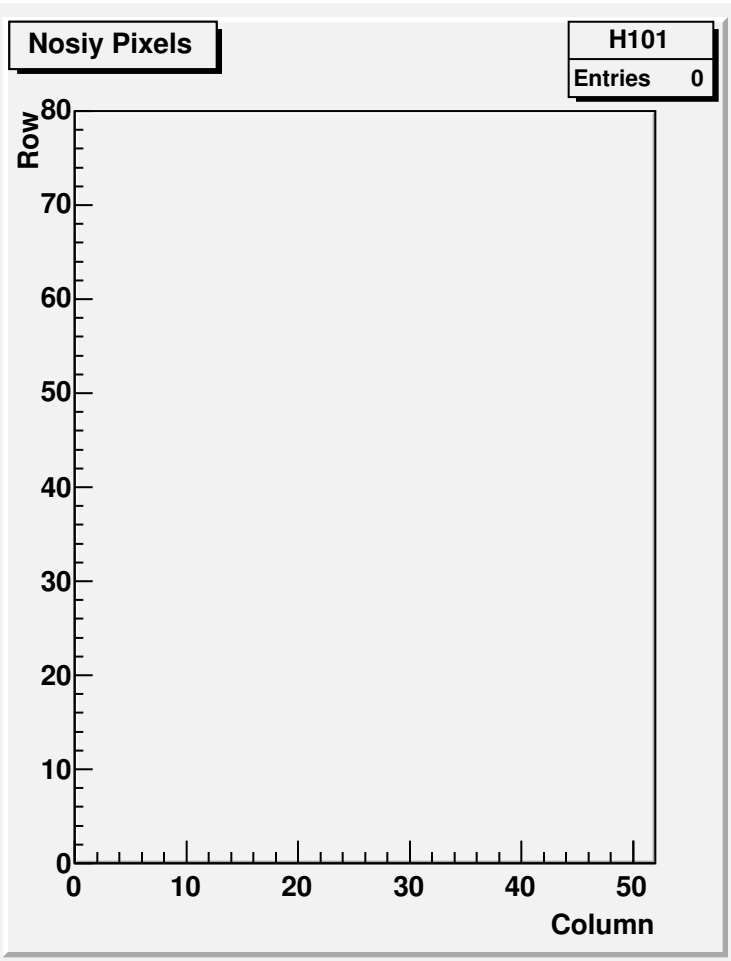
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H101
Entries 0
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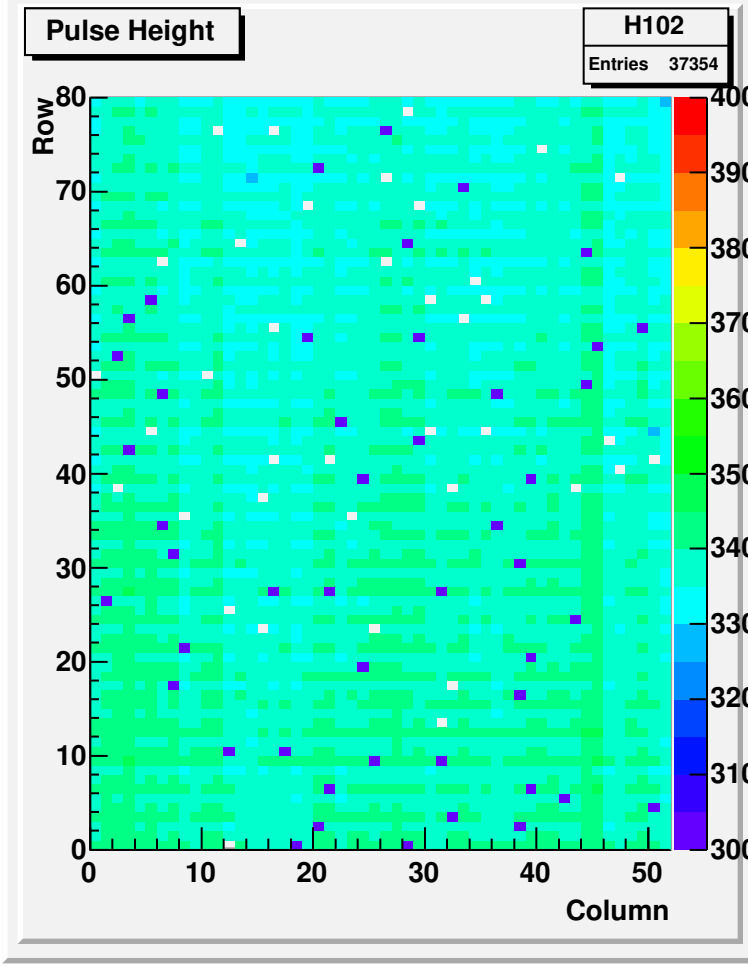
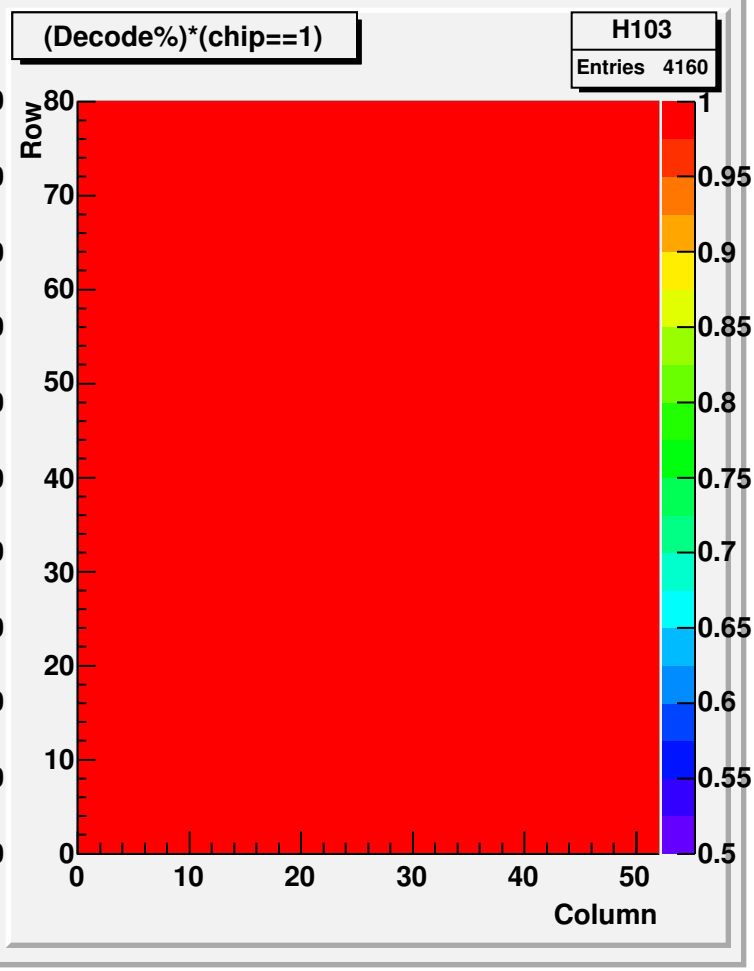

Pulse Height
H102
Entries 37354
Row
Column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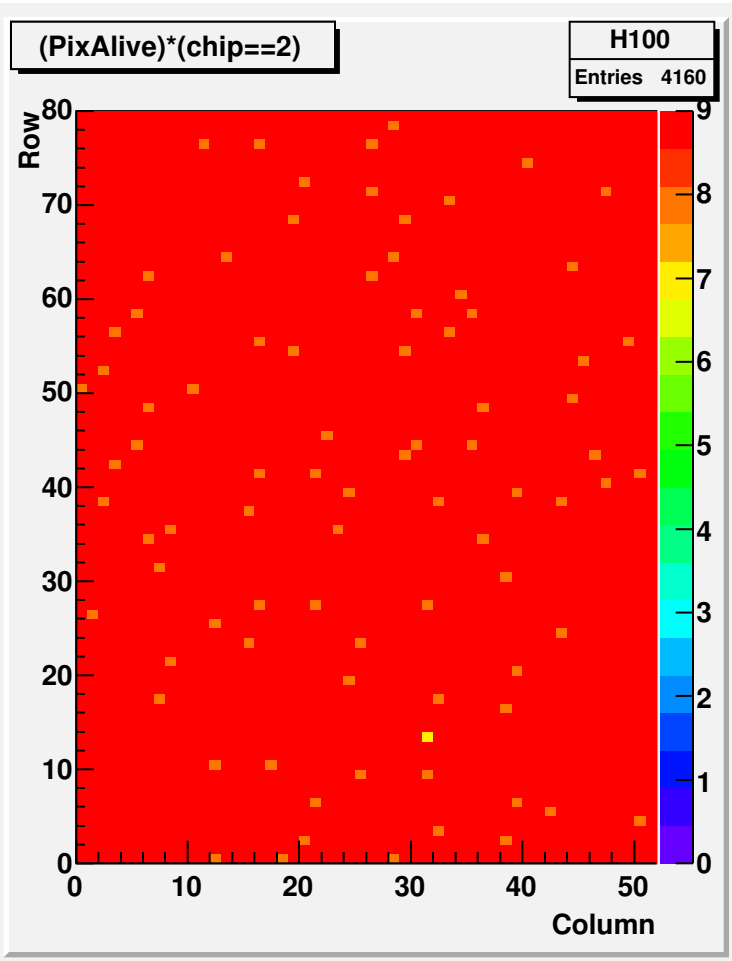
H103
Entries 4160
Row
Column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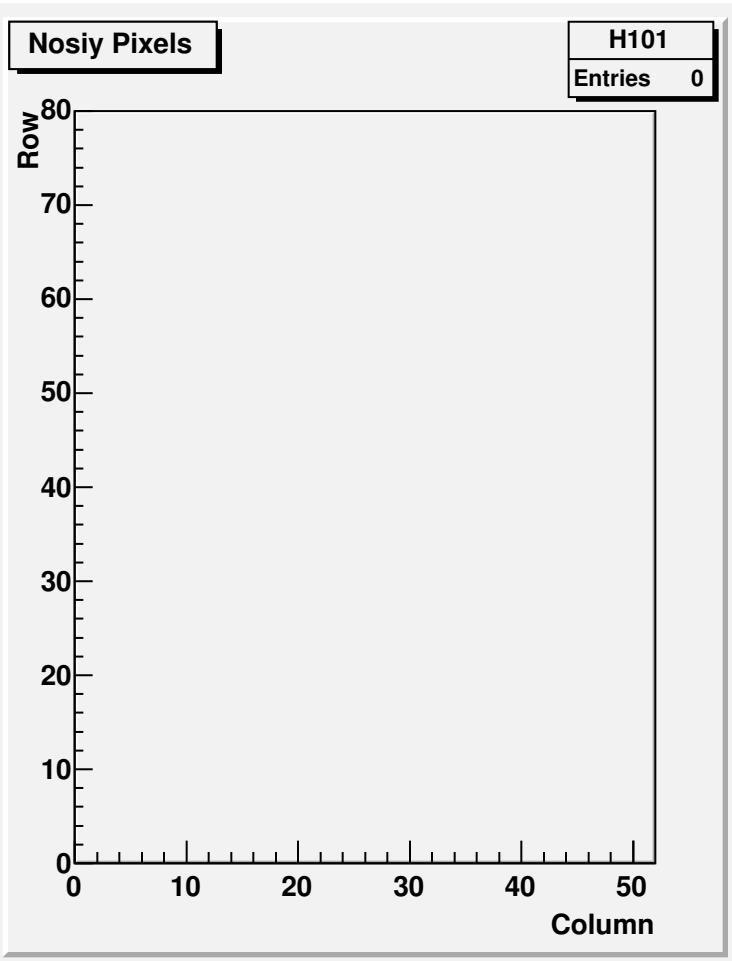
Column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H101
Entries 0
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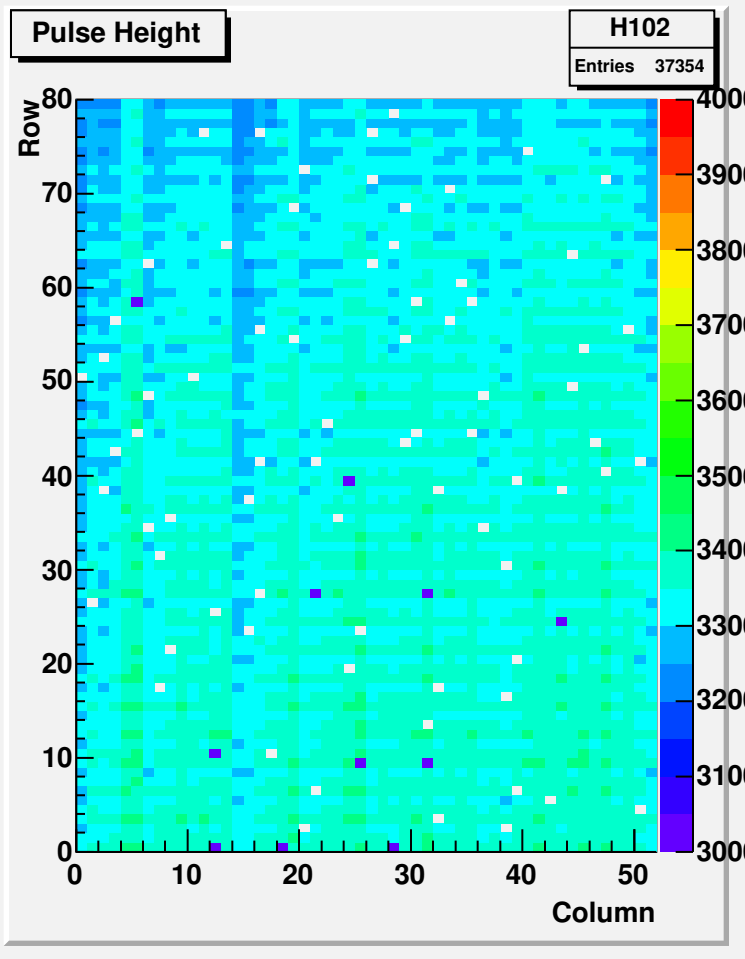
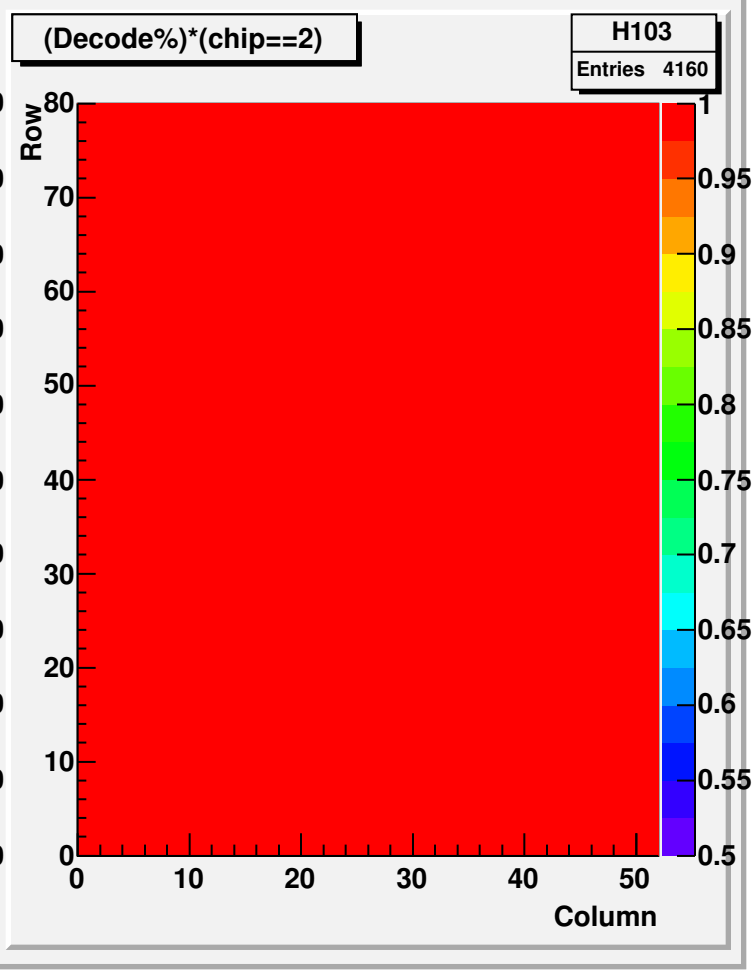
Column


Pulse Height
H102
Entries 37354
Row
Column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H103
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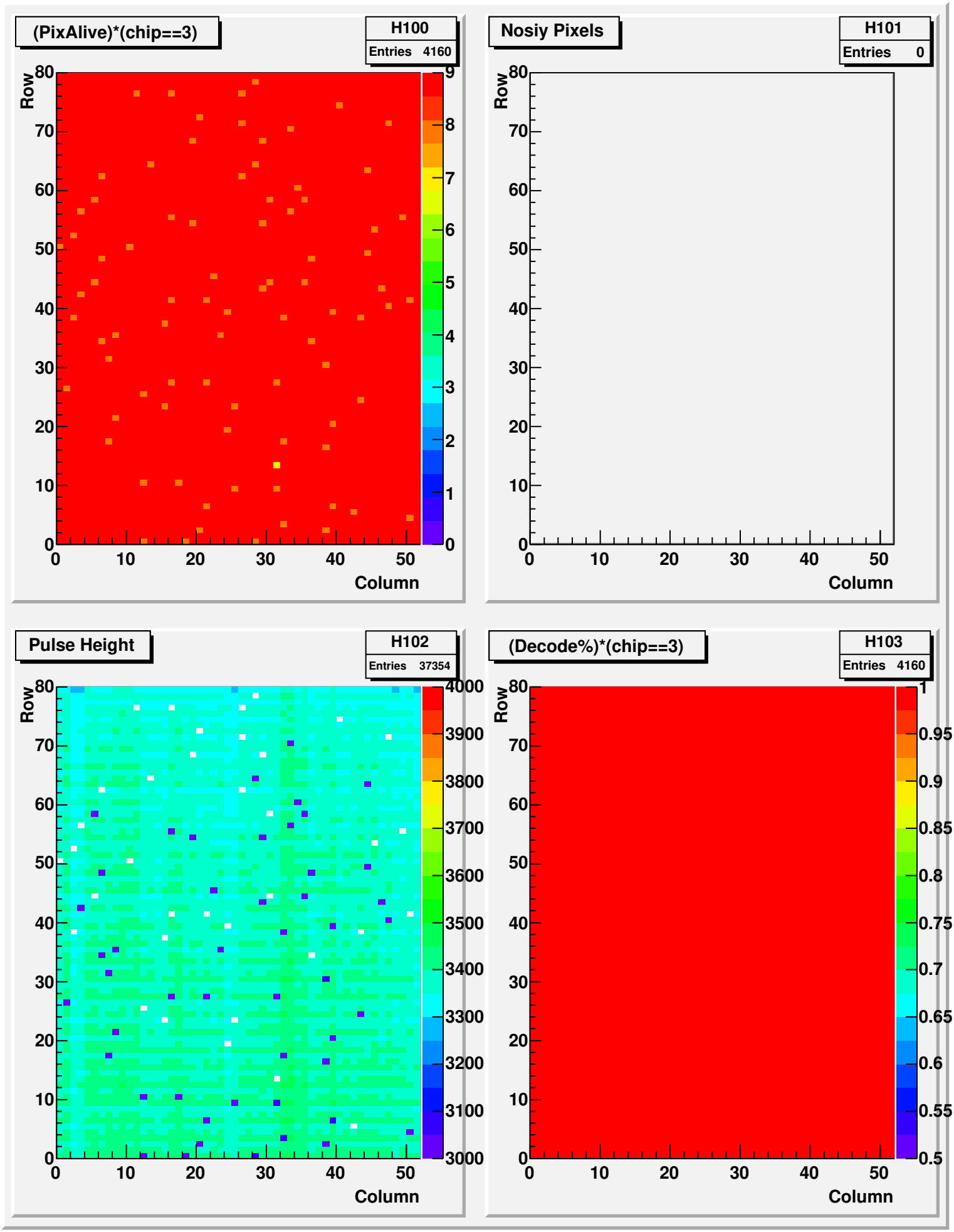
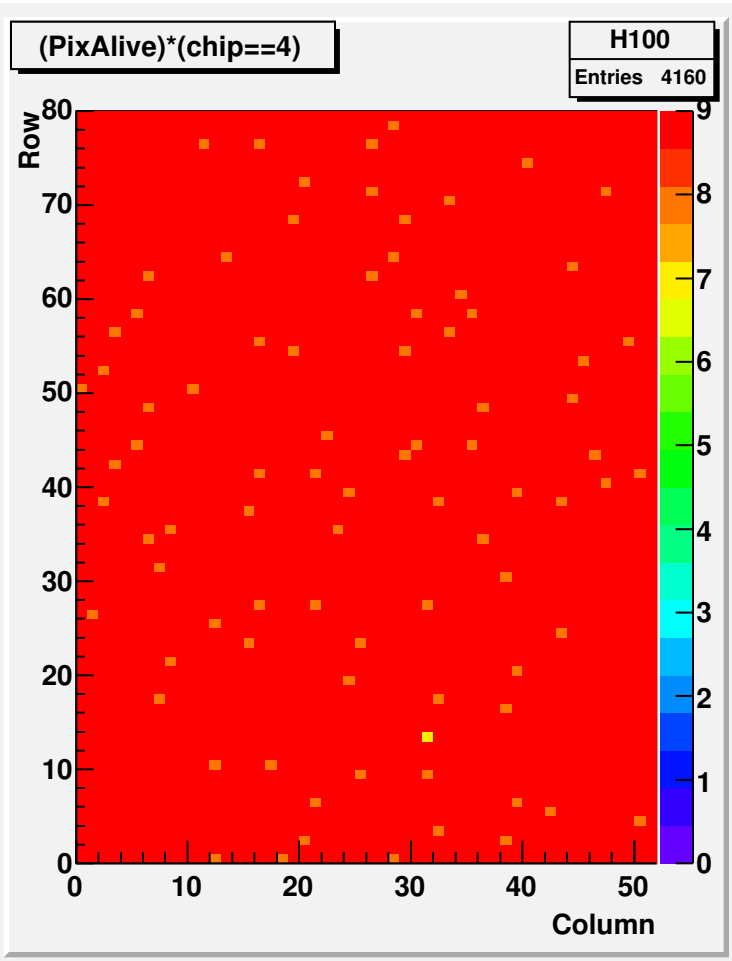
Column


(PixAlive)*(chip==3)
H100
Entries 4160
Row
Column
Nosiy Pixels
H101
Entries 0
Row
Column
Pulse Height
H102
Entries 37354
Row
Column
(Decode%)*(chip==3)
H103
Entries 4160
Row
Column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H100
Entries 4160
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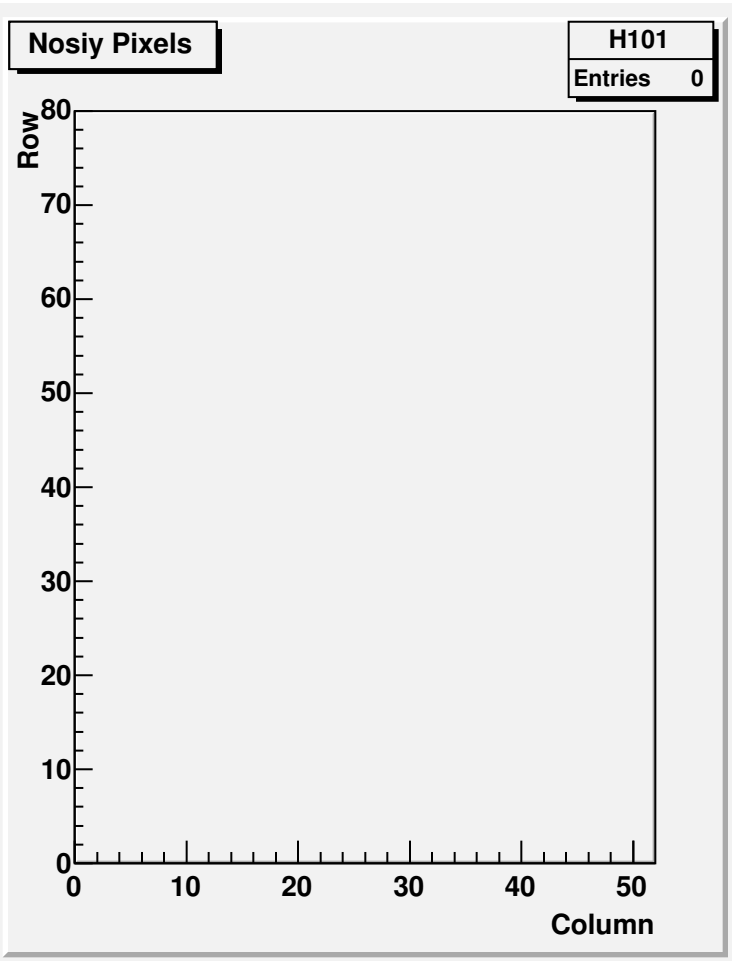
Column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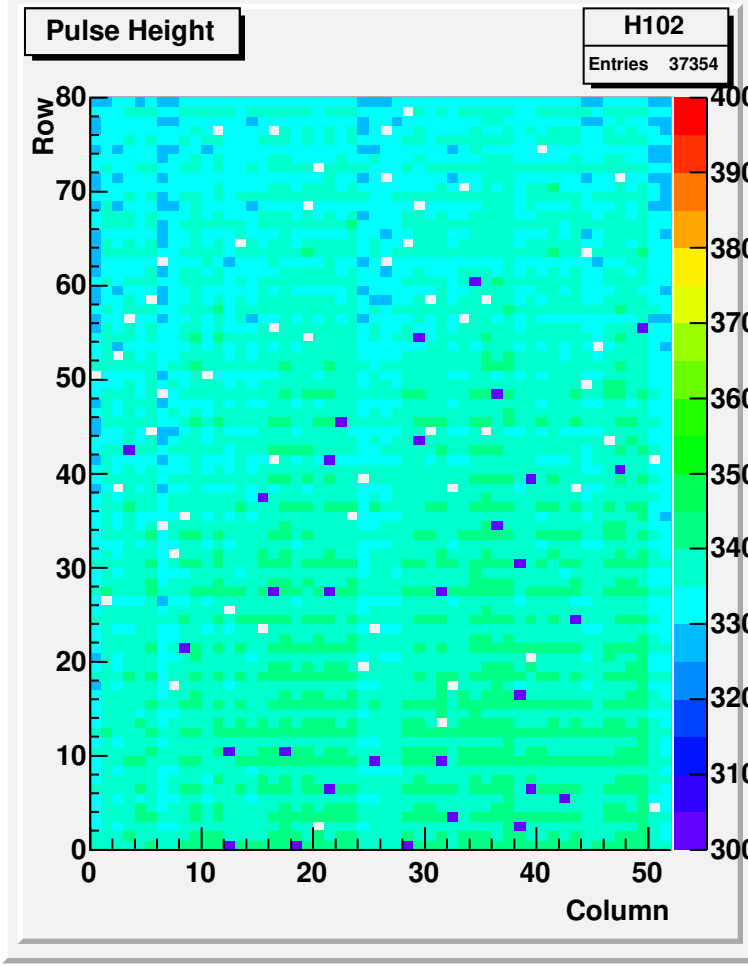
H101
Entries 0
Row
Column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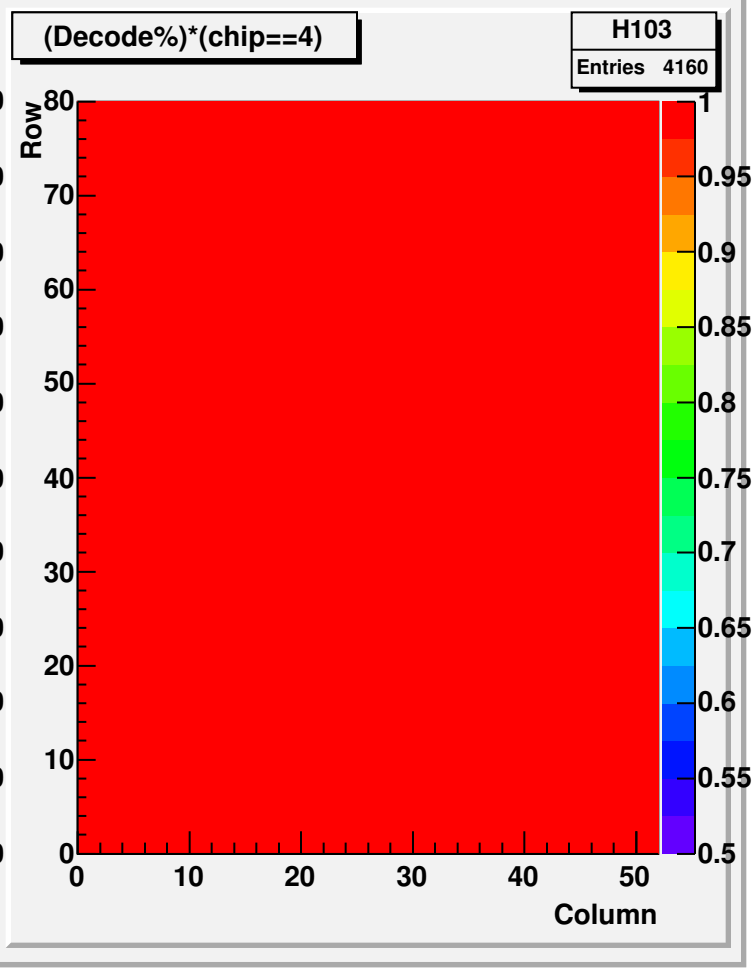
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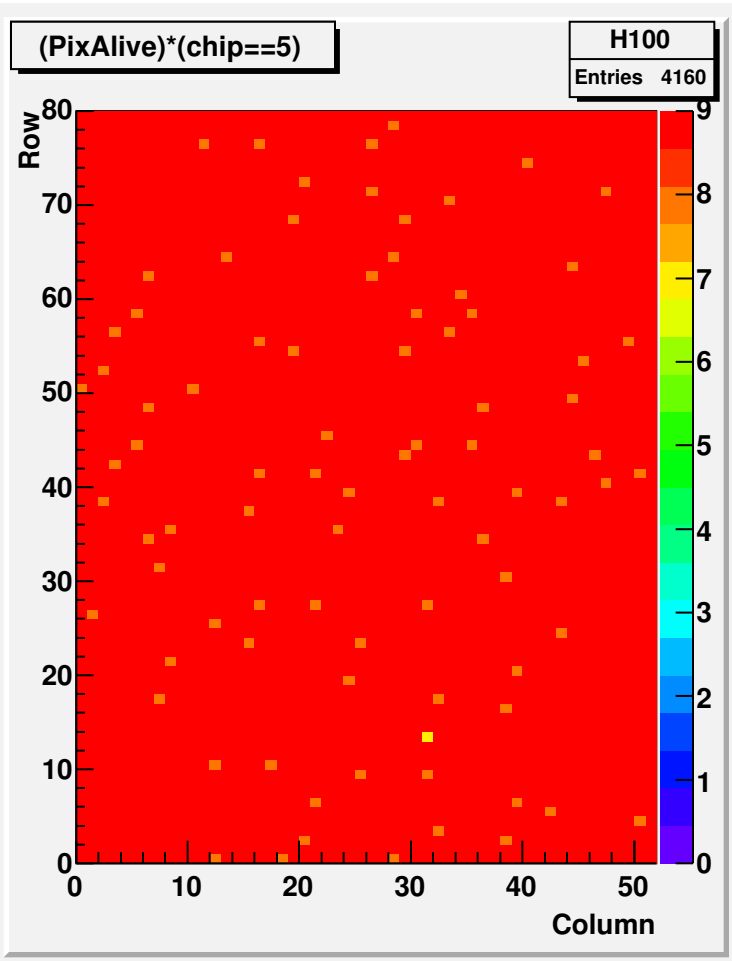
Column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H100
Entries 4160
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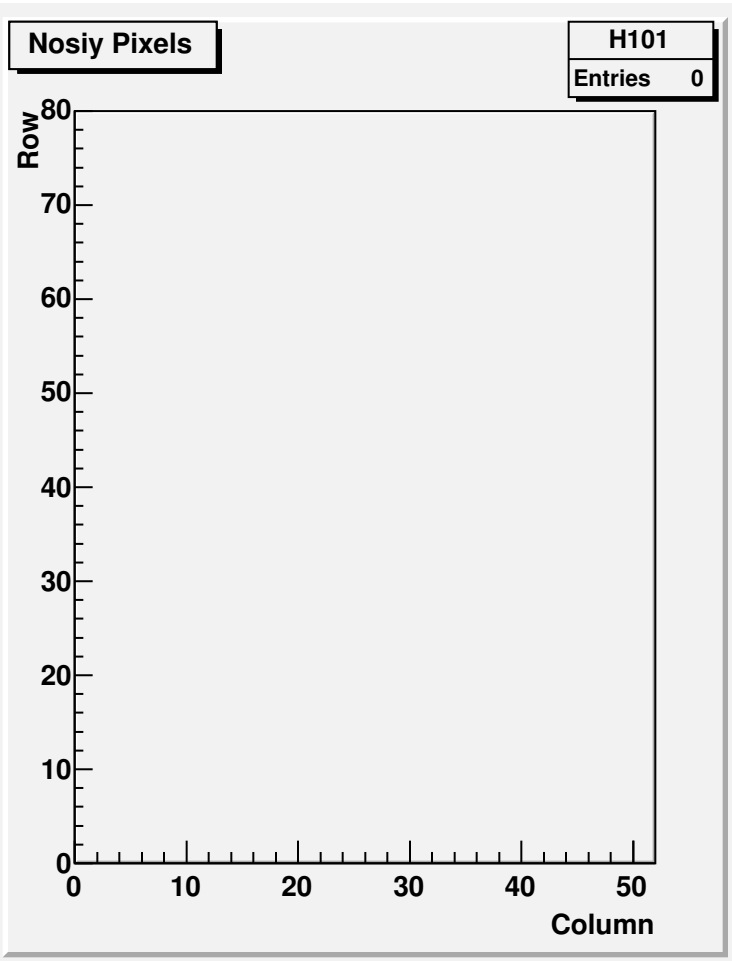
Column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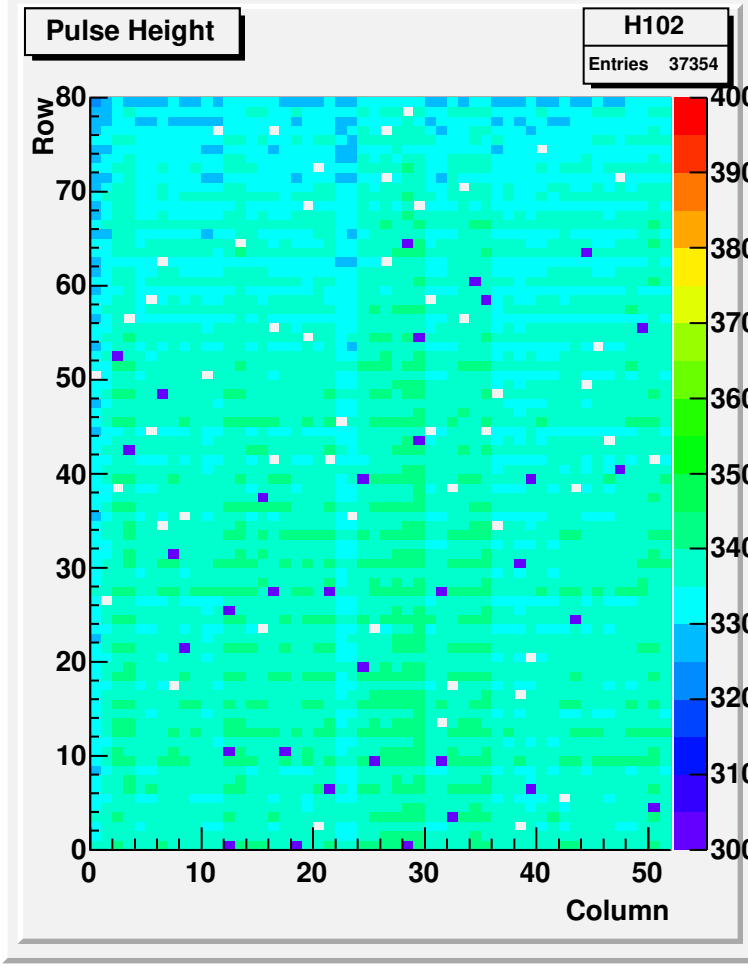
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H102
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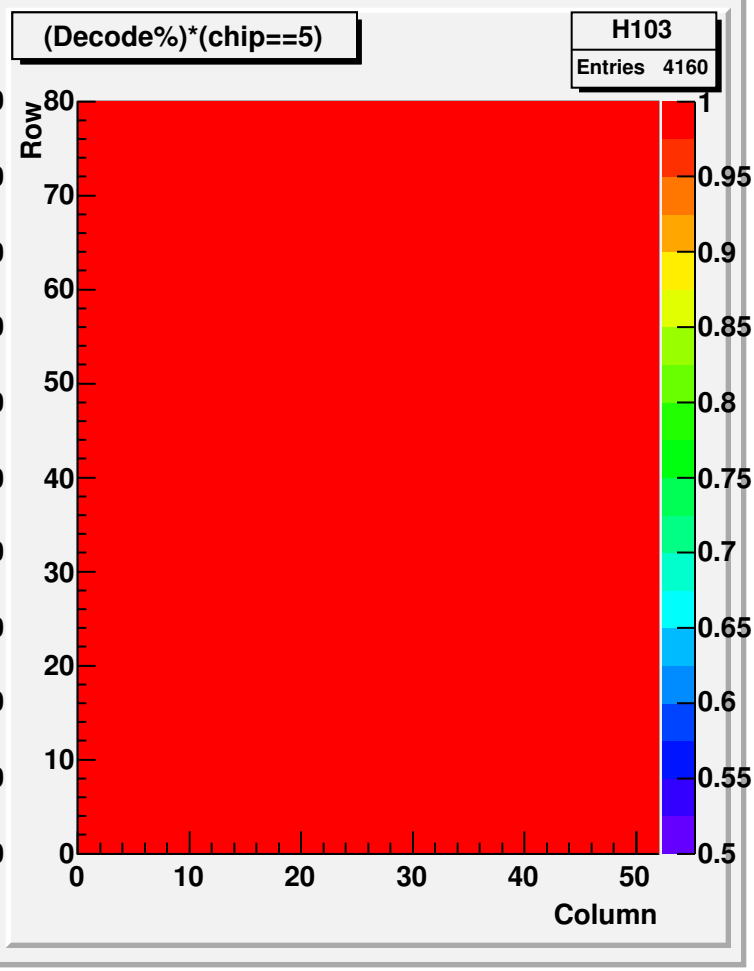
Row
Column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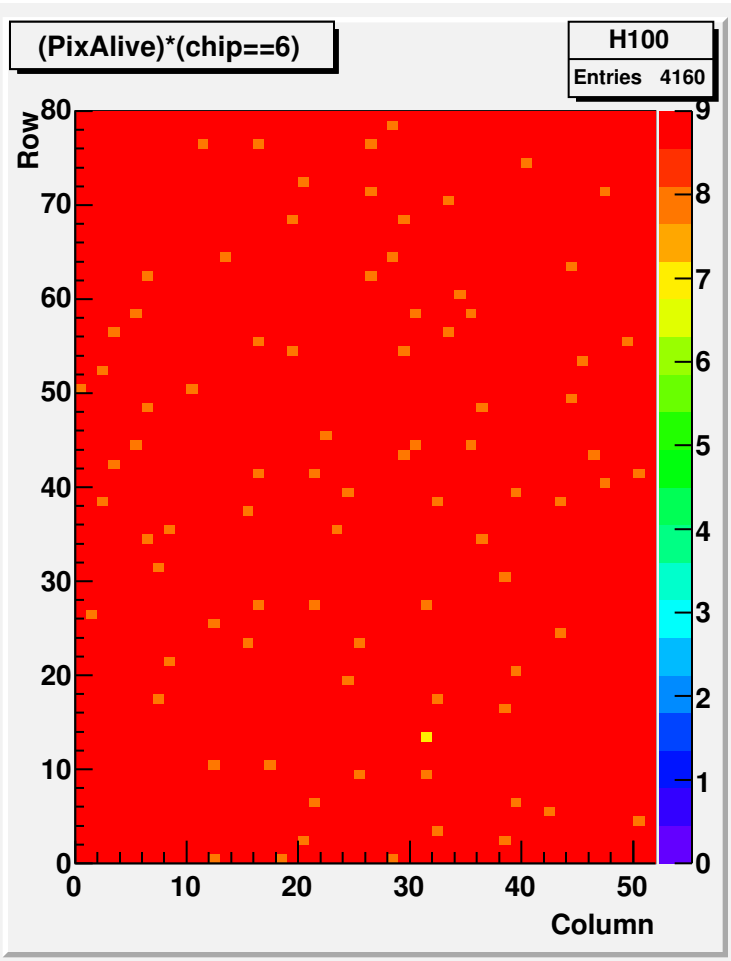
H103
Entries 4160
Row
Column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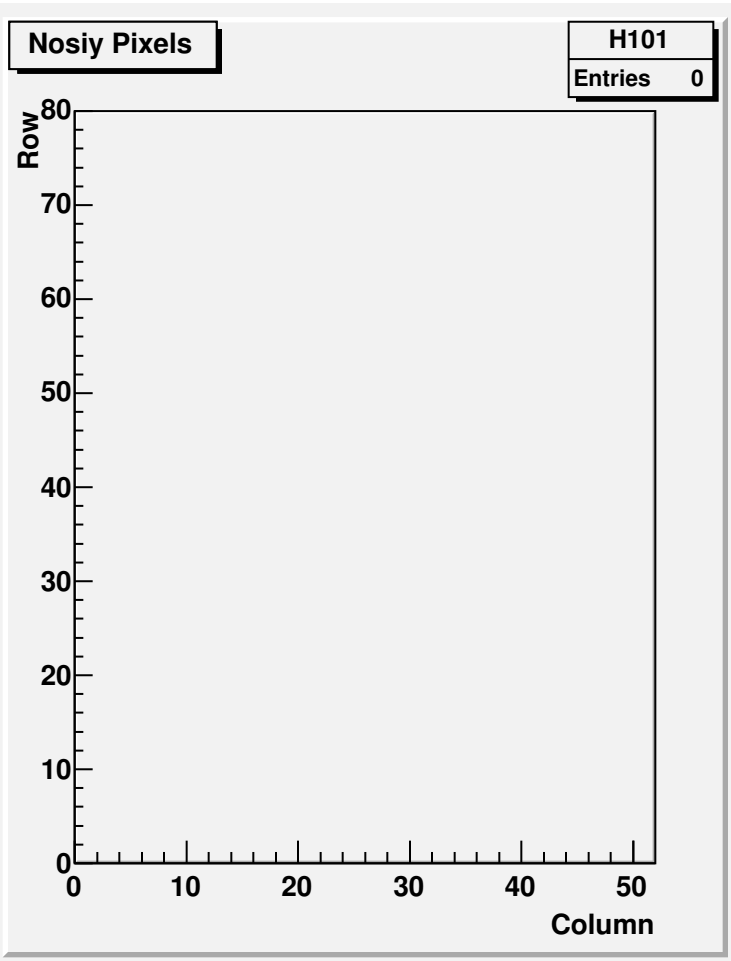
Column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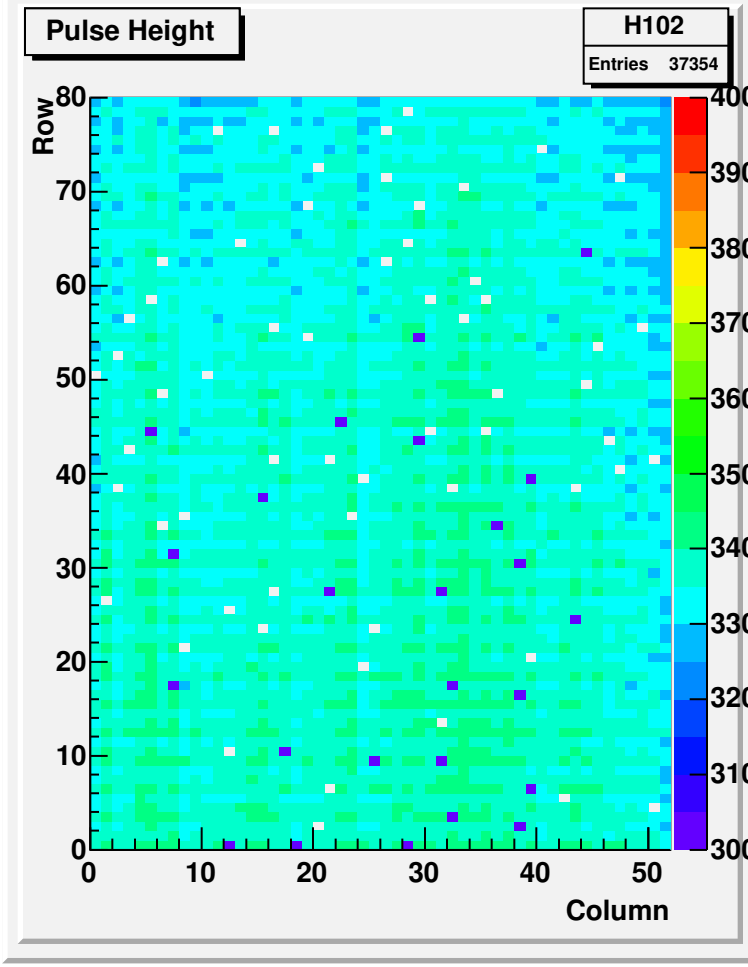
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Entries 37354
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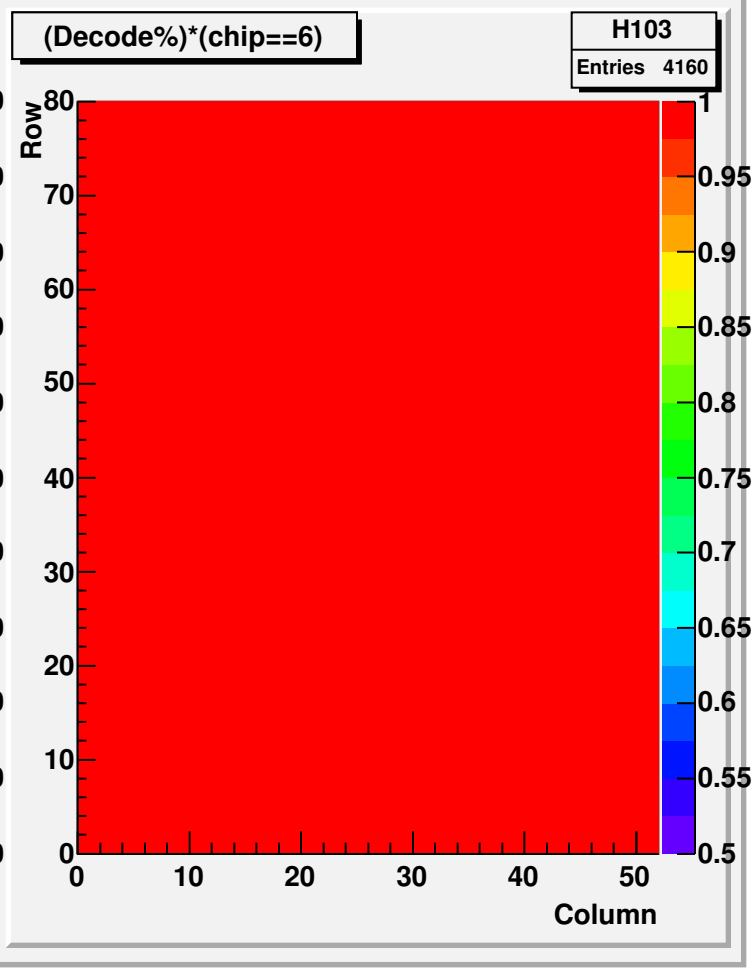
Column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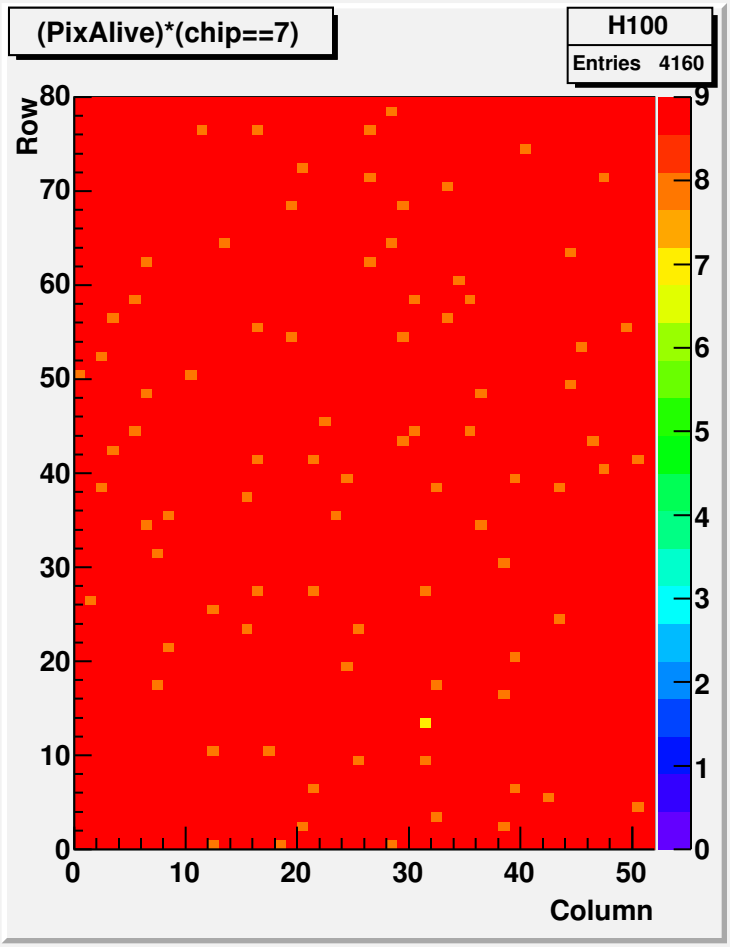
Row
Column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Column
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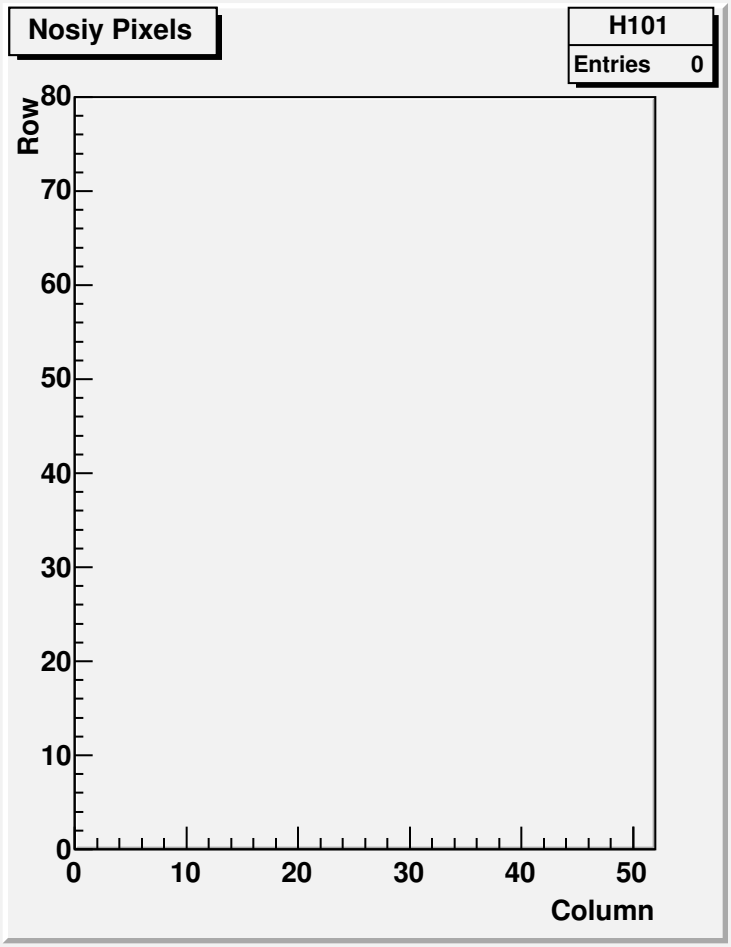
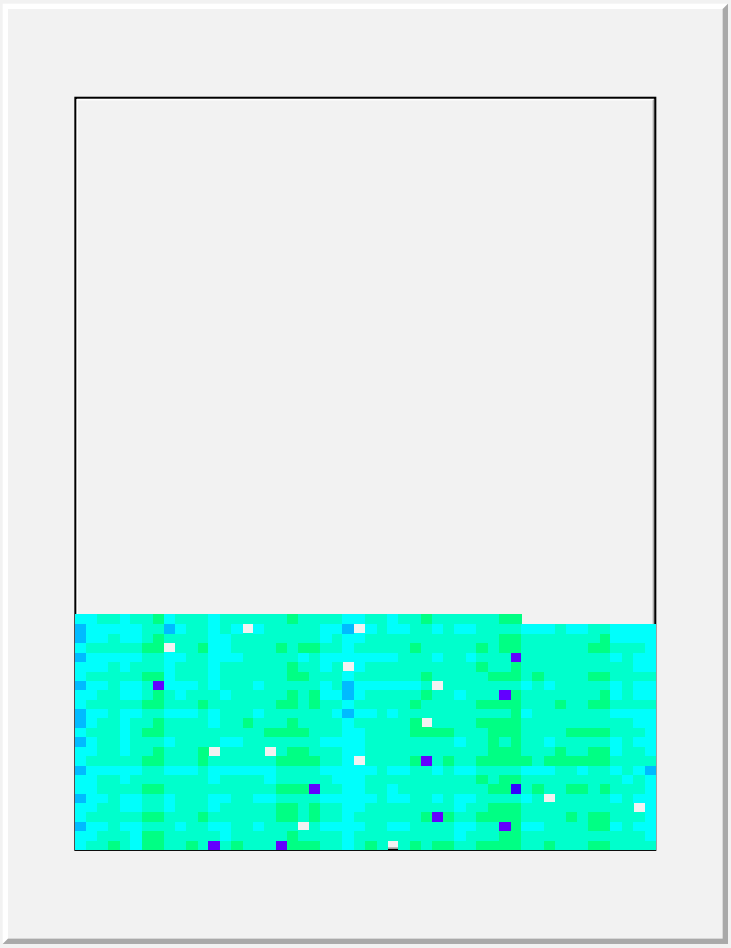
6
7
8
9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H101
Entries 0
Row
Column
0
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20
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50
60
70
80