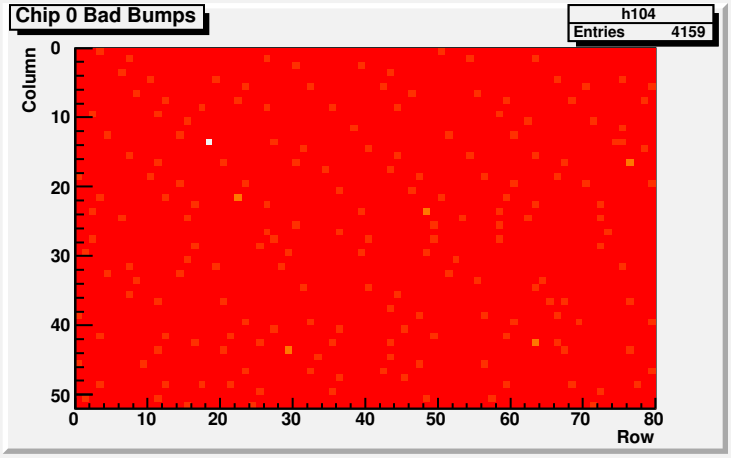
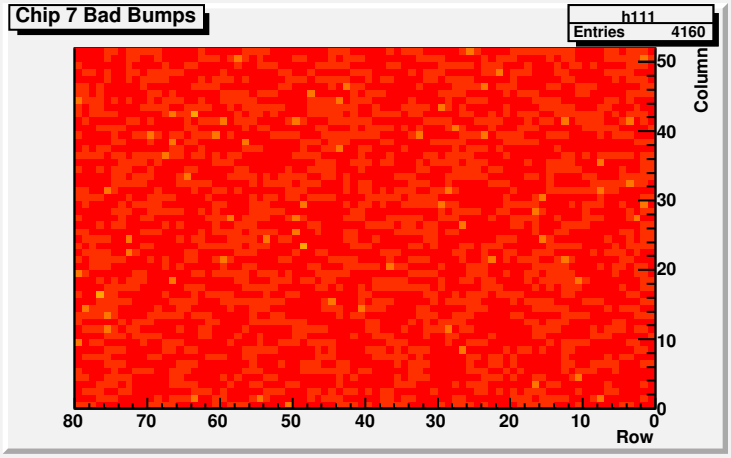
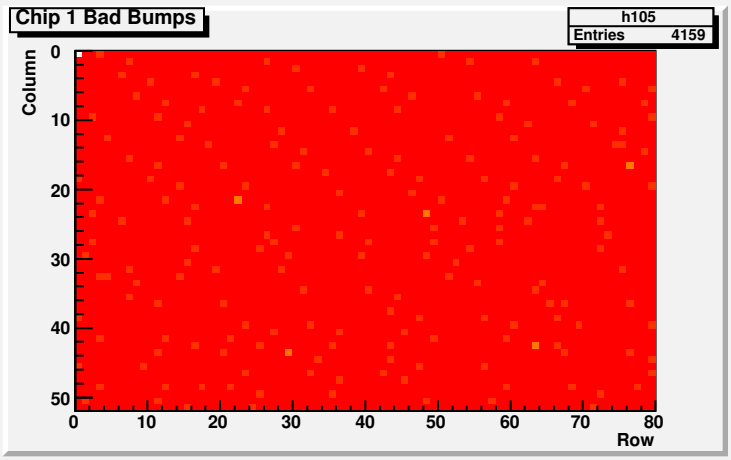
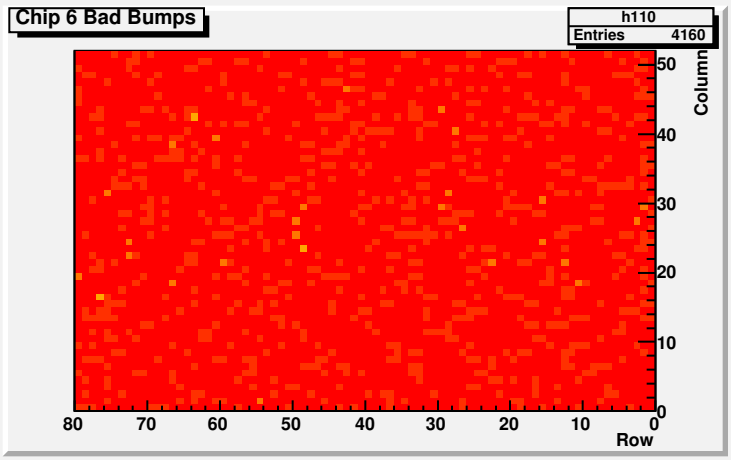
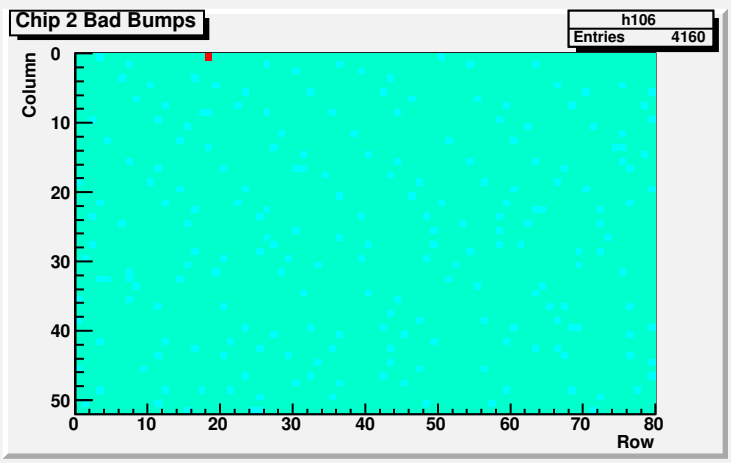
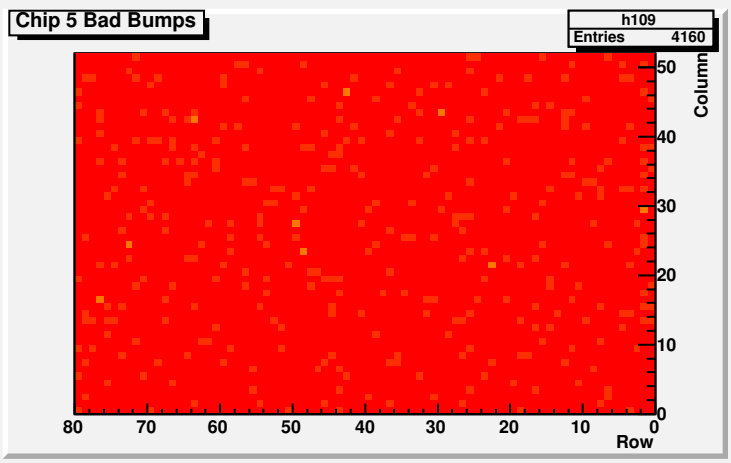

Chip 0 Bad Bumps
h104
Entries 4159
Column
Row

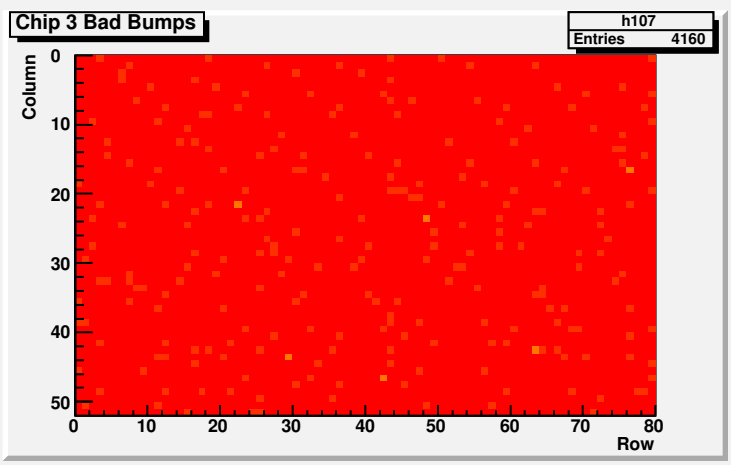

Chip 7 Bad Bumps
h111
Entries 4160
Column
Row

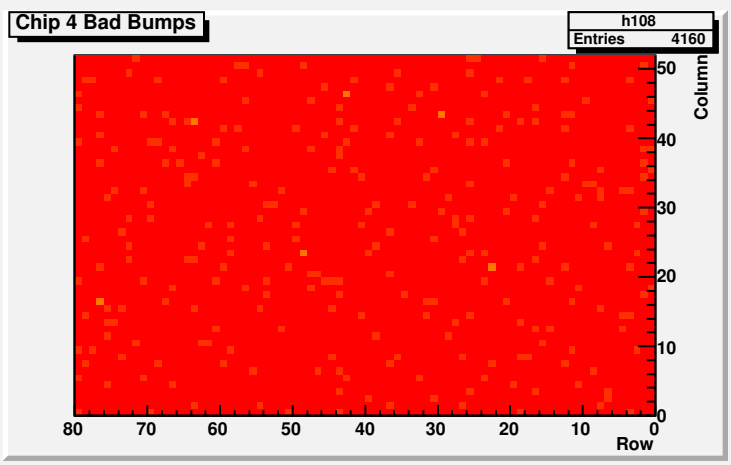

Chip 1 Bad Bumps
h105
Entries 4159
Column
Row

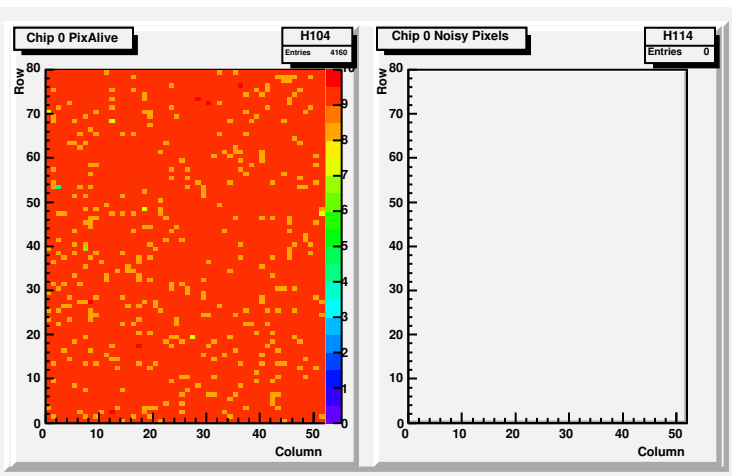

Chip 6 Bad Bumps
h110
Entries 4160
Column
Row


Chip 2 Bad Bumps
h106
Entries 4160
Column
Row

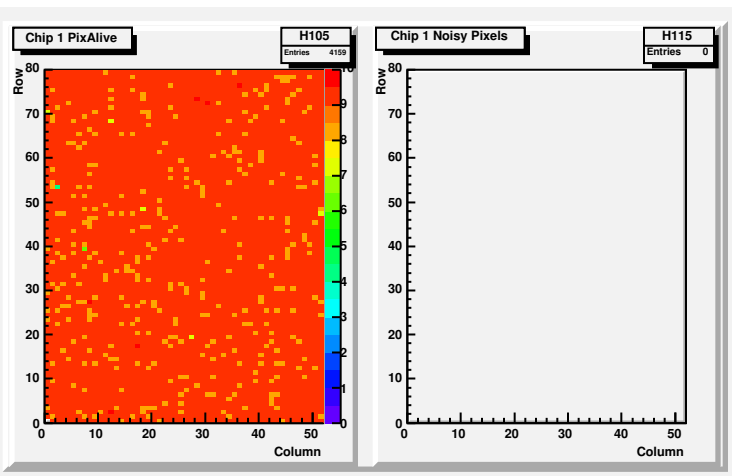

Chip 5 Bad Bumps
h109
Entries 4160
Column
Row


Chip 3 Bad Bumps
h107
Entries 4160
Column
Row

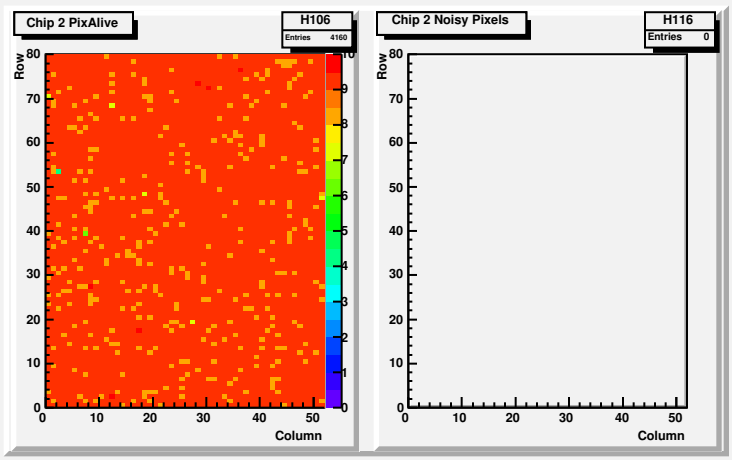

Chip 4 Bad Bumps
h108
Entries 4160
Column
Row


Chip 0 PixAlive
H104
Entries 4160
Row
Column

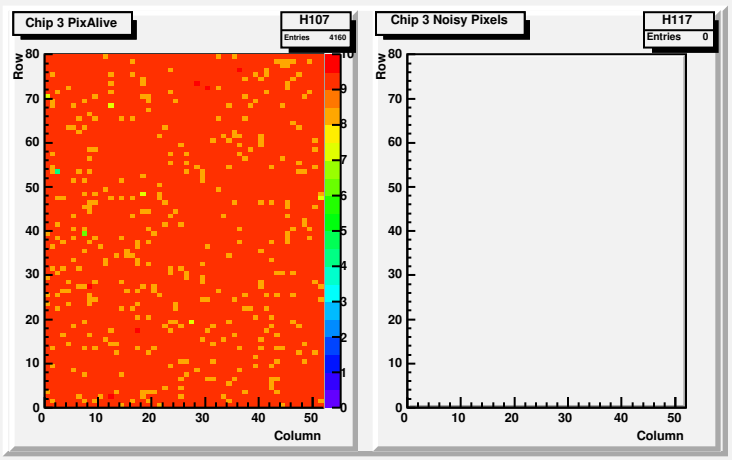
Chip 0 Noisy Pixels
H114
Entries 0
Row
Column


Chip 1 PixAlive
H105
Entries 4159
Row
Column

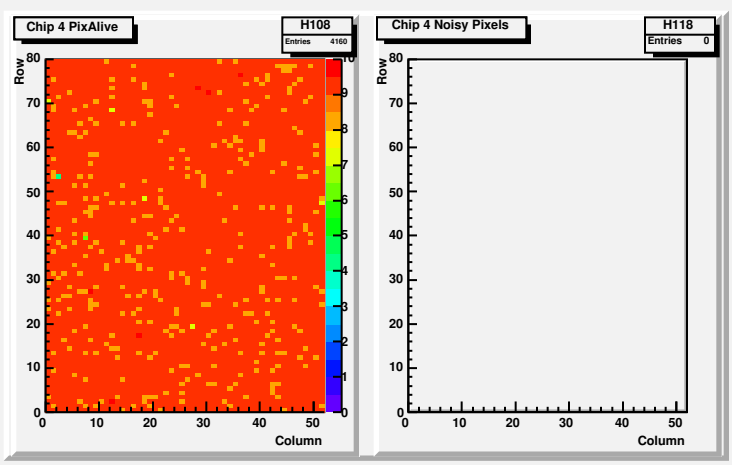
Chip 1 Noisy Pixels
H115
Entries 0
Row
Column


Chip 2 PixAlive
H106
Entries 4160
Row
Column

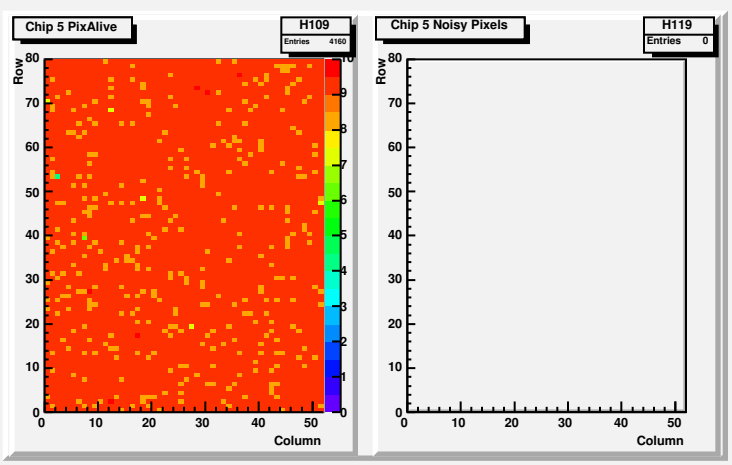
Chip 2 Noisy Pixels
H116
Entries 0
Row
Column


Chip 3 PixAlive
H107
Entries 4160
Row
Column

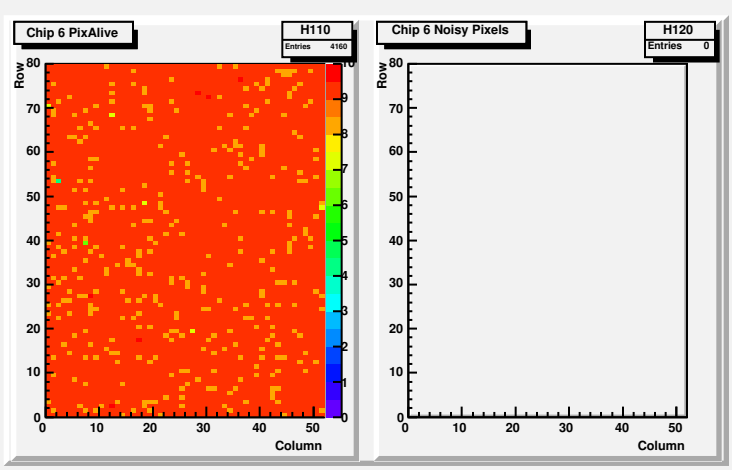
Chip 3 Noisy Pixels
H117
Entries 0
Row
Column


Chip 4 PixAlive
H108
Entries 4160
Row
Column

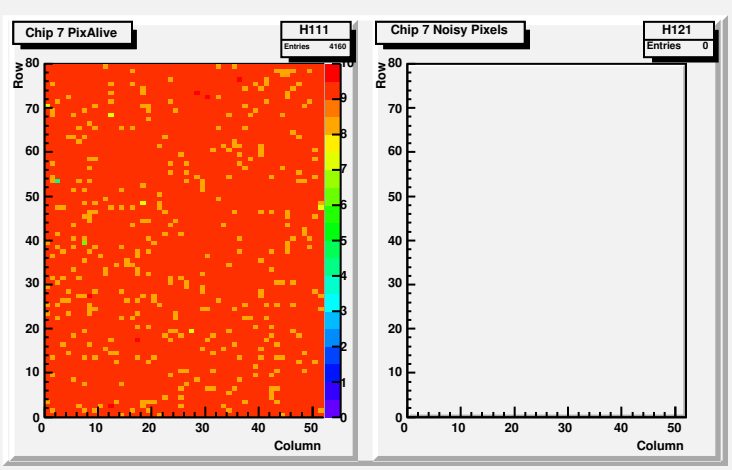
Chip 4 Noisy Pixels
H118
Entries 0
Row
Column


Chip 5 PixAlive
H109
Entries 4160
Row
Column

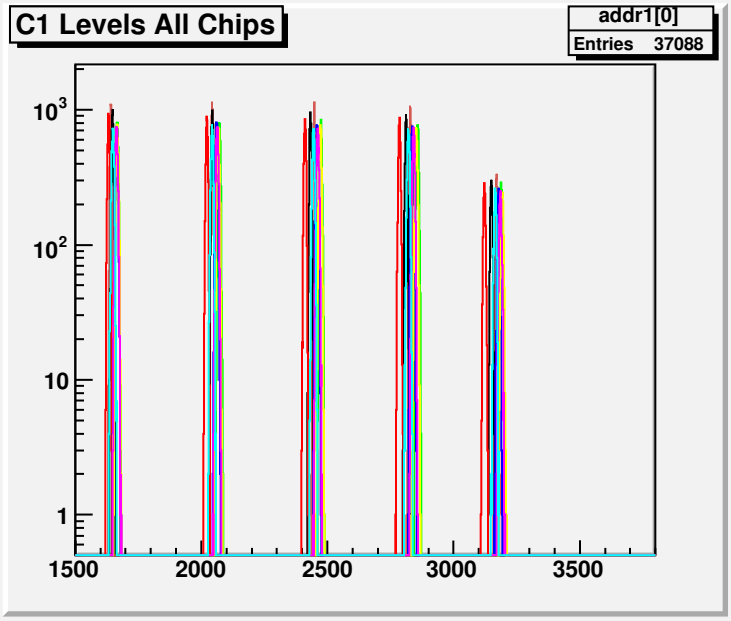
Chip 5 Noisy Pixels
H119
Entries 0
Row
Column

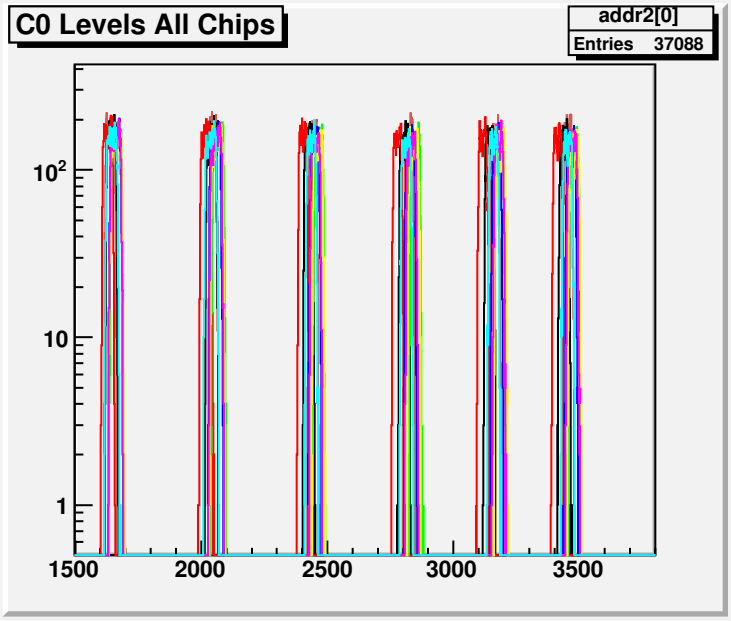

Chip 6 PixAlive
H110
Entries 4160
Row
Column

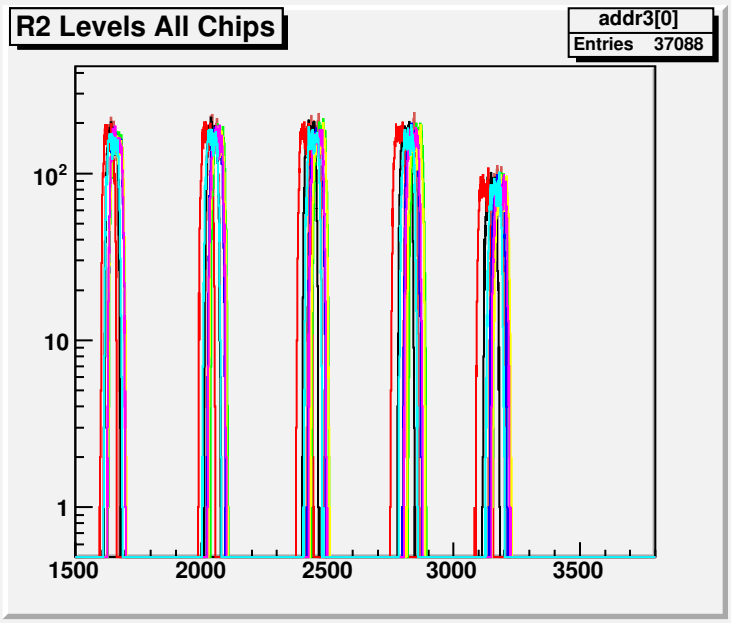
Chip 6 Noisy Pixels
H120
Entries 0
Row
Column

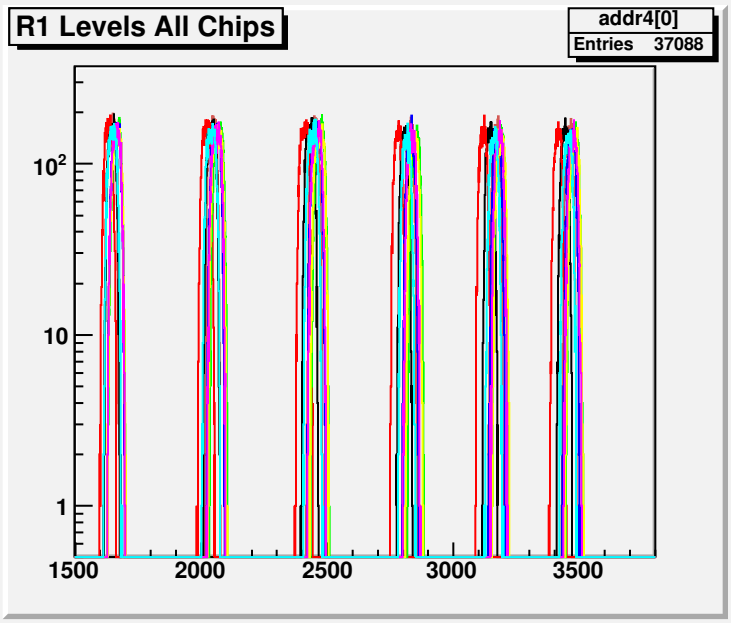

Chip 7 PixAlive
H111
Entries 4160
Row
Column

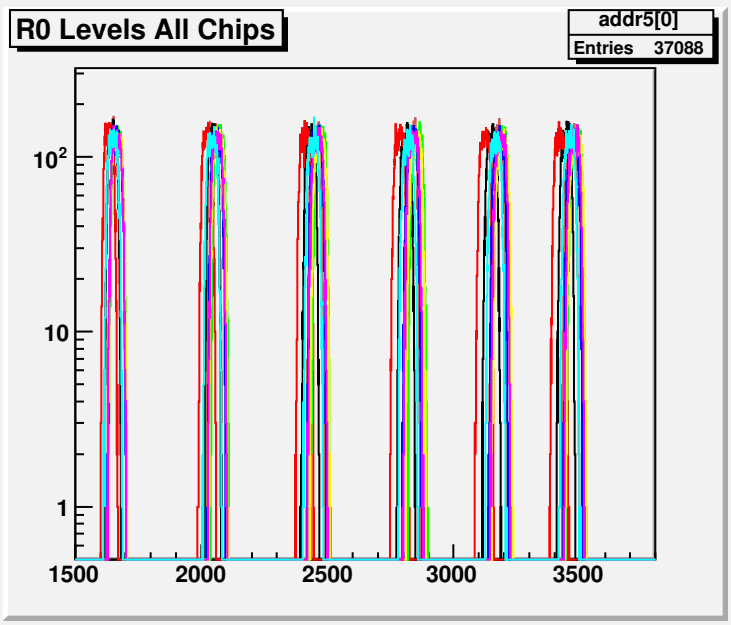
Chip 7 Noisy Pixels
H121
Entries 0
Row
Column

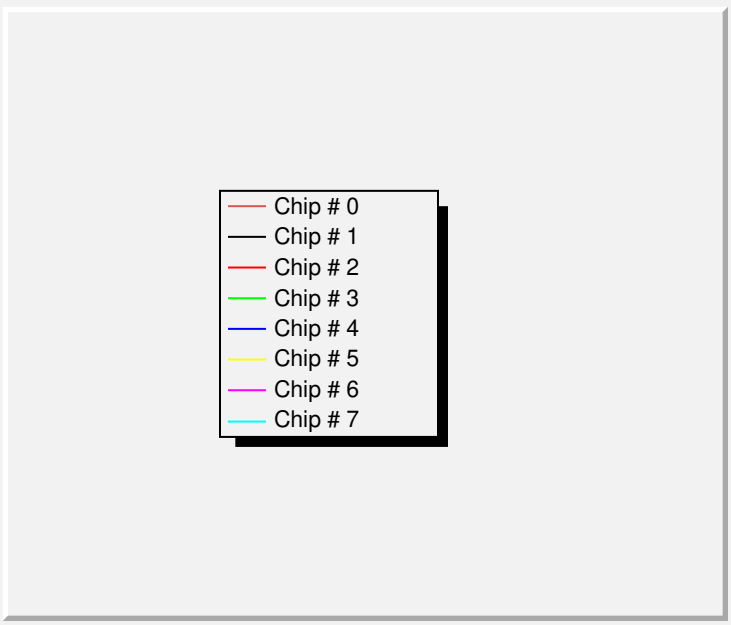

C1 Levels All Chips
addr1[0]
Entries 37088
10^3
10^2
10
1
1500
2000
2500
3000
3500

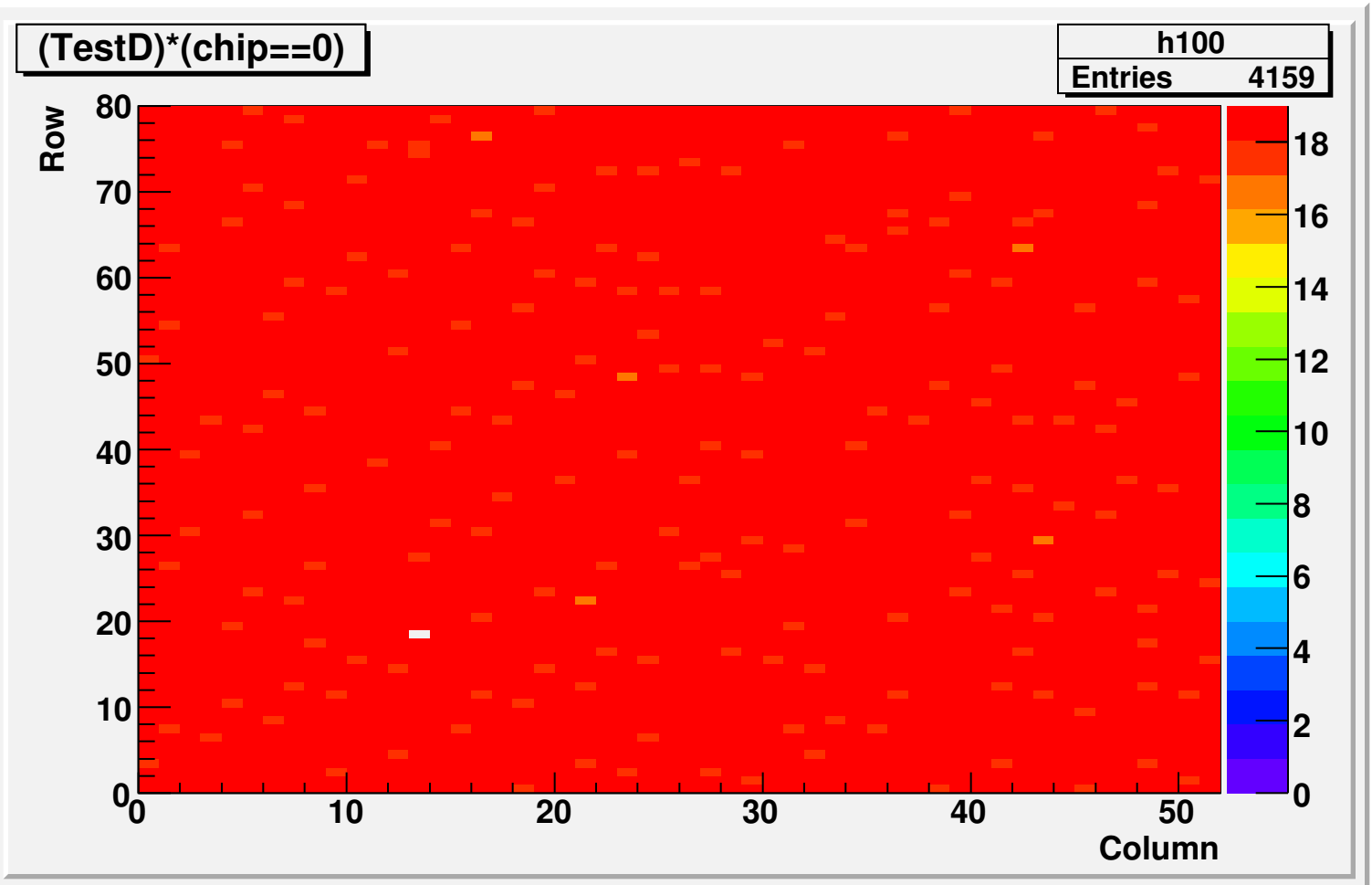

C0 Levels All Chips
addr2[0]
Entries 37088
10^2
10
1
1500
2000
2500
3000
3500

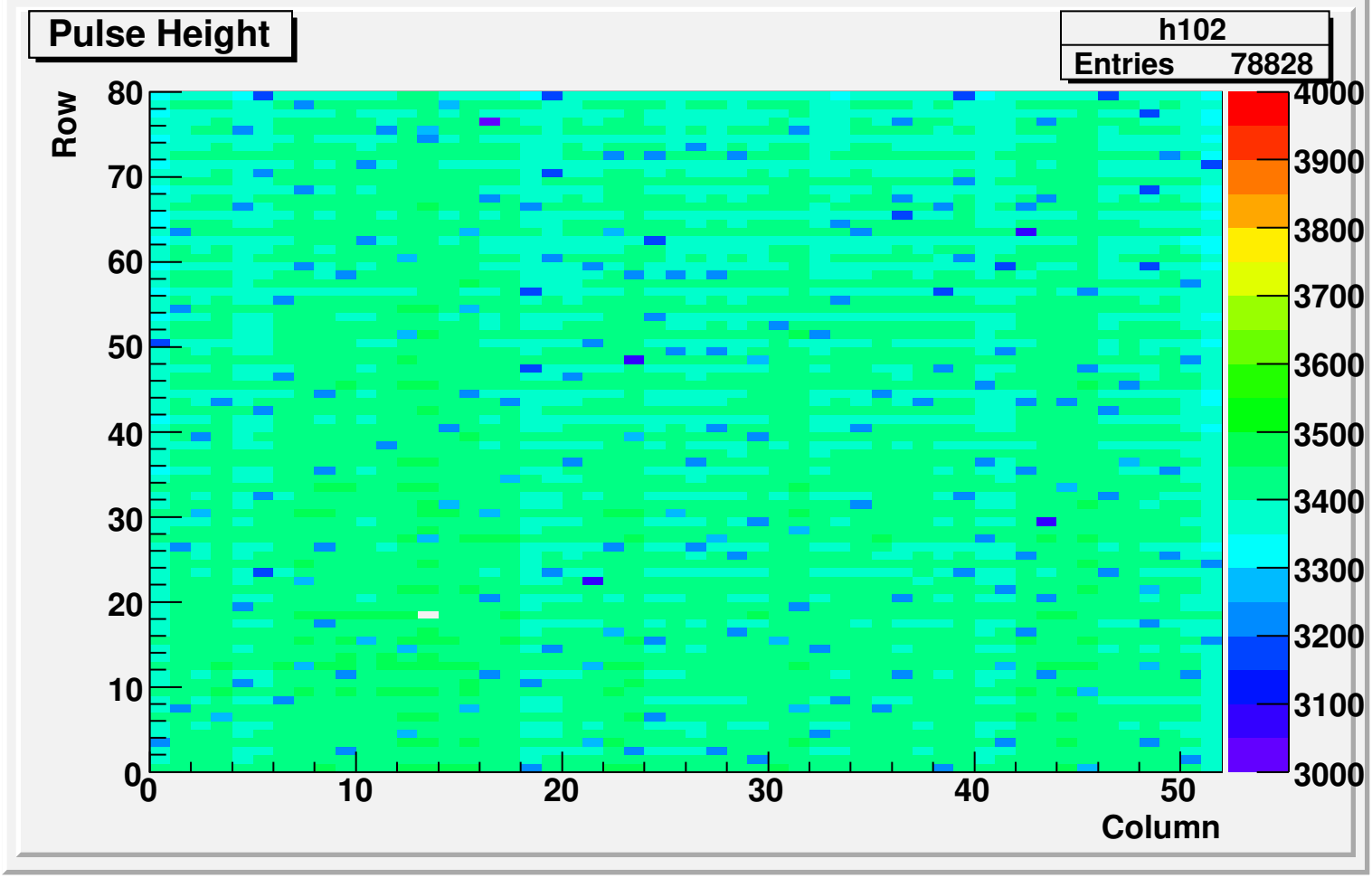

R2 Levels All Chips
addr3[0]
Entries 37088
10^2
10
1
1500
2000
2500
3000
3500

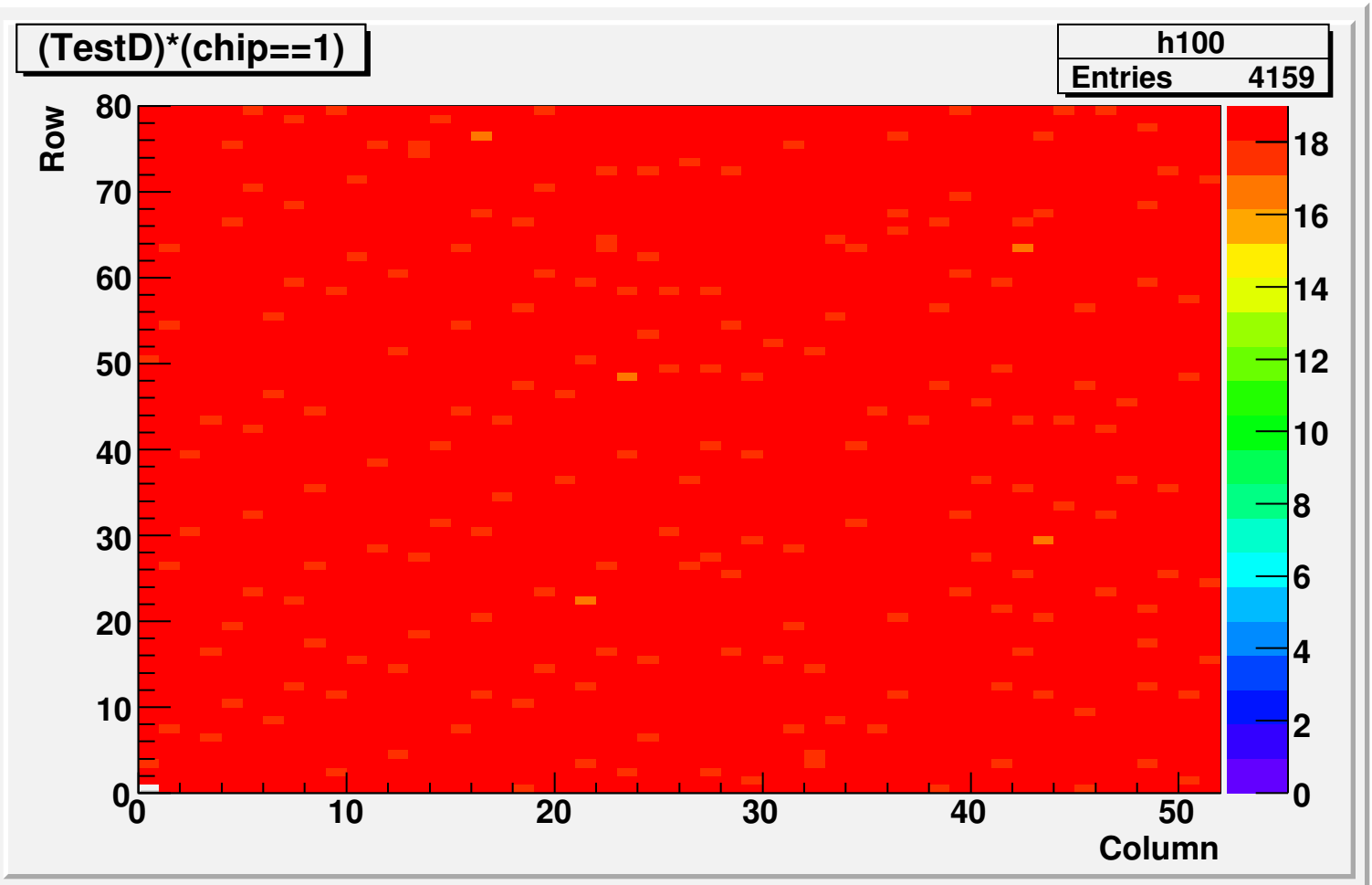

R1 Levels All Chips
addr4[0]
Entries 37088
10^2
10
1
1500
2000
2500
3000
3500

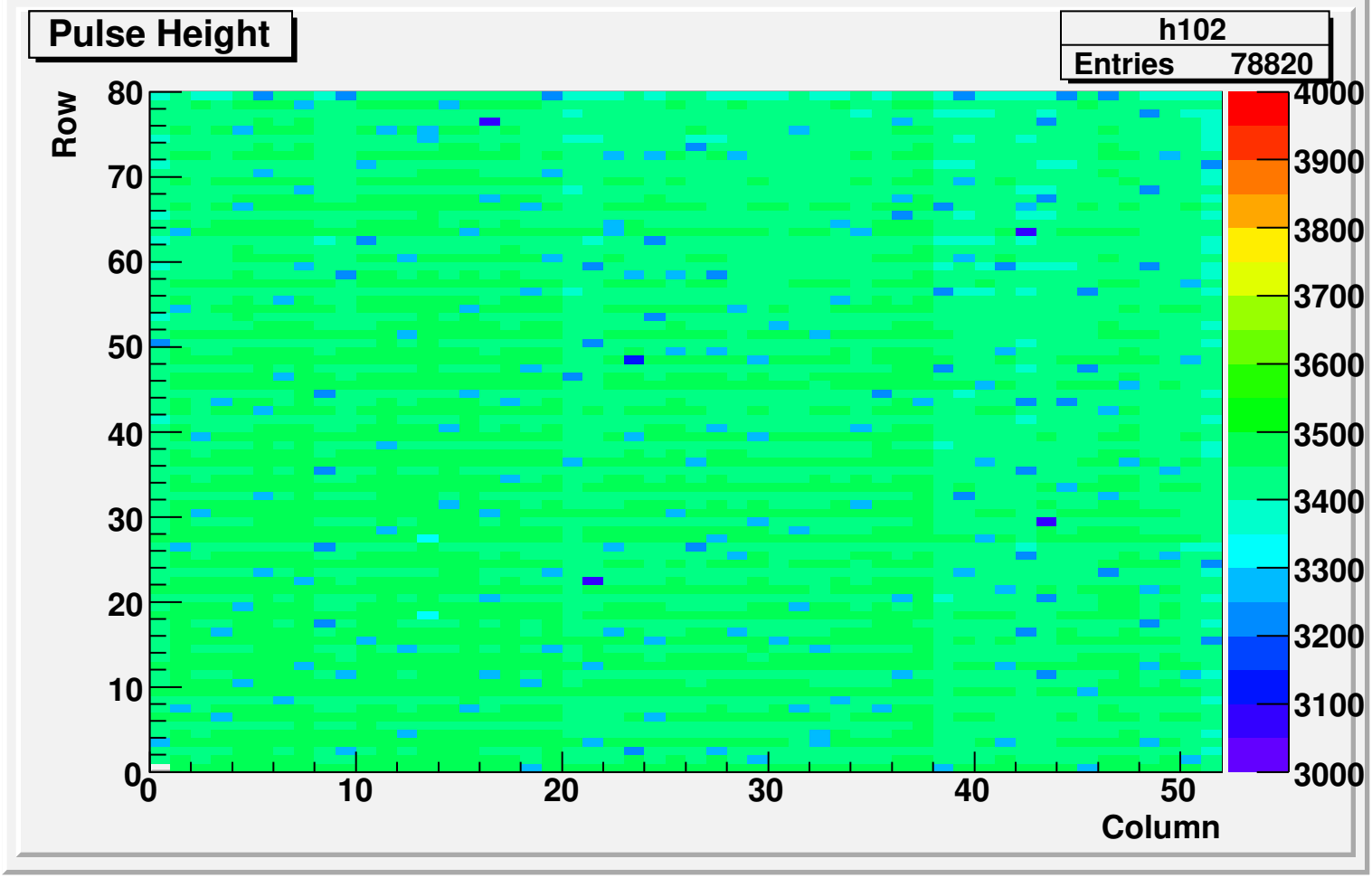

R0 Levels All Chips
addr5[0]
Entries 37088
10^2
10
1
1500
2000
2500
3000
3500

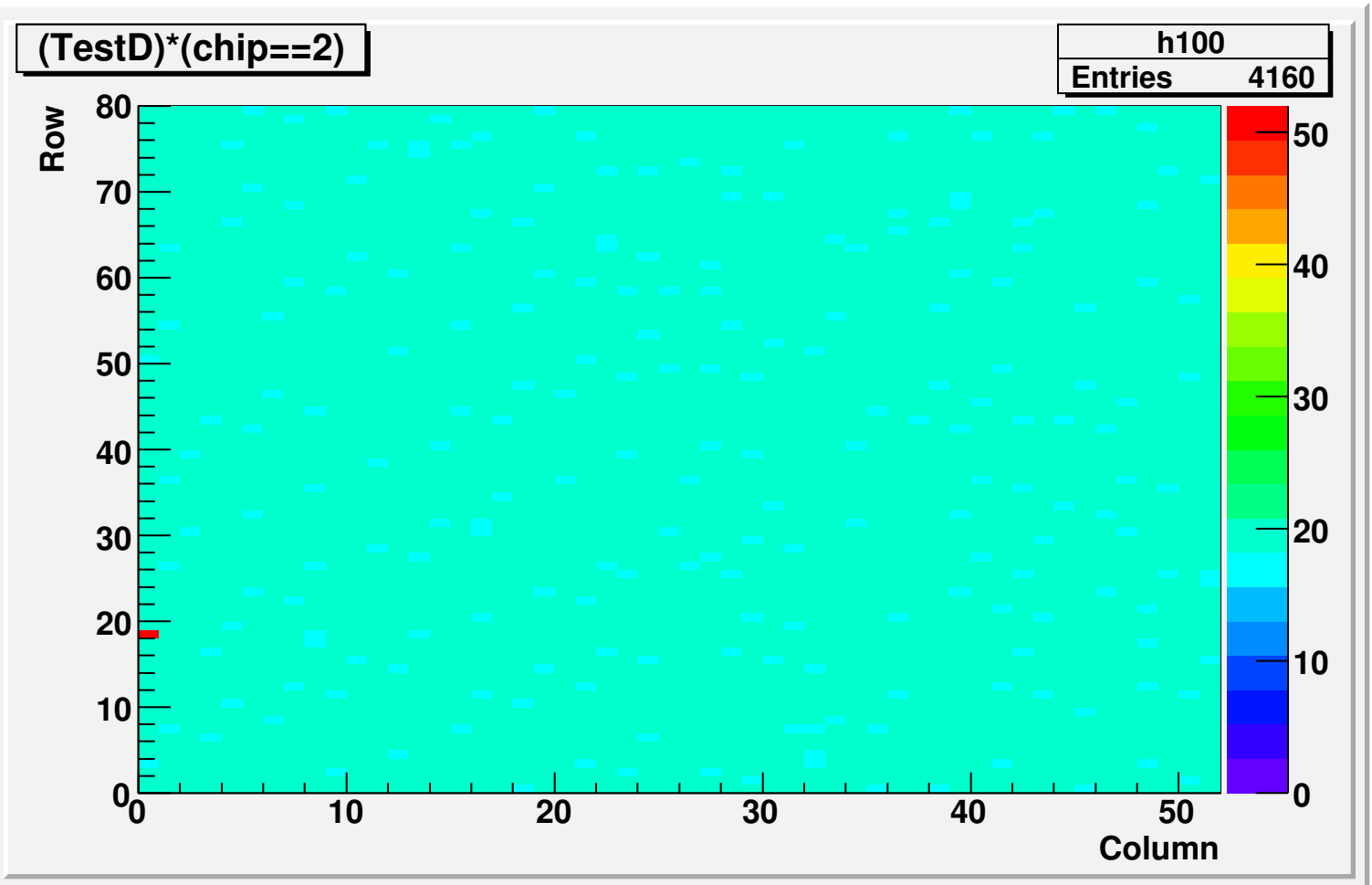

Chip # 0
Chip # 1
Chip # 2
Chip # 3
Chip # 4
Chip # 5
Chip # 6
Chip # 7

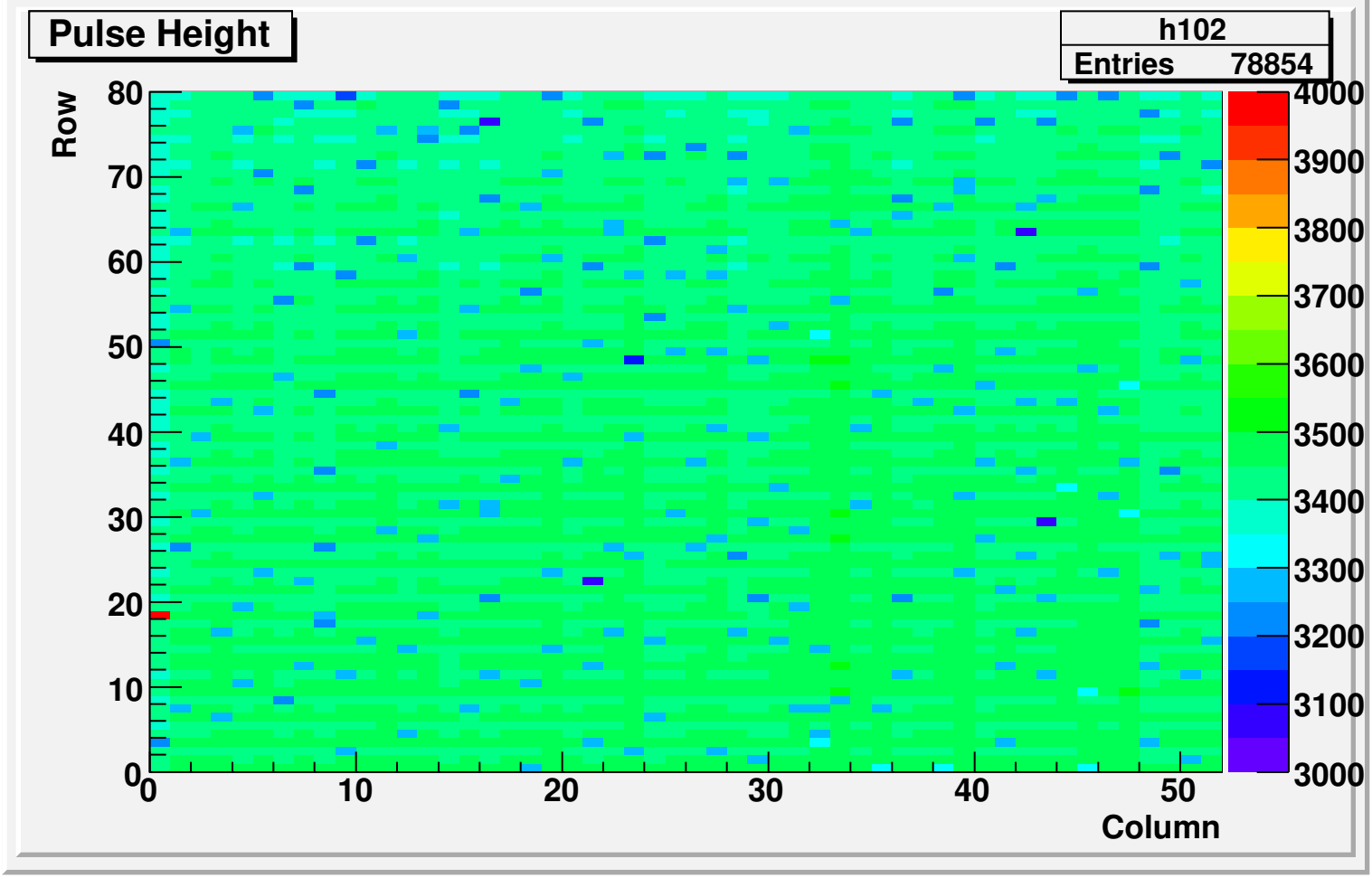

(TestD)*(chip==0)
h100
Entries 4159
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

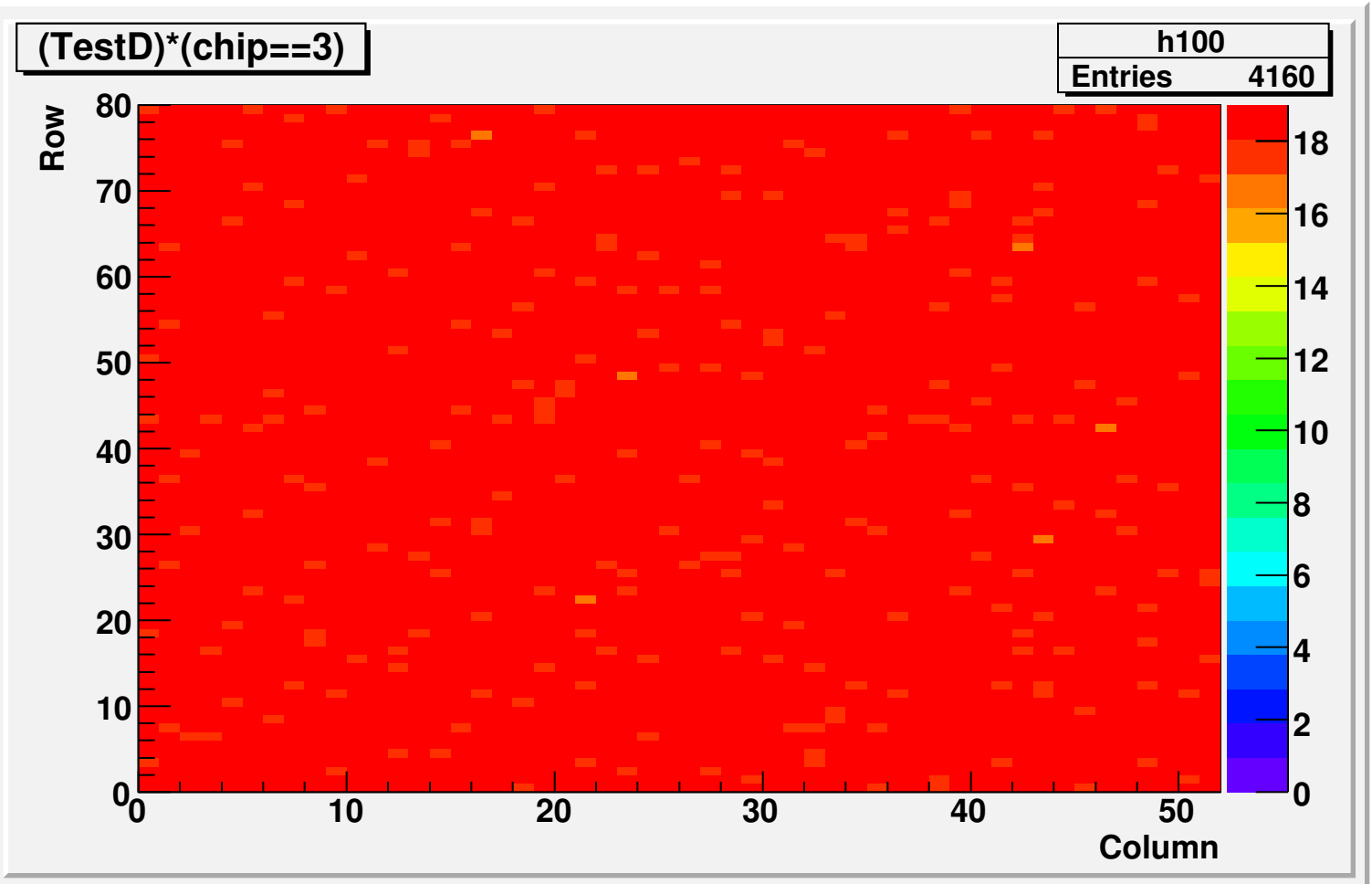

Pulse Height
h102
Entries 78828
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

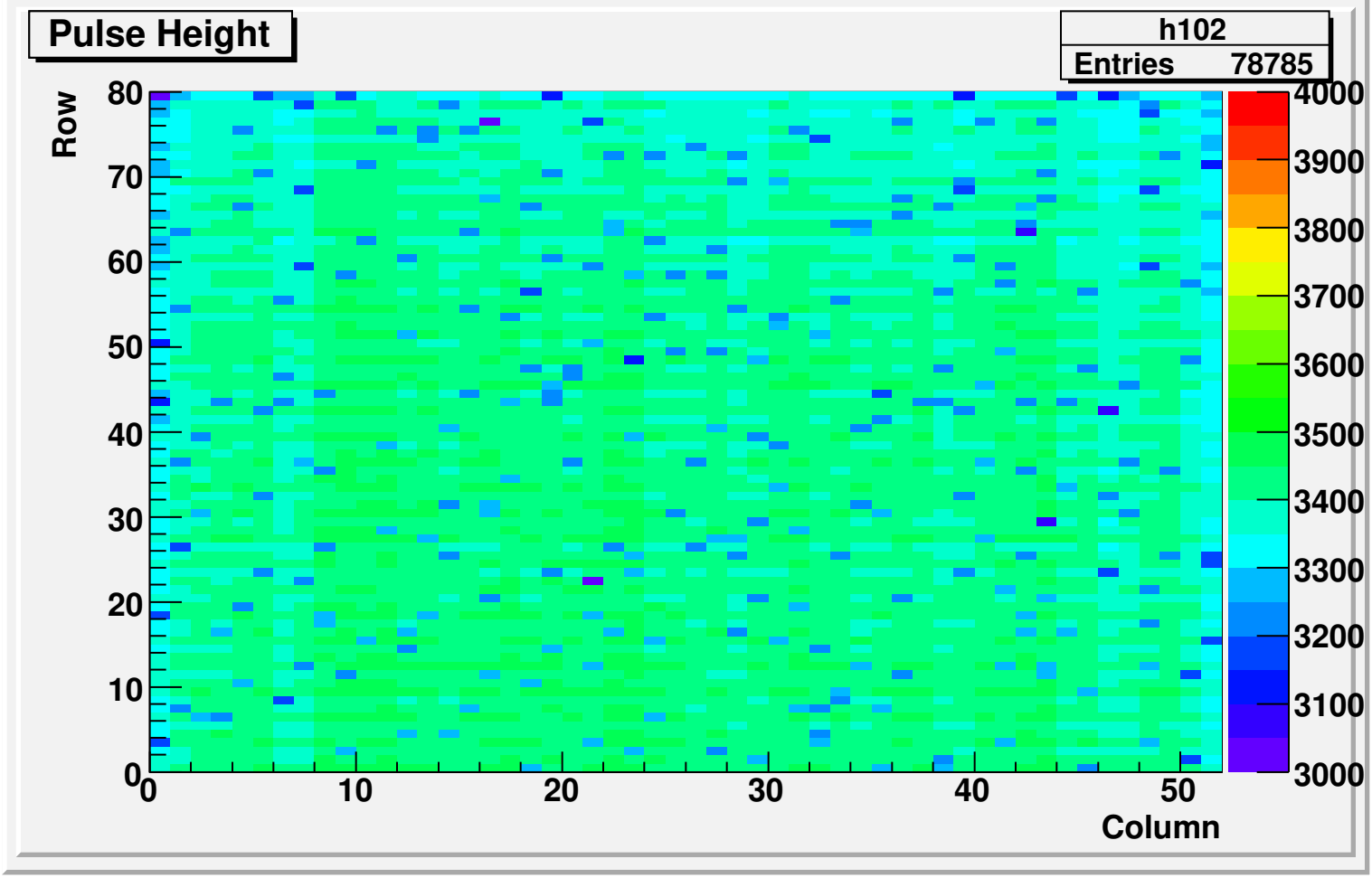

(TestD)*(chip==1)
h100
Entries 4159
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

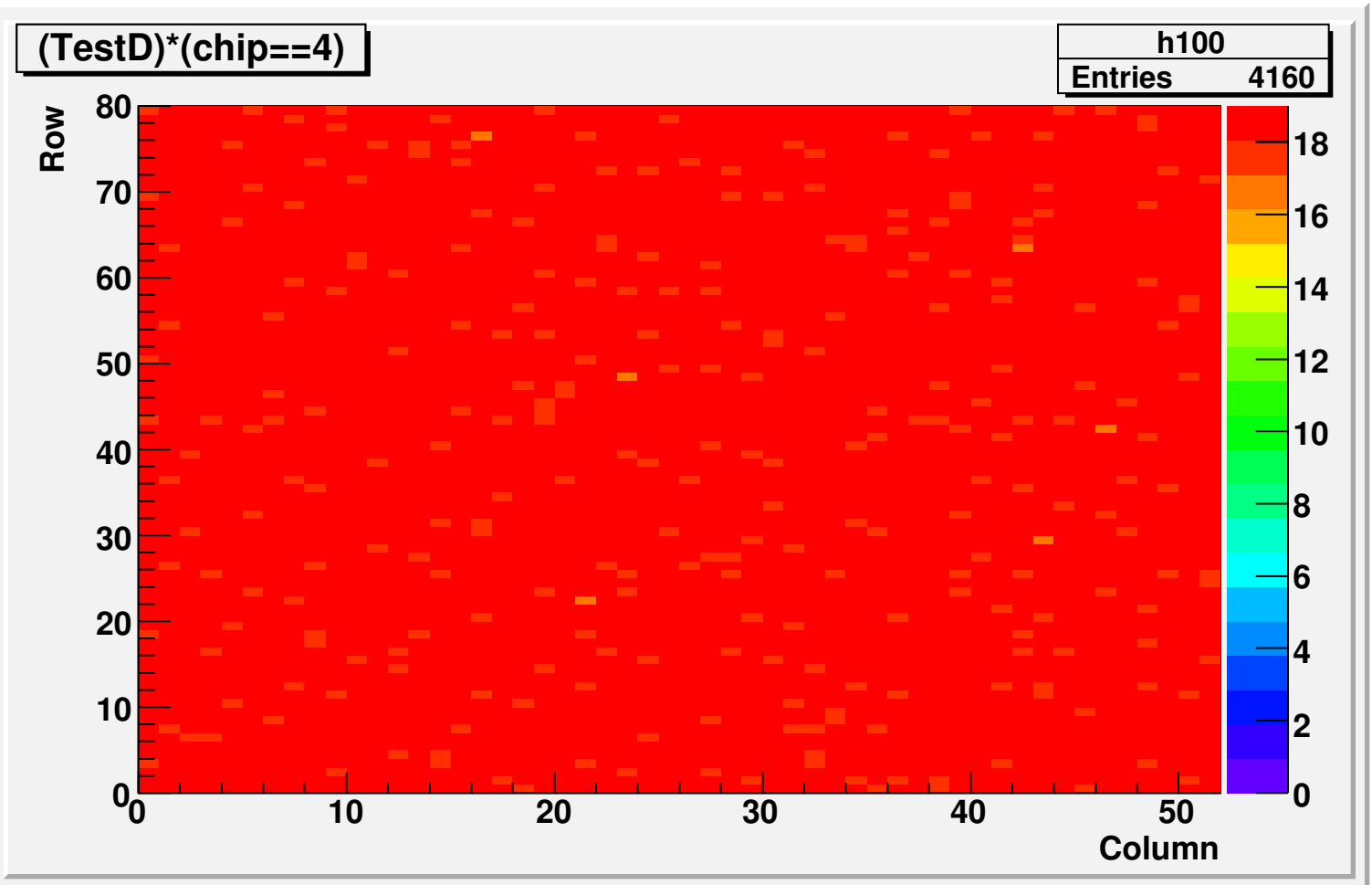

Pulse Height
h102
Entries 78820
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

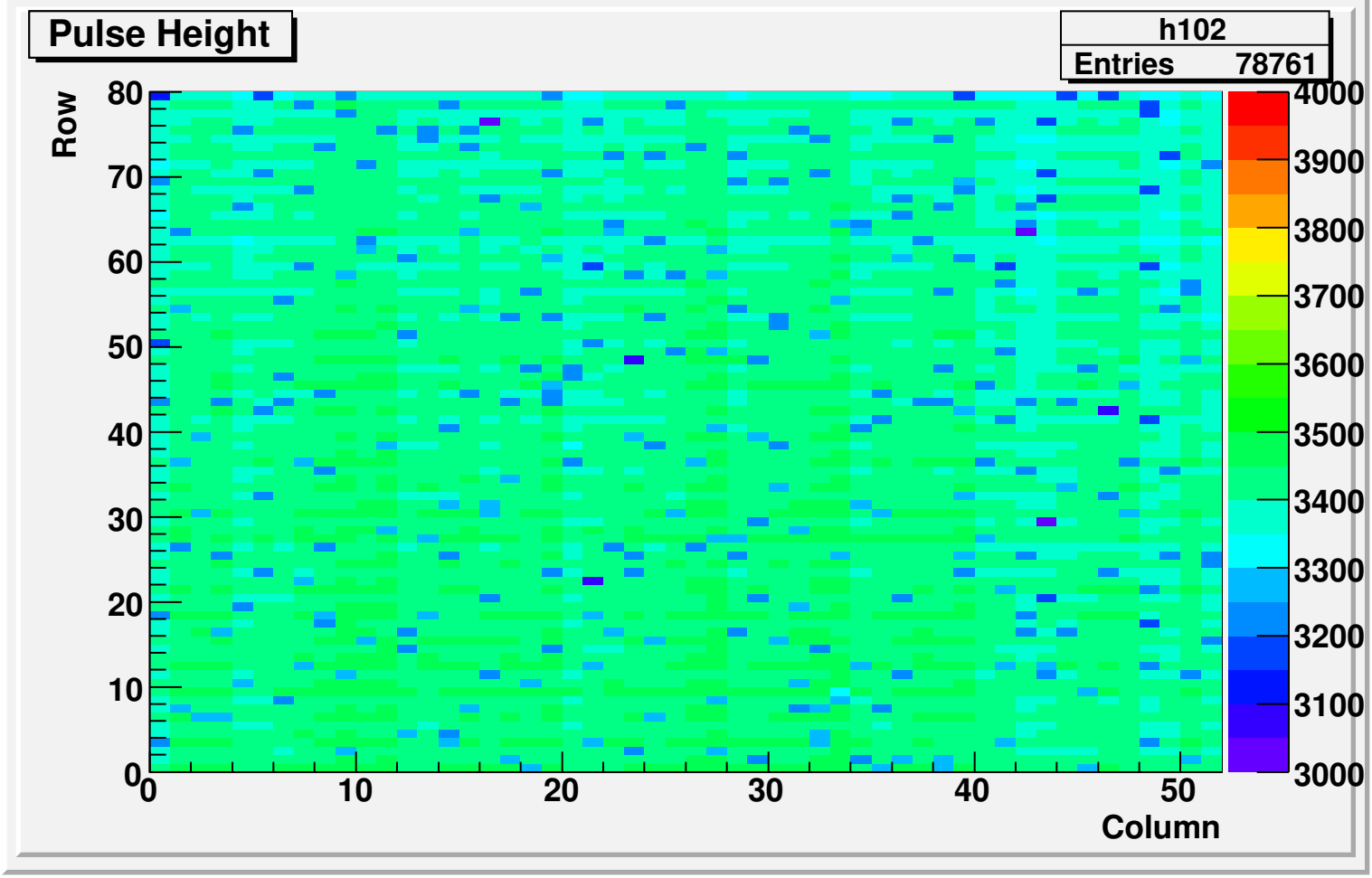

(TestD)*(chip==2)
h100
Entries 4160
Row
Column
0
10
20
30
40
50
60
70
80
0
10
20
30
40
50

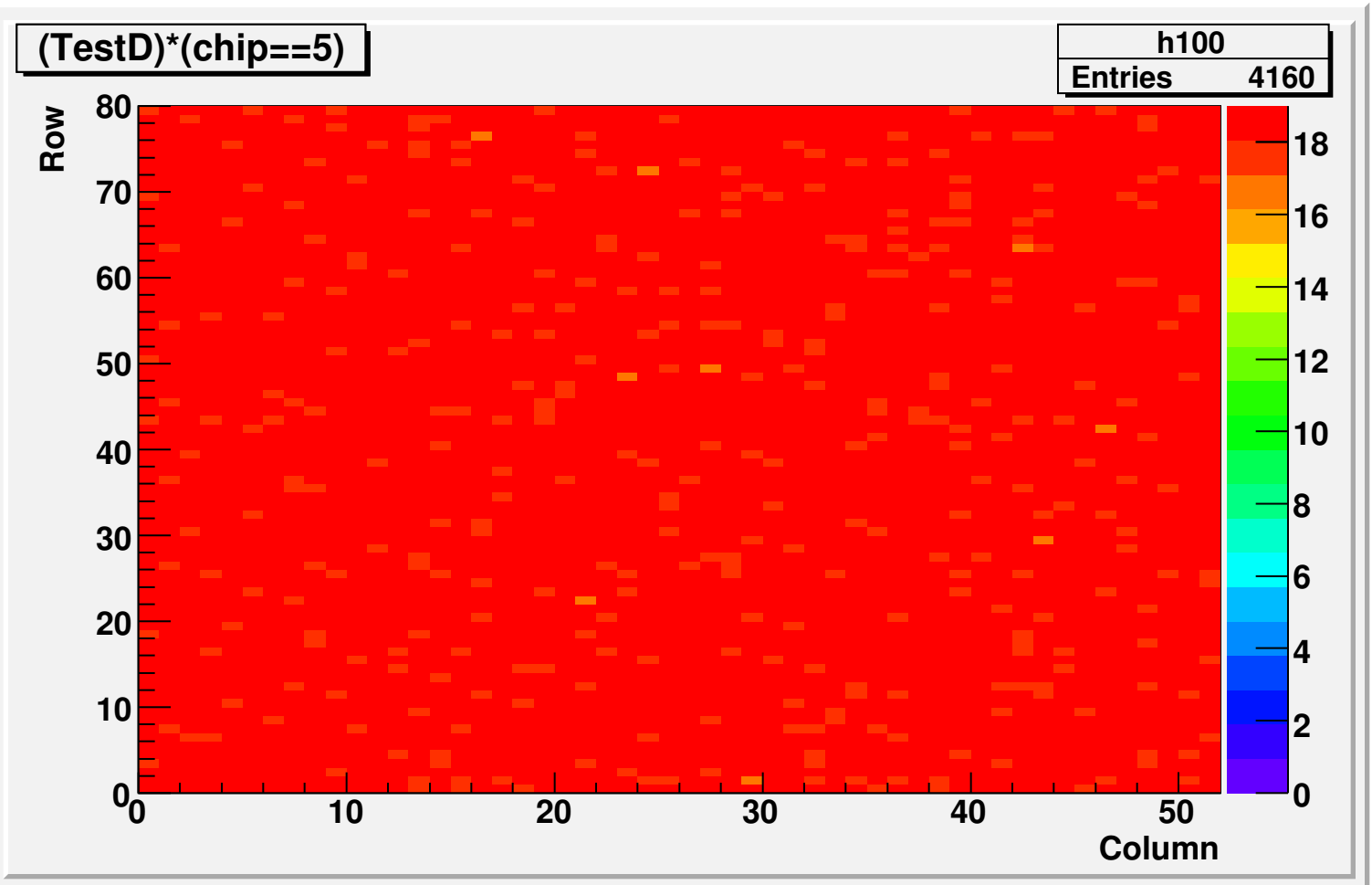

Pulse Height
h102
Entries 78854
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

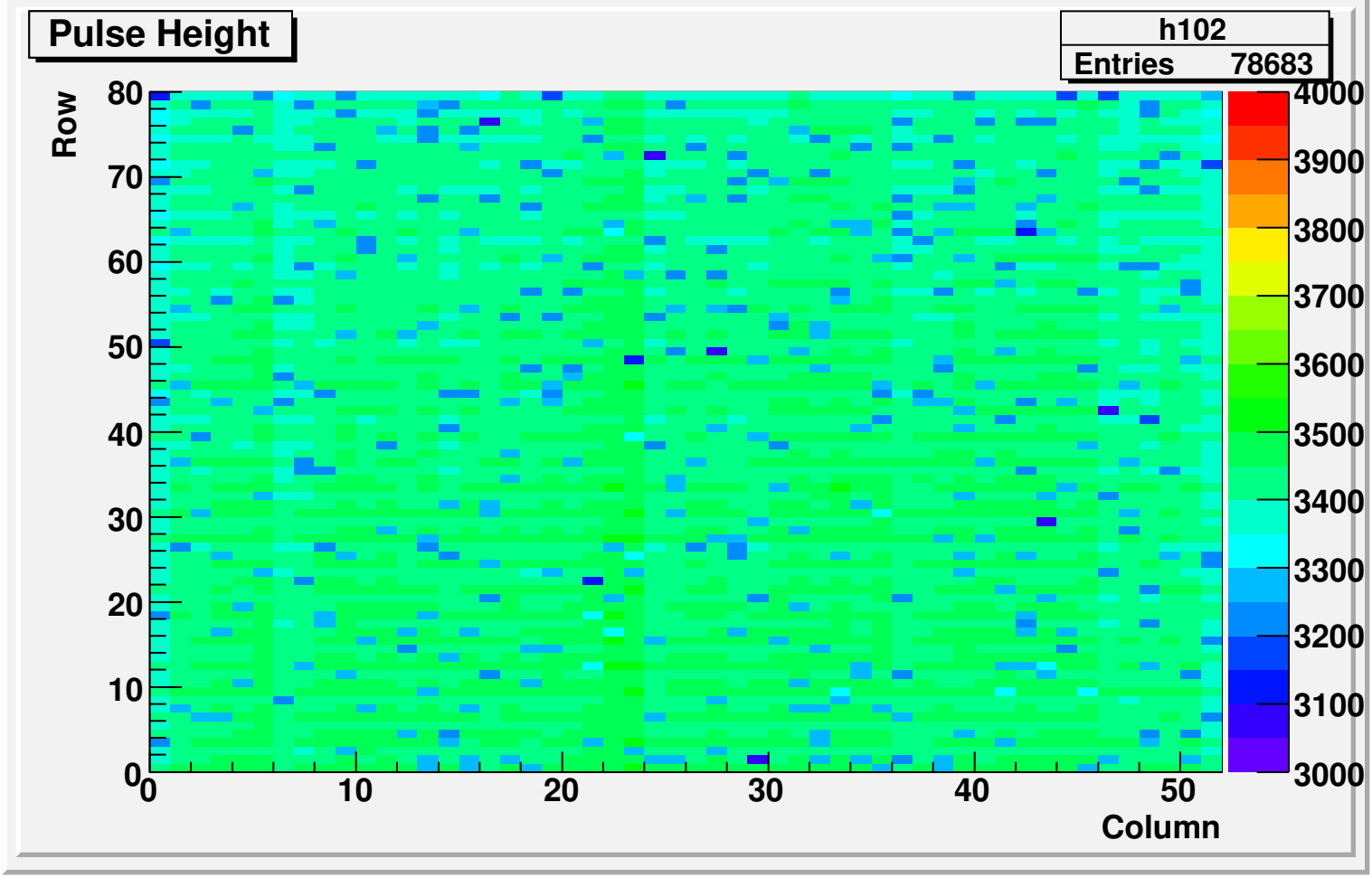

(TestD)*(chip==3)
h100
Entries 4160
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

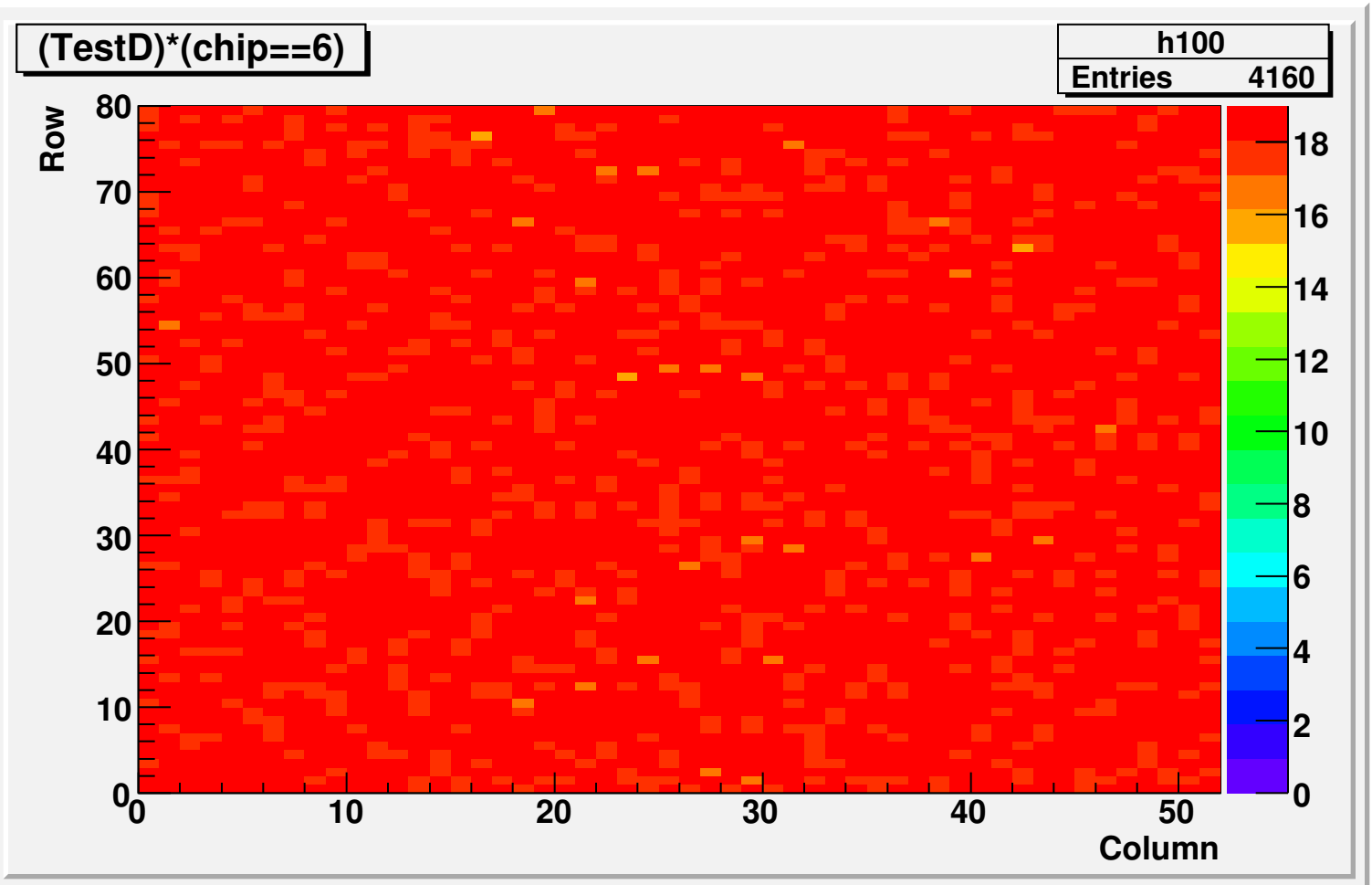

Pulse Height
h102
Entries 78785
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

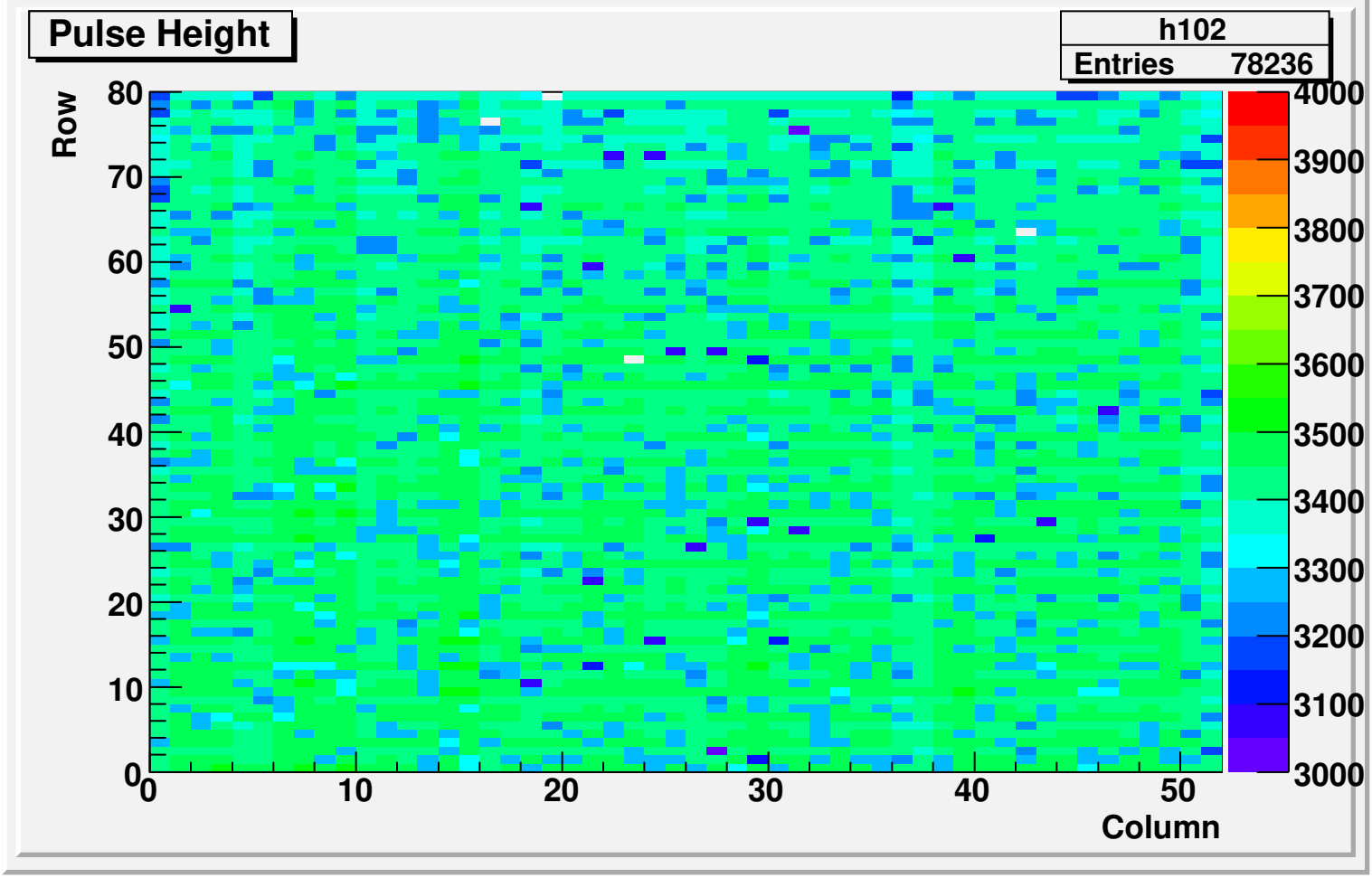

(TestD)*(chip==4)
h100
Entries 4160
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

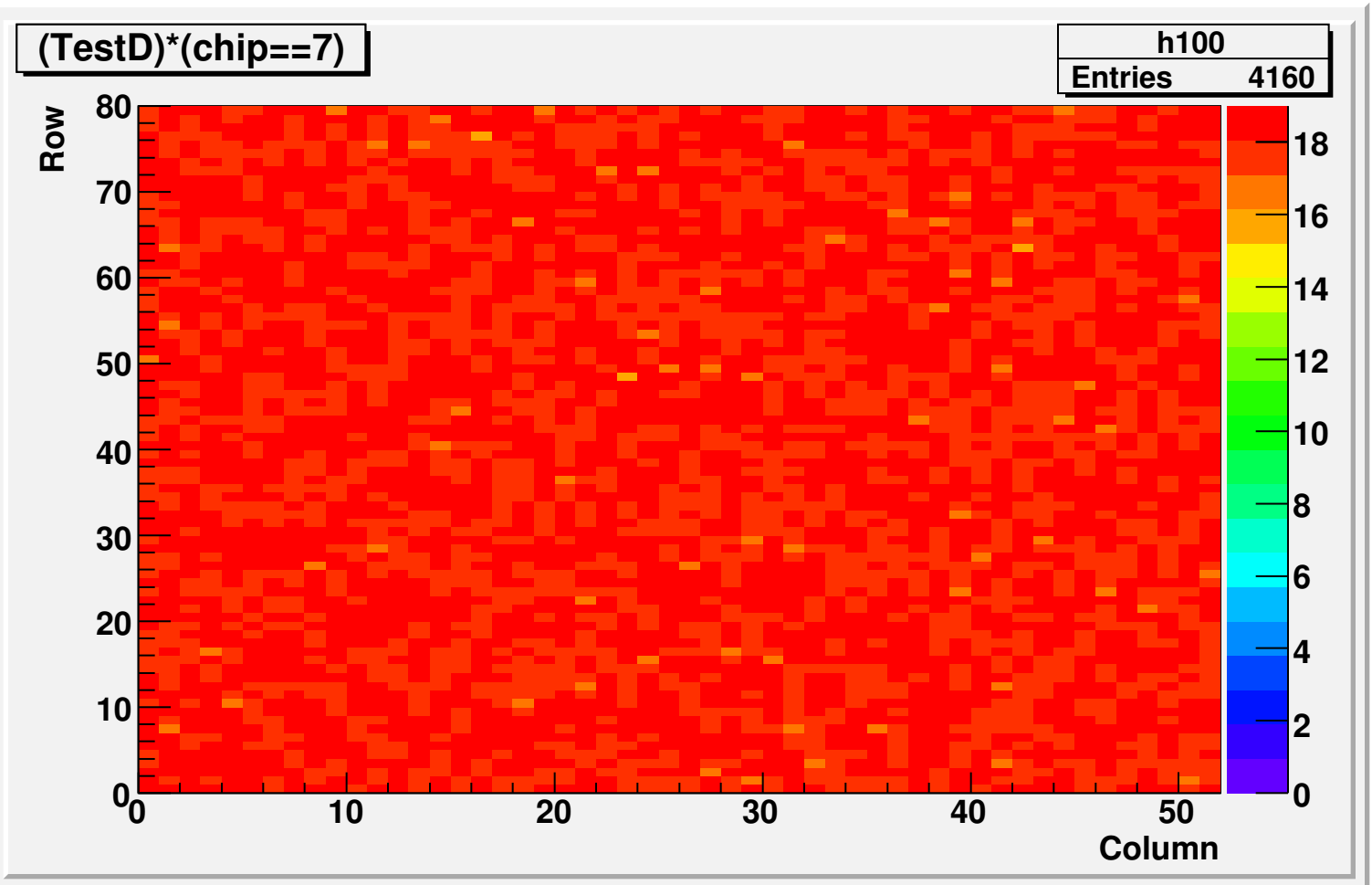

Pulse Height
h102
Entries 78761
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

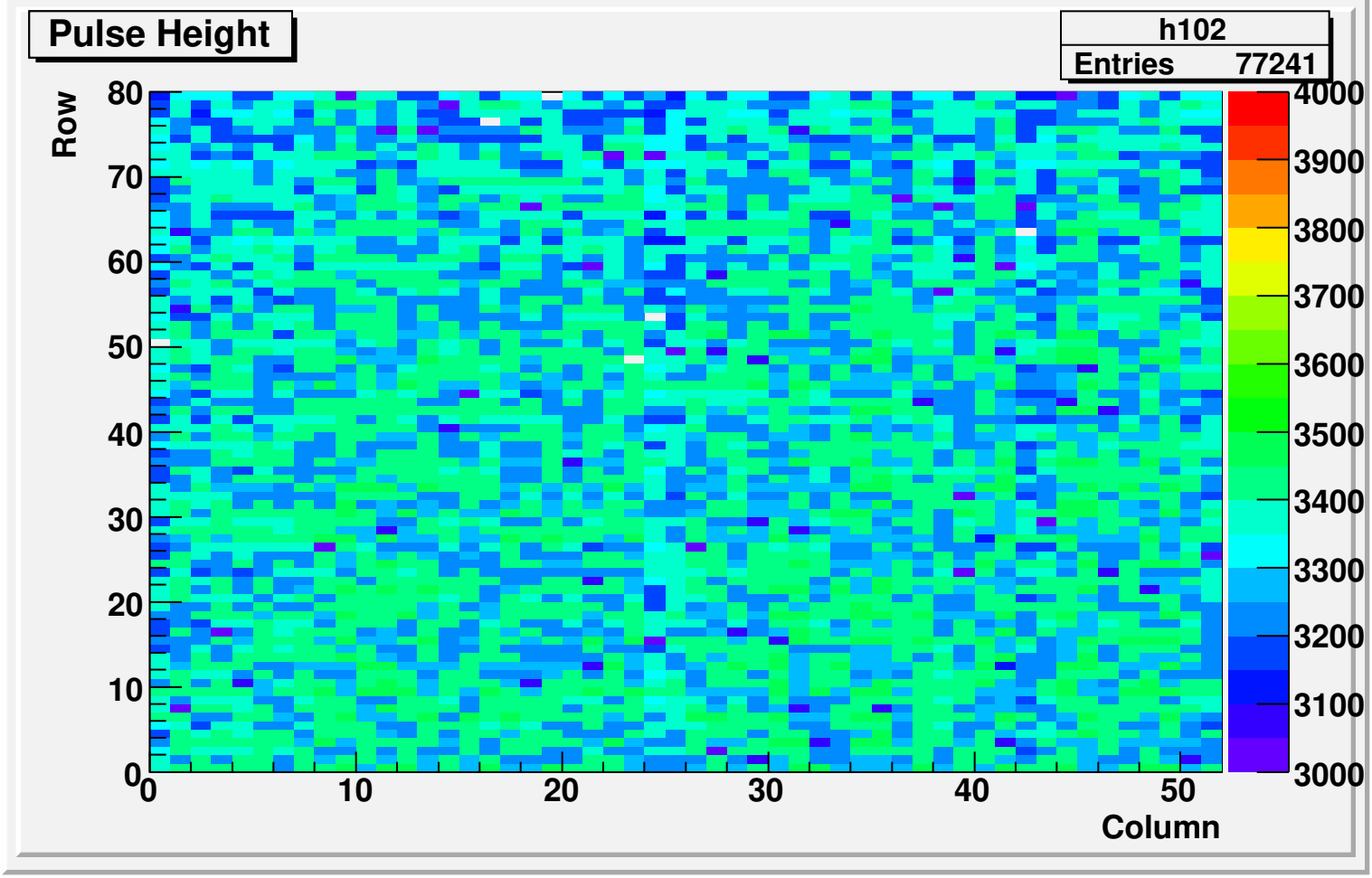

(TestD)*(chip==5)
h100
Entries 4160
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

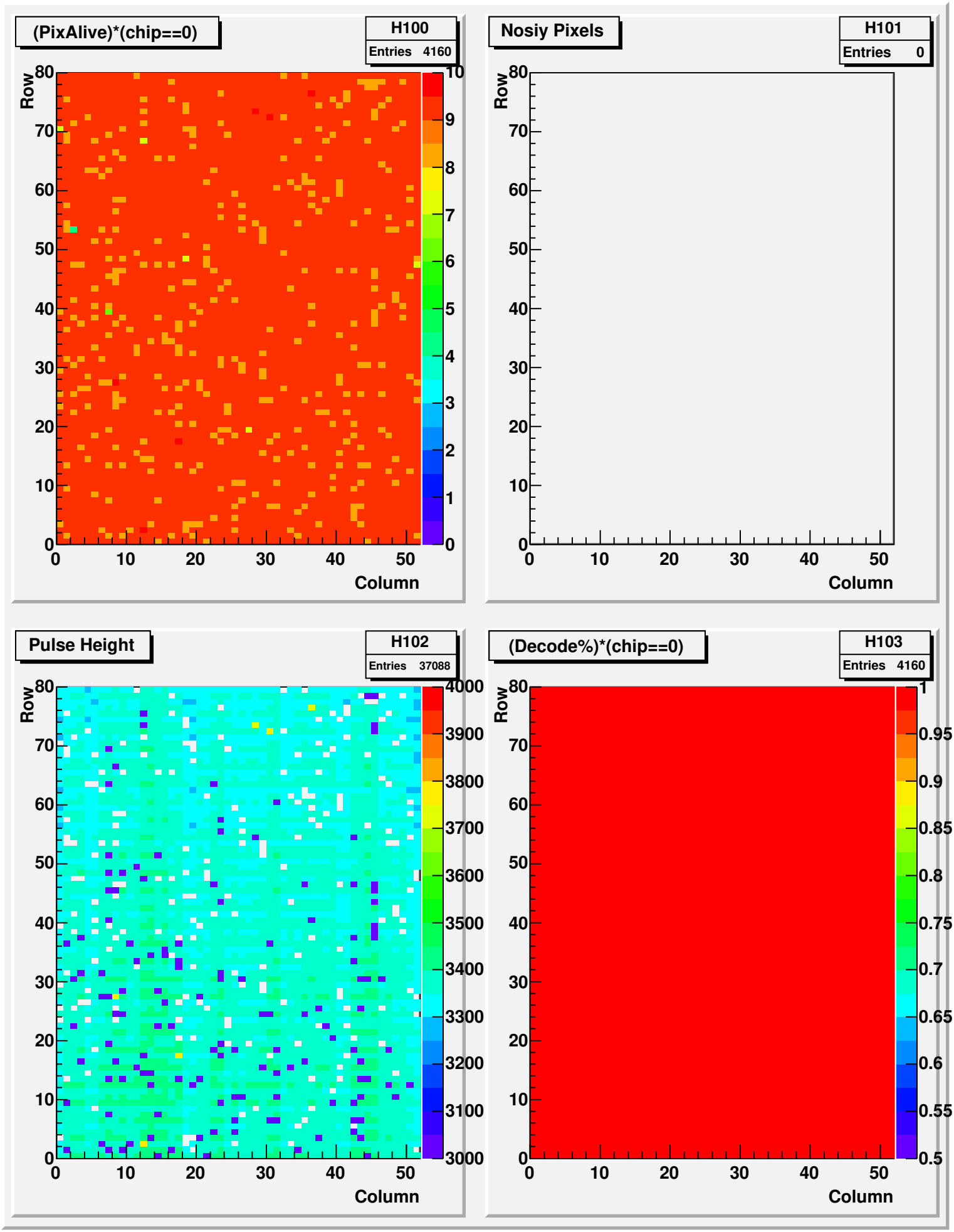

Pulse Height
h102
Entries 78683
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

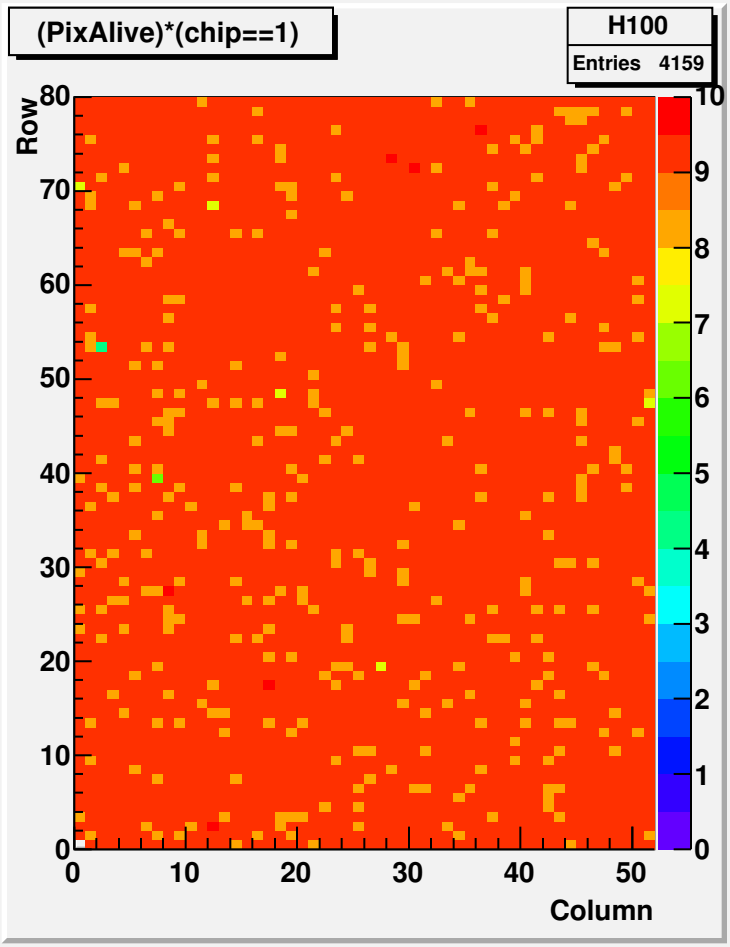

(TestD)*(chip==6)
h100
Entries 4160
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

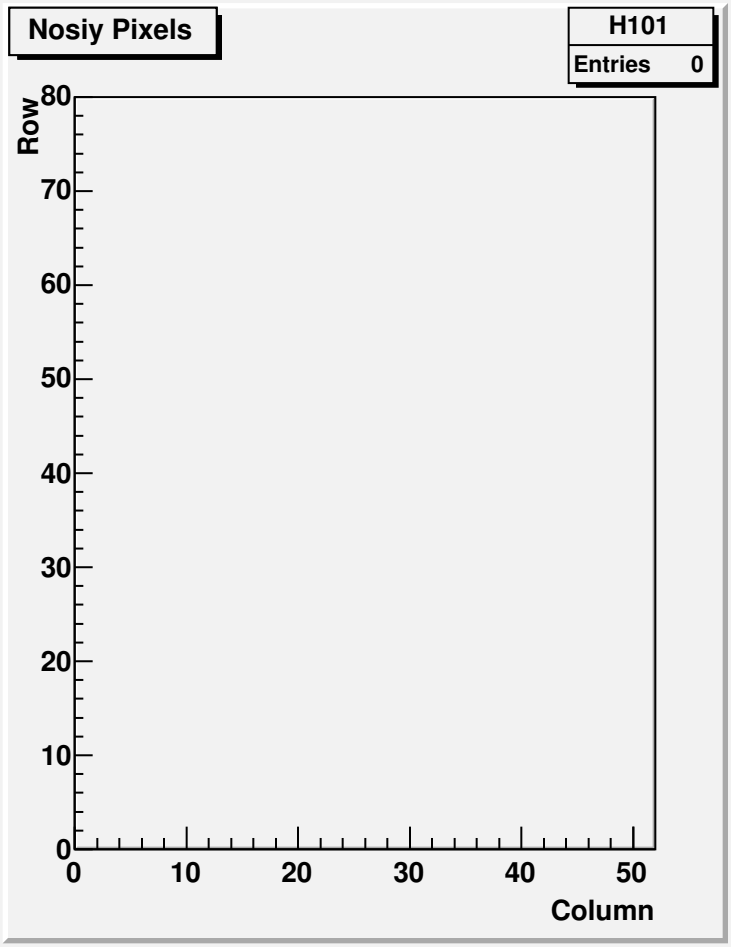

Pulse Height
h102
Entries 78236
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

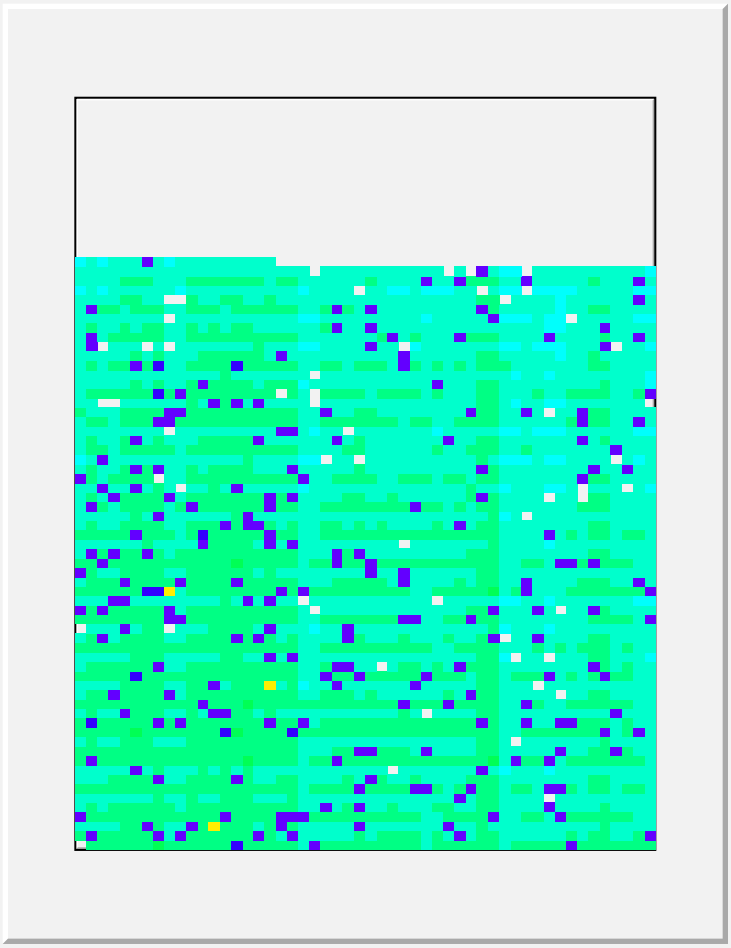

(TestD)*(chip==7)
h100
Entries 4160
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

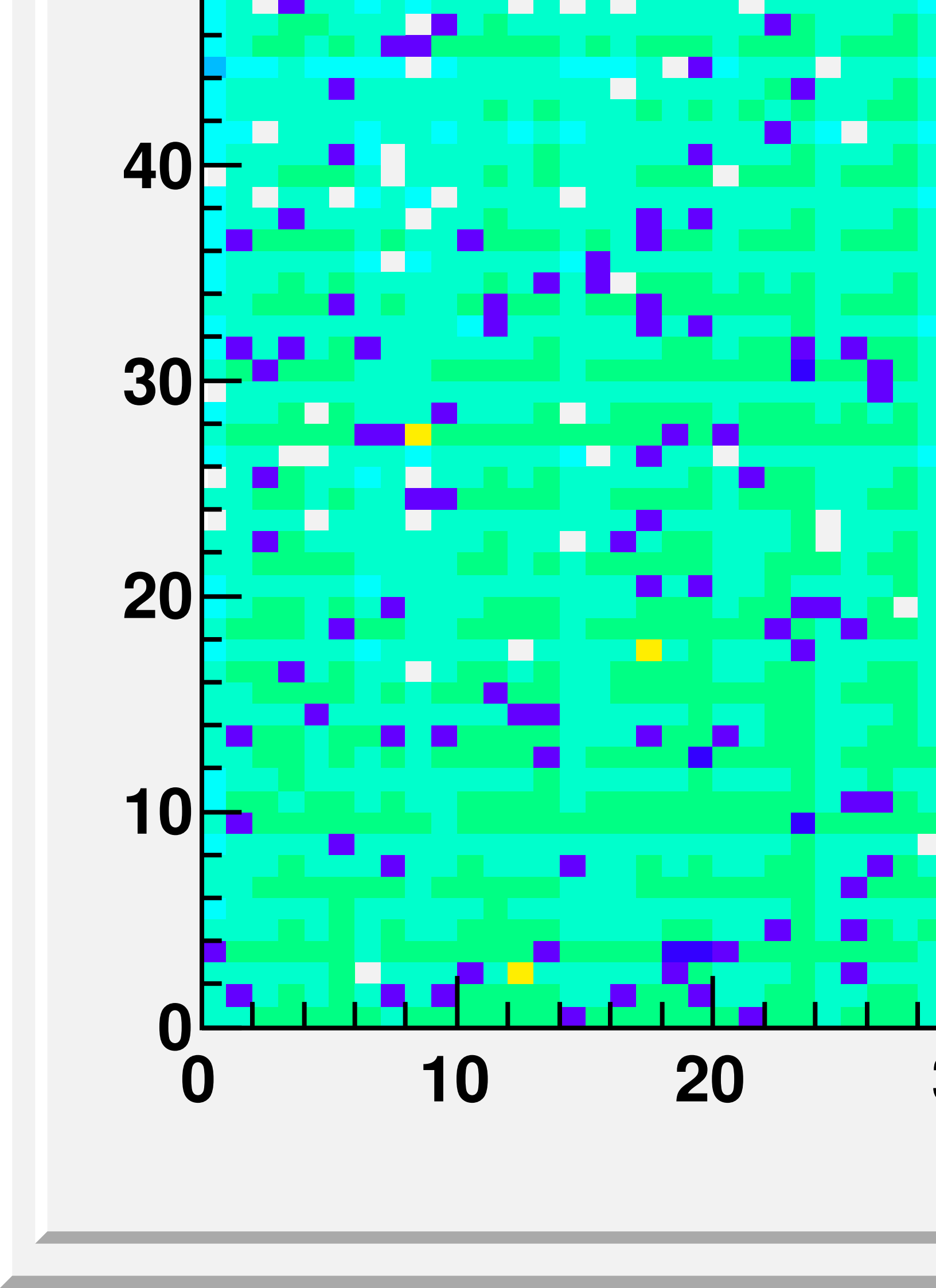

Pulse Height
h102
Entries 77241
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

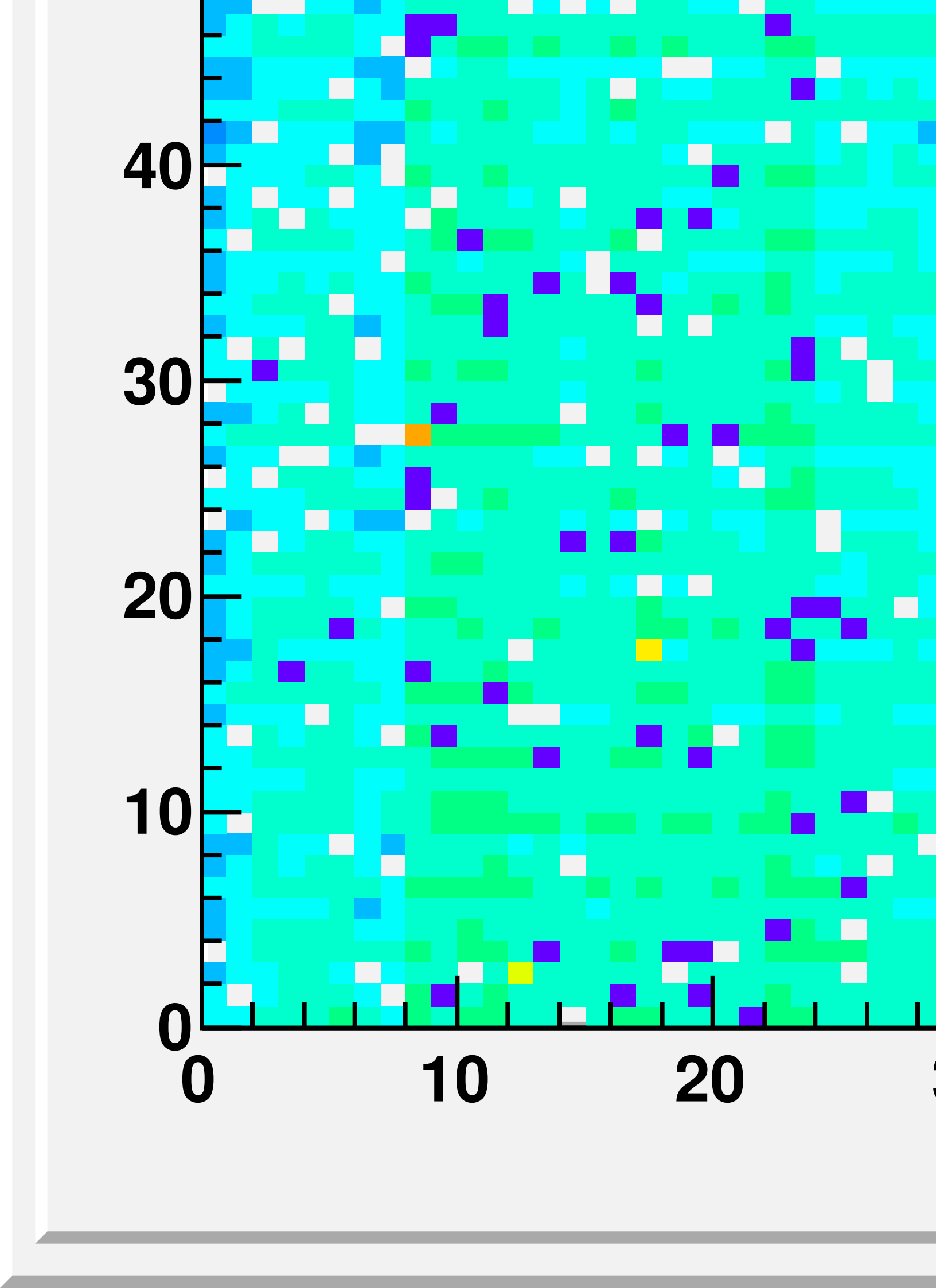

(PixAlive)*(chip==0)
H100
Entries 4160
Row
Column
Nosiy Pixels
H101
Entries 0
Row
Column
Pulse Height
H102
Entries 37088
Row
Column
(Decode%)*(chip==0)
H103
Entries 4160
Row
Column

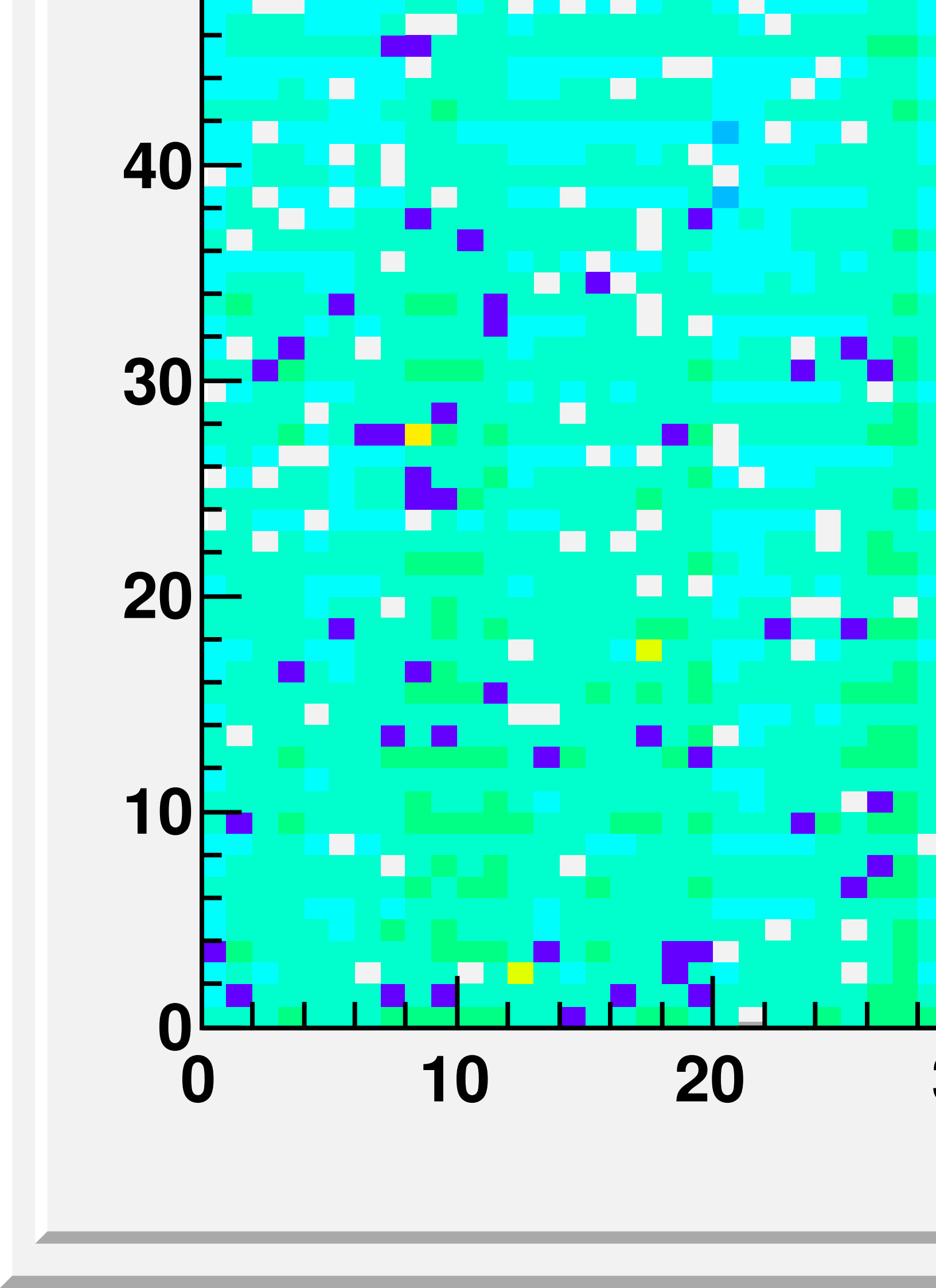

(PixAlive)*(chip==1)
H100
Entries 4159
Row
Column
0
10
20
30
40
50
60
70
80
0
1
2
3
4
5
6
7
8
9
10

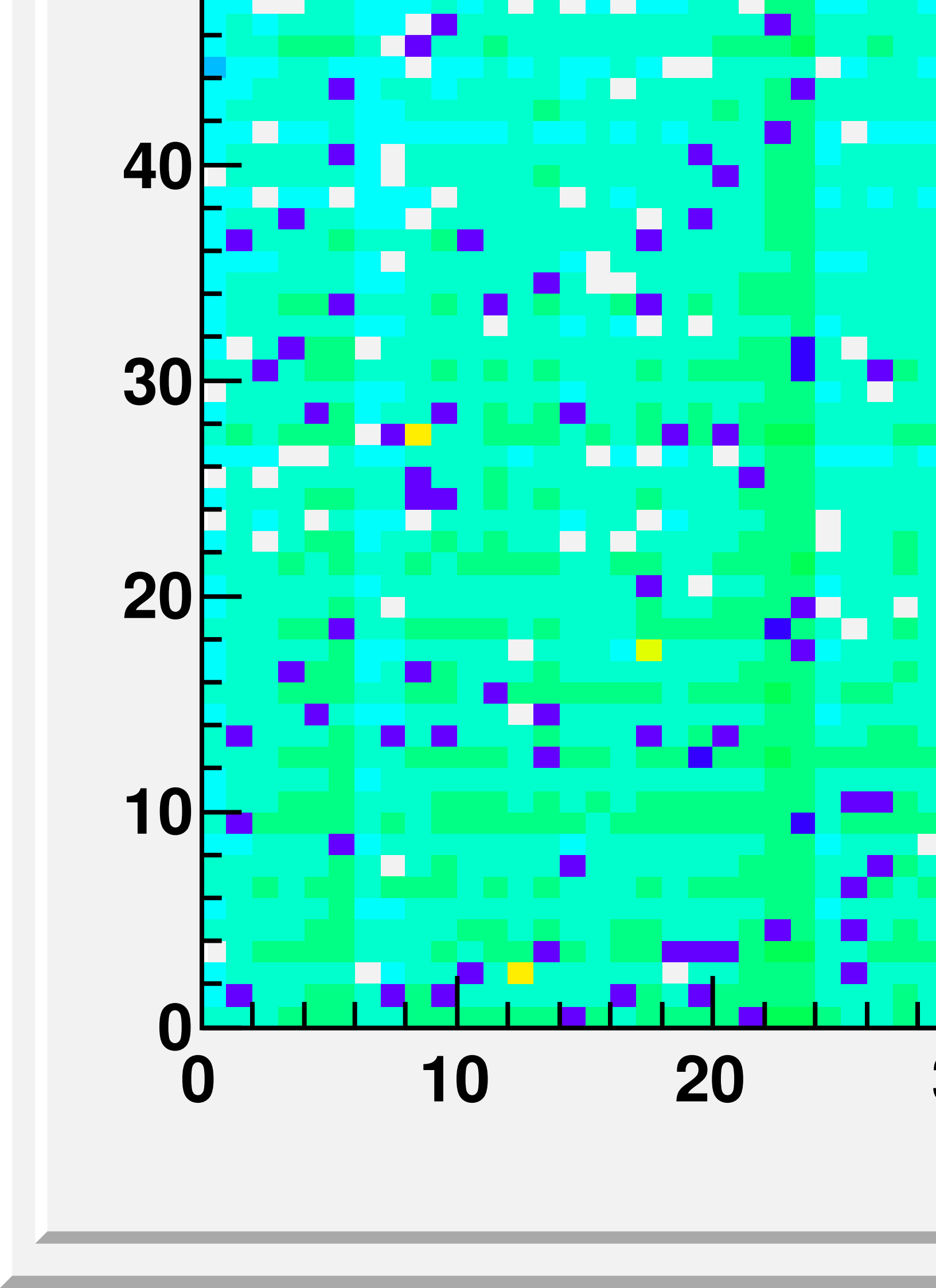

Nosiy Pixels
H101
Entries 0
Row
Column
0
10
20
30
40
50
60
70
80

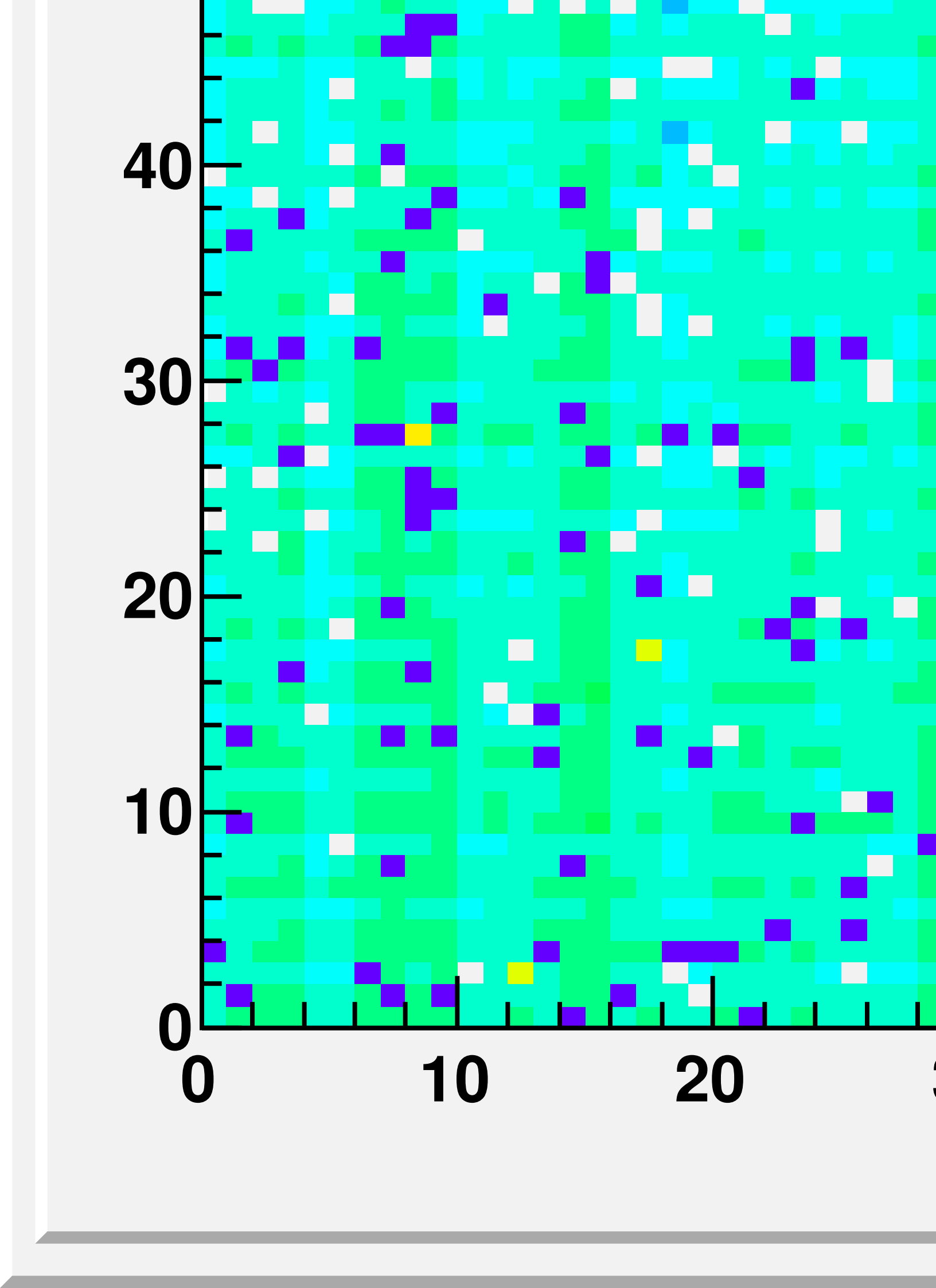
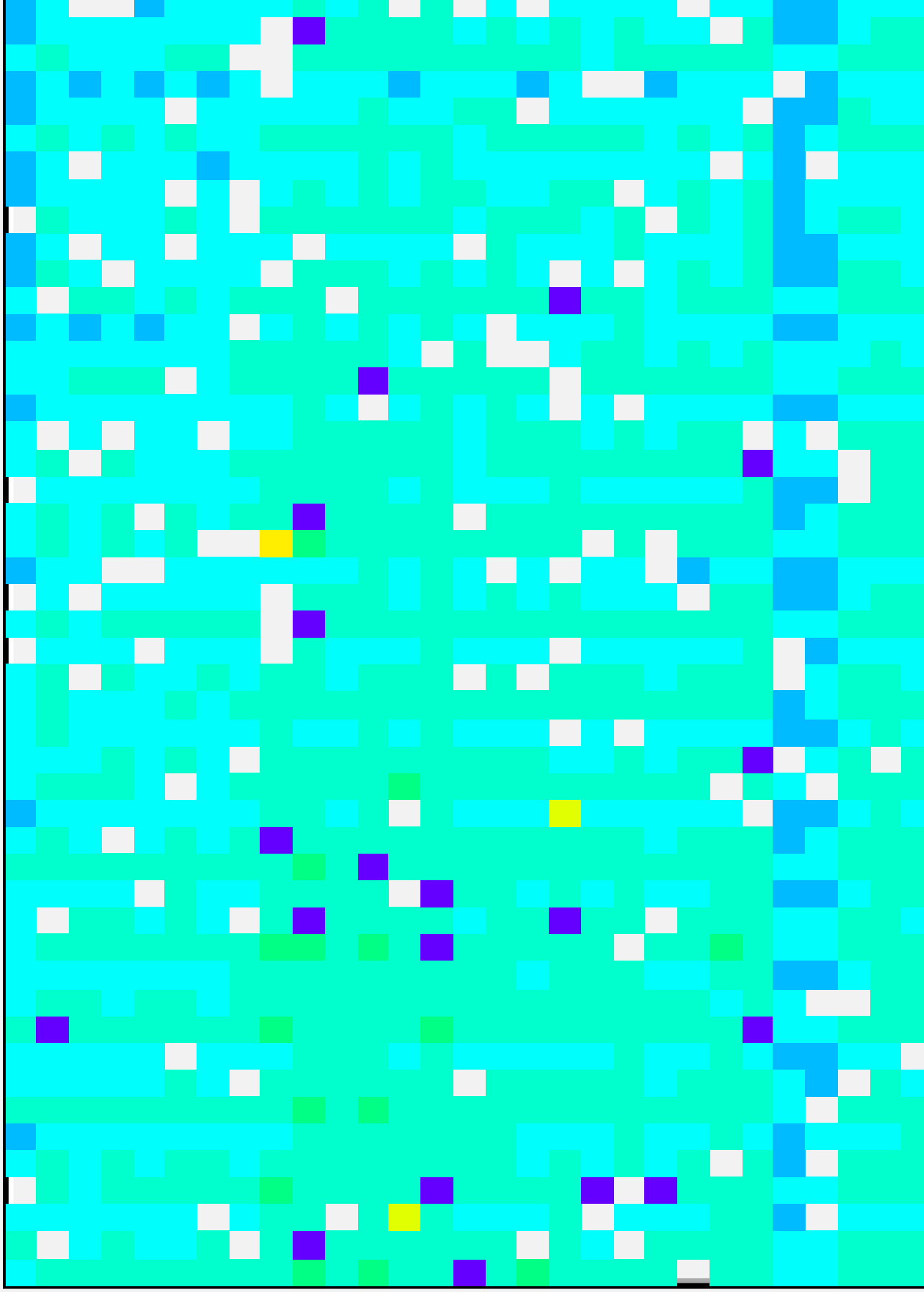



40
30
20
10
0
0
10
20


40
30
20
10
0
0
10
20


40
30
20
10
0
0
10
20


40
30
20
10
0
0
10
20


40
30
20
10
0
0
10
20