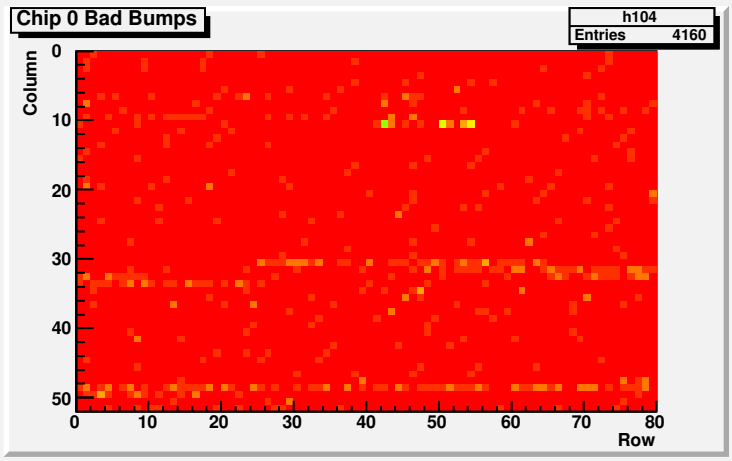
Chip 0 Bad Bumps
h104
Entries 4160
Column
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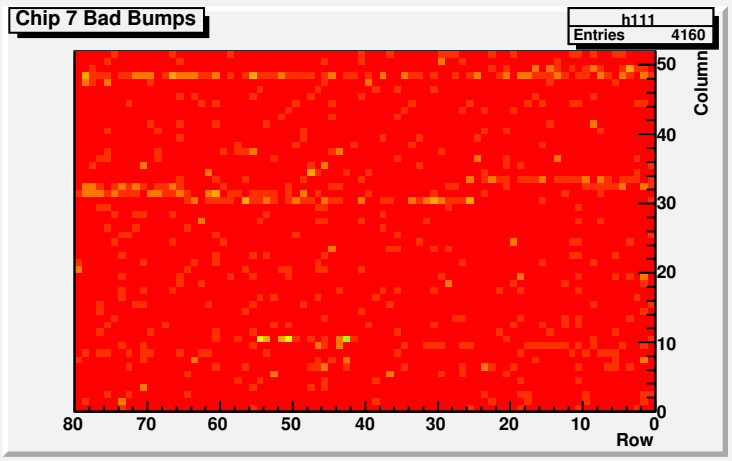
Chip 7 Bad Bumps
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Entries 4160
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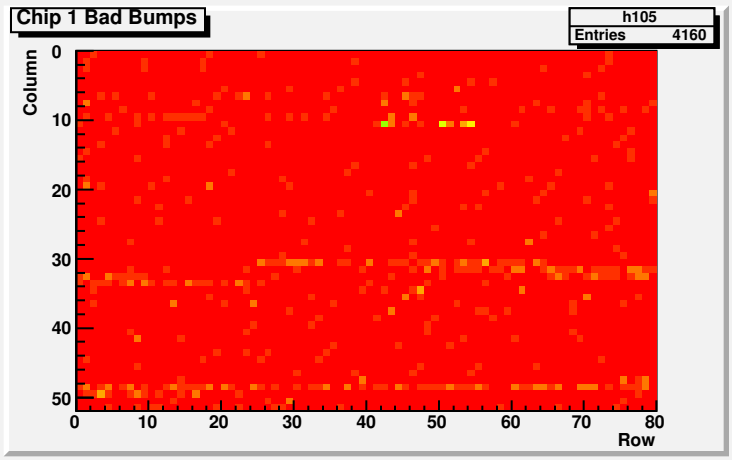
Chip 1 Bad Bumps
h105
Entries 4160
Column
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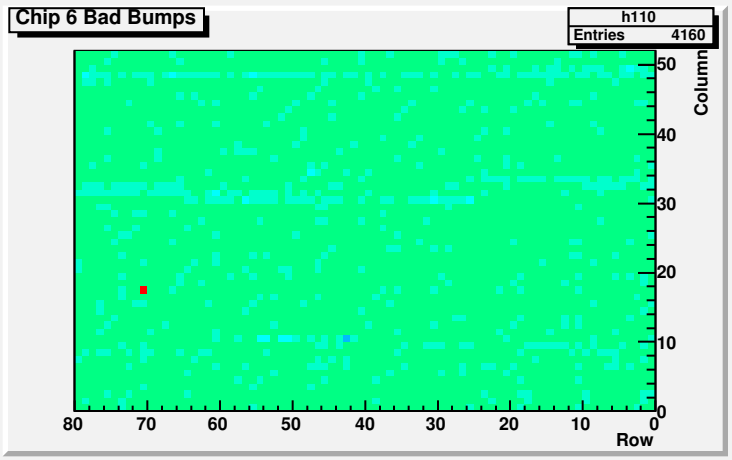
Chip 6 Bad Bumps
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Entries 4160
Column
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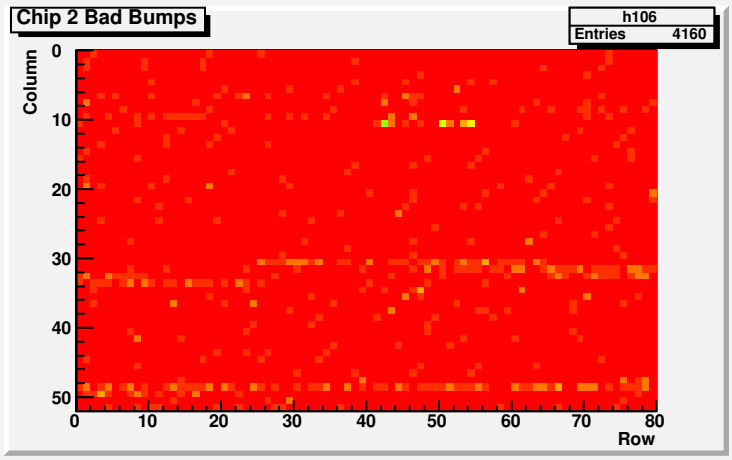
Chip 2 Bad Bumps
h106
Entries 4160
Column
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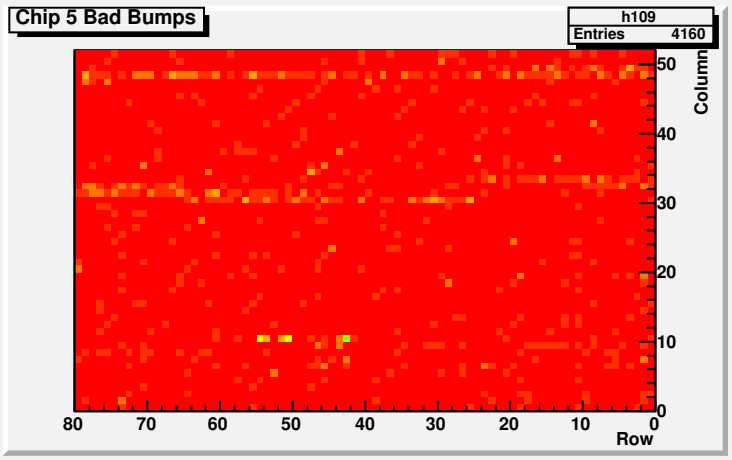
Chip 5 Bad Bumps
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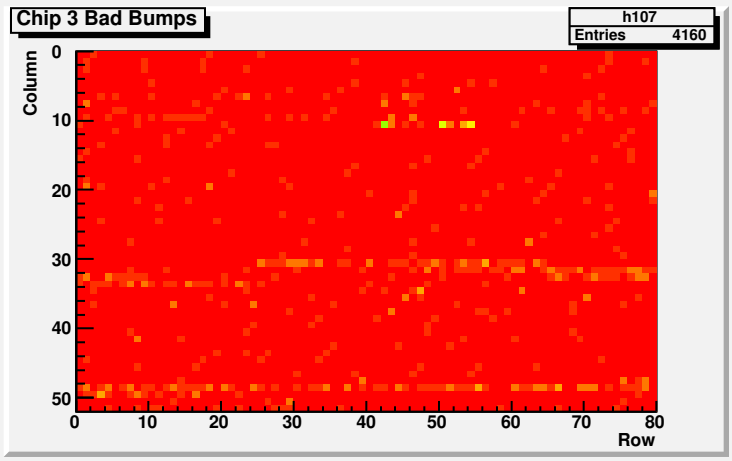
Chip 3 Bad Bumps
h107
Entries 4160
Column
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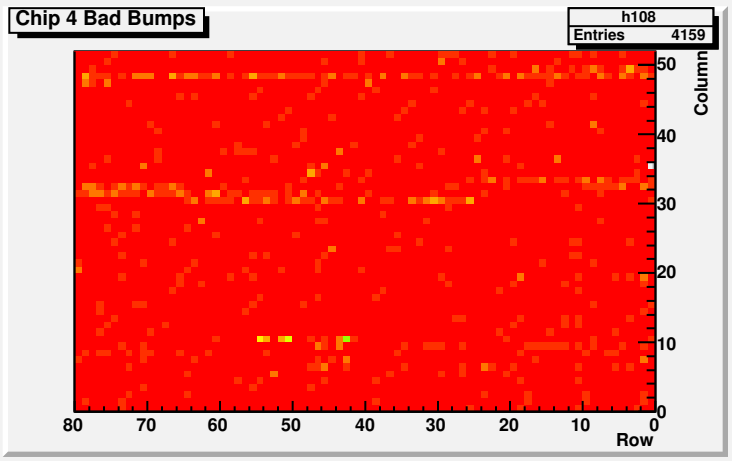
Chip 4 Bad Bumps
h108
Entries 4159
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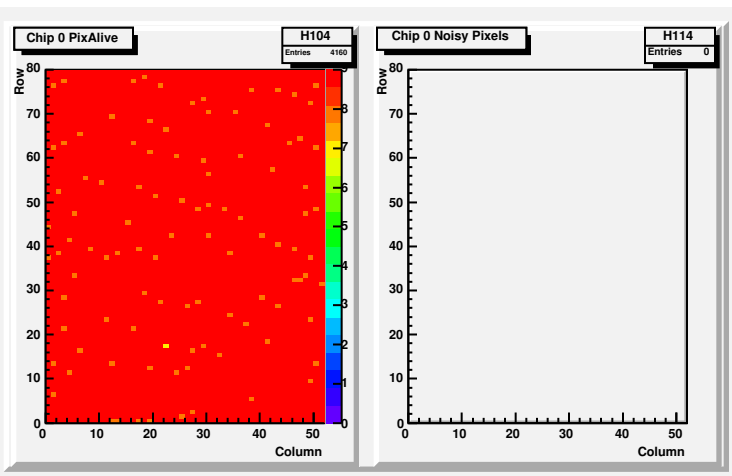
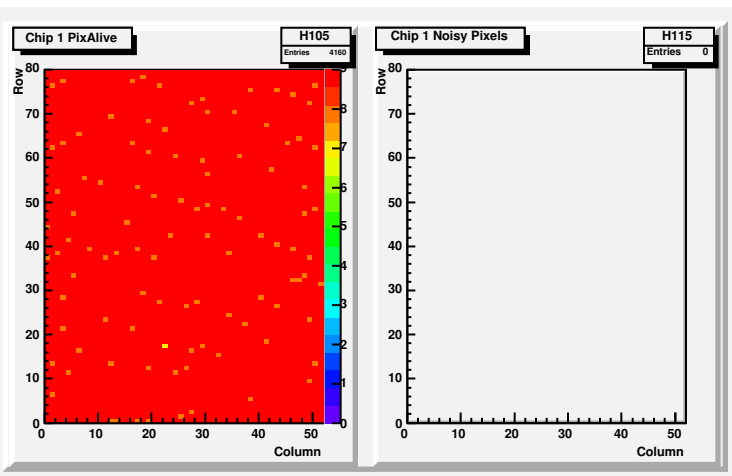
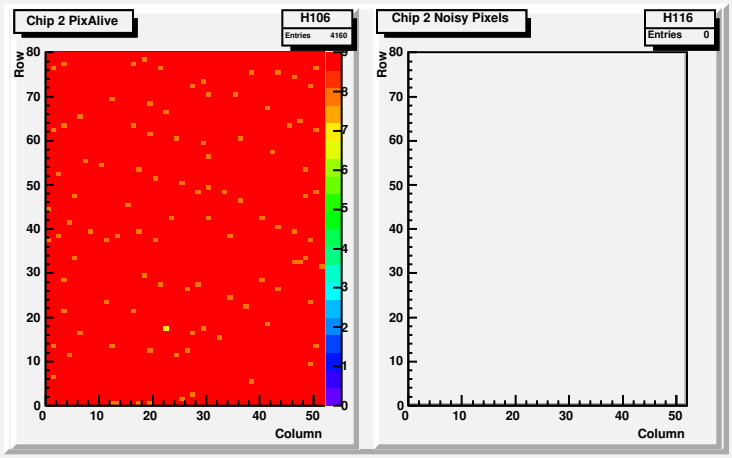

Chip 0 PixAlive
H104
Entries 4160
Row
Column

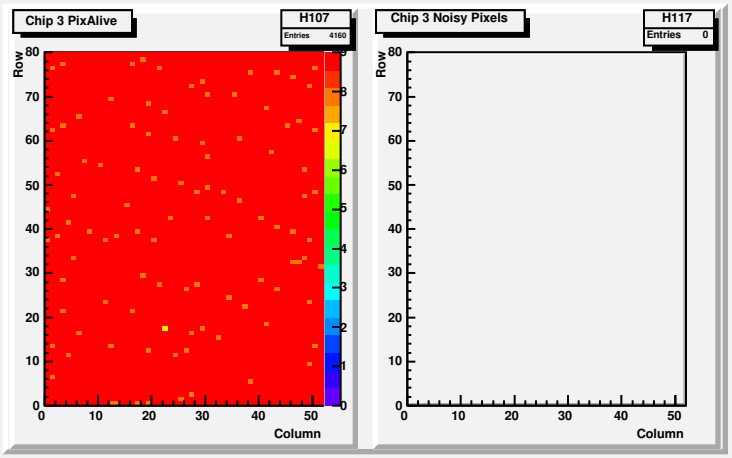
Chip 0 Noisy Pixels
H114
Entries 0
Row
Column


Chip 1 PixAlive
H105
Entries 4160
Row
Column

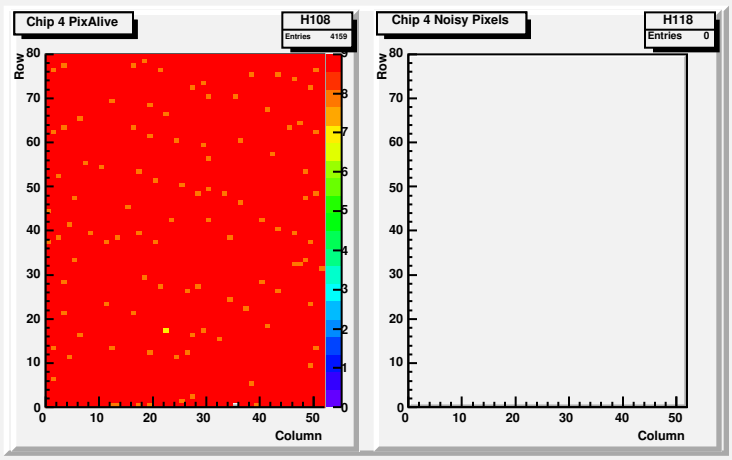
Chip 1 Noisy Pixels
H115
Entries 0
Row
Column


Chip 2 PixAlive
H106
Entries 4160
Row
Column

Chip 2 Noisy Pixels
H116
Entries 0
Row
Column


Chip 3 PixAlive
H107
Entries 4160
Row
Column

Chip 3 Noisy Pixels
H117
Entries 0
Row
Column


Chip 4 PixAlive
H108
Entries 4159
Row
Column

Chip 4 Noisy Pixels
H118
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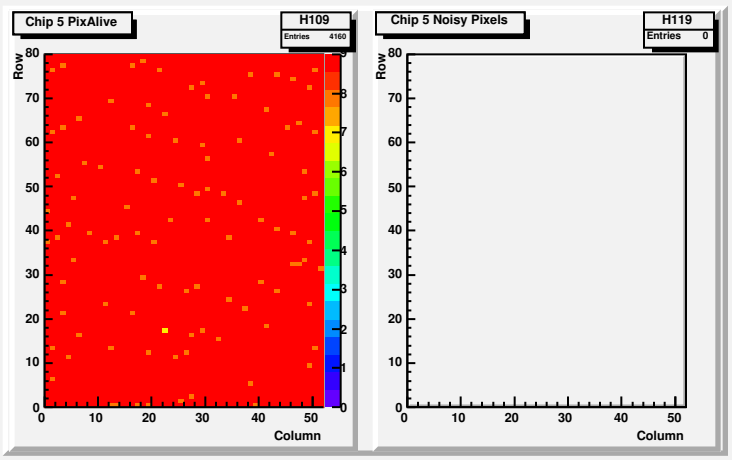
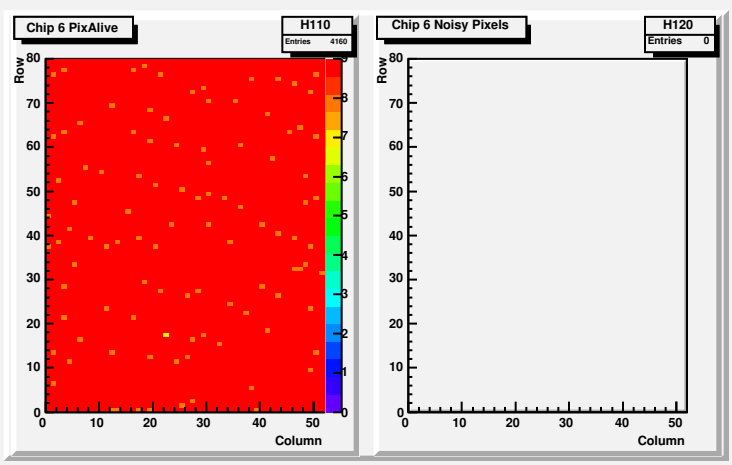
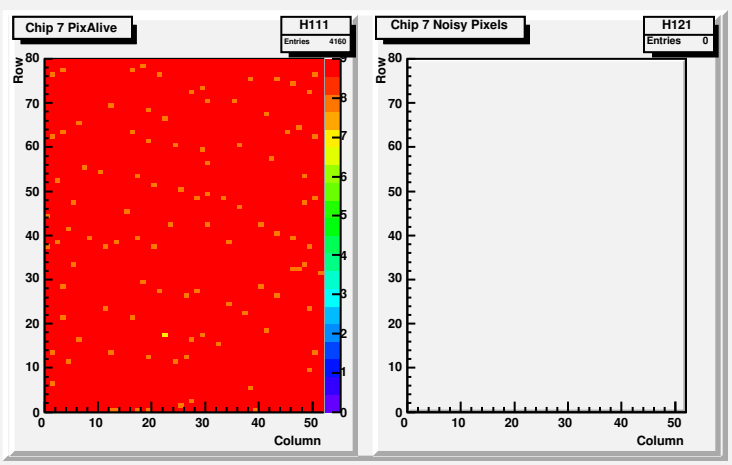
Row
Column


Chip 5 PixAlive
H109
Entries 4160
Row
Column

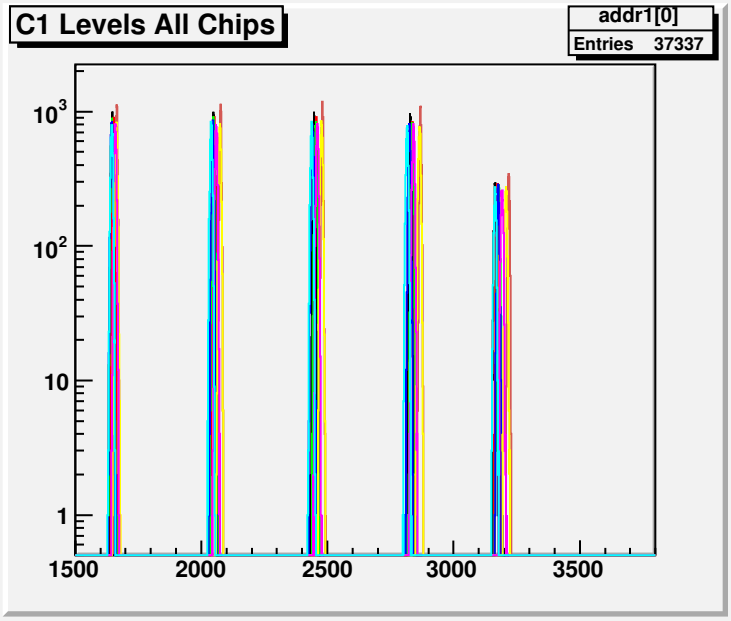
Chip 5 Noisy Pixels
H119
Entries 0
Row
Column

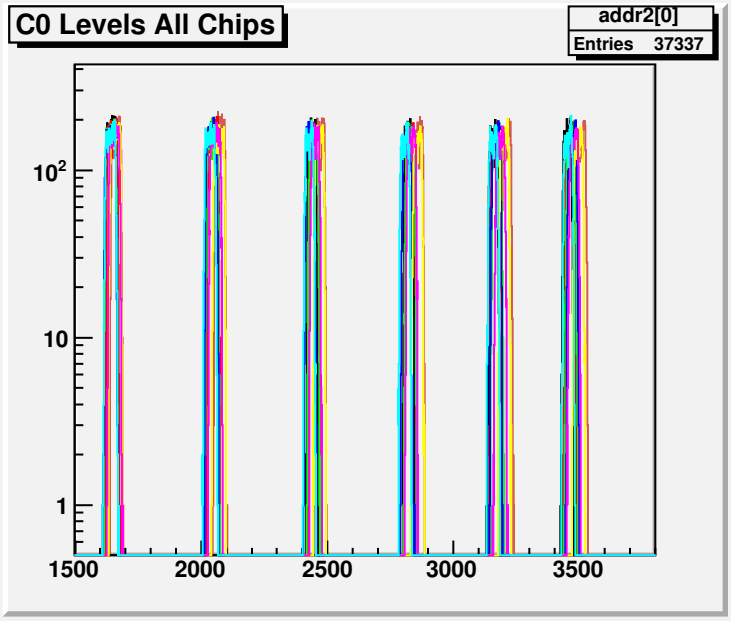

Chip 6 PixAlive
H110
Entries 4160
Row
Column

Chip 6 Noisy Pixels
H120
Entries 0
Row
Column


Chip 7 PixAlive
H111
Entries 4160
Row
Column

Chip 7 Noisy Pixels
H121
Entries 0
Row
Column


C1 Levels All Chips
addr1[0]
Entries 37337
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37337
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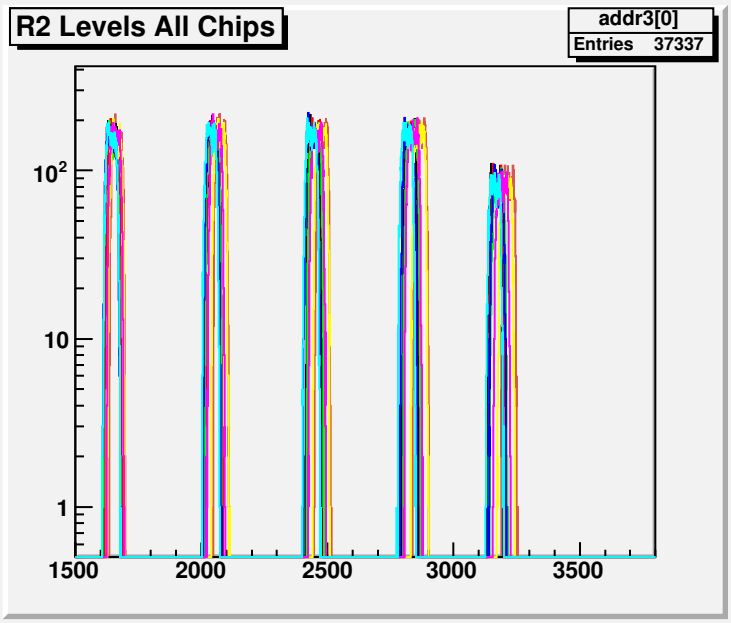
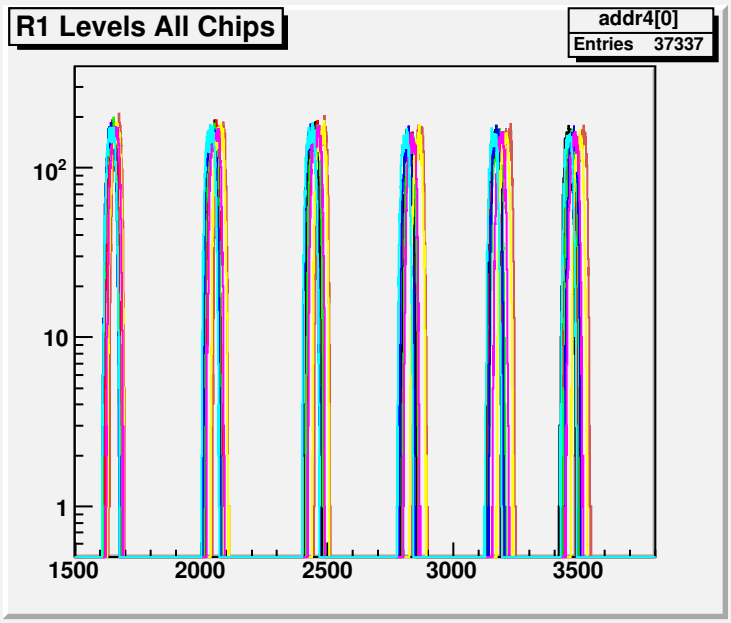
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37337
10^2
10
1
1500
2000
2500
3000
3500


R1 Levels All Chips
addr4[0]
Entries 37337
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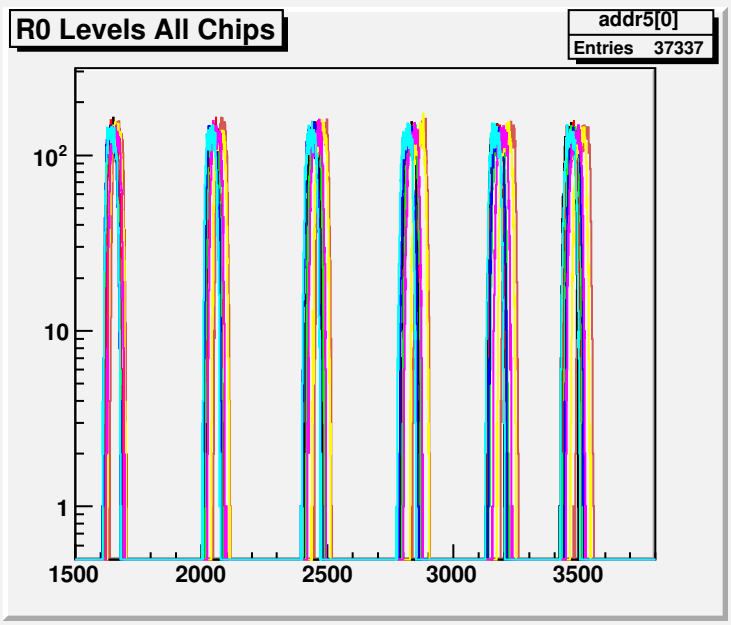
1
1500
2000
2500
3000
3500


R0 Levels All Chips
addr5[0]
Entries 37337
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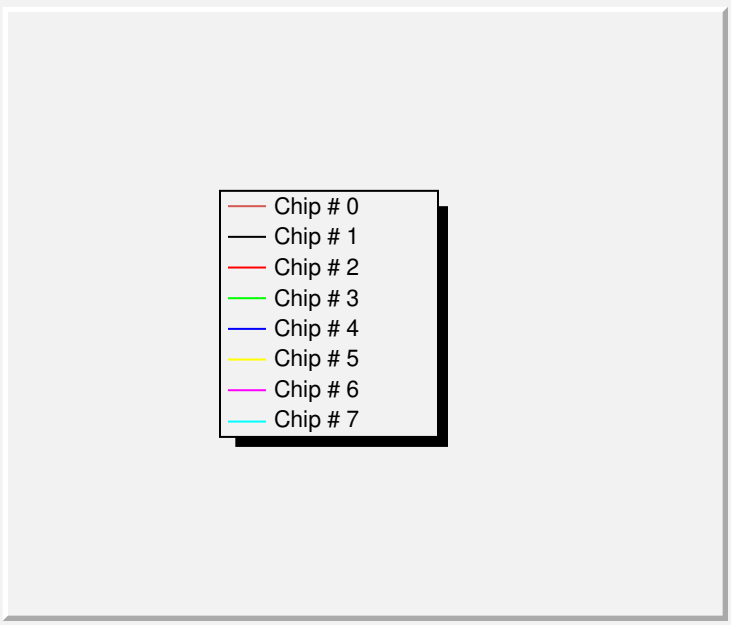
1
1500
2000
2500
3000
3500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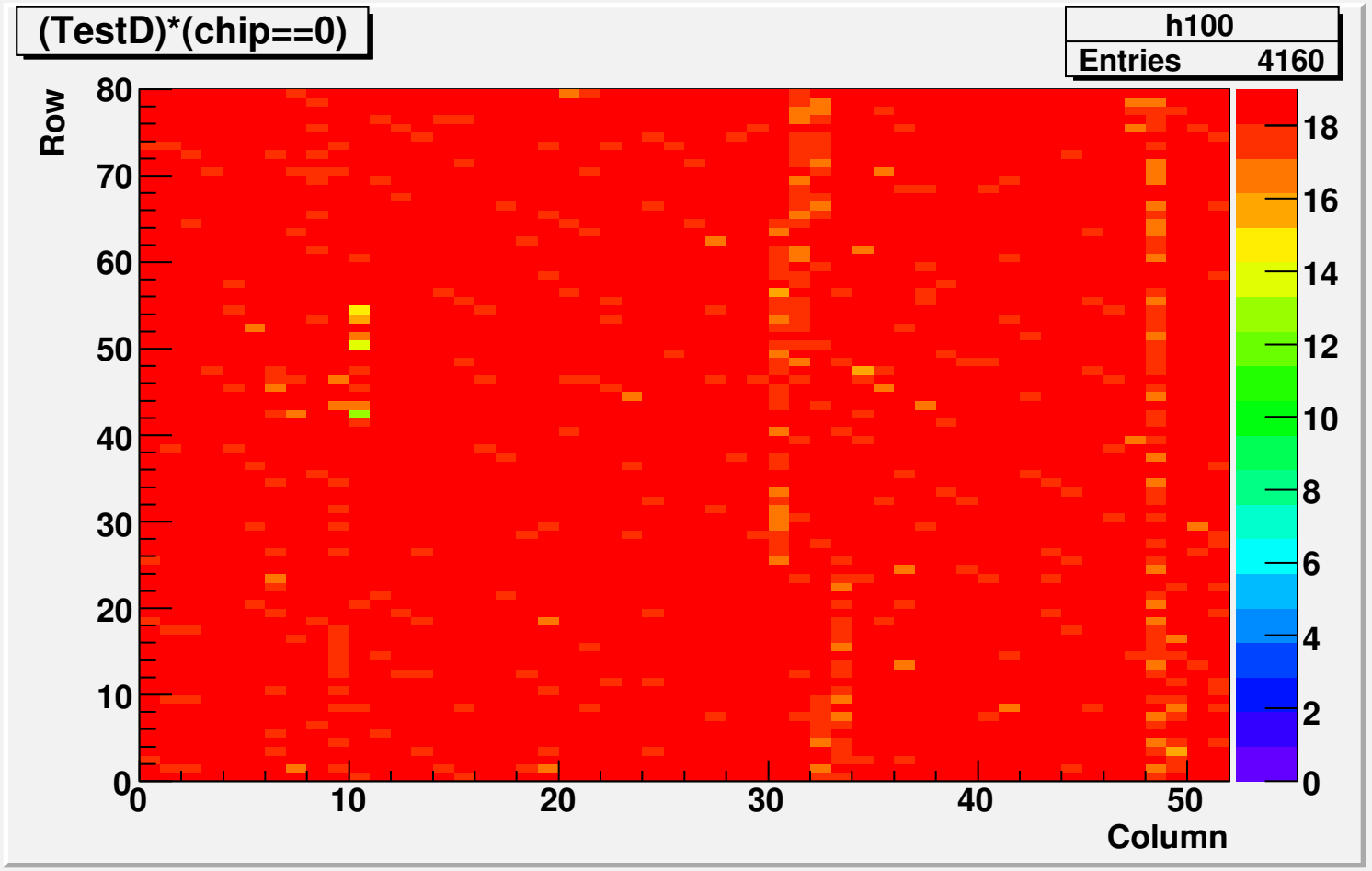
Chip # 1
Chip # 2
Chip # 3
Chip # 4
Chip # 5
Chip # 6
Chip # 7


(TestD)*(chip==0)
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Entries 4160
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Column
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16
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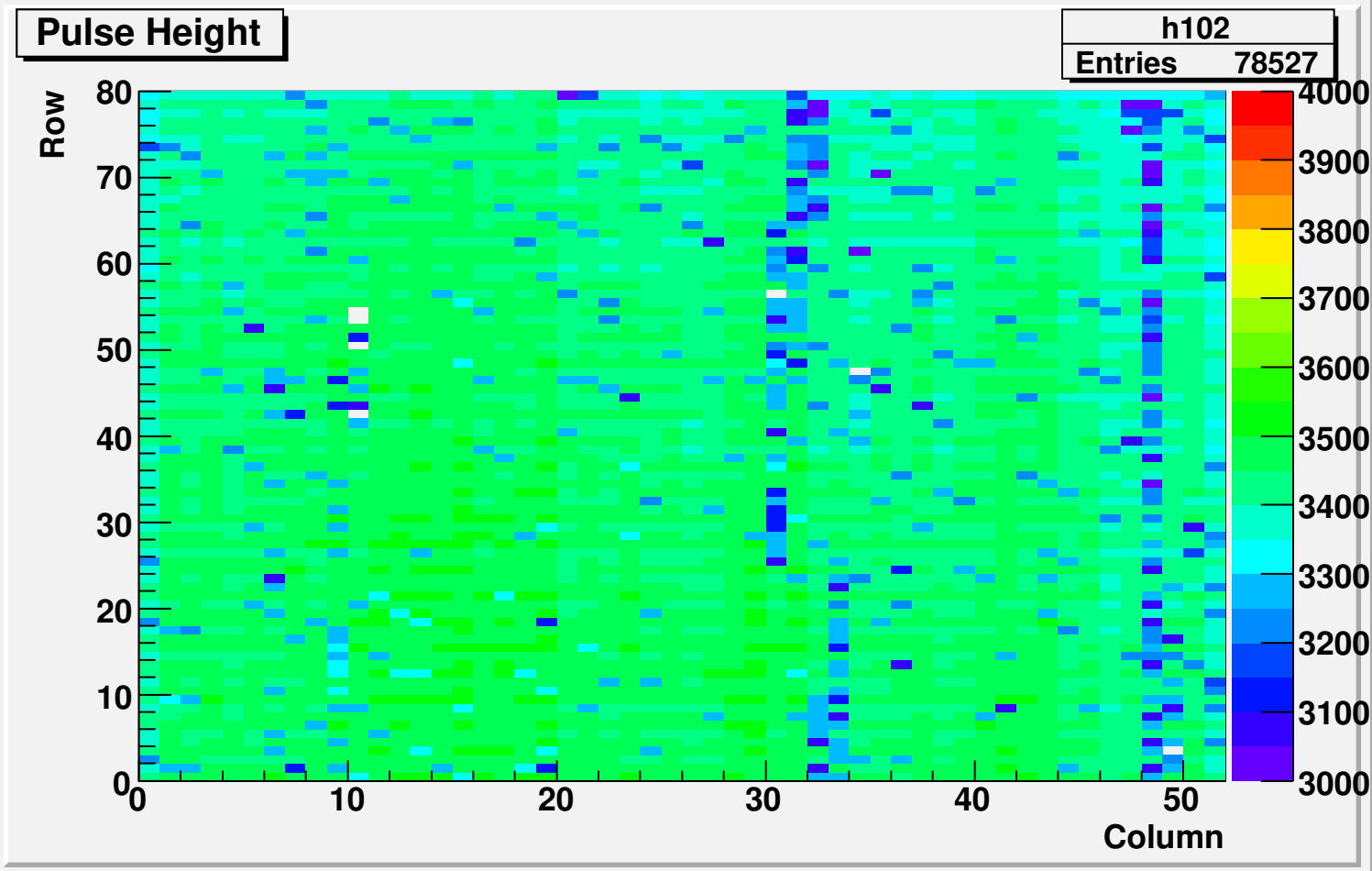
Pulse Height
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Entries 78527
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Column
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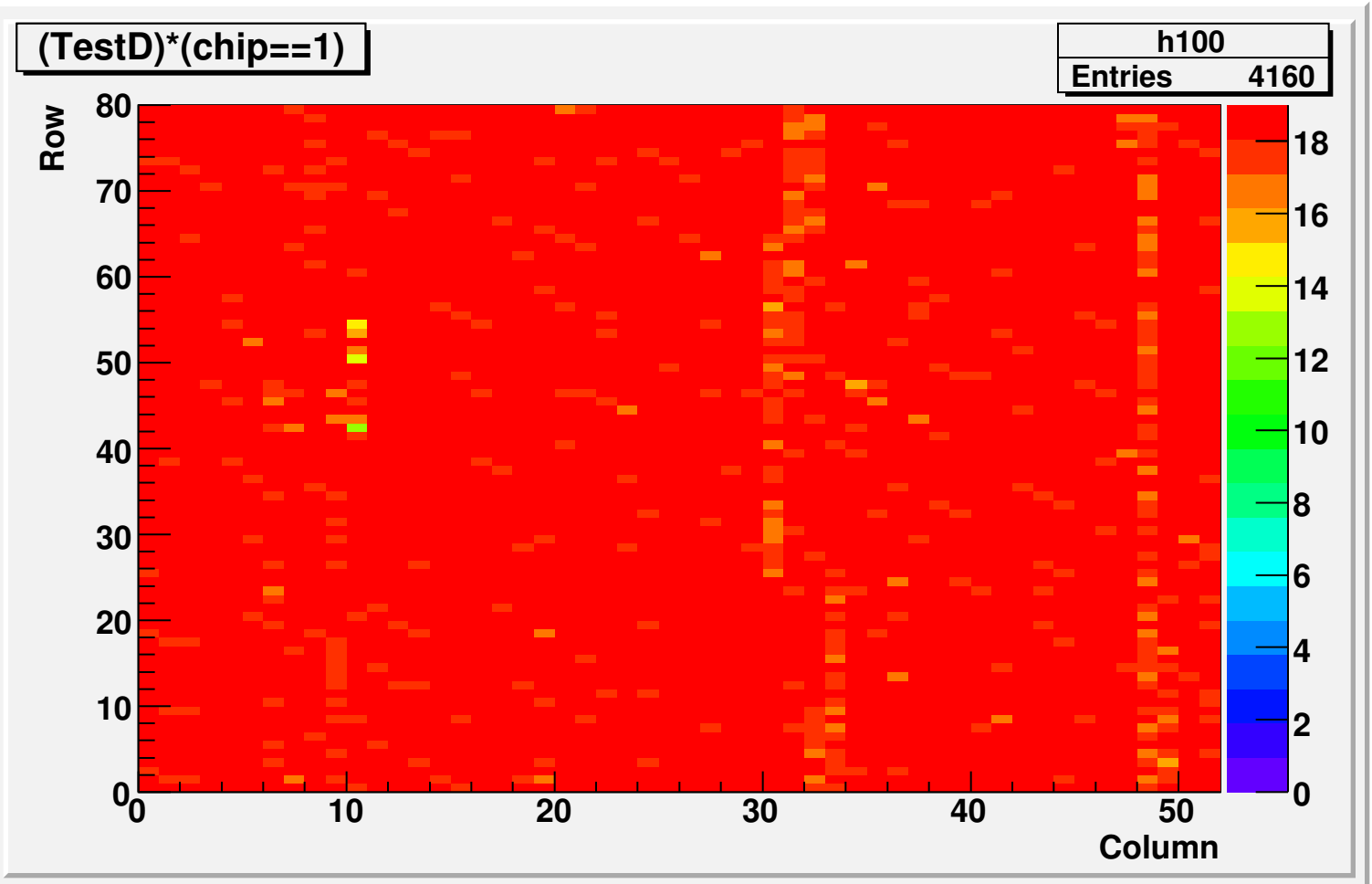
(TestD)*(chip==1)
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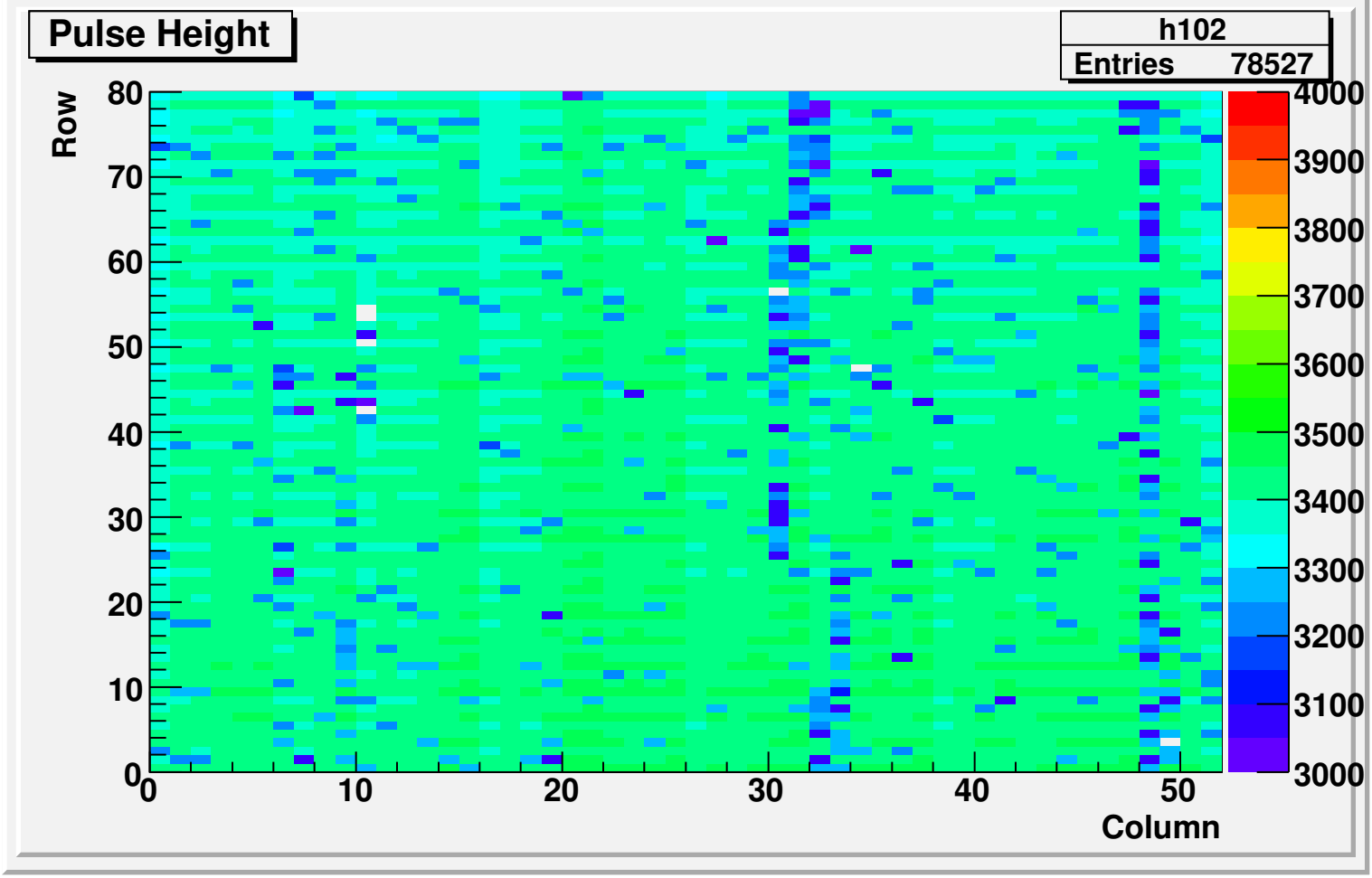
Pulse Height
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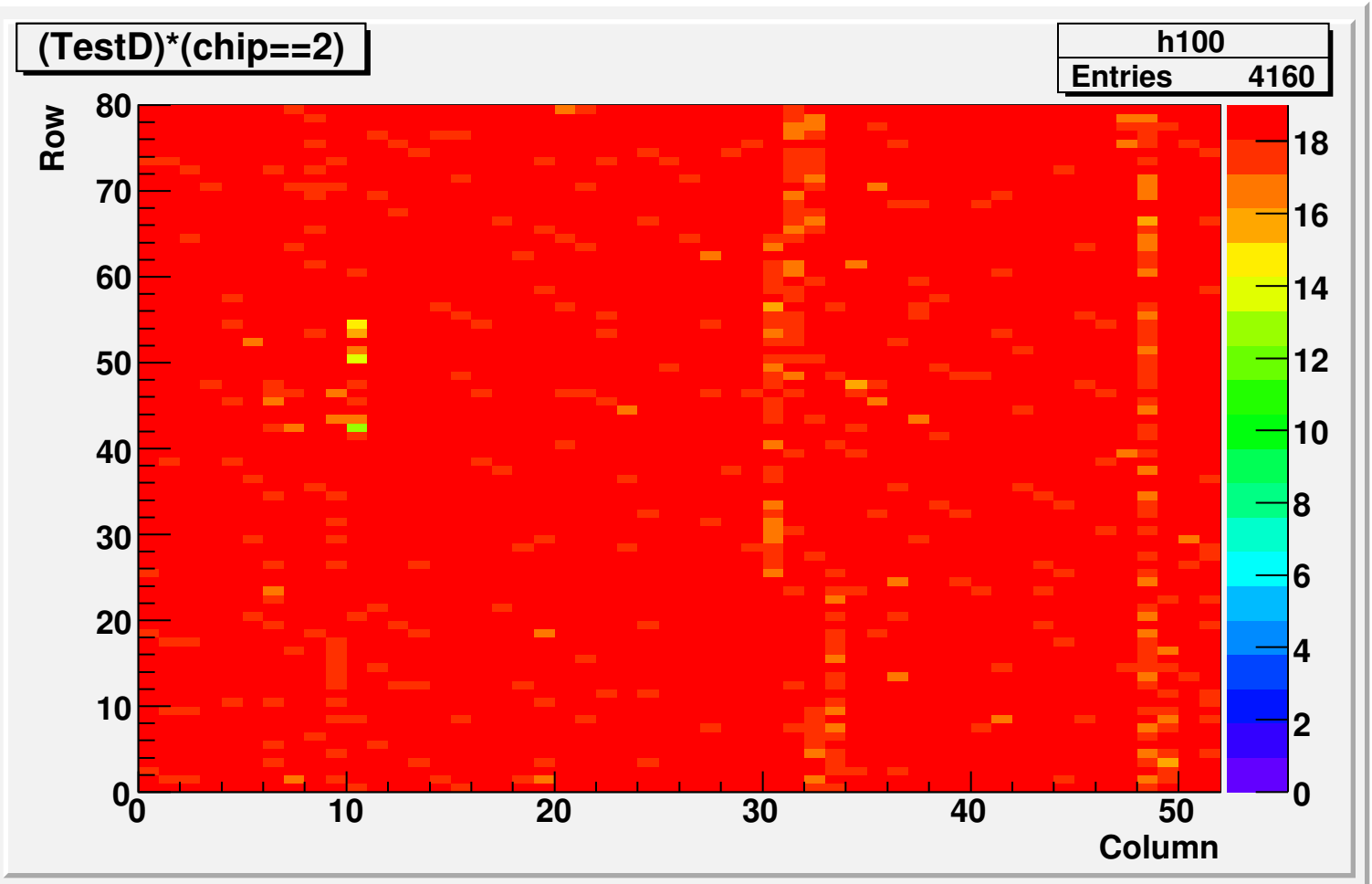
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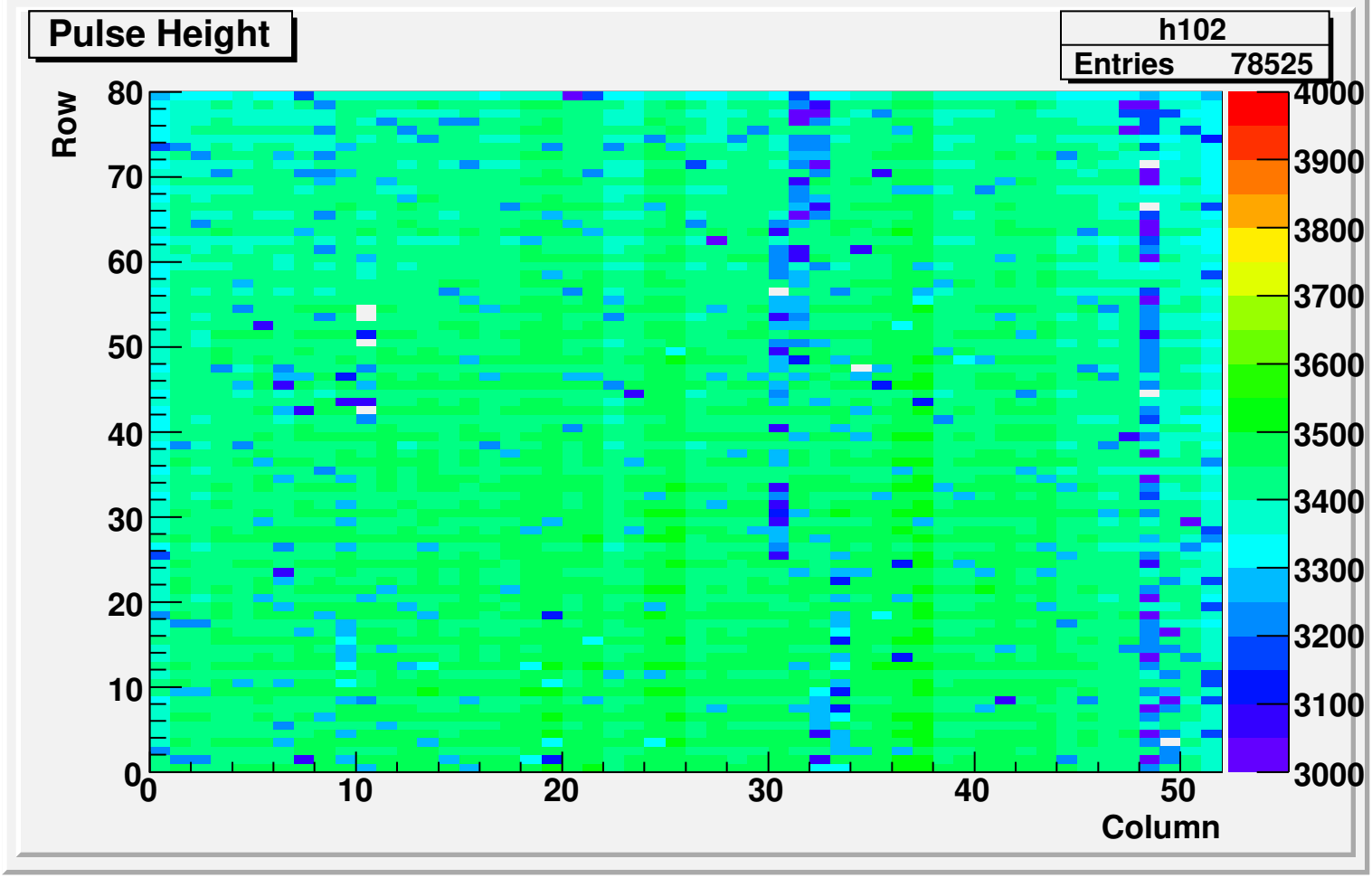
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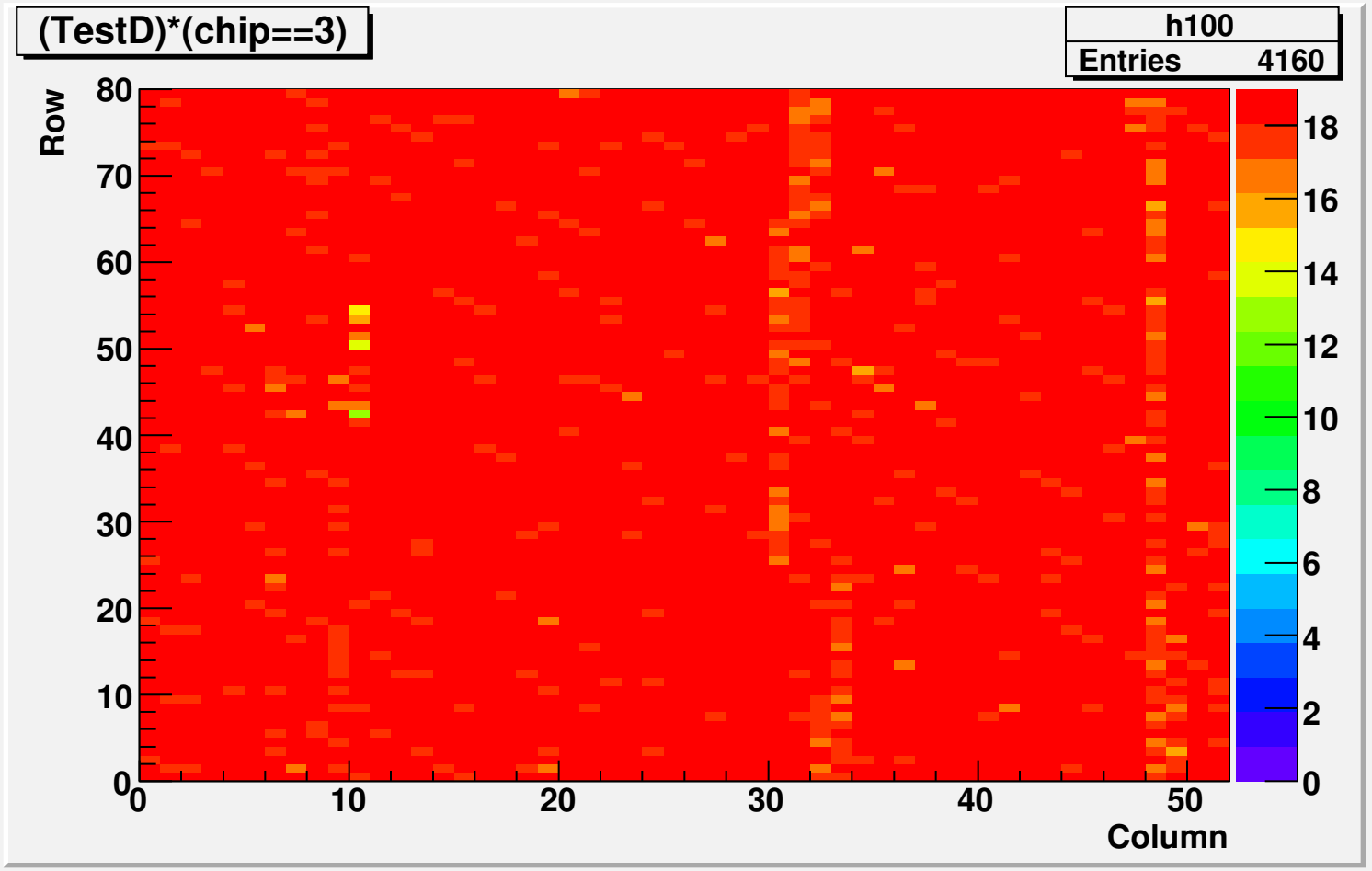
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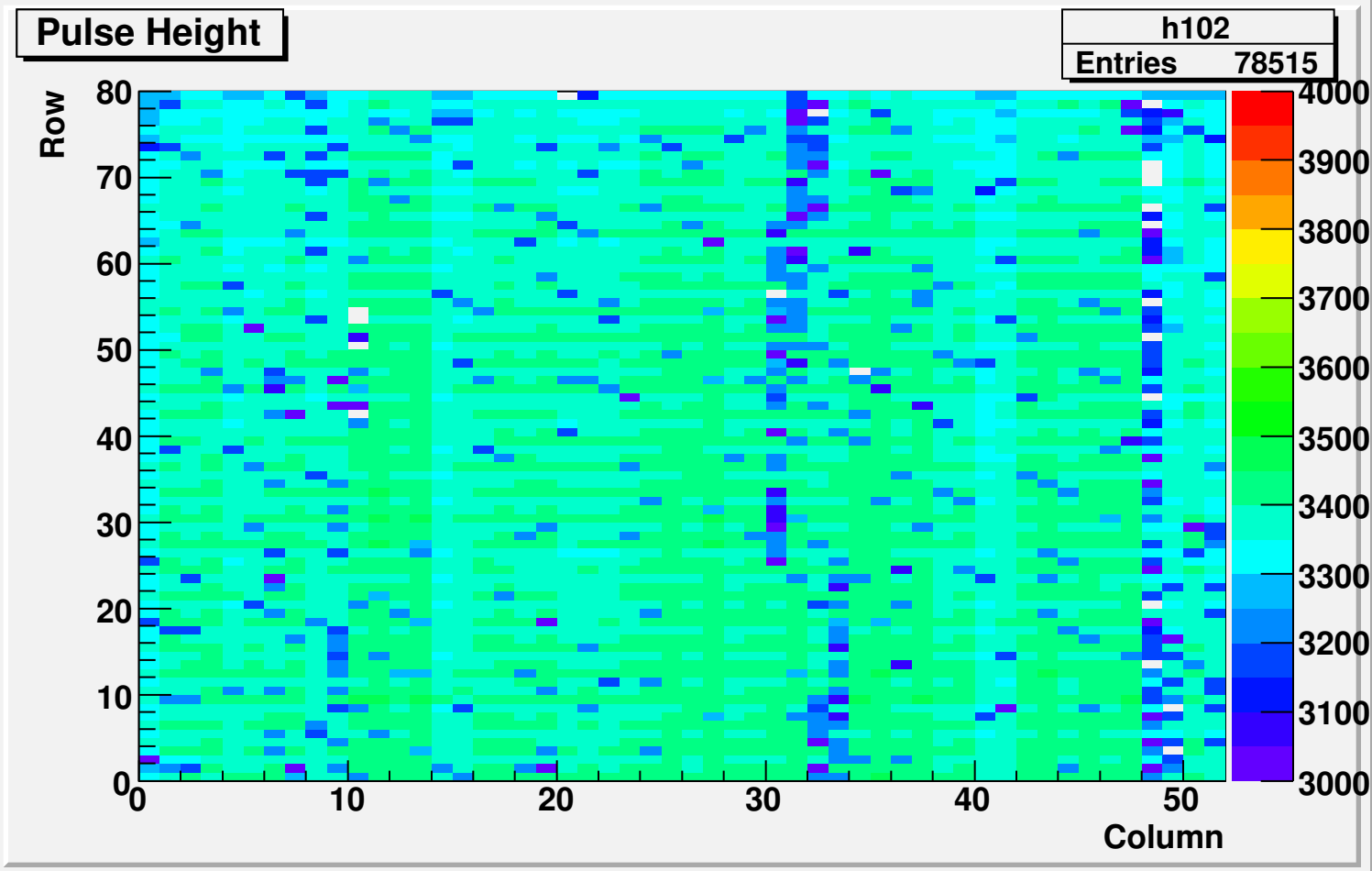
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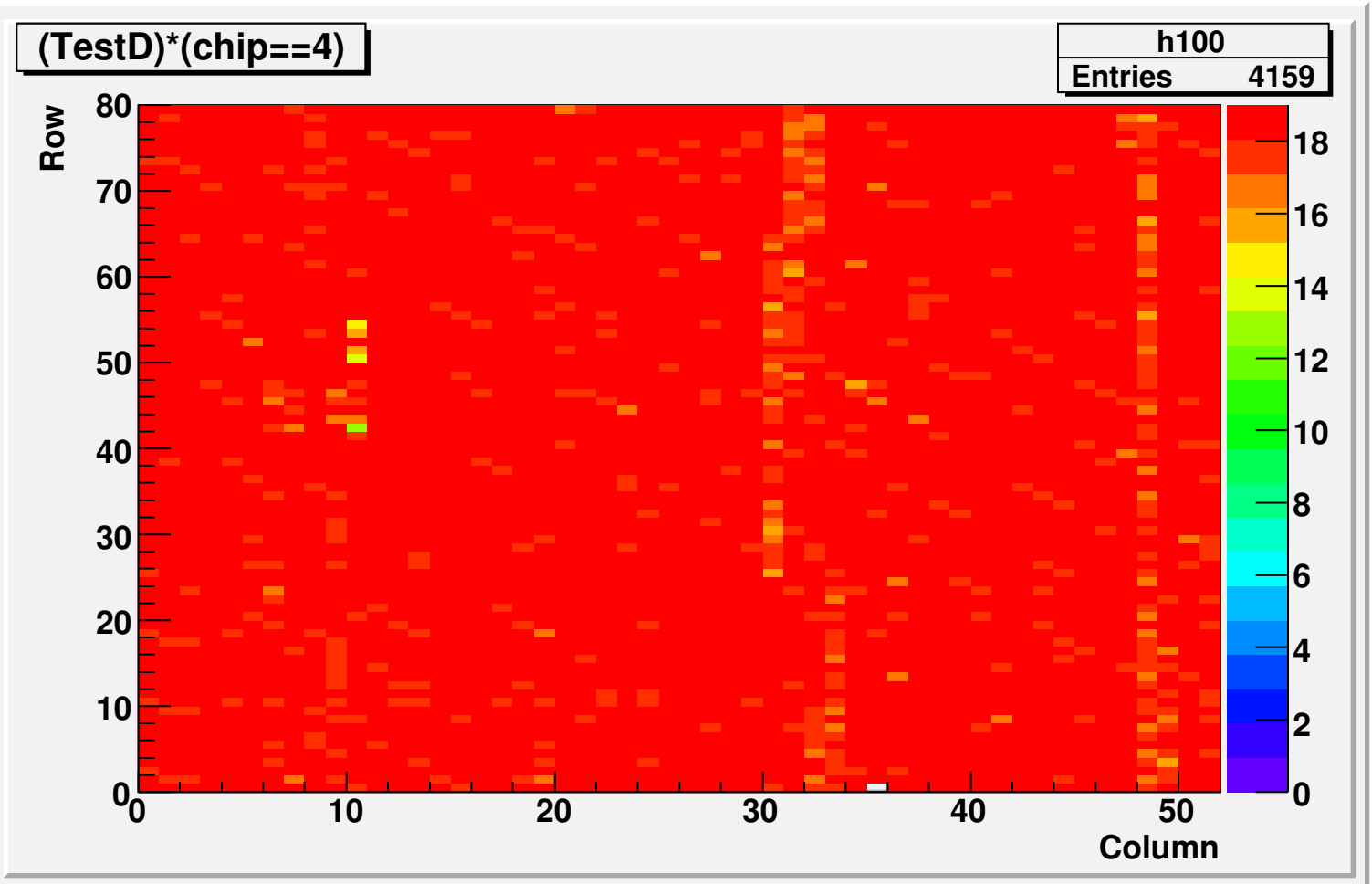
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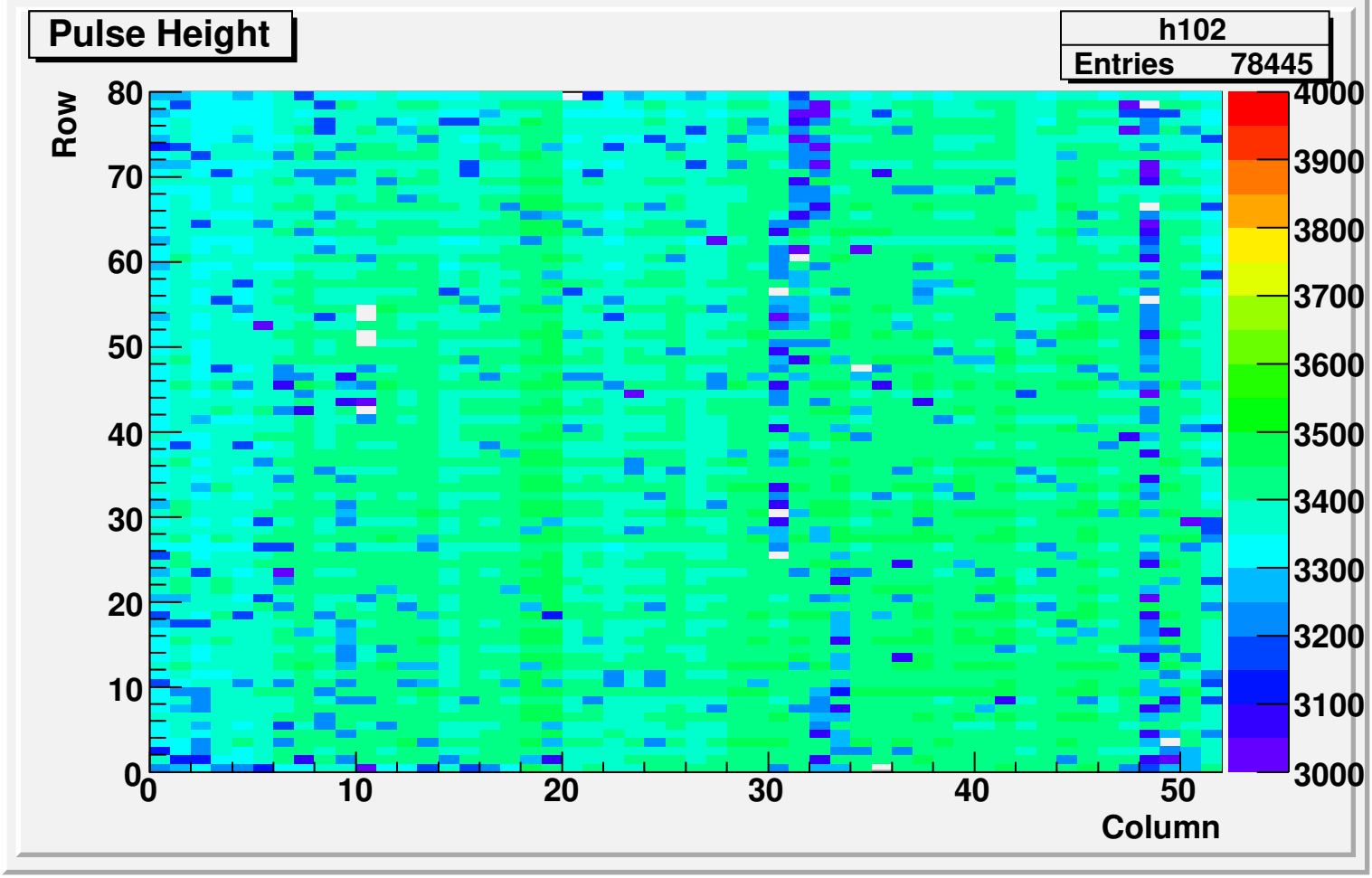
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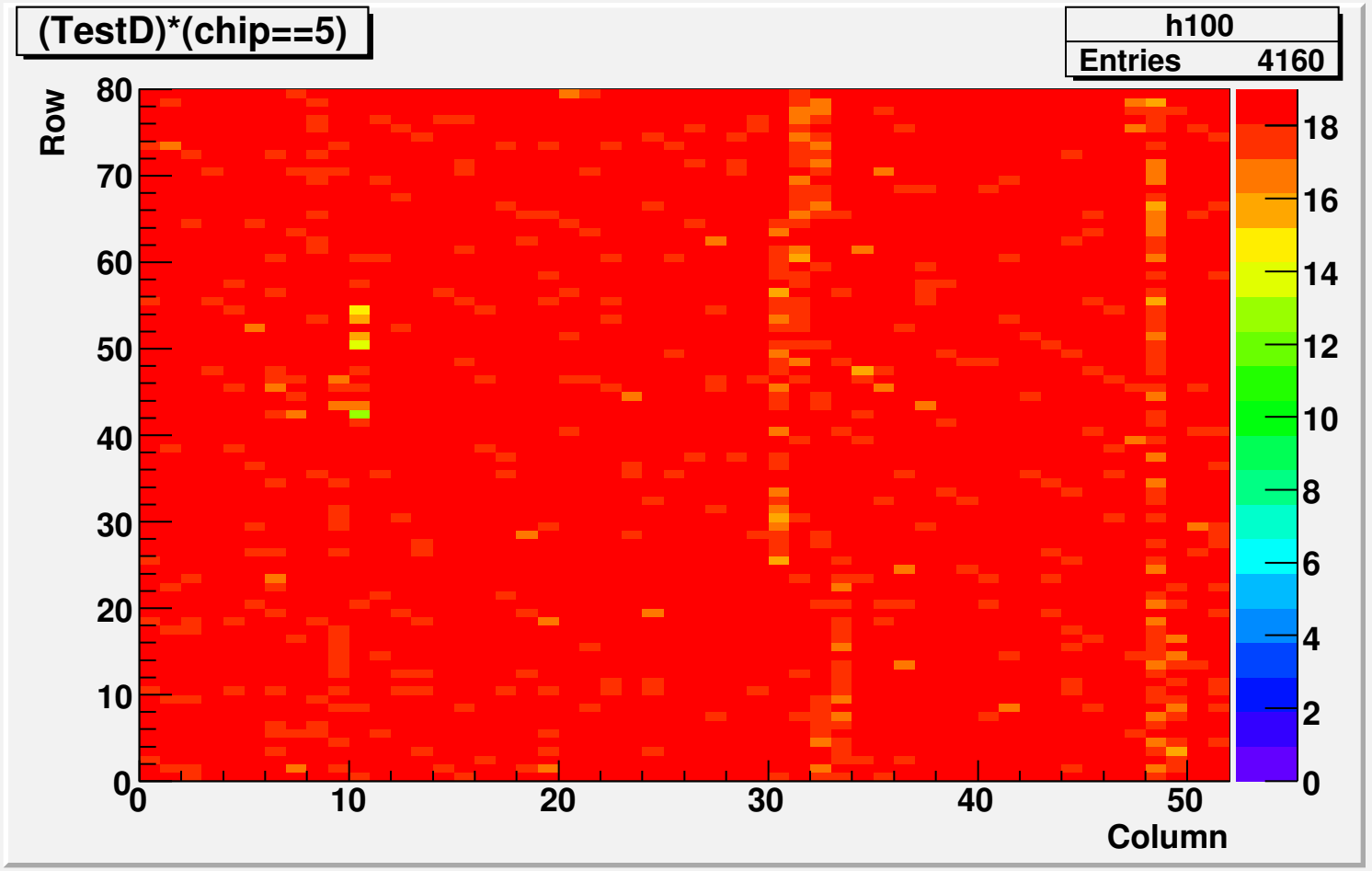
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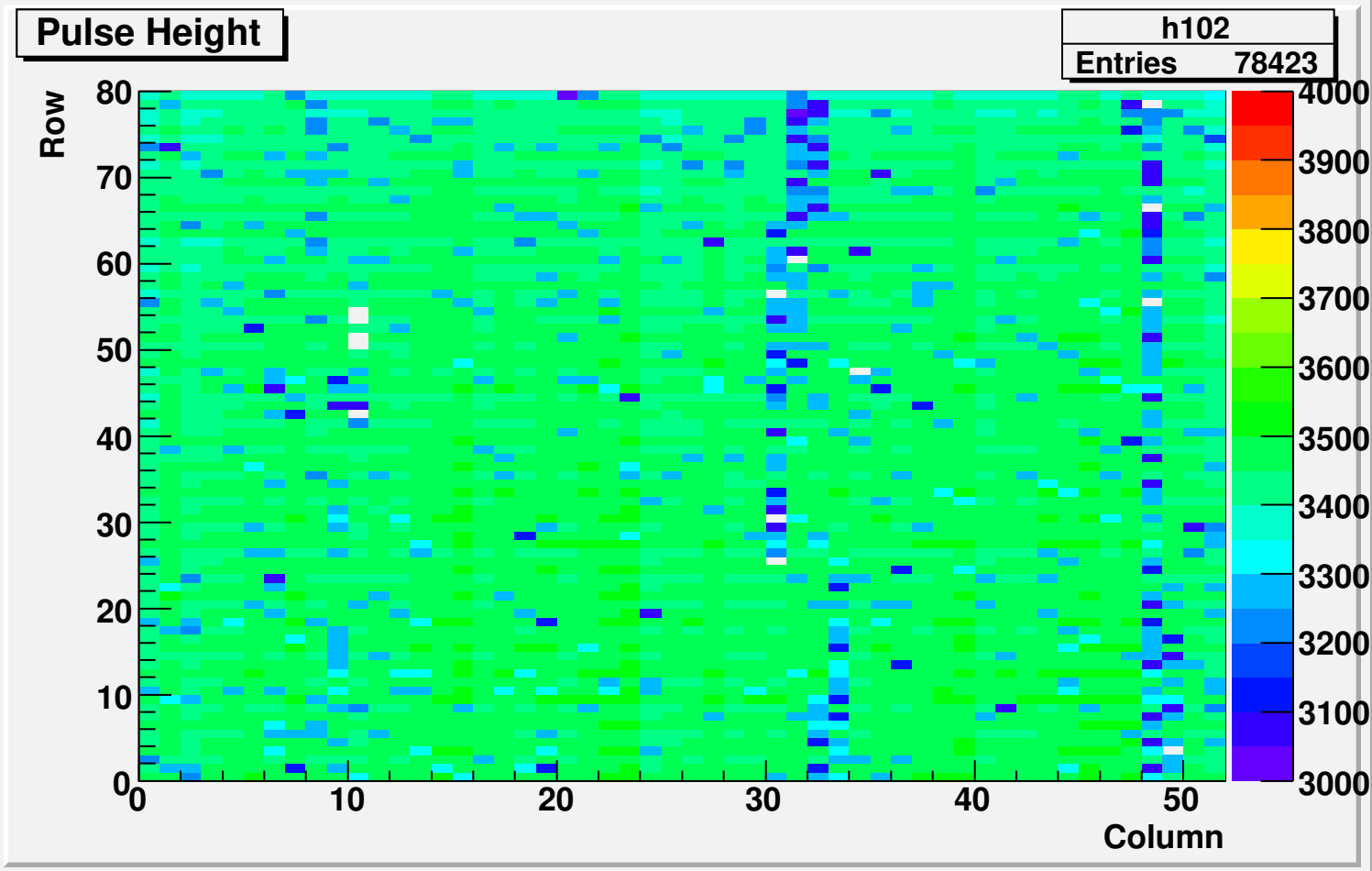
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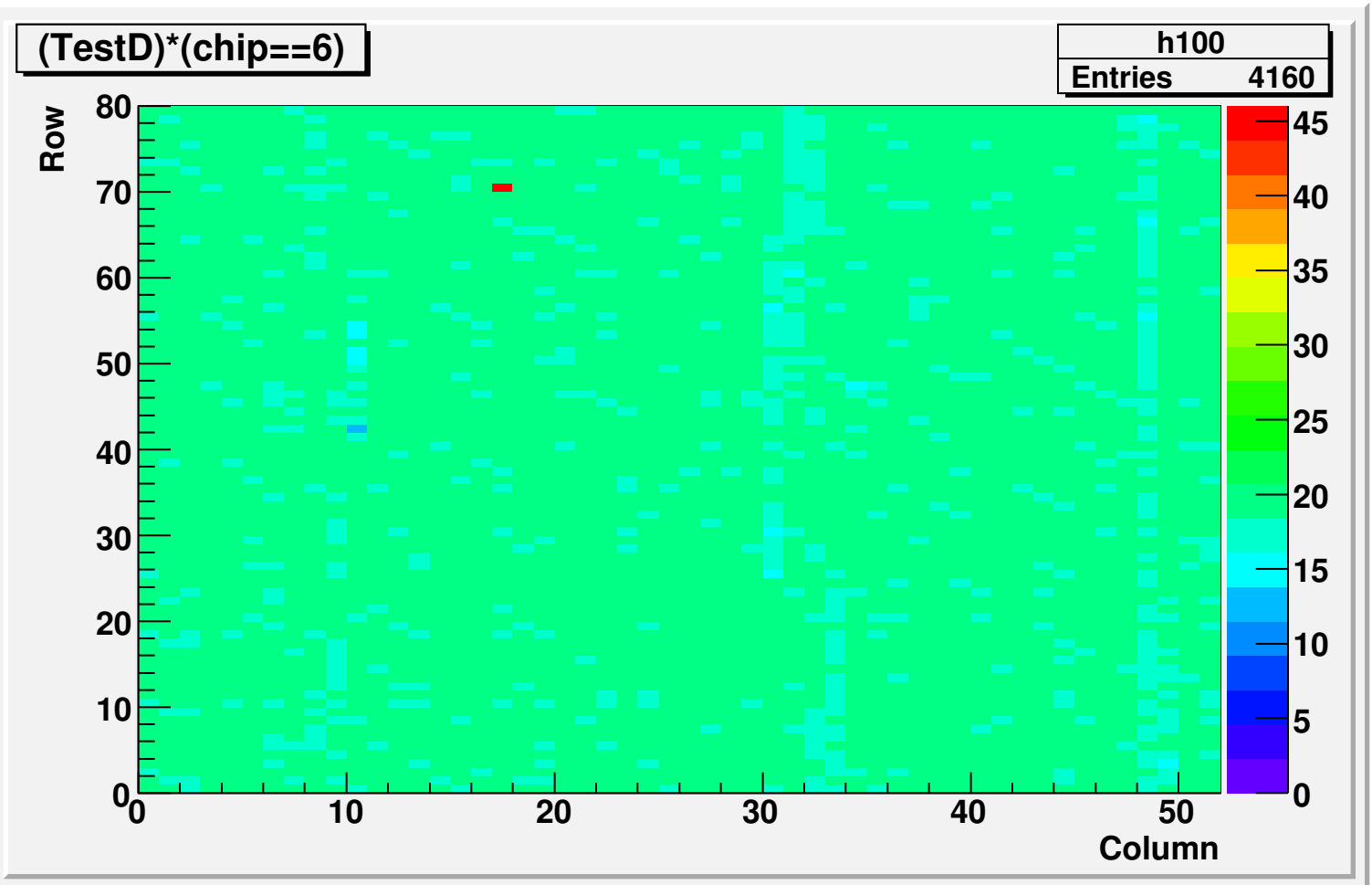
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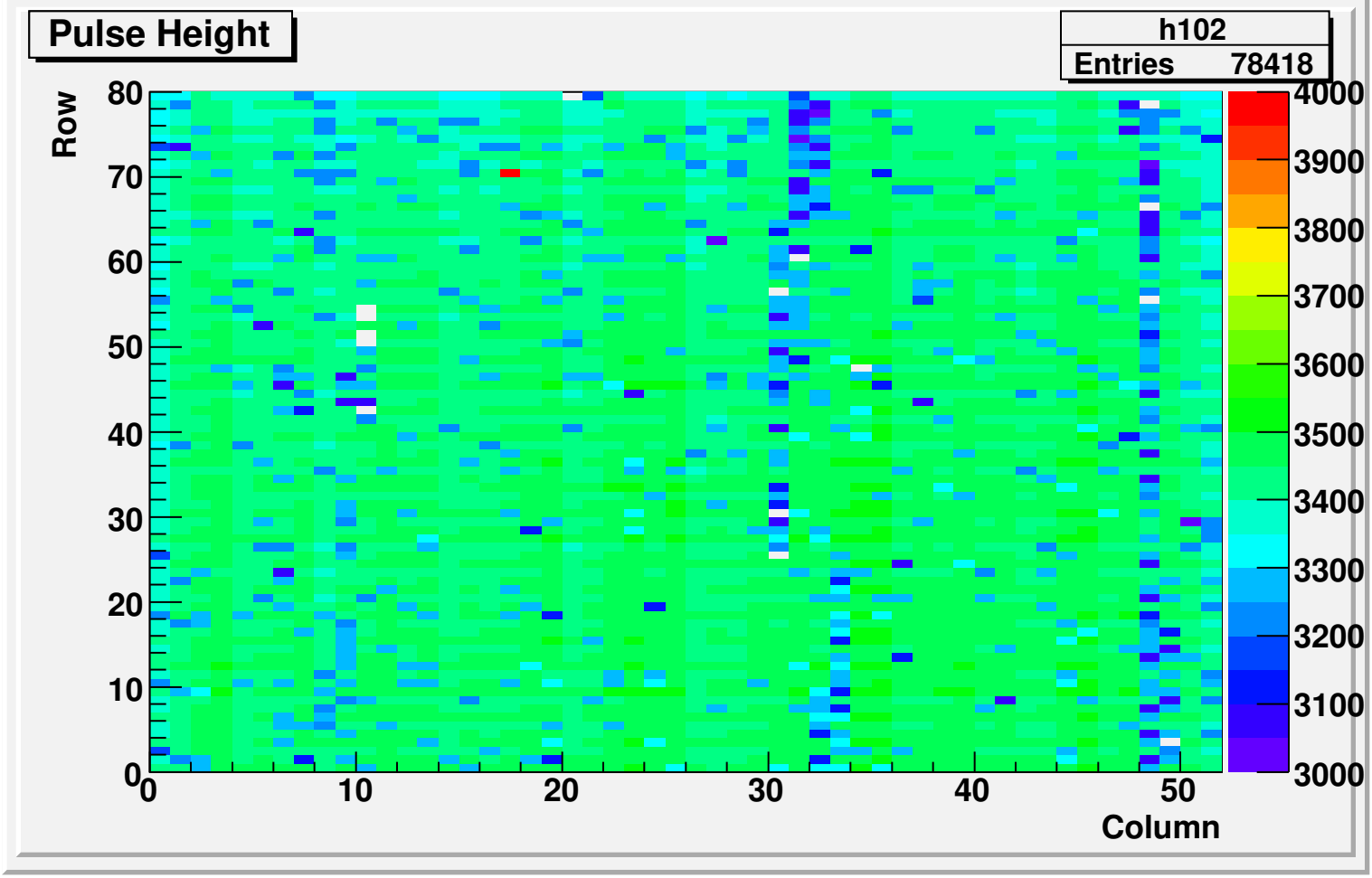
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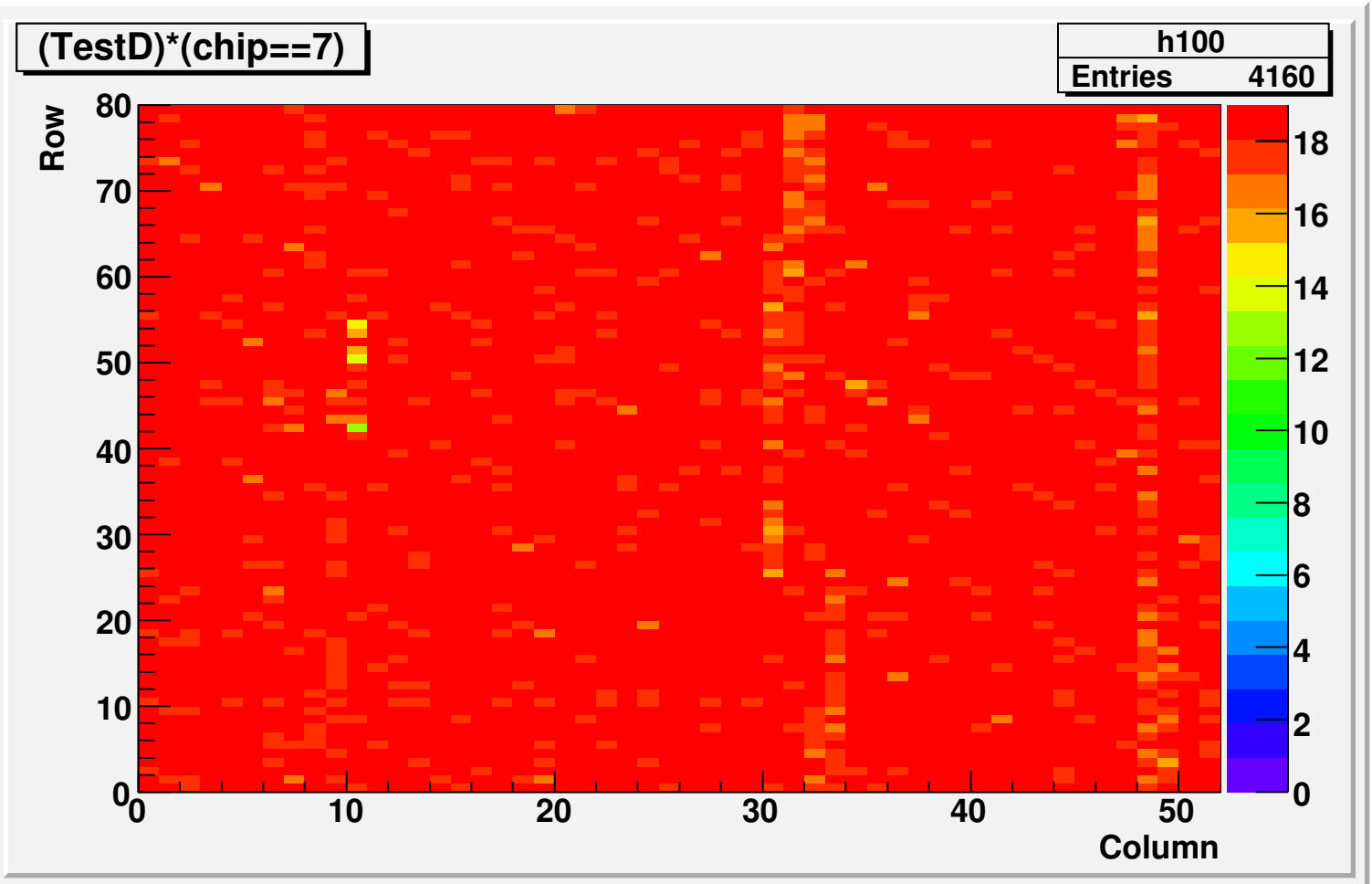
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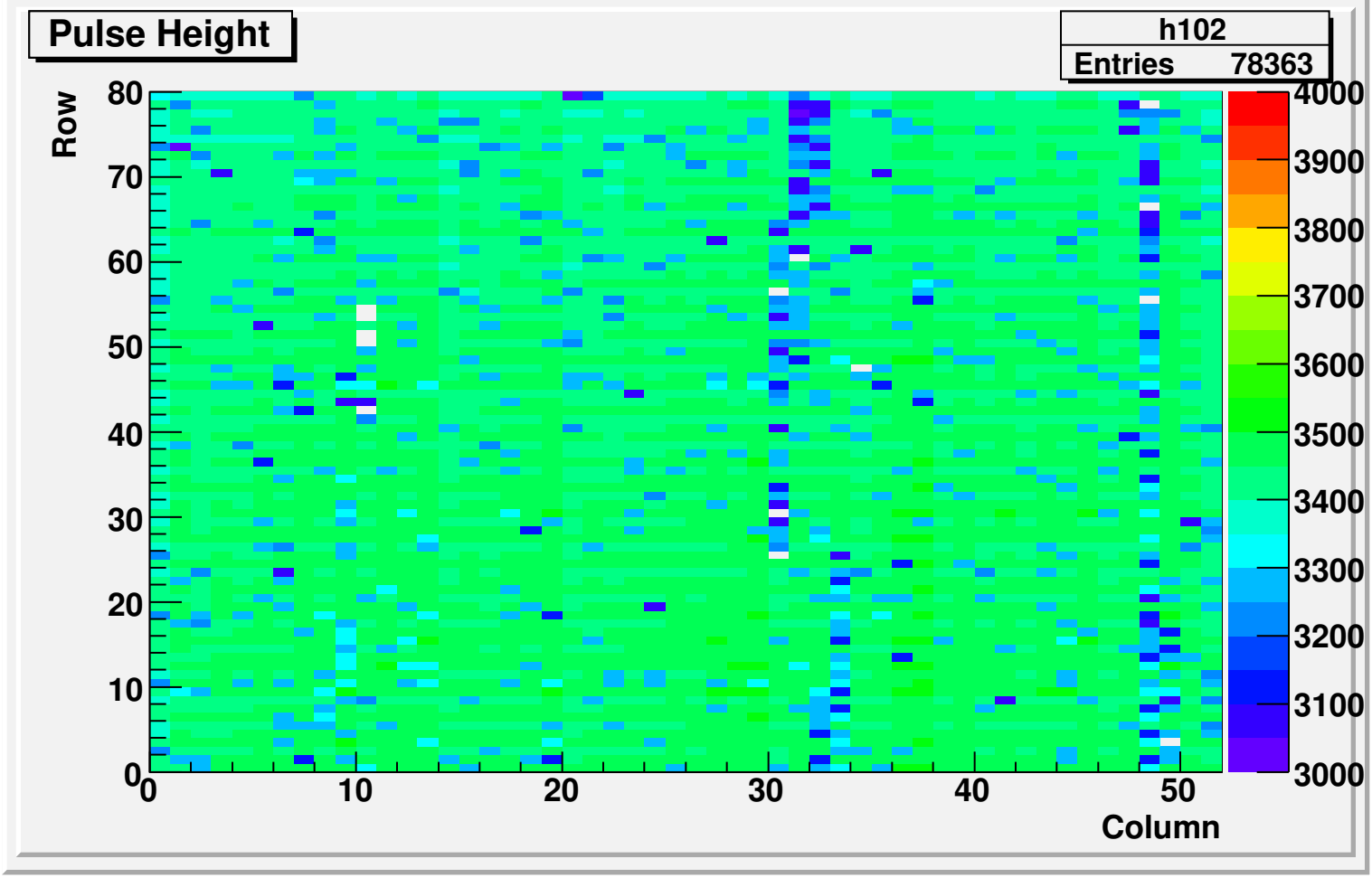
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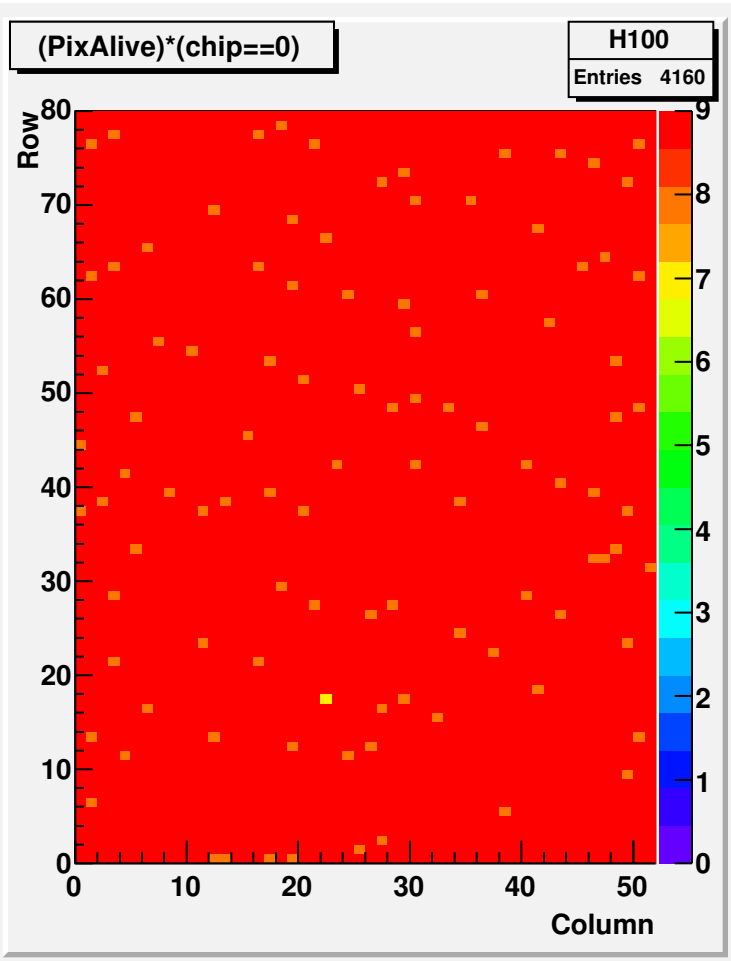
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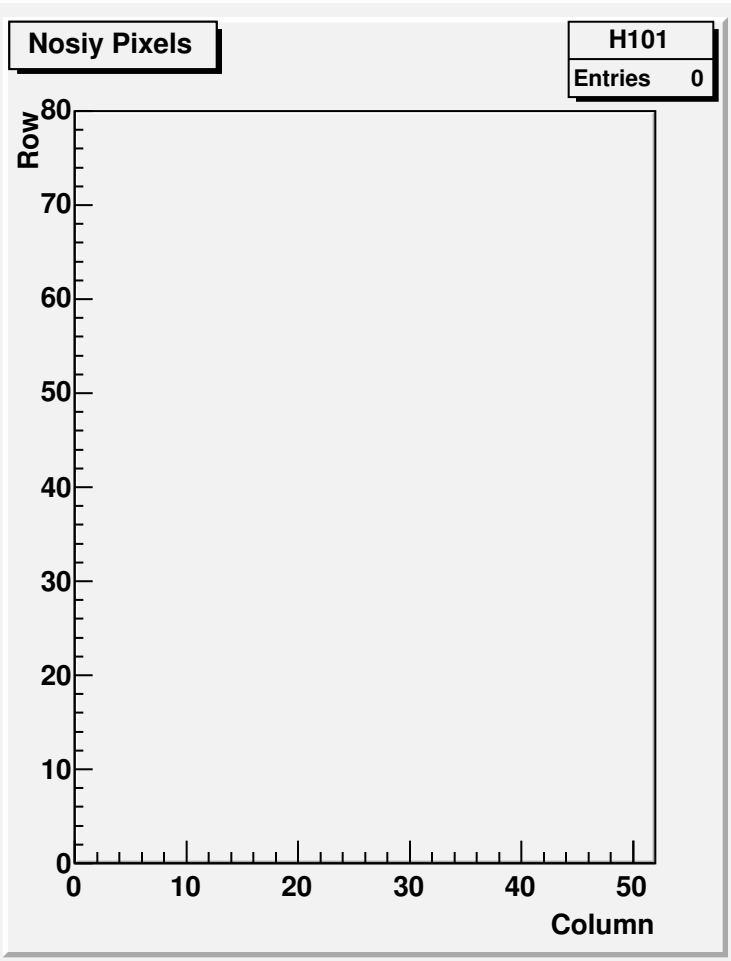
Column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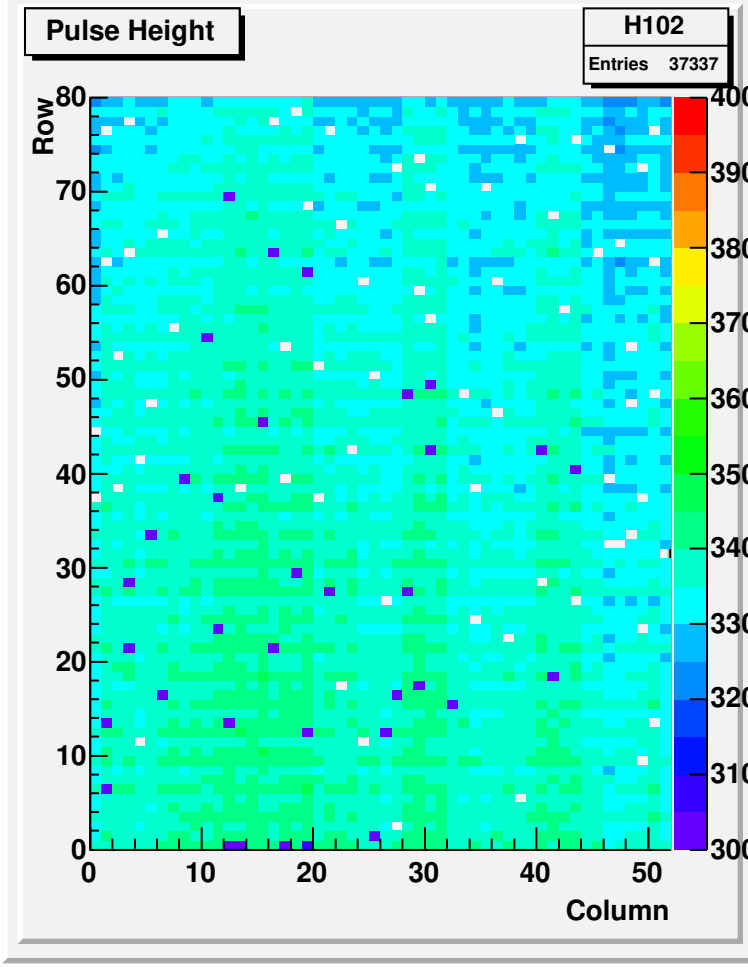
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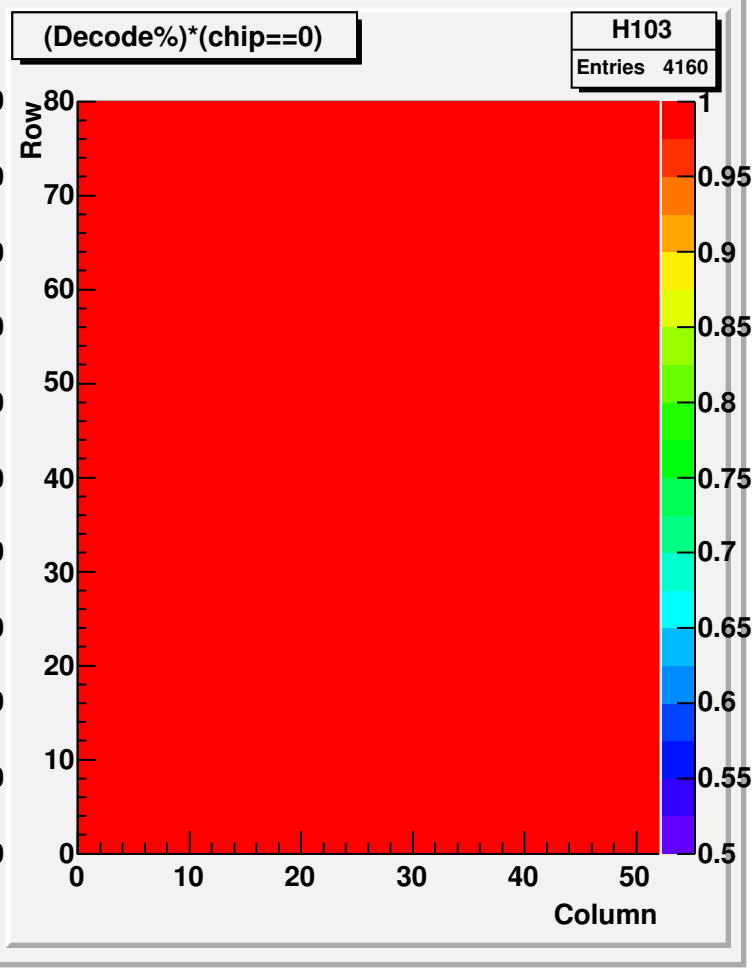
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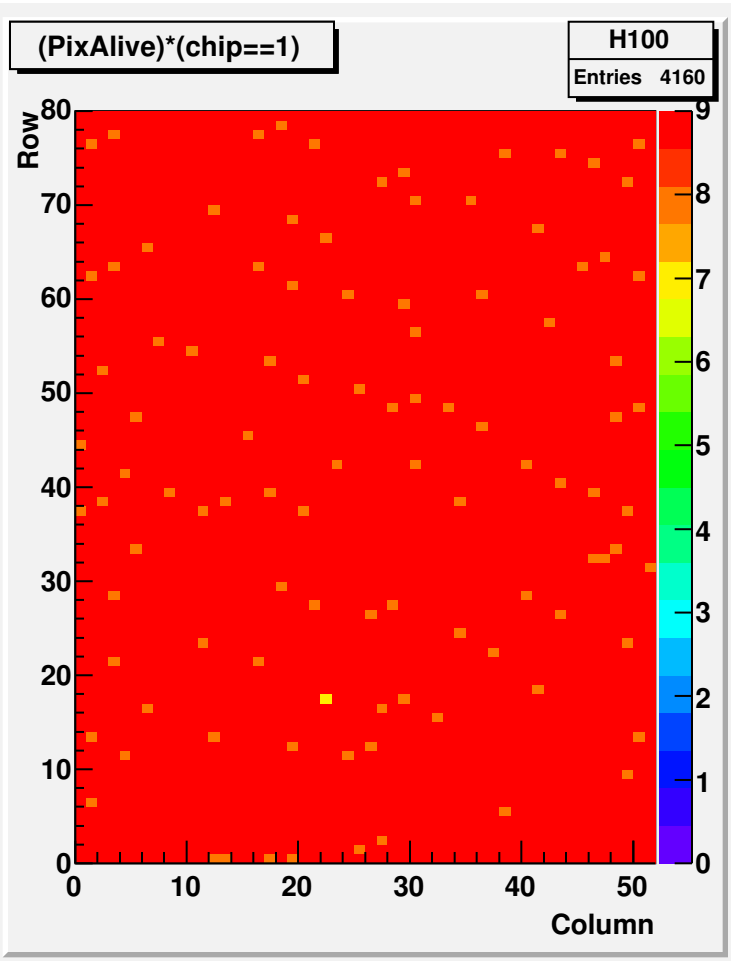
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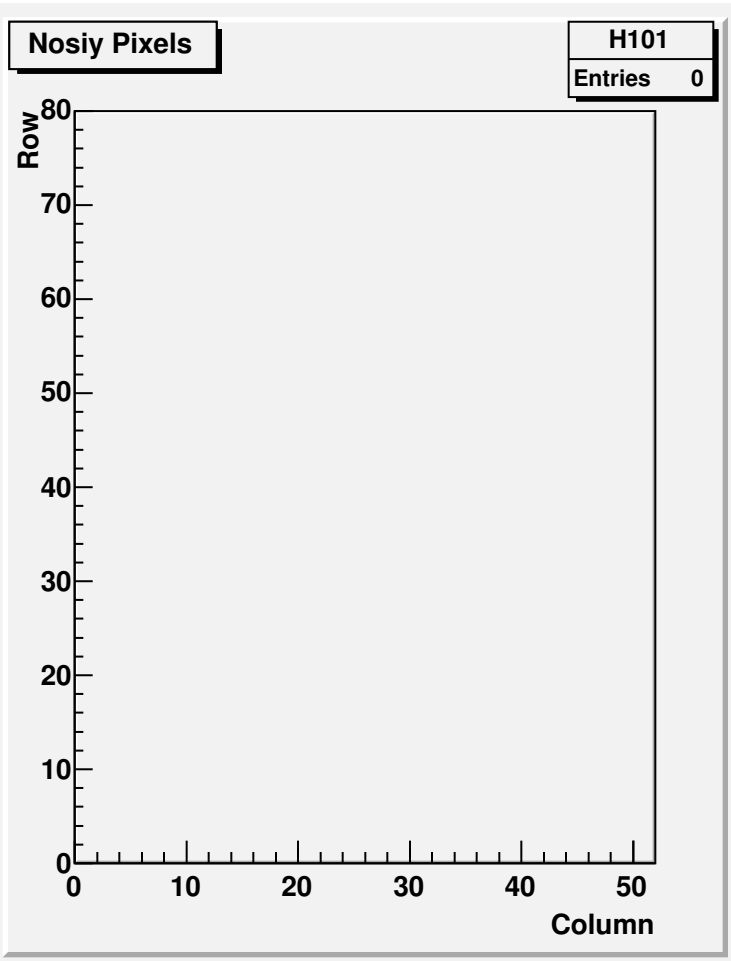
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H101
Entries 0
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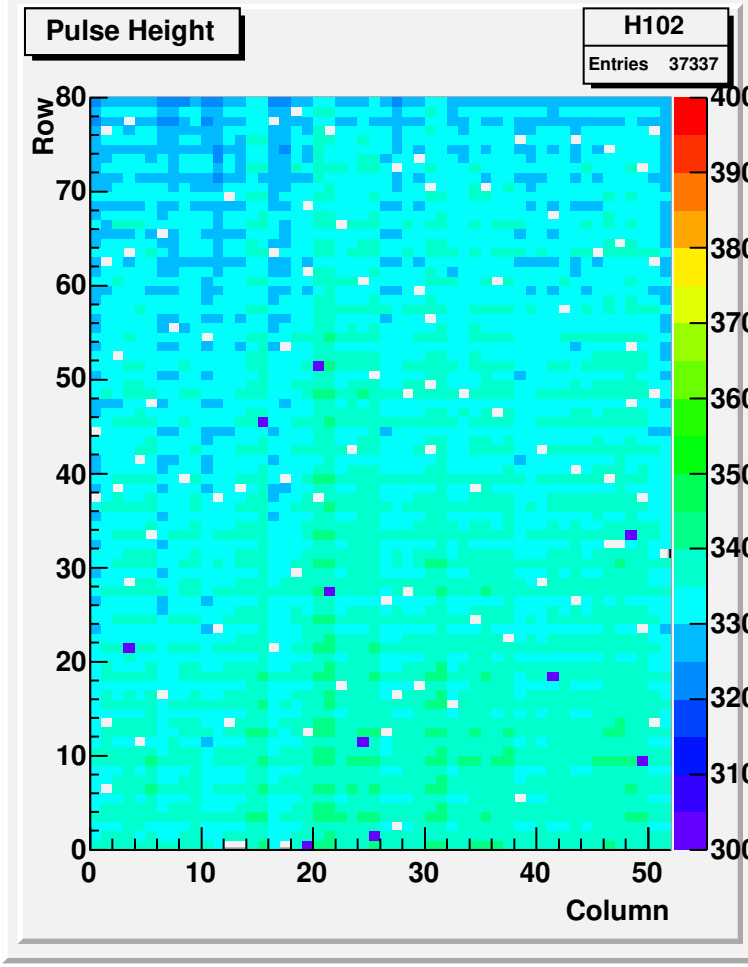
Column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H102
Entries 37337
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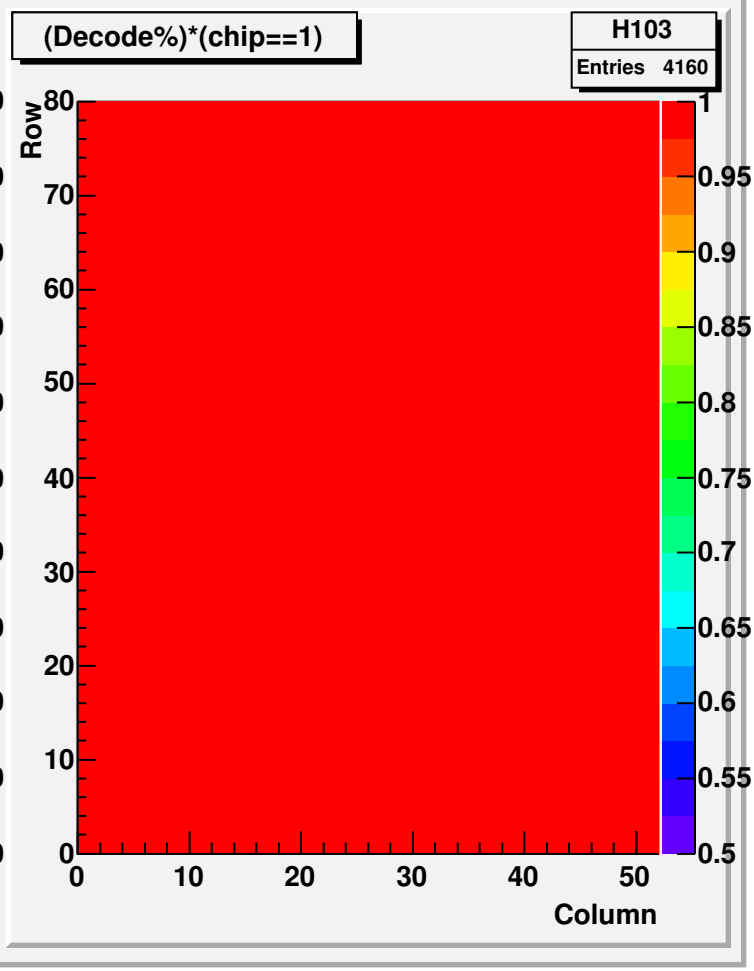
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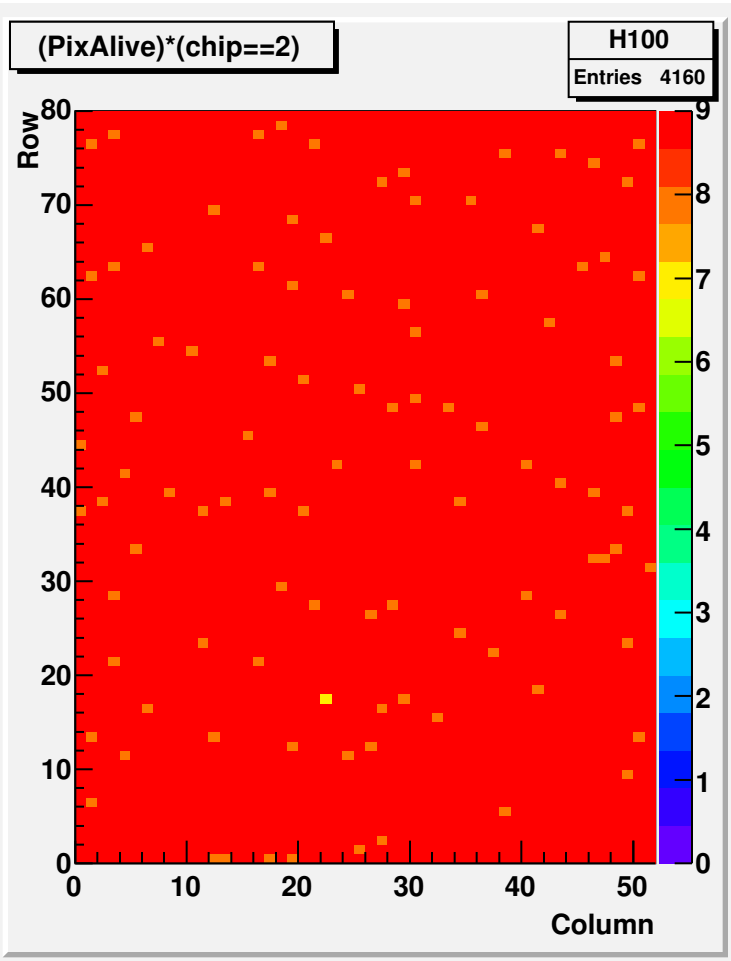
H103
Entries 4160
Row
Column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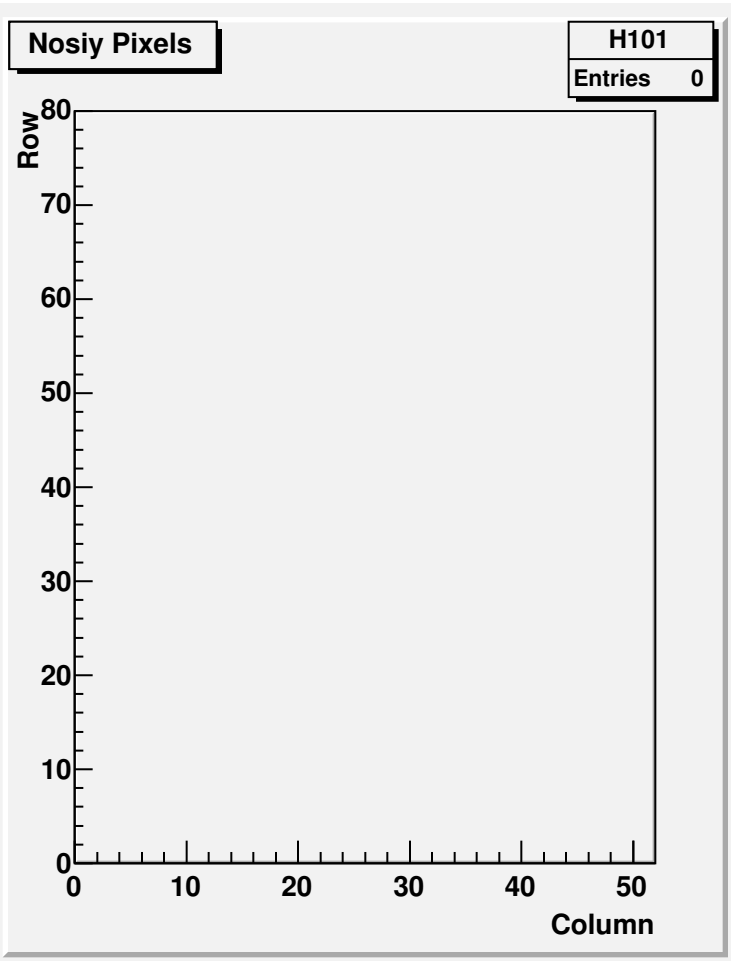
H100
Entries 4160
Row
Column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Row
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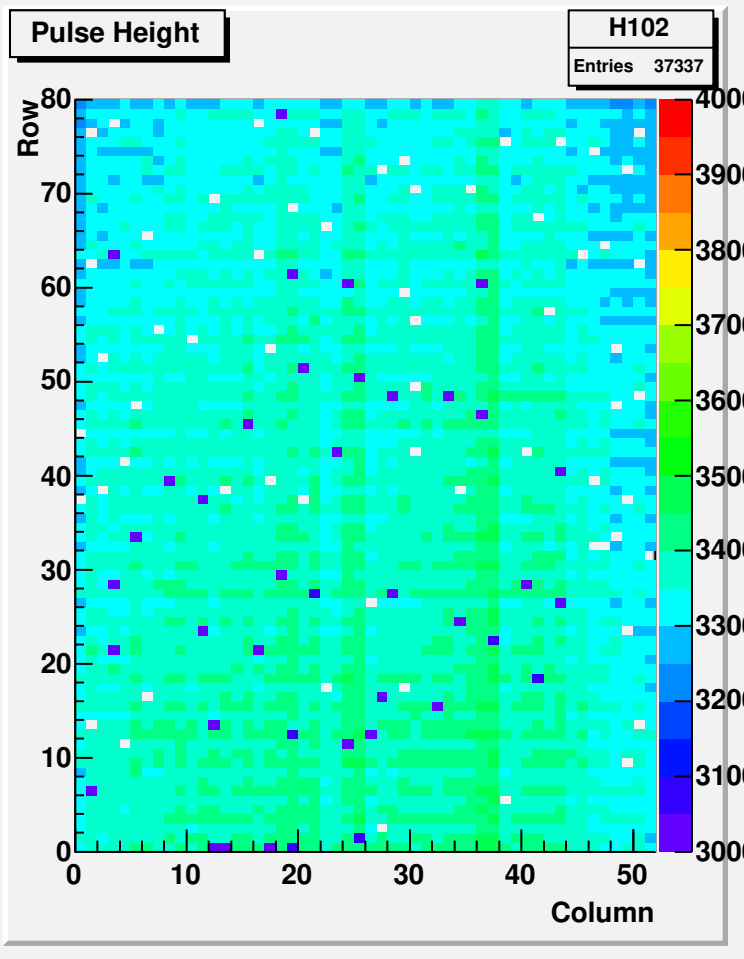
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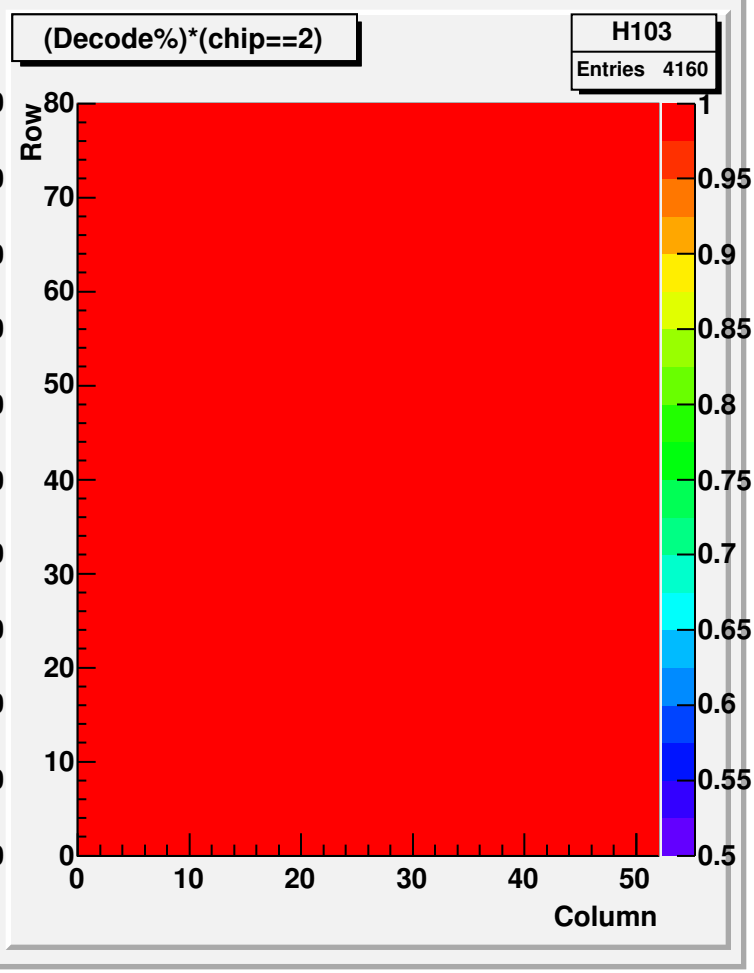
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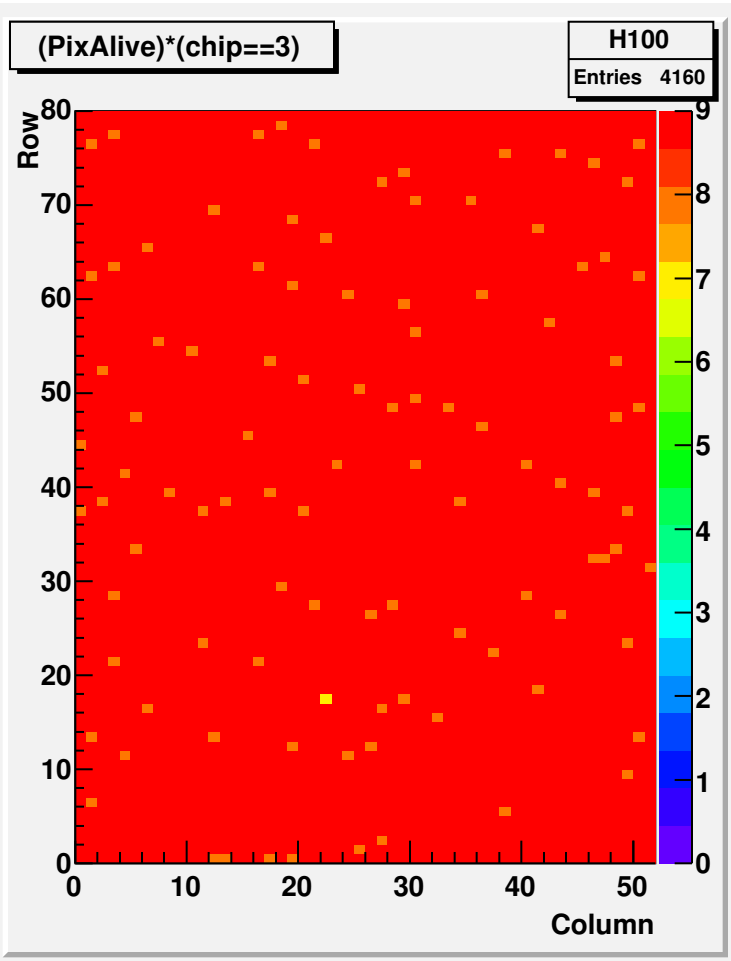
H103
Entries 4160
Row
Column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H100
Entries 4160
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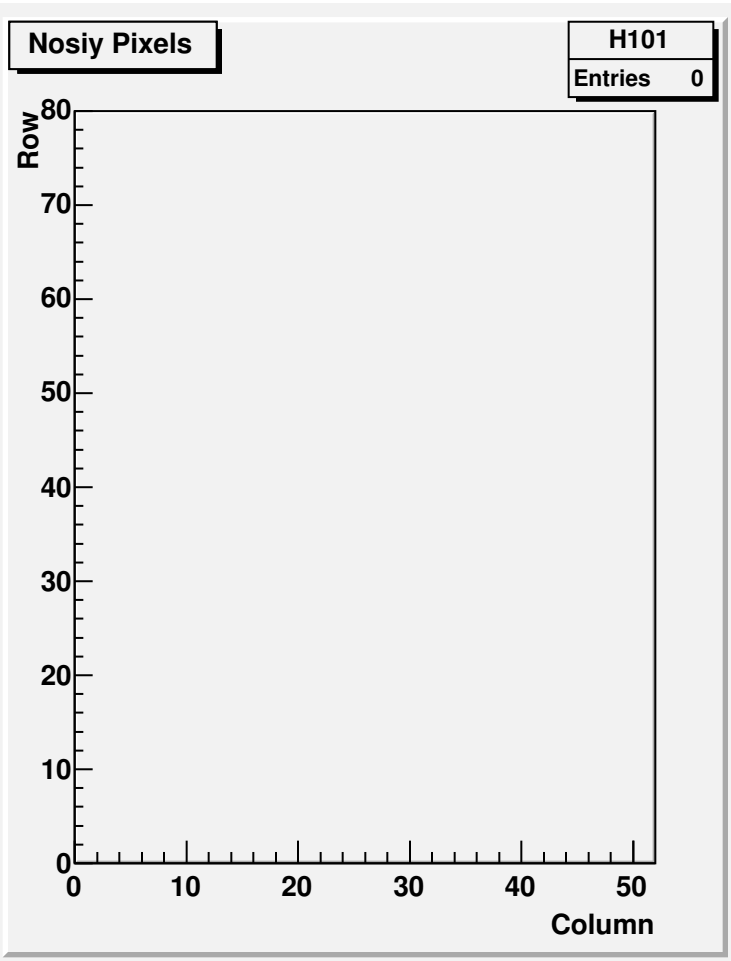
Column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H101
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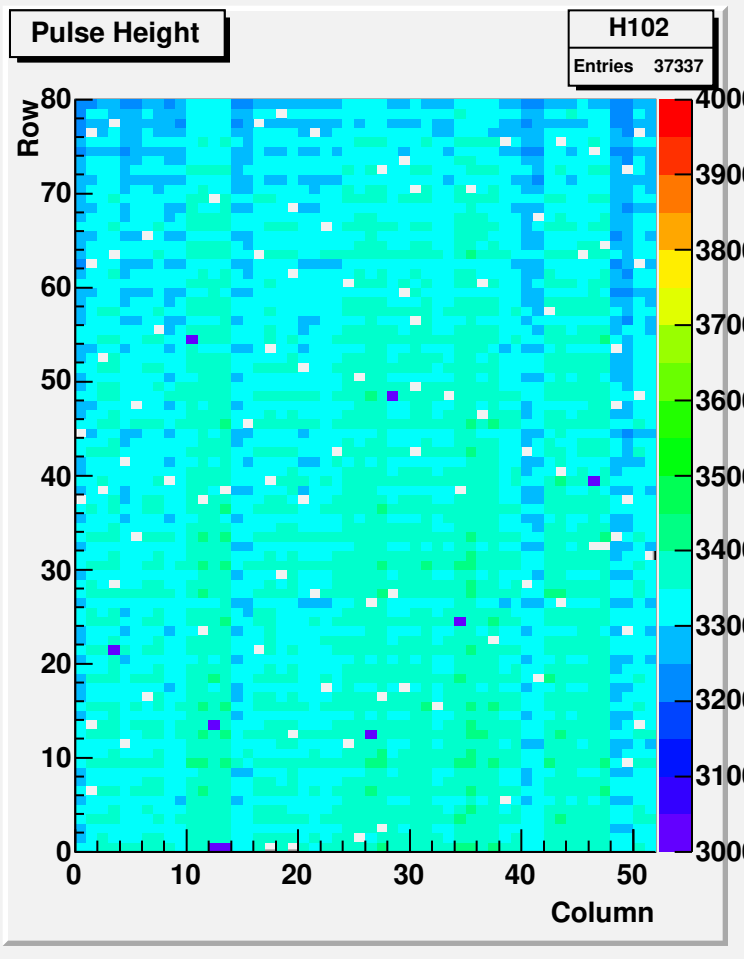
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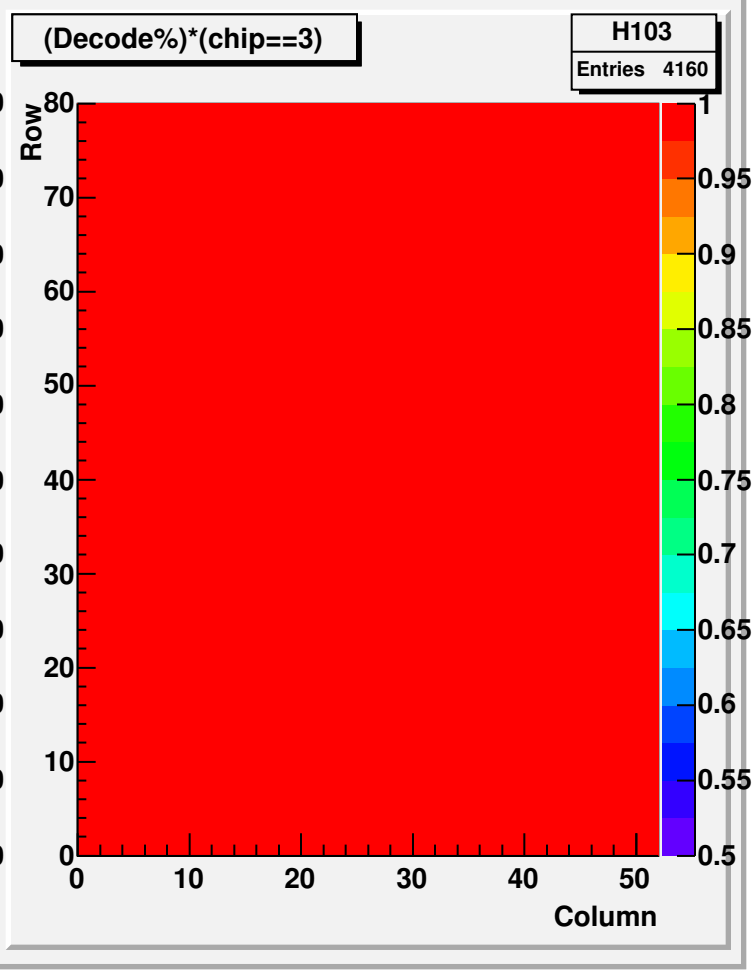
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H103
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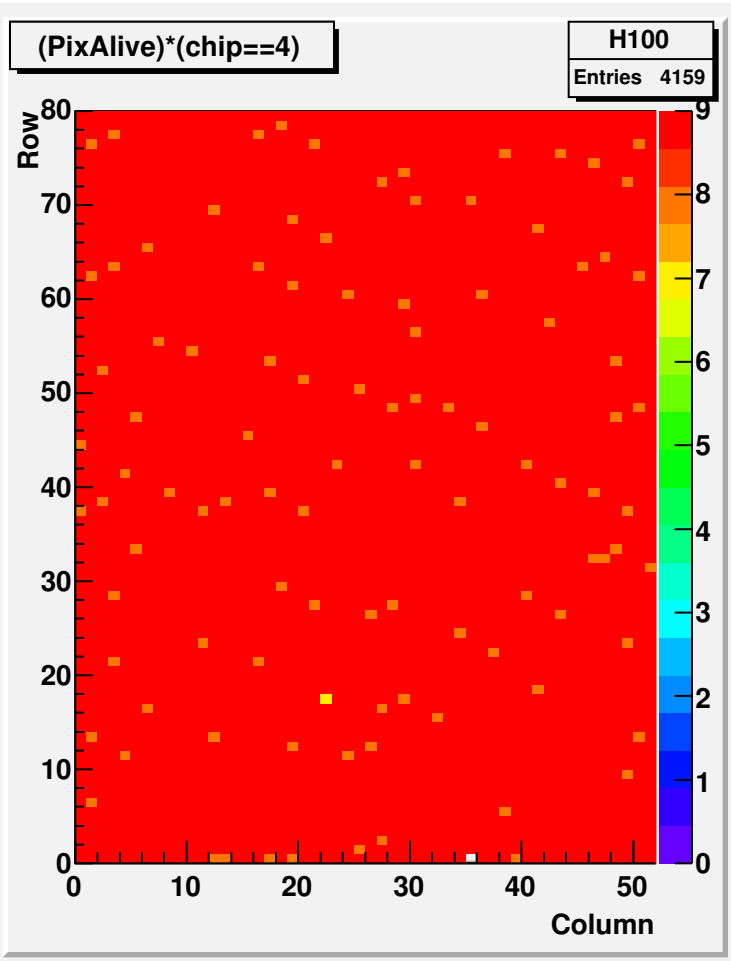
Column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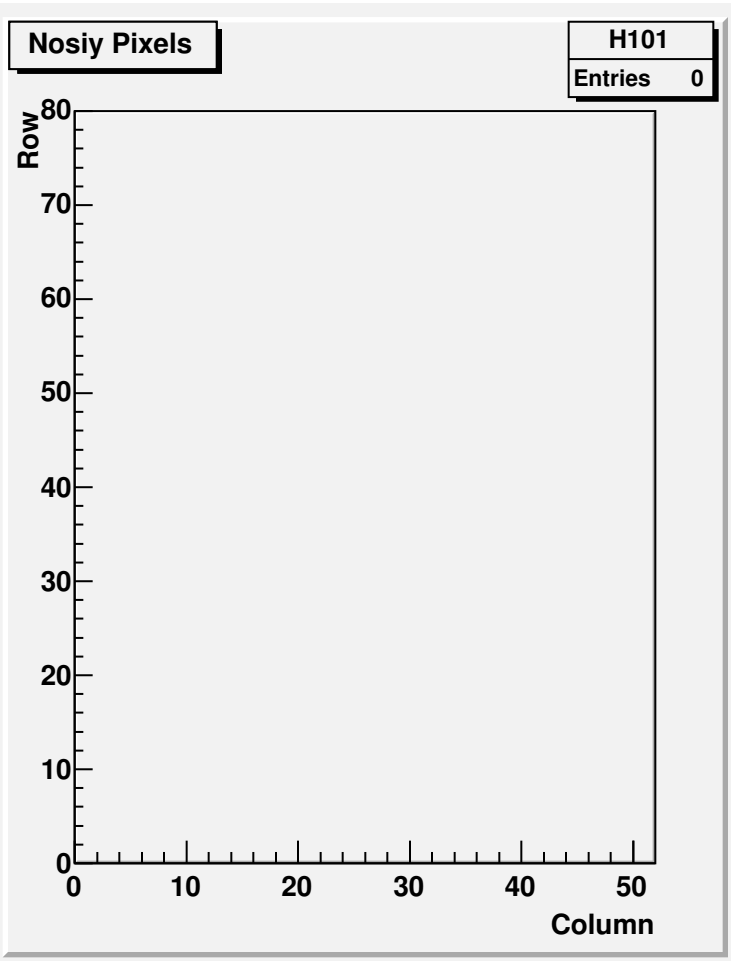
H100
Entries 4159
Row
Column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H101
Entries 0
Row
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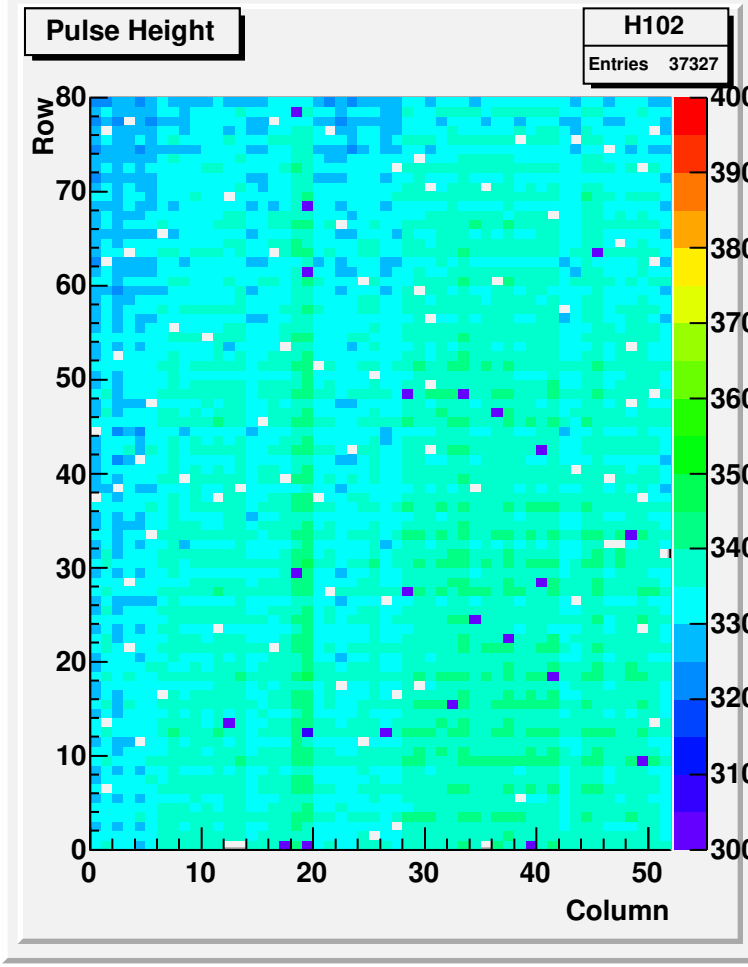
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H102
Entries 37327
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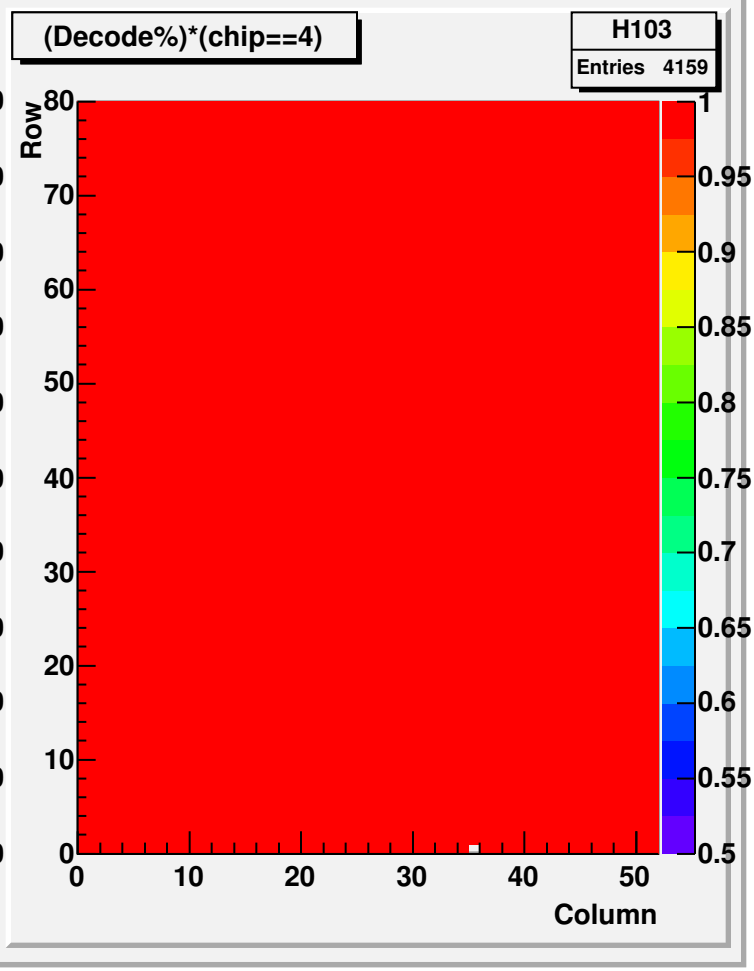
Column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H103
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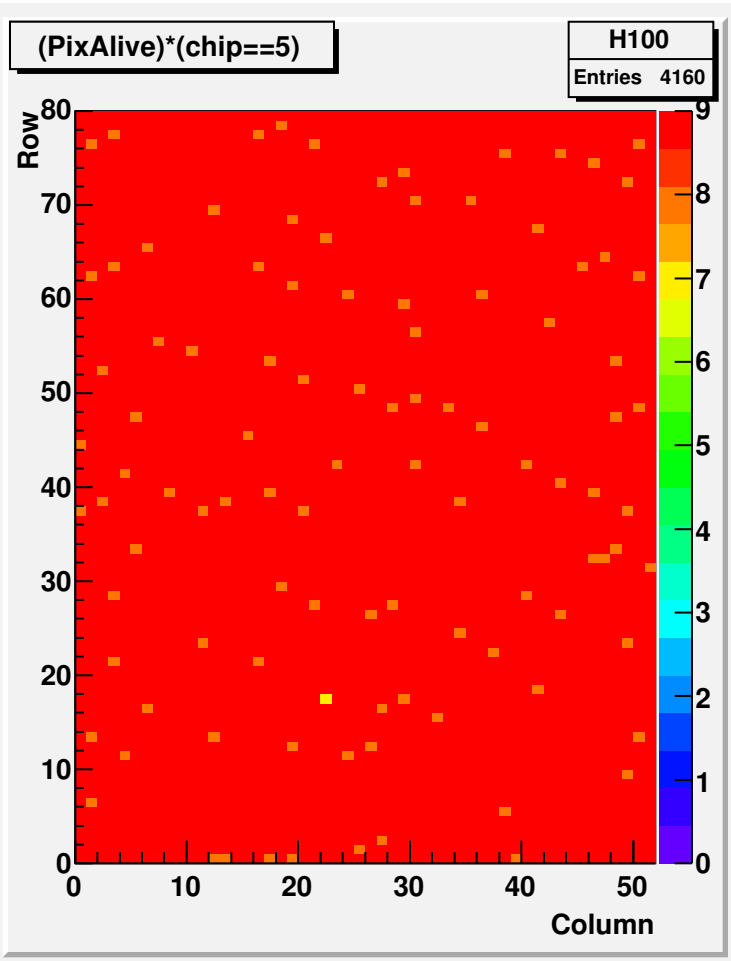
Column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H100
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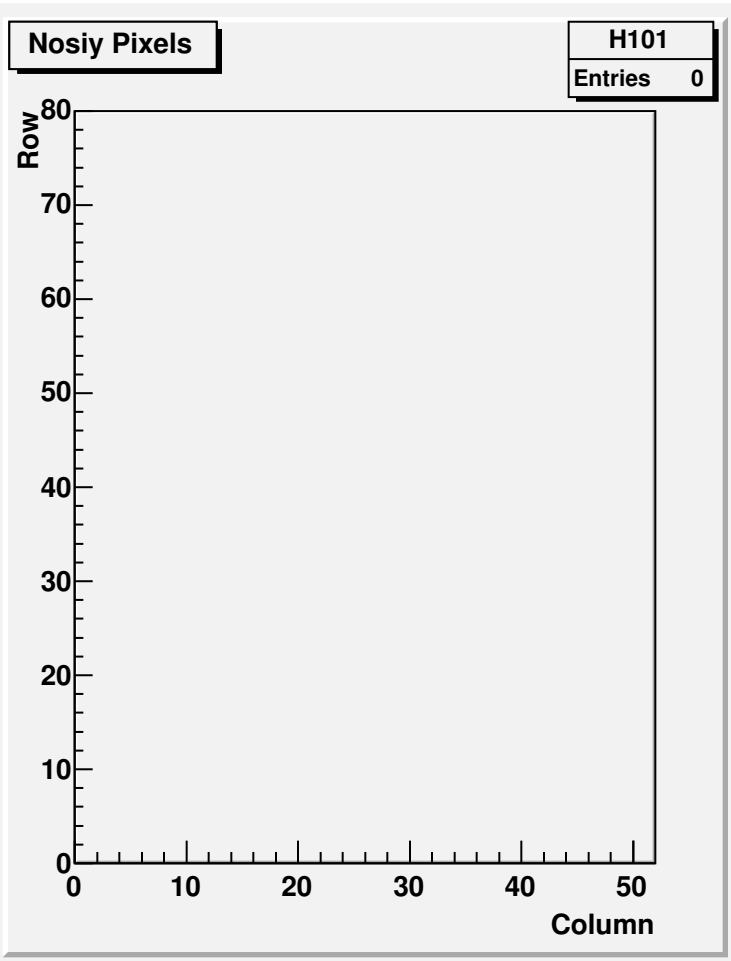
Column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H101
Entries 0
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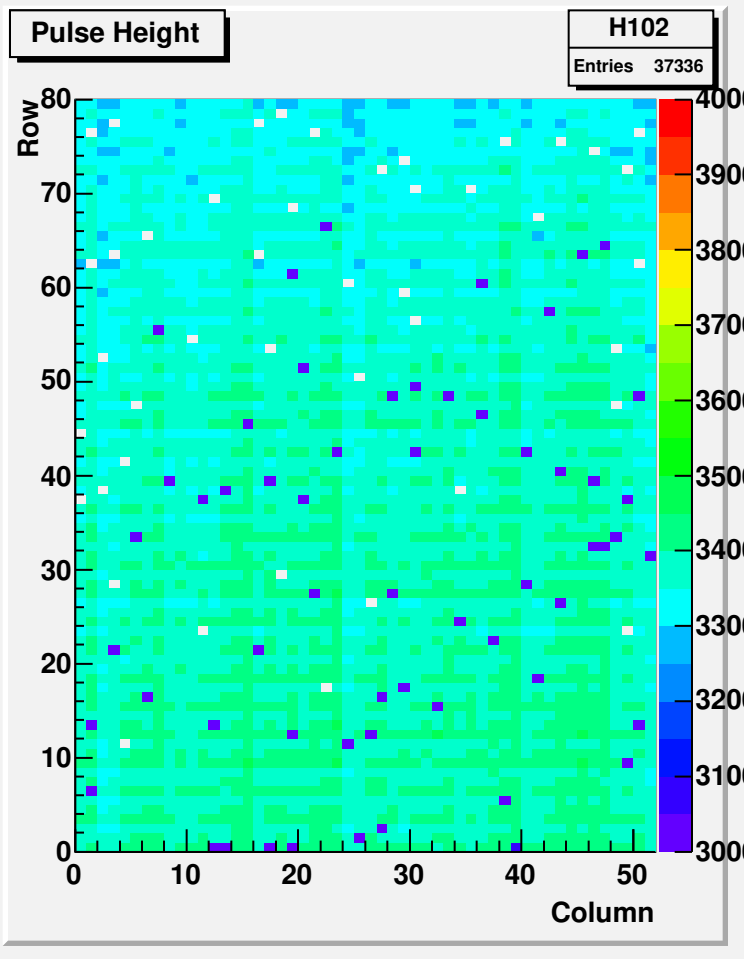
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H102
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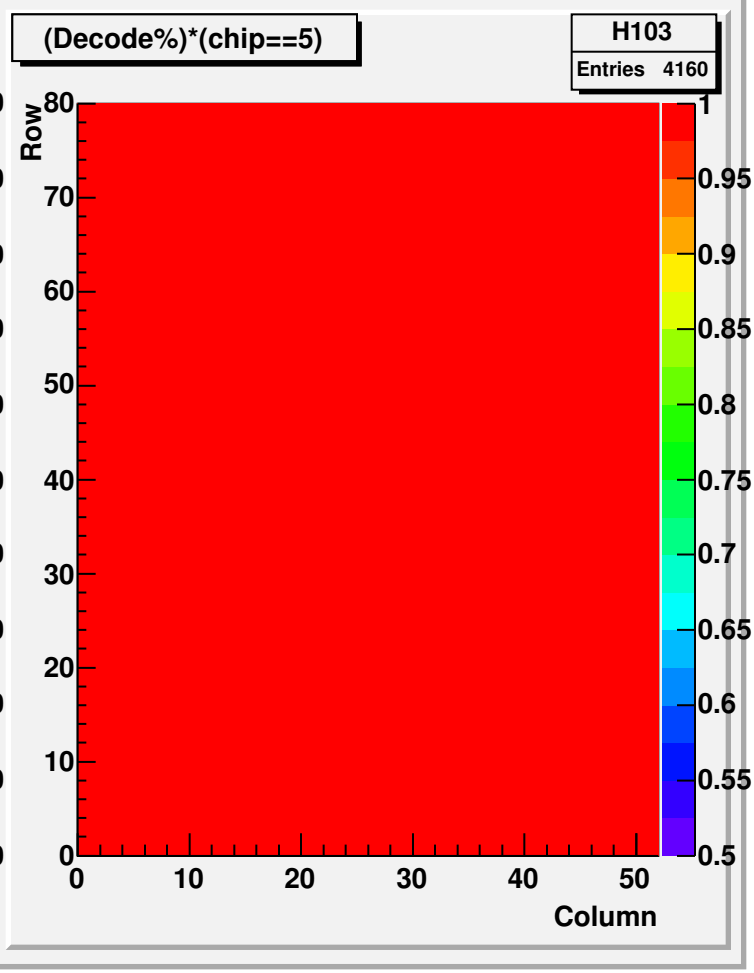
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H103
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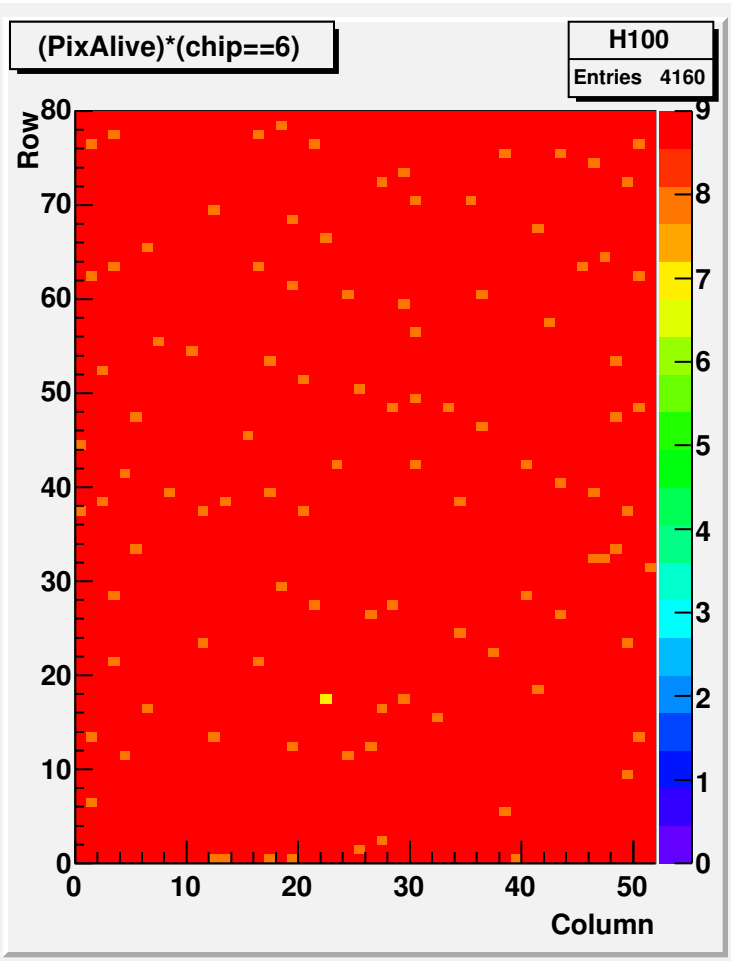
Column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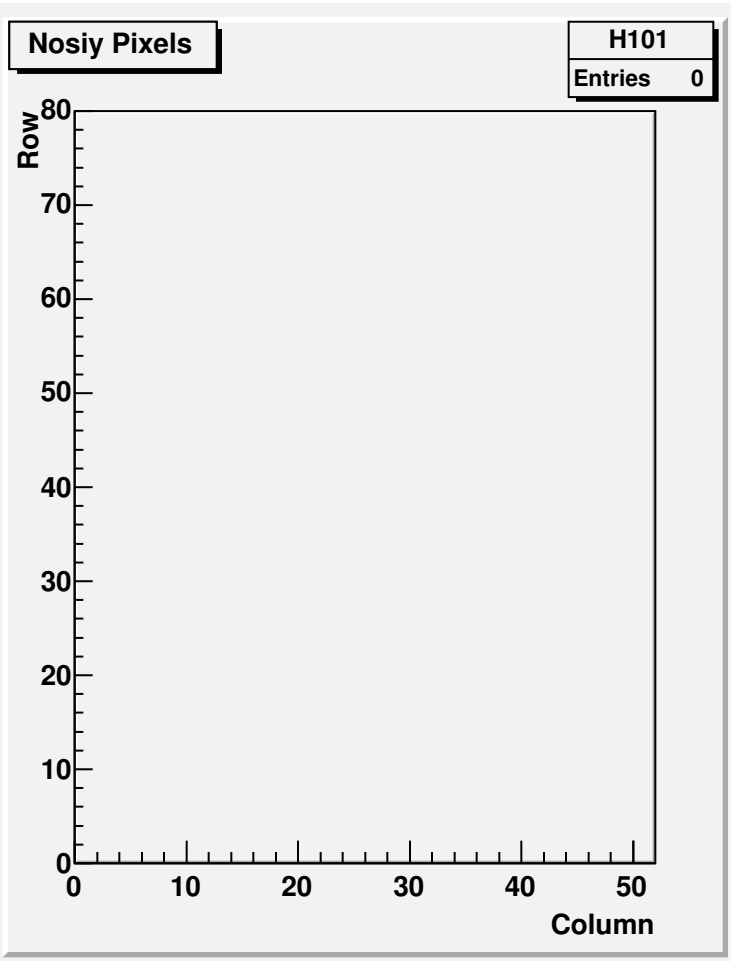
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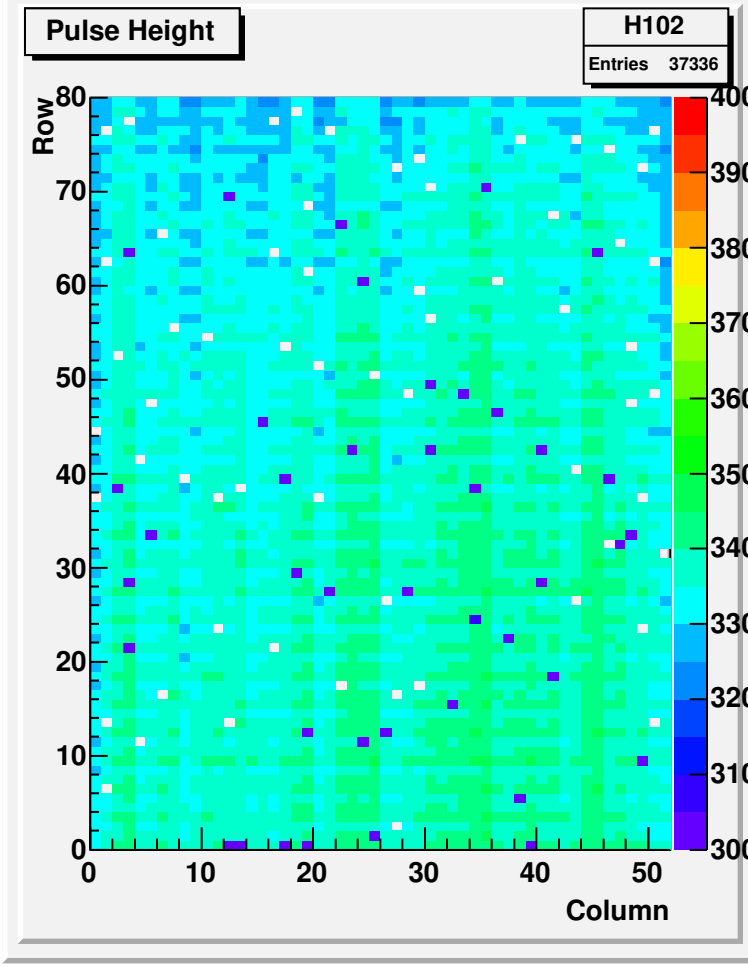
H101
Entries 0
Row
Column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H102
Entries 37336
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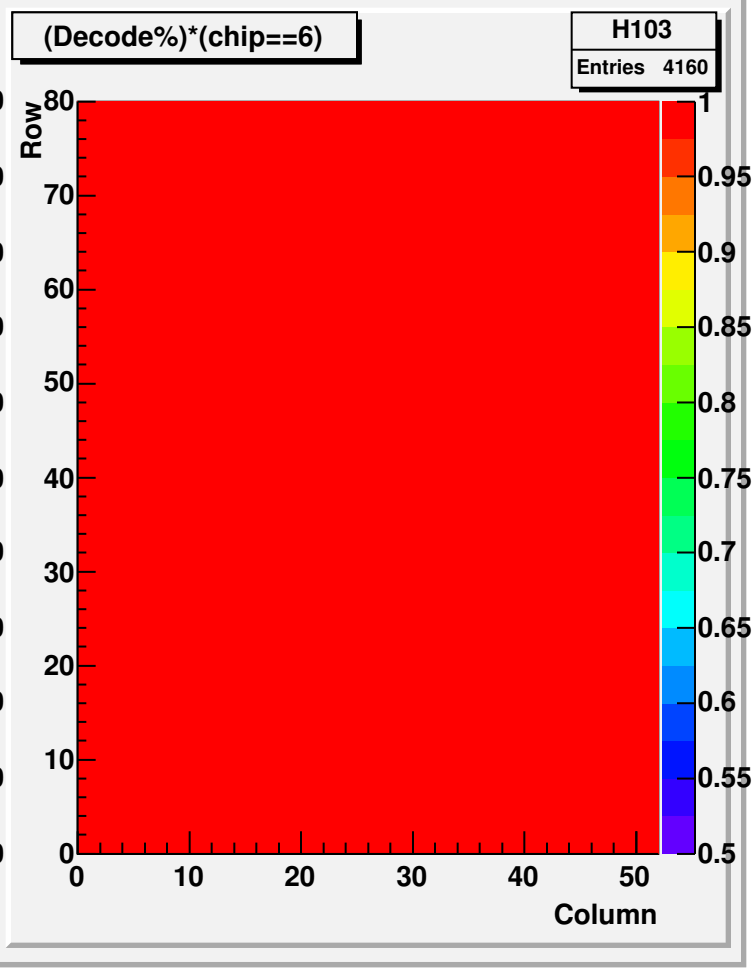
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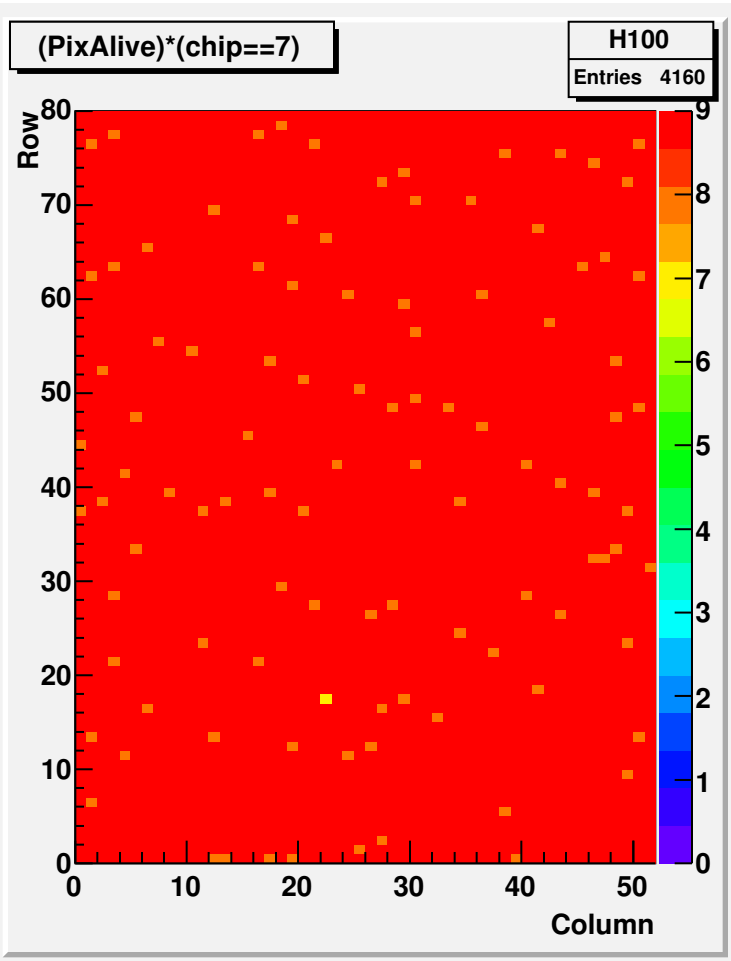
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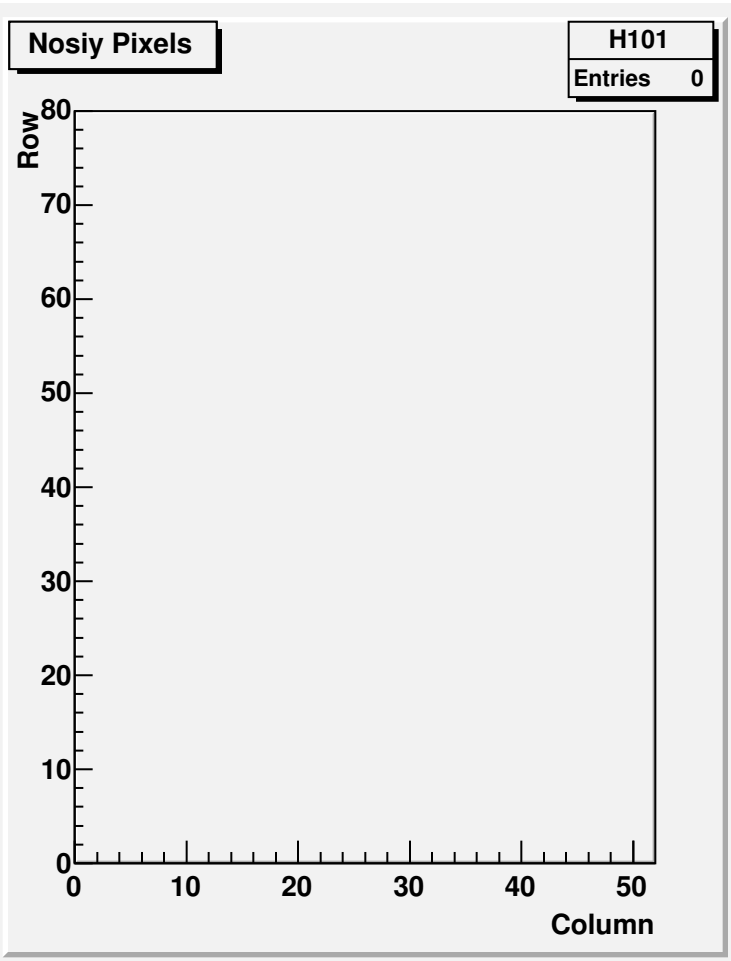
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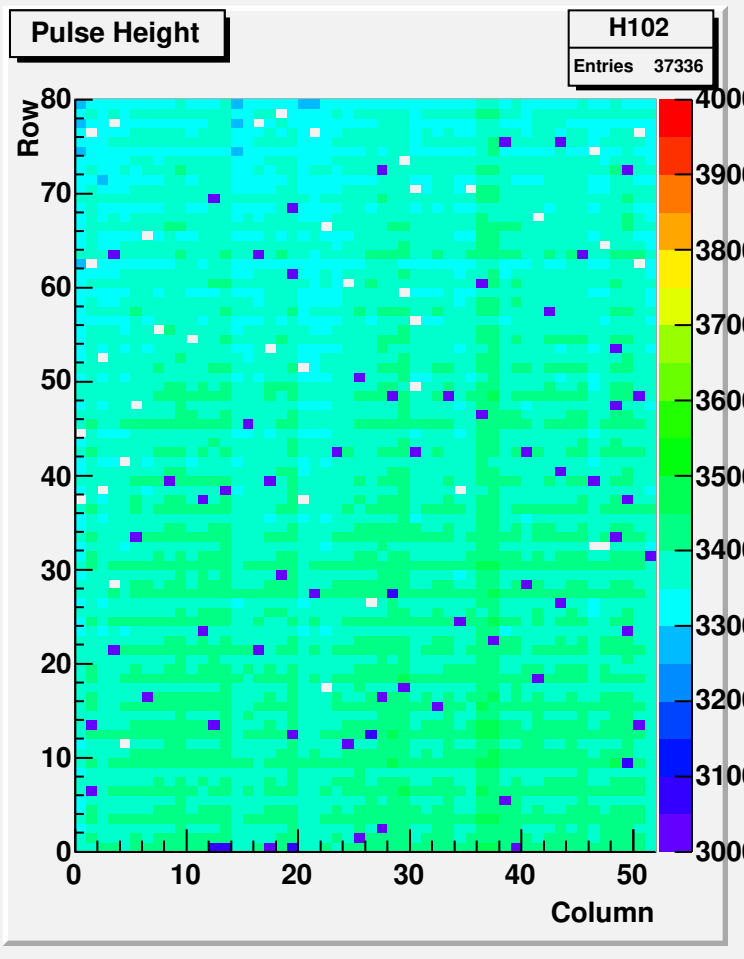
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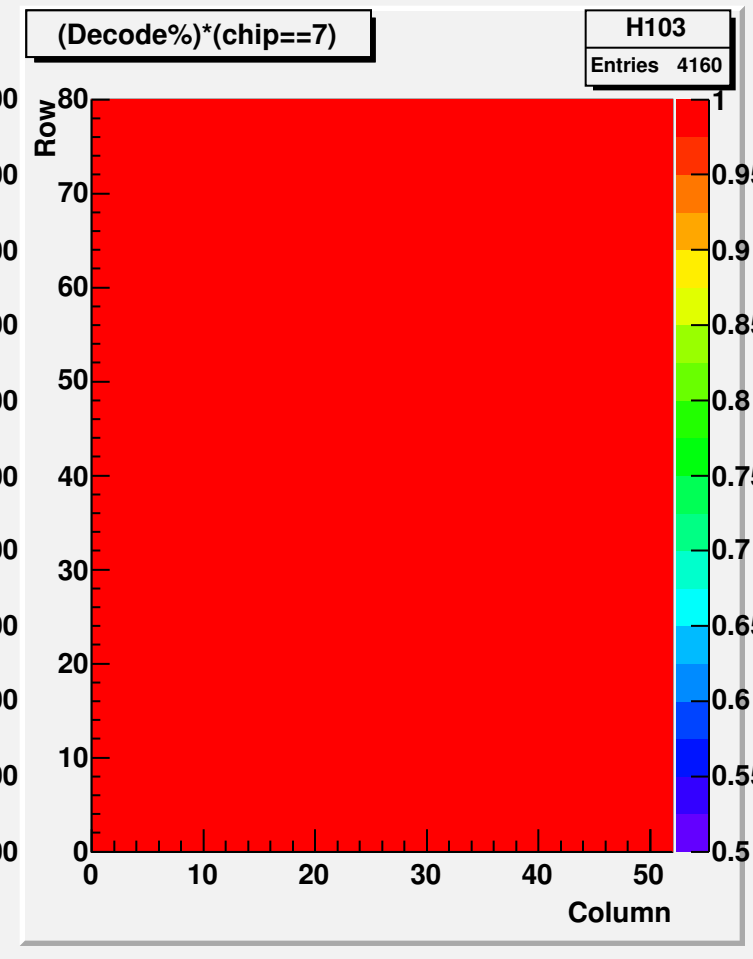
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H103
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