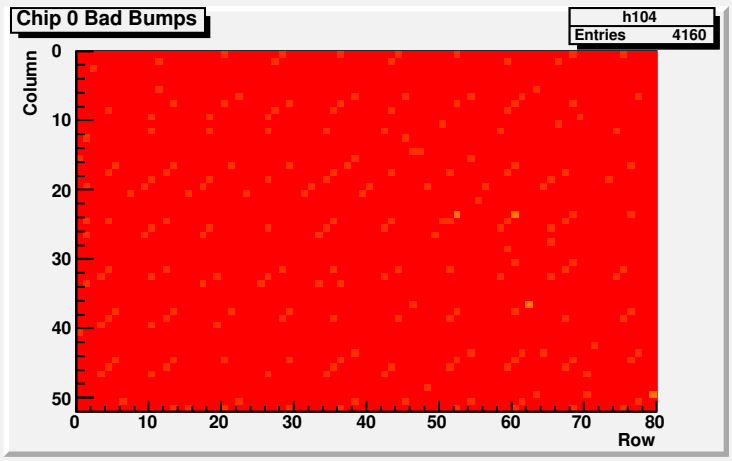
Chip 0 Bad Bumps
h104
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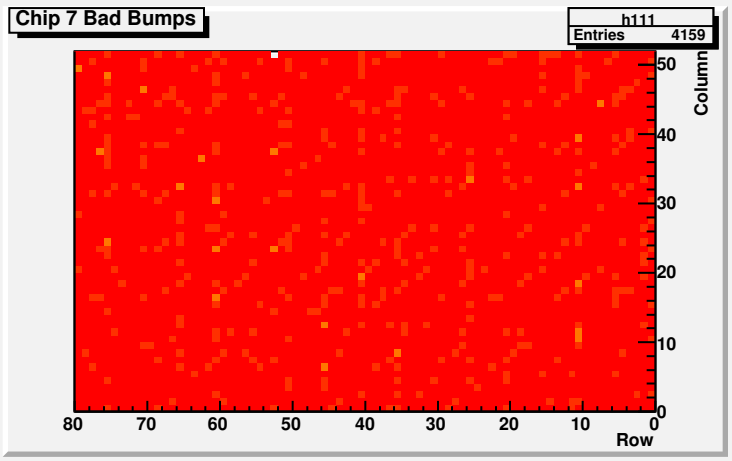
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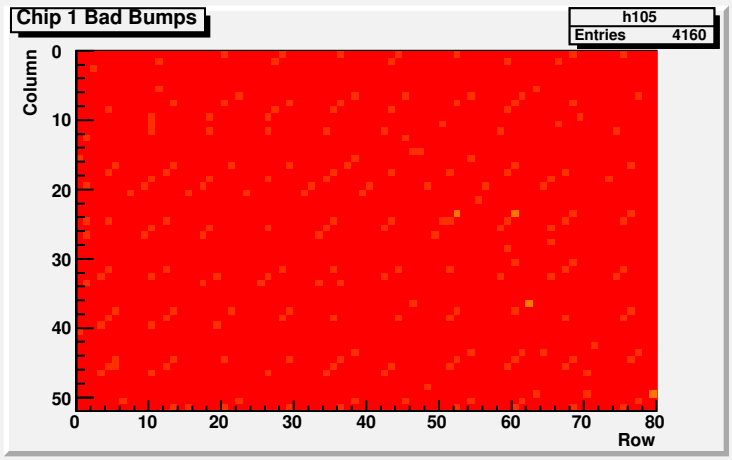
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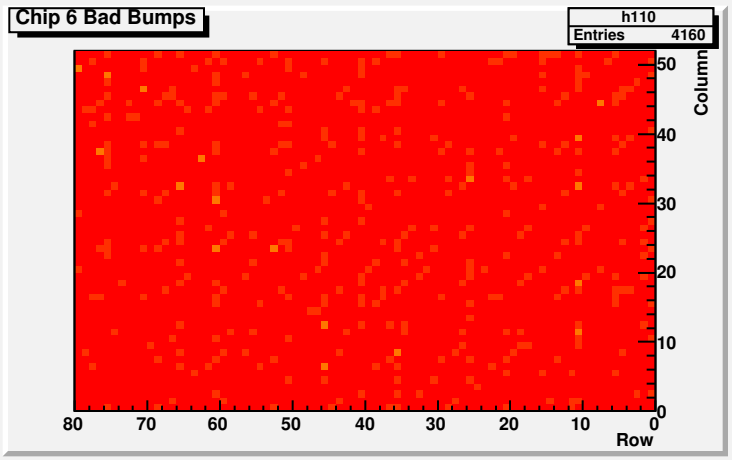
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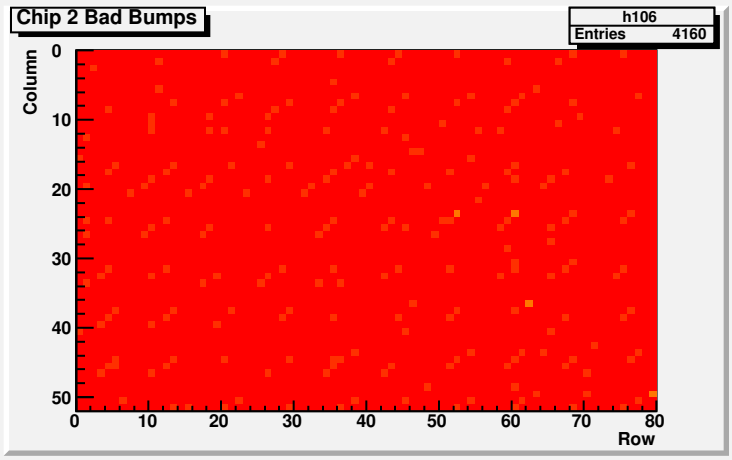
Chip 2 Bad Bumps
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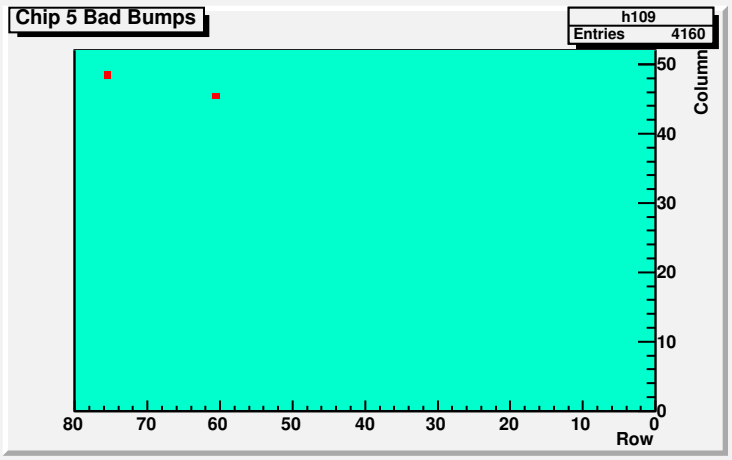
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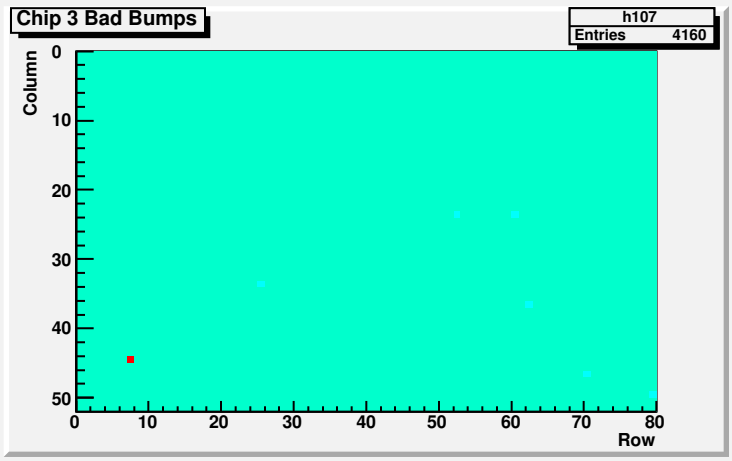
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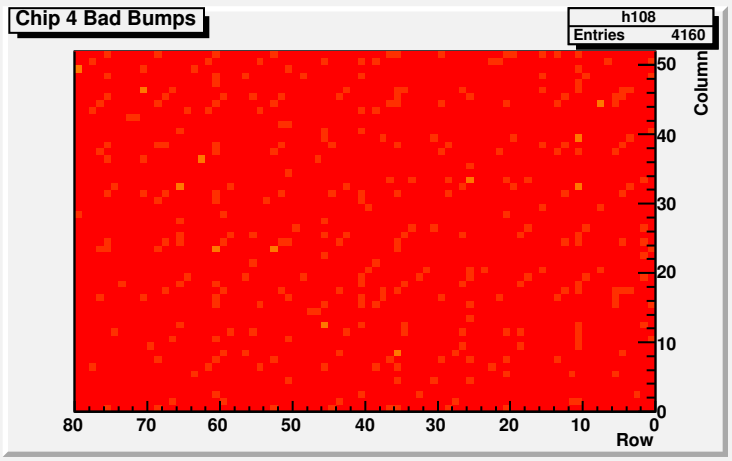
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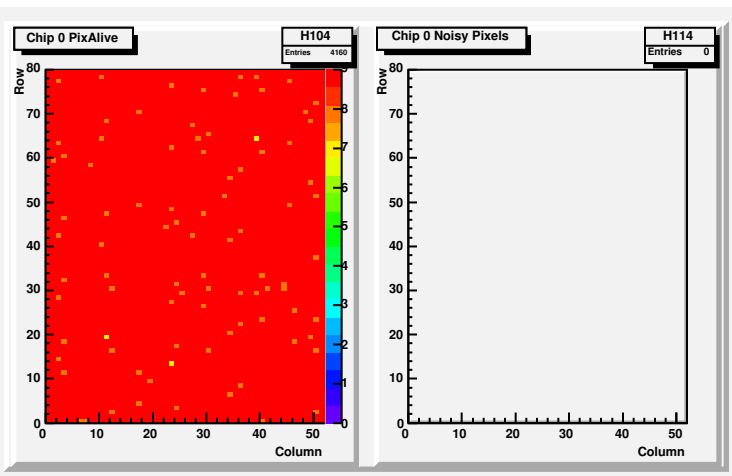
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Entries 4160
Row
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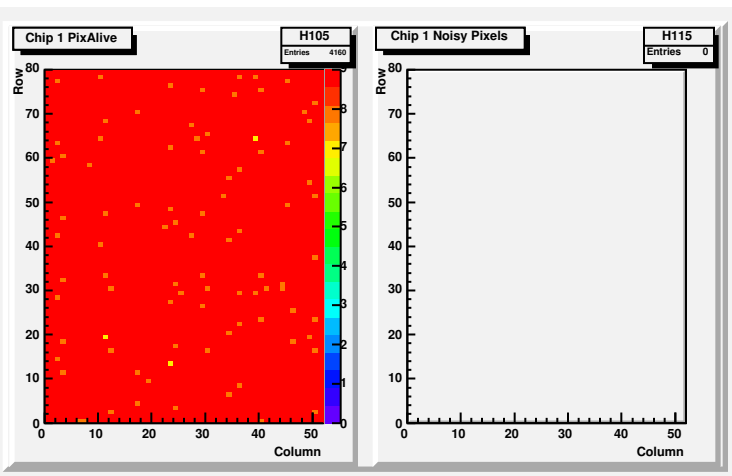
Chip 0 Noisy Pixels
H114
Entries 0
Row
Column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H105
Entries 4160
Chip 1 Noisy Pixels
H115
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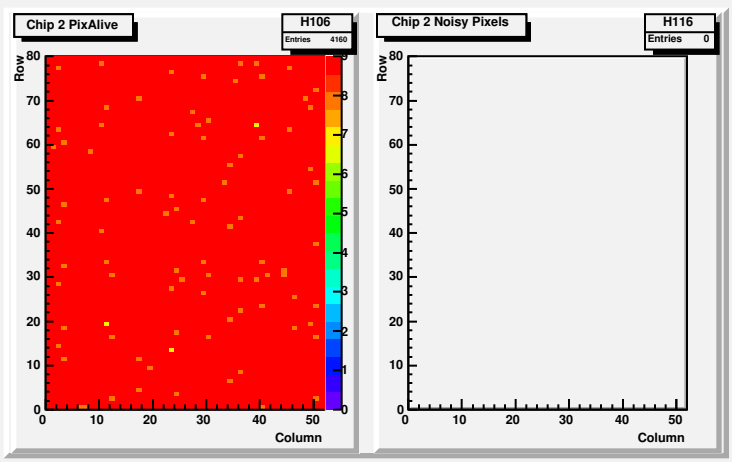
Column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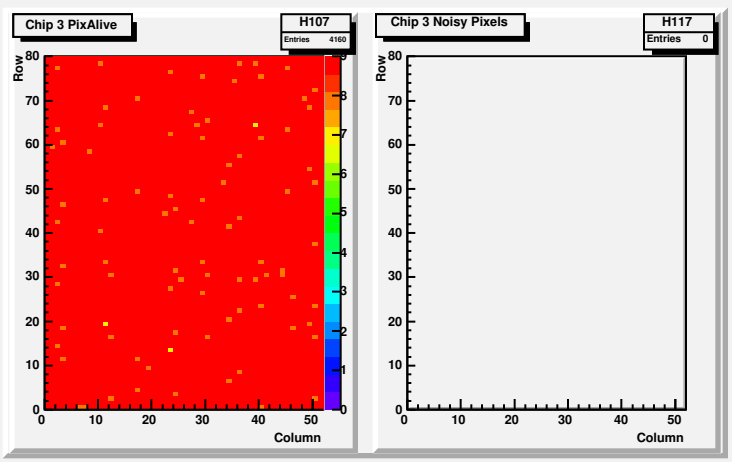
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Entries 4160
Chip 3 Noisy Pixels
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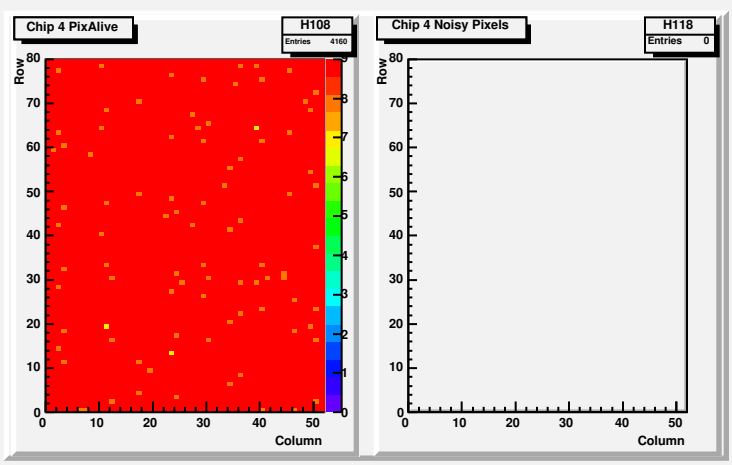
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Chip 4 Noisy Pixels
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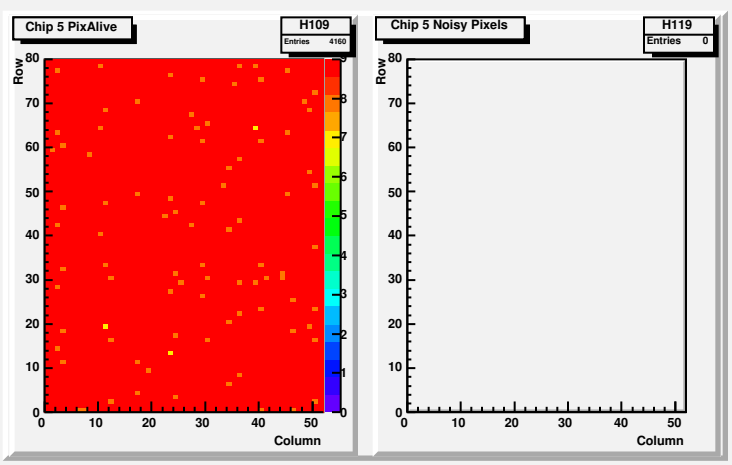
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Entries 4160
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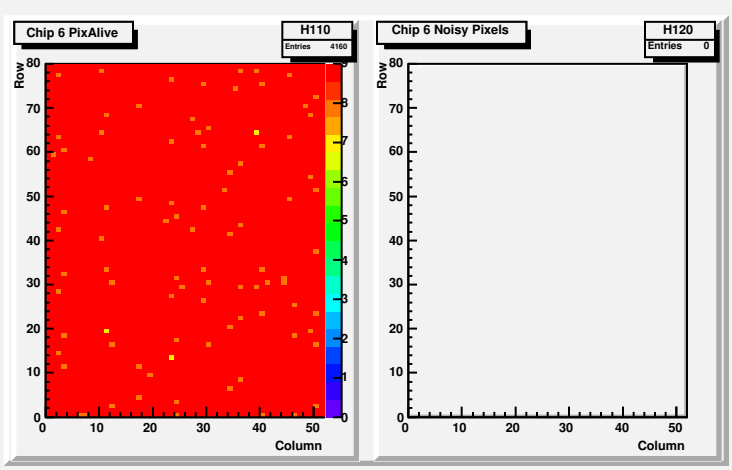
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Entries 4160
Chip 6 Noisy Pixels
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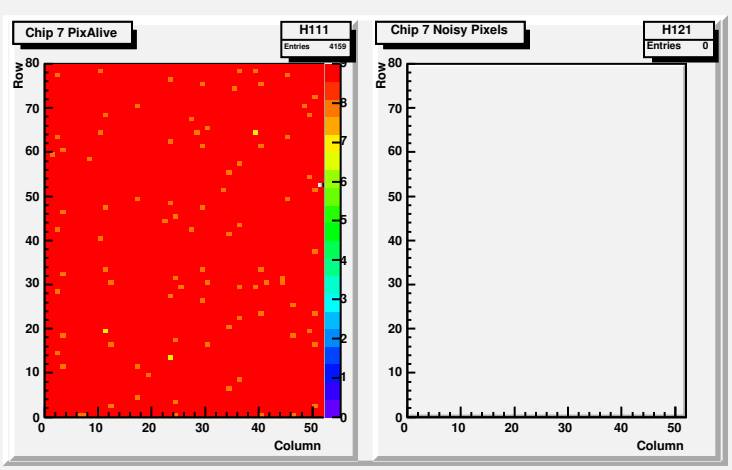
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Entries 4159
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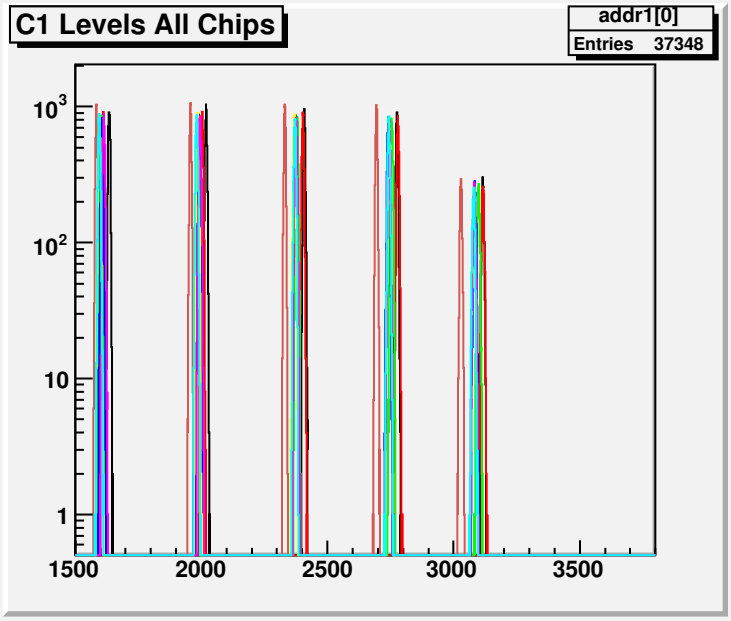
Column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Entries 37348
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10^2
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1500
2000
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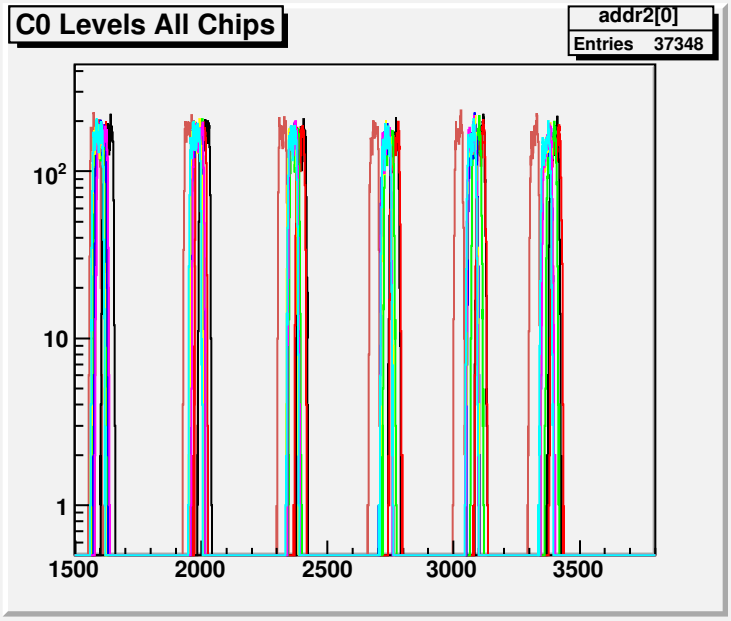
C0 Levels All Chips
addr2[0]
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2000
2500
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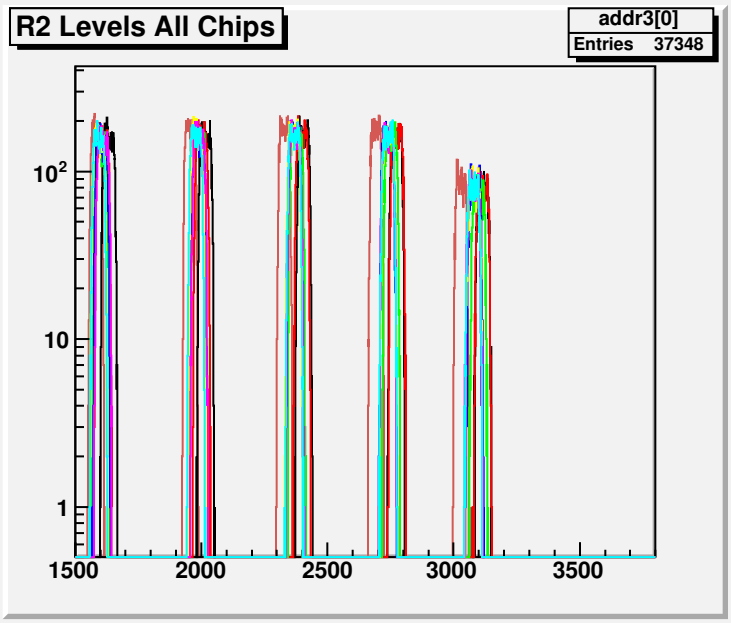
R2 Levels All Chips
addr3[0]
Entries 37348
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1
1500
2000
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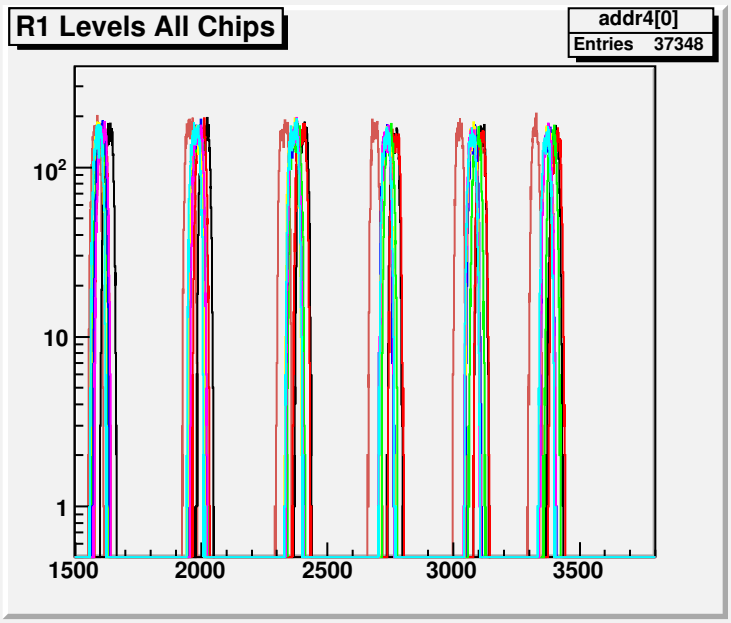
3000
3500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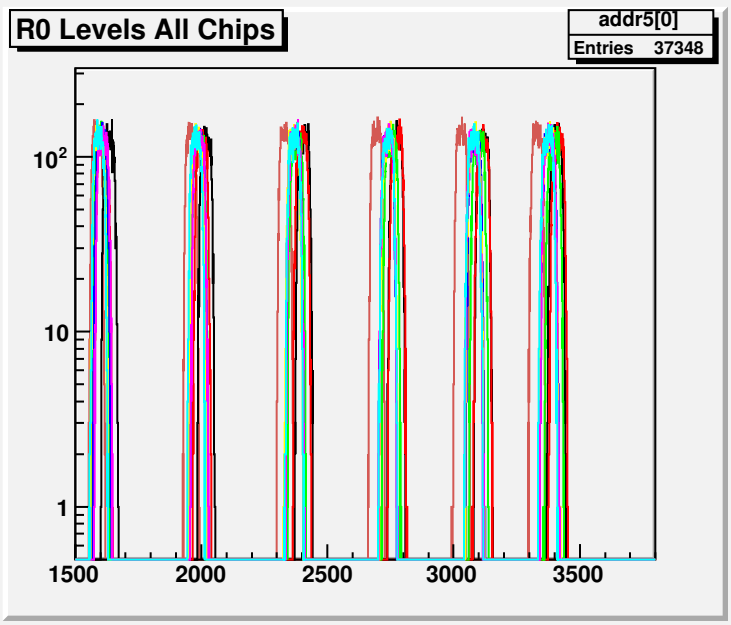
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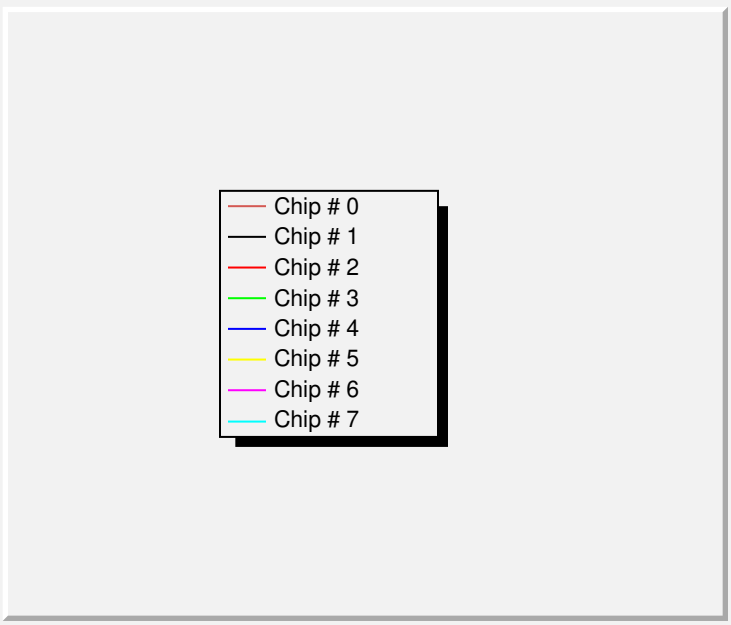
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Chip # 1
Chip # 2
Chip # 3
Chip # 4
Chip # 5
Chip # 6
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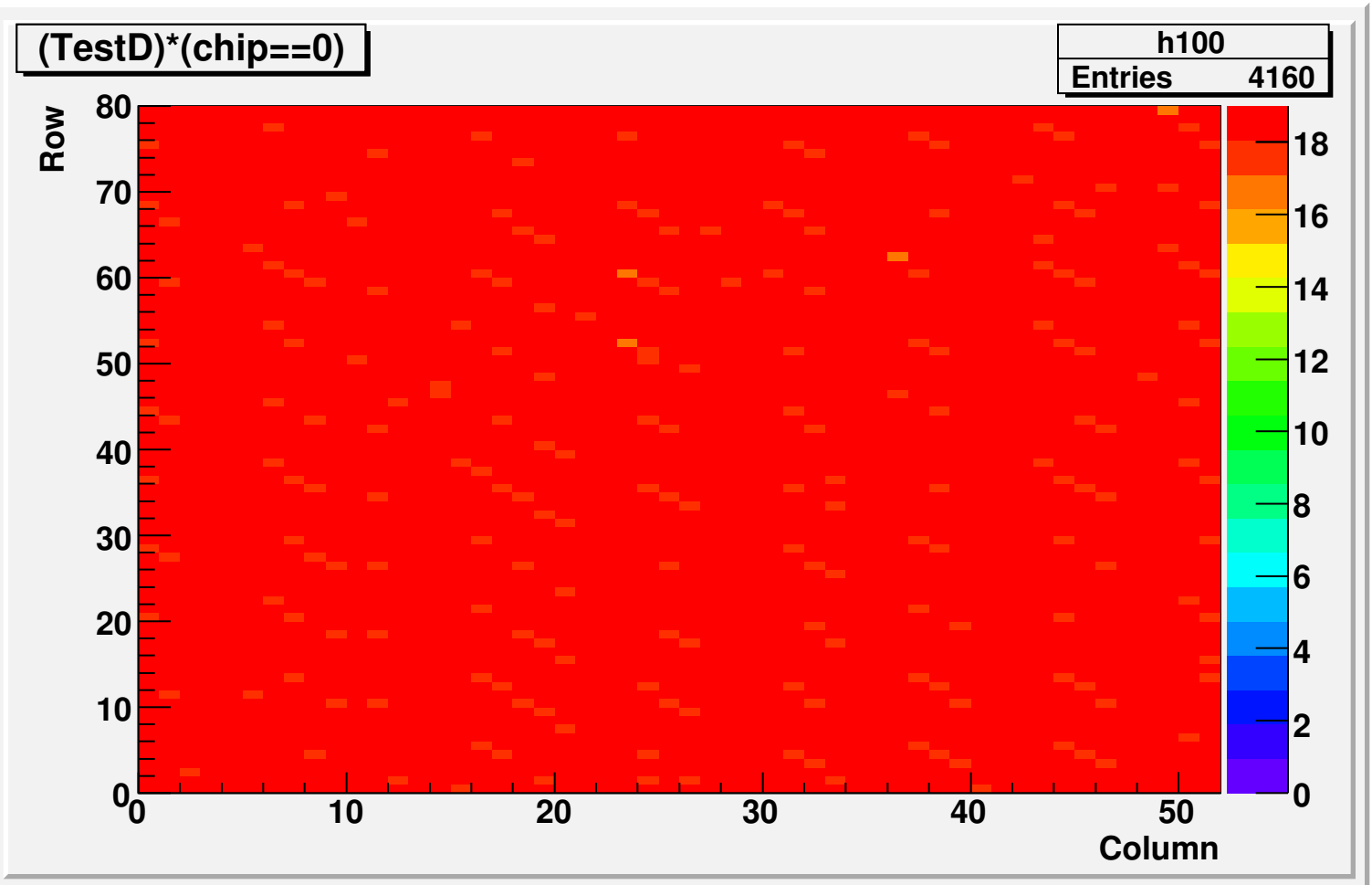
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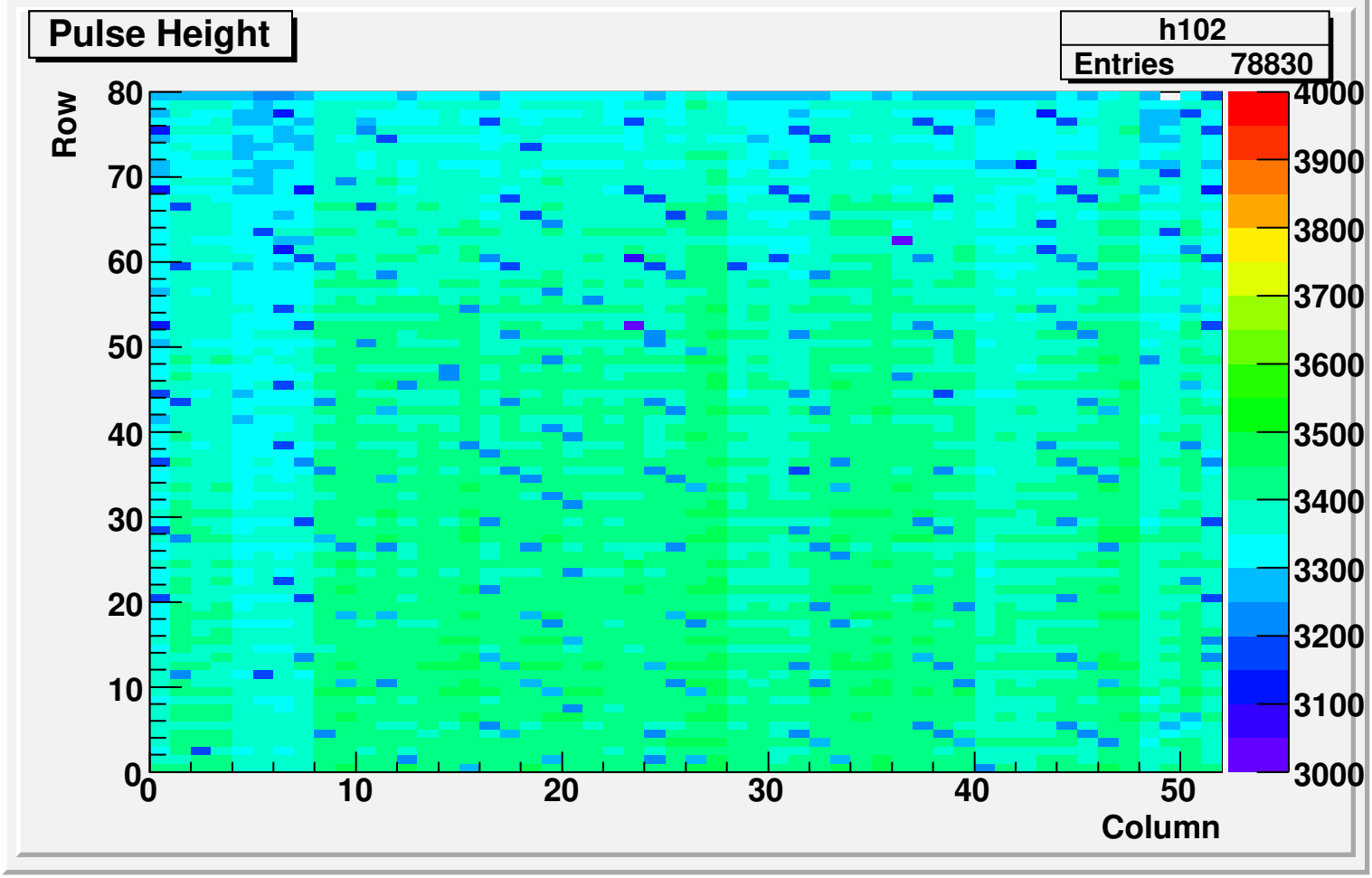
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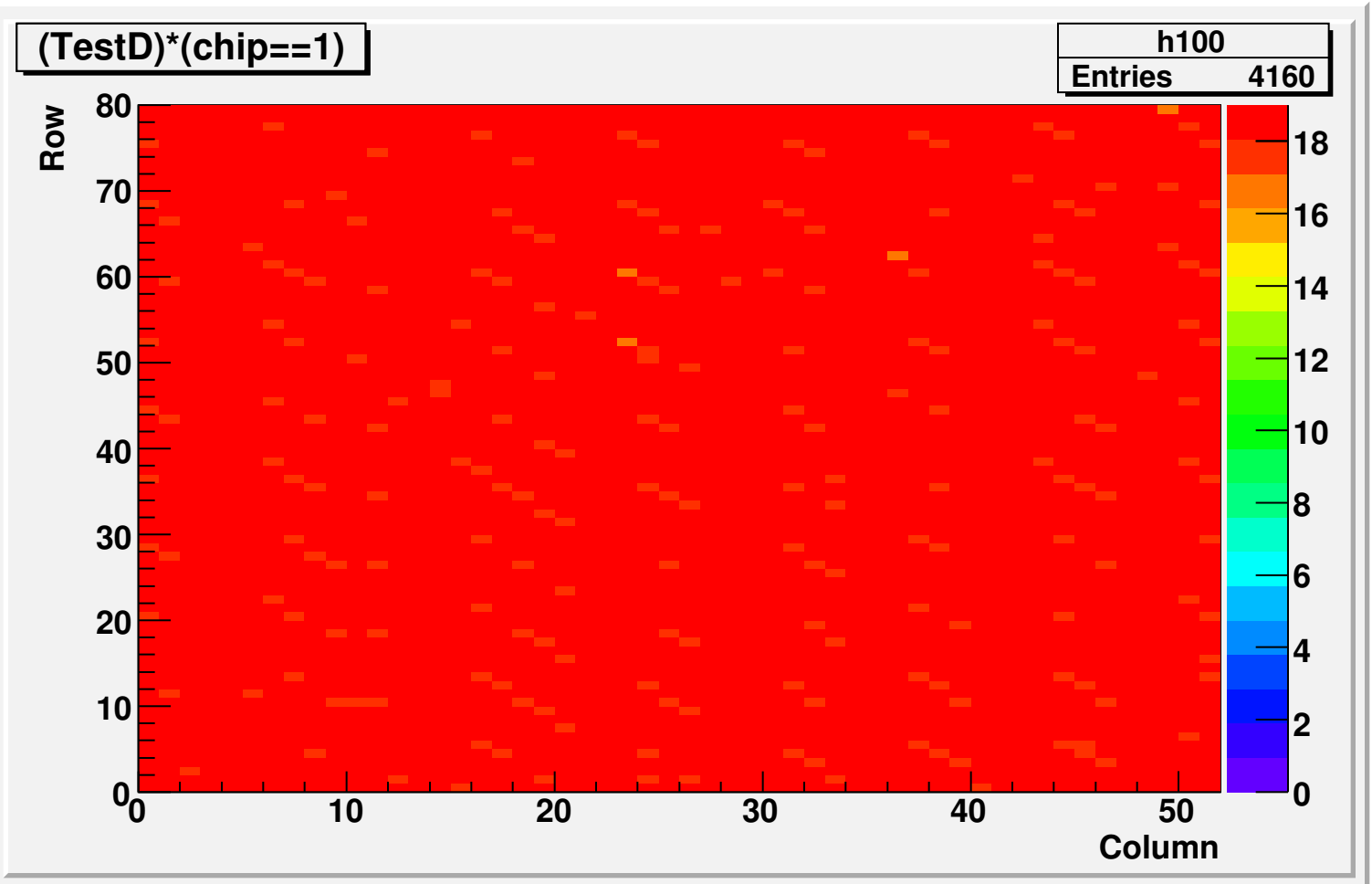
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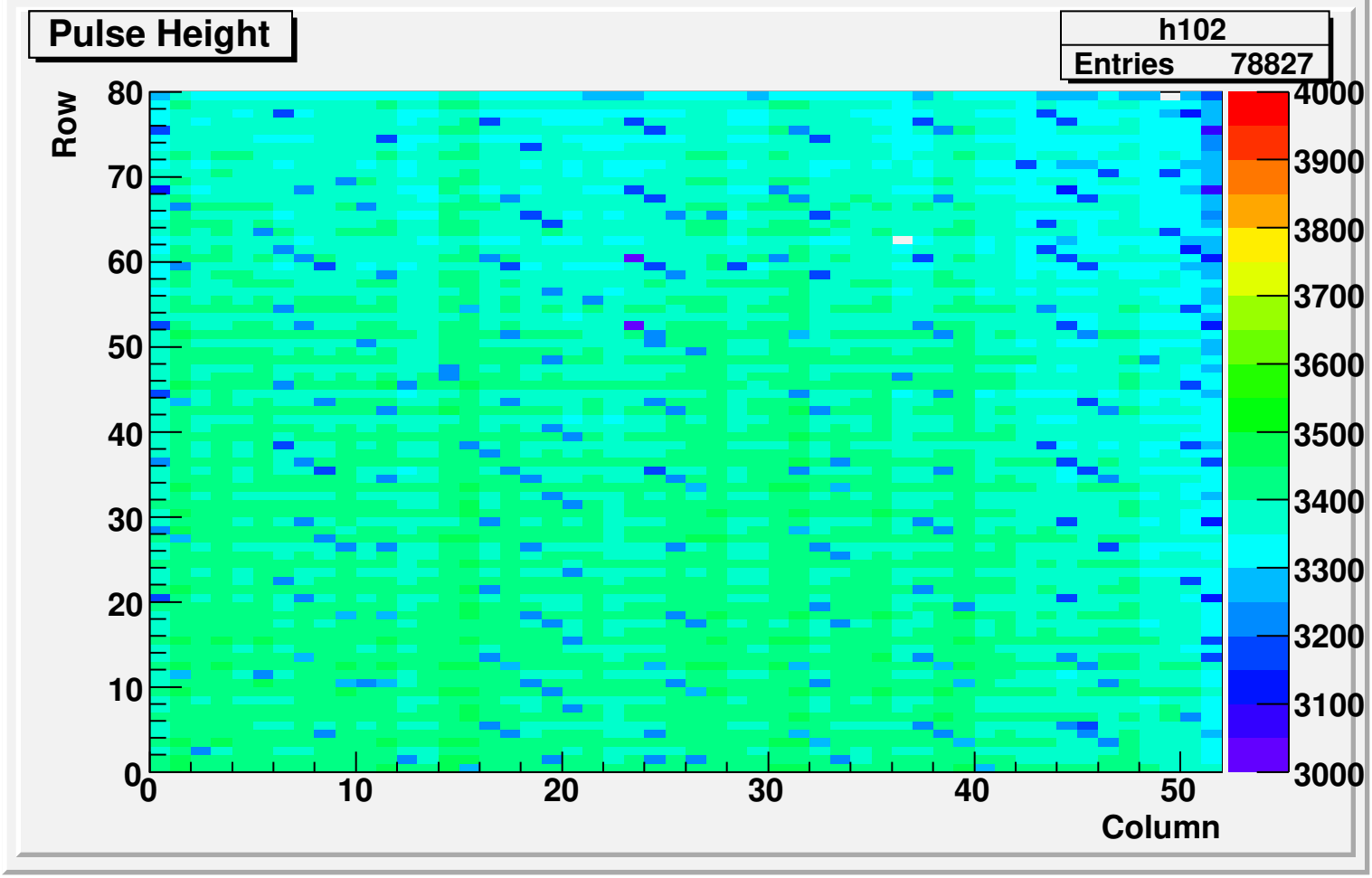
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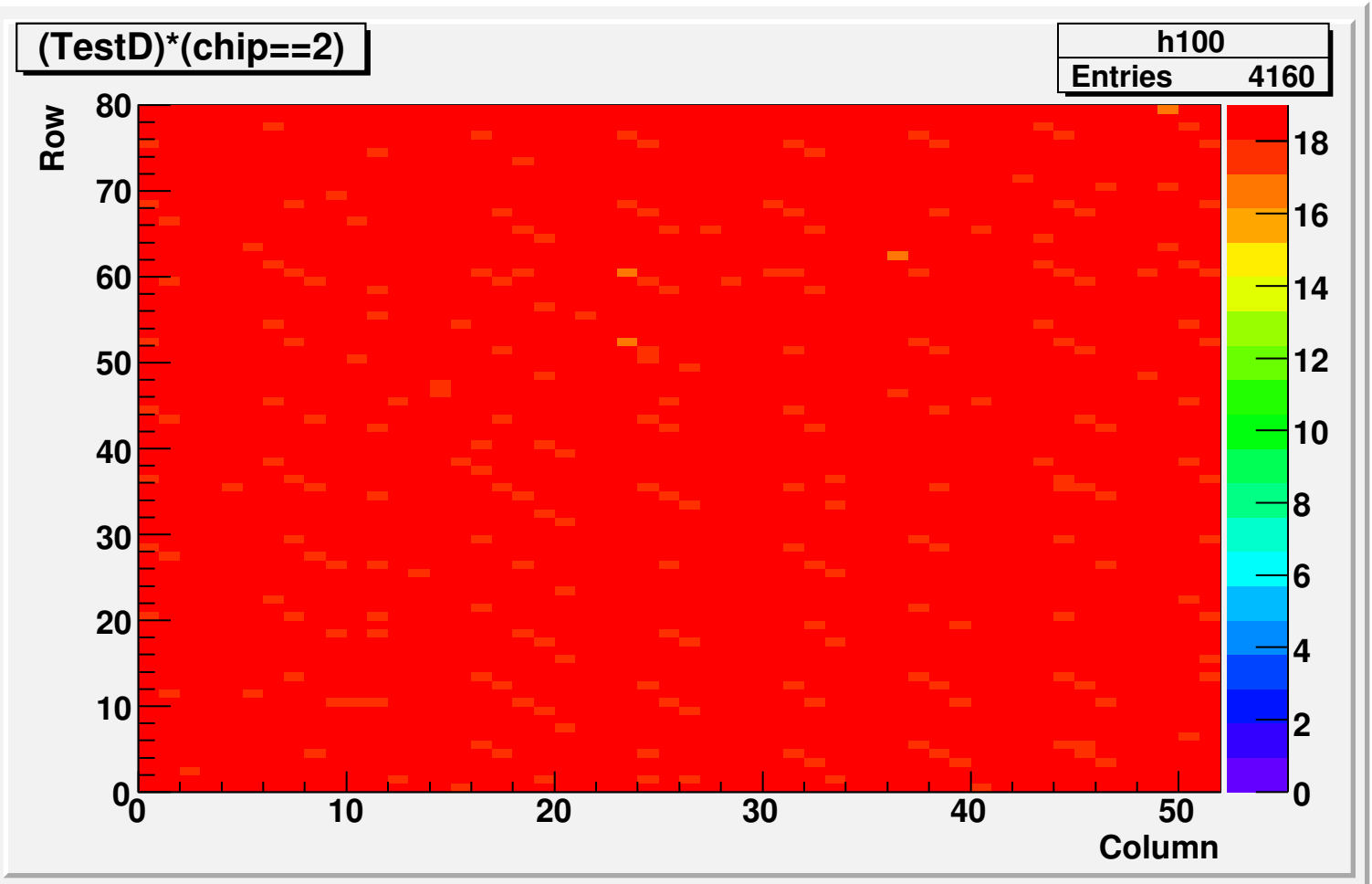
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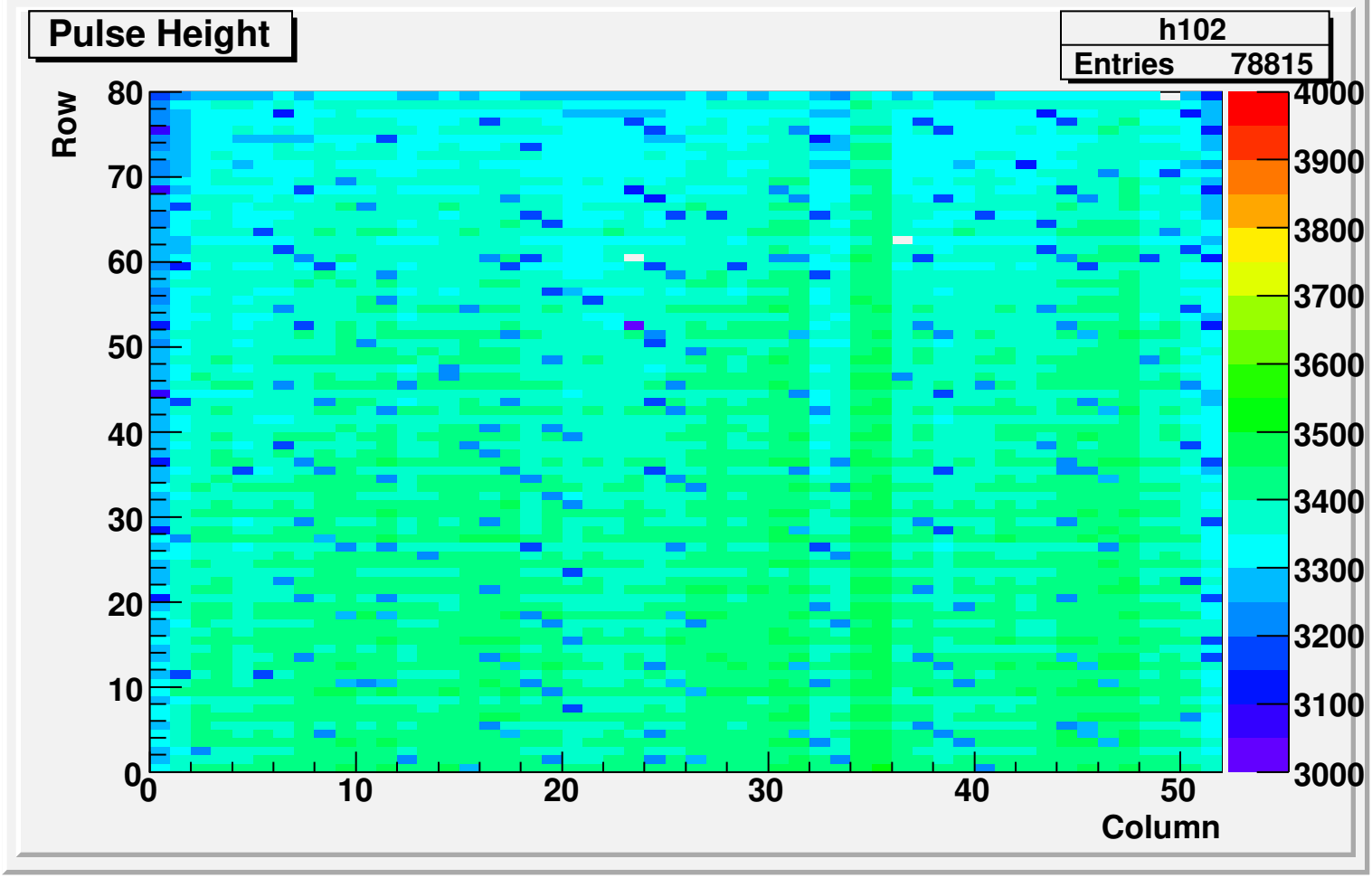
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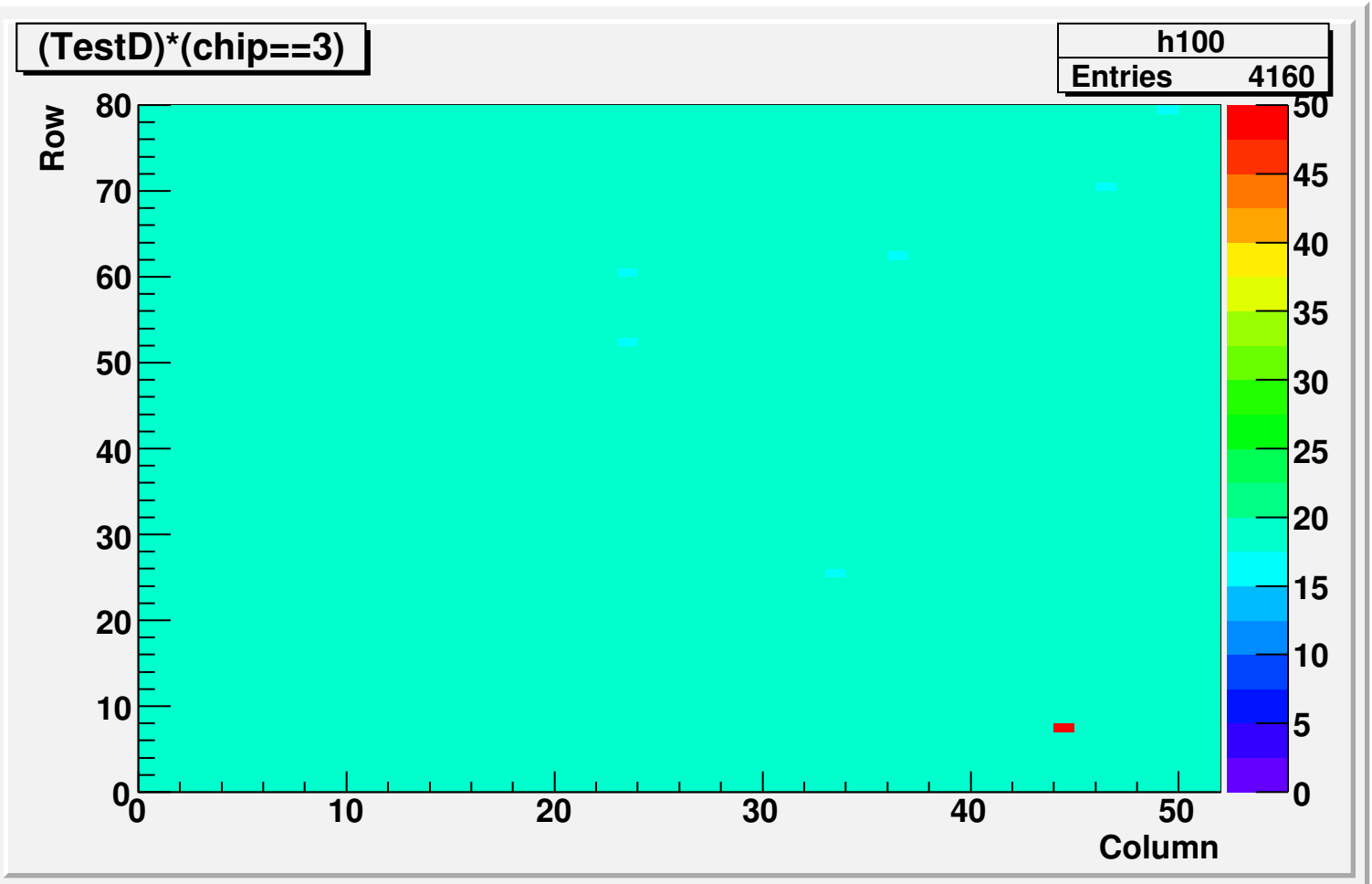
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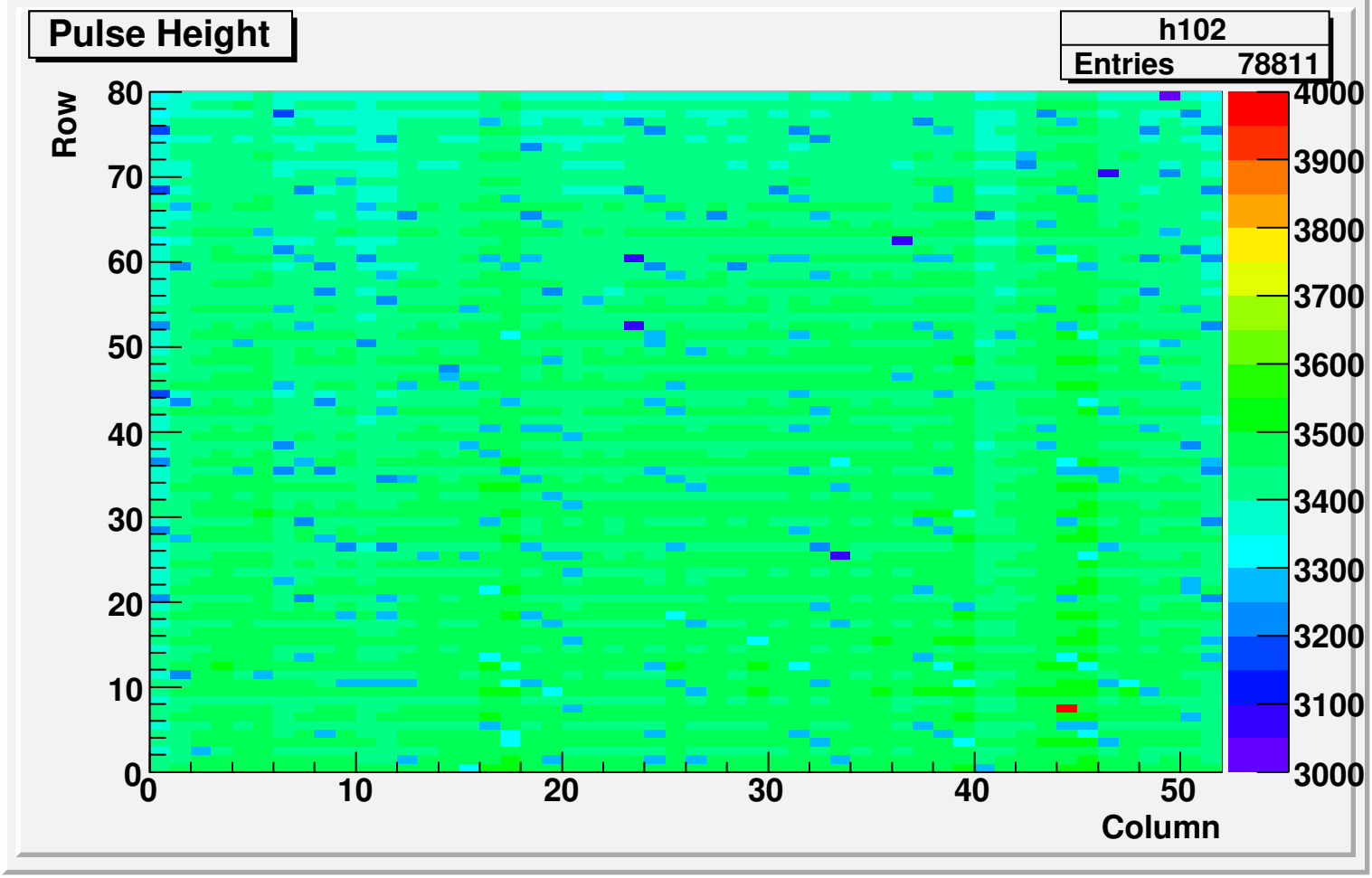
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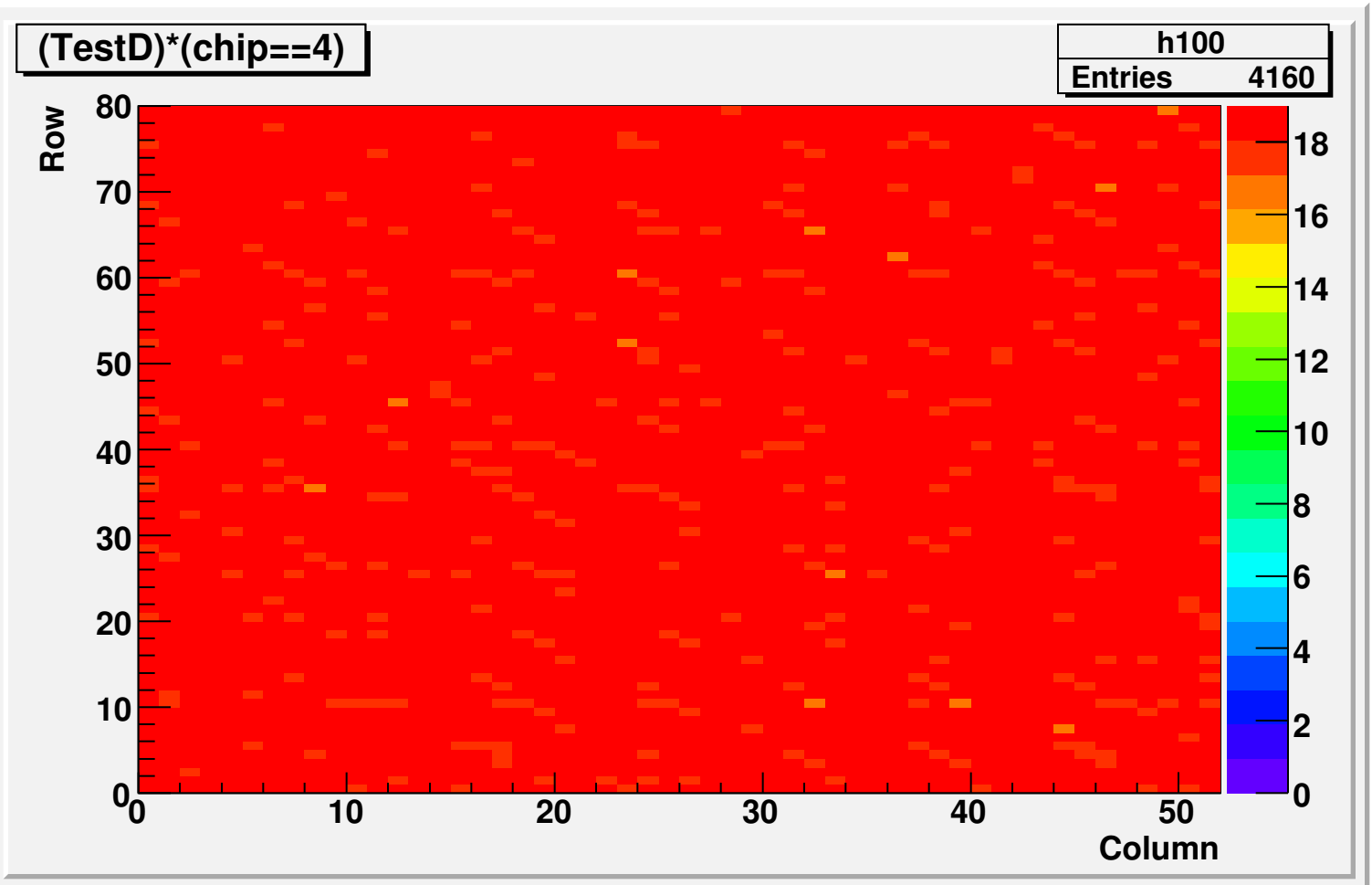
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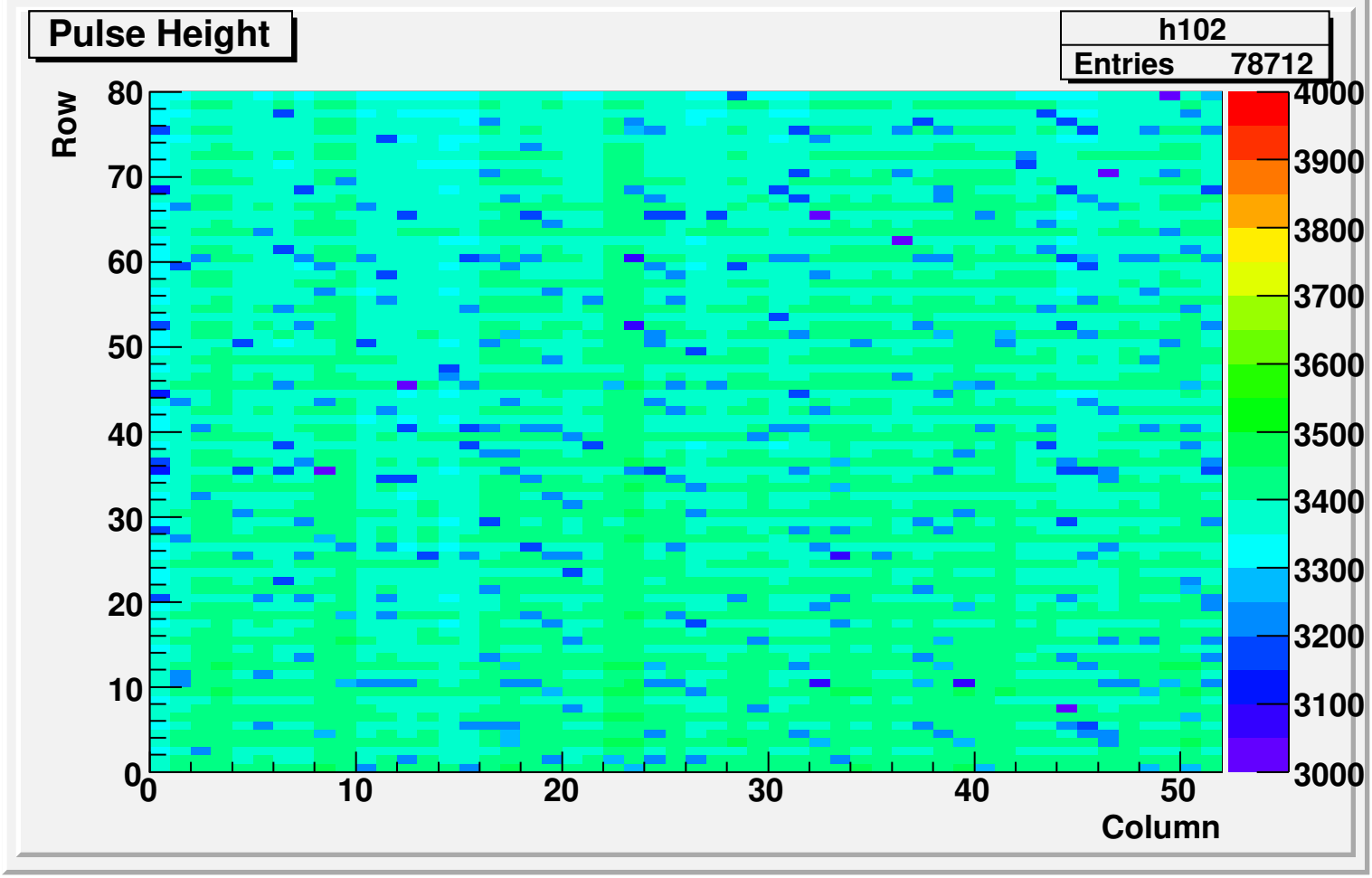
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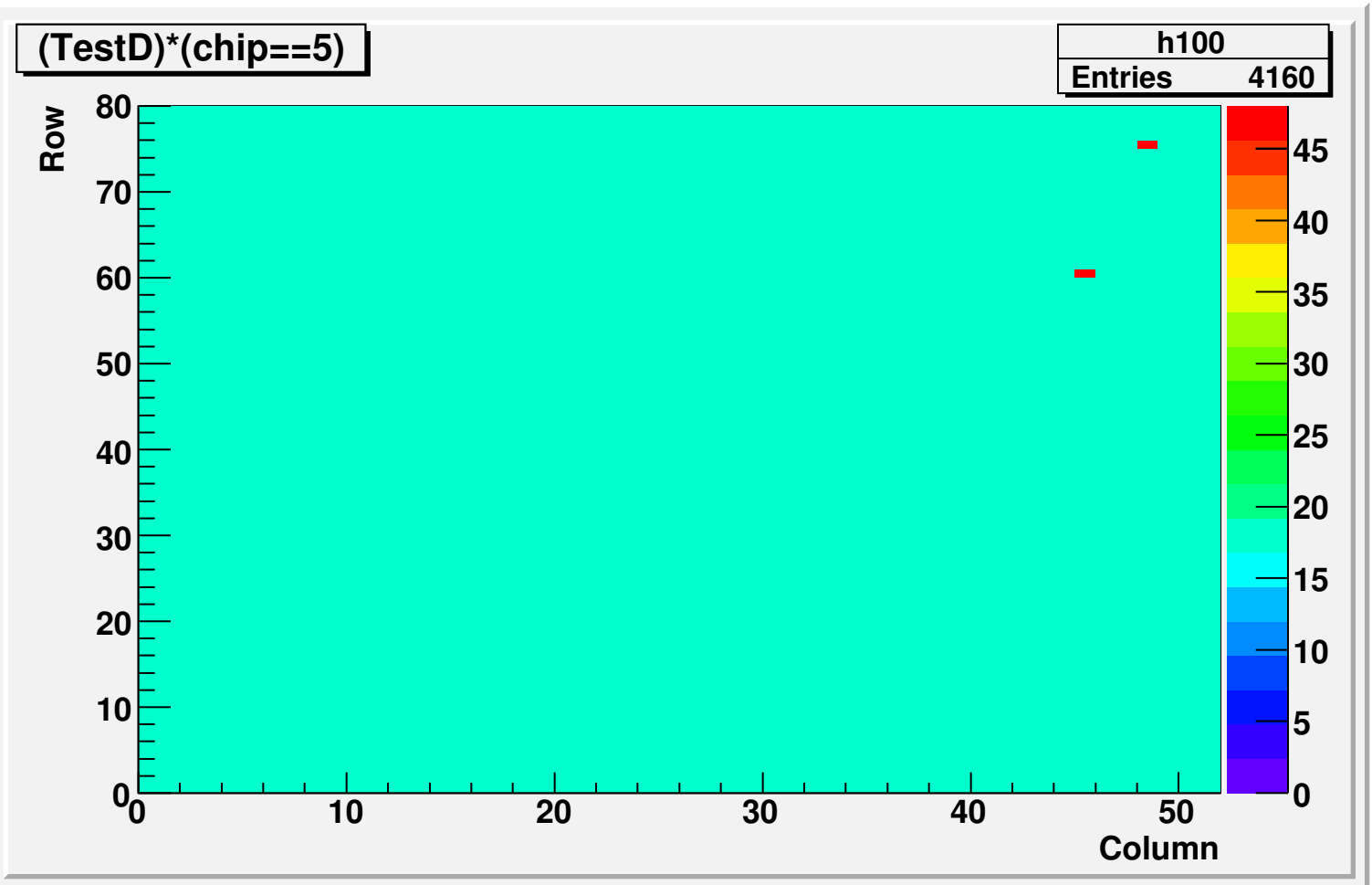
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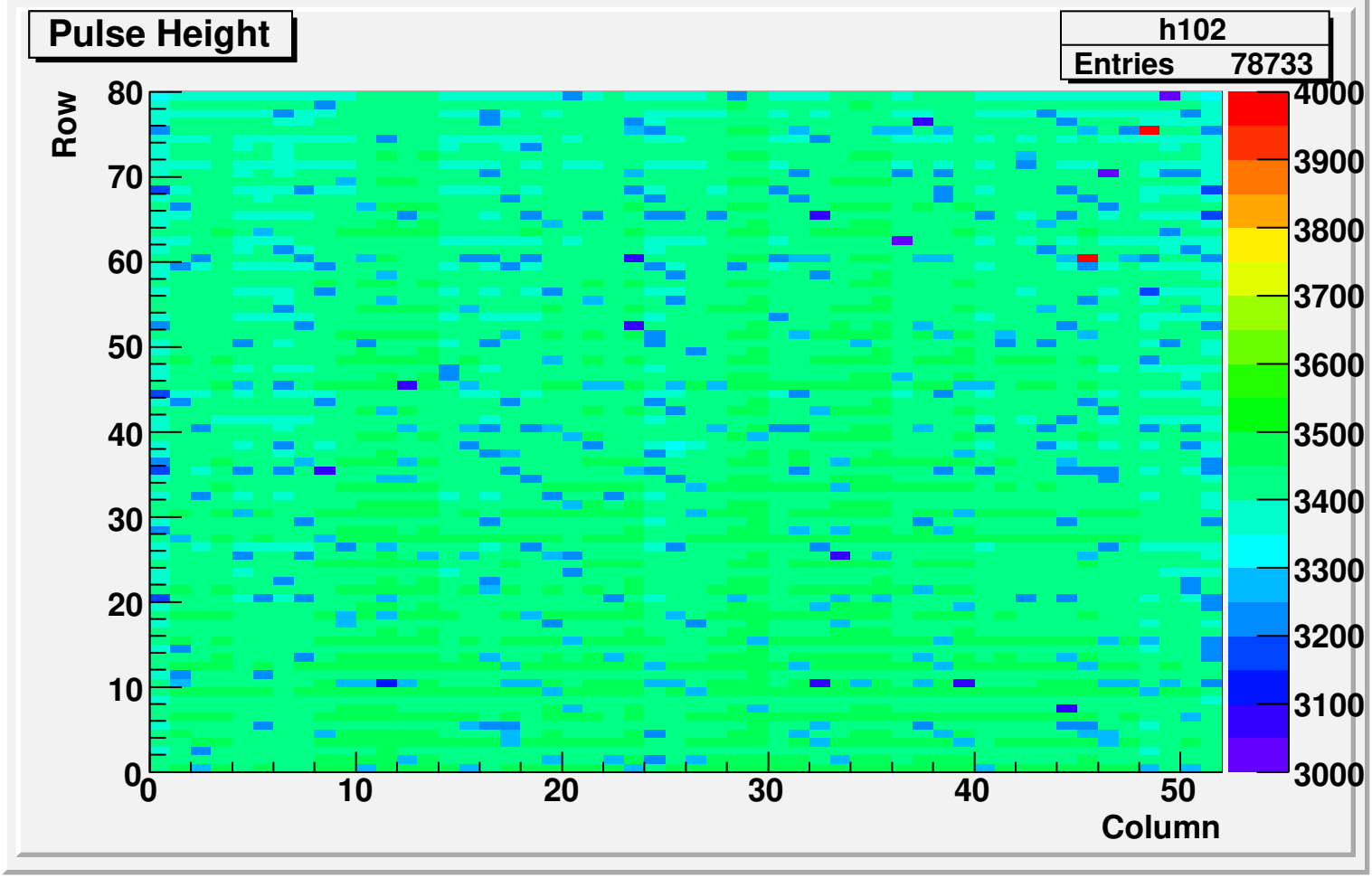
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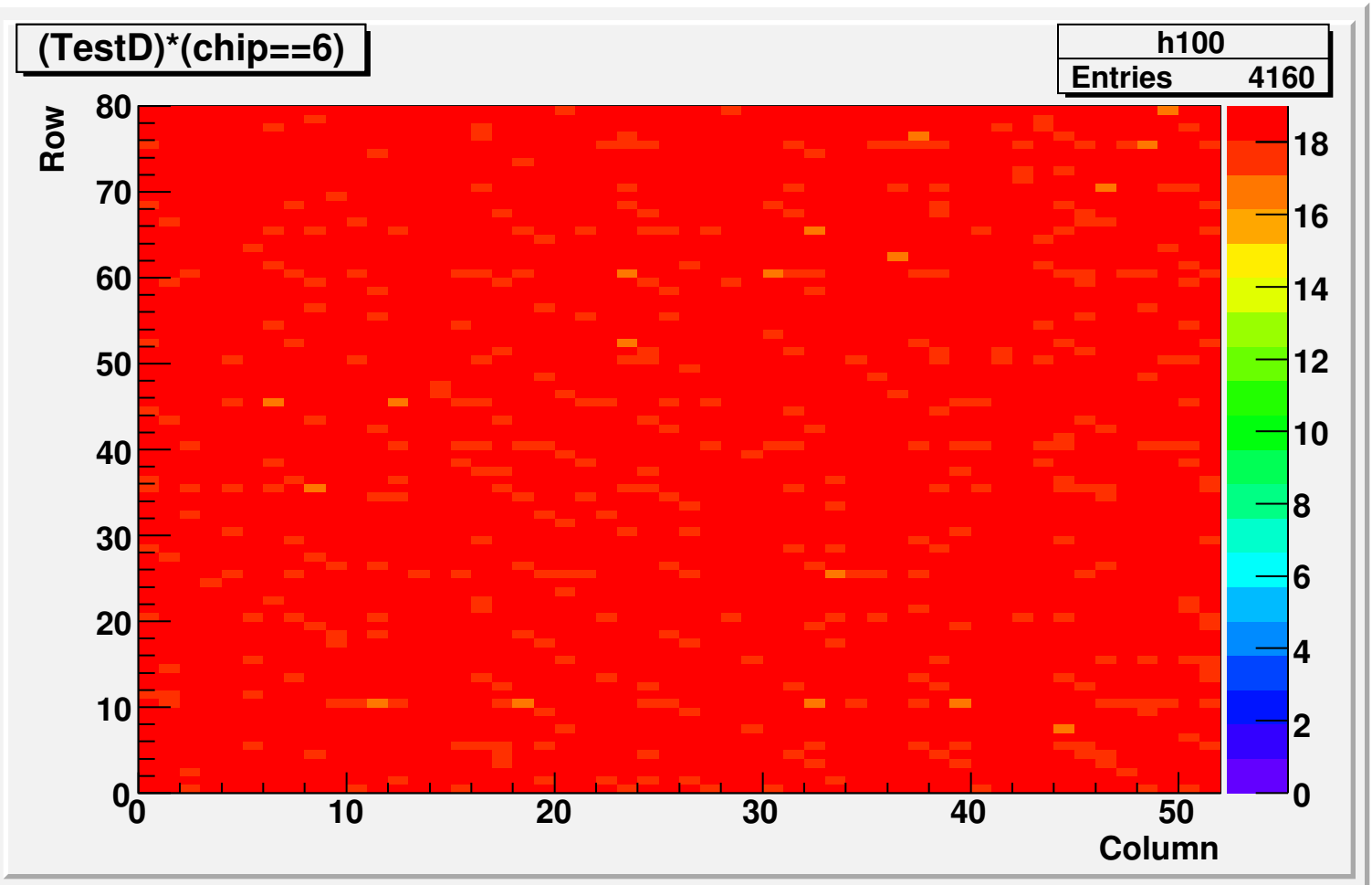
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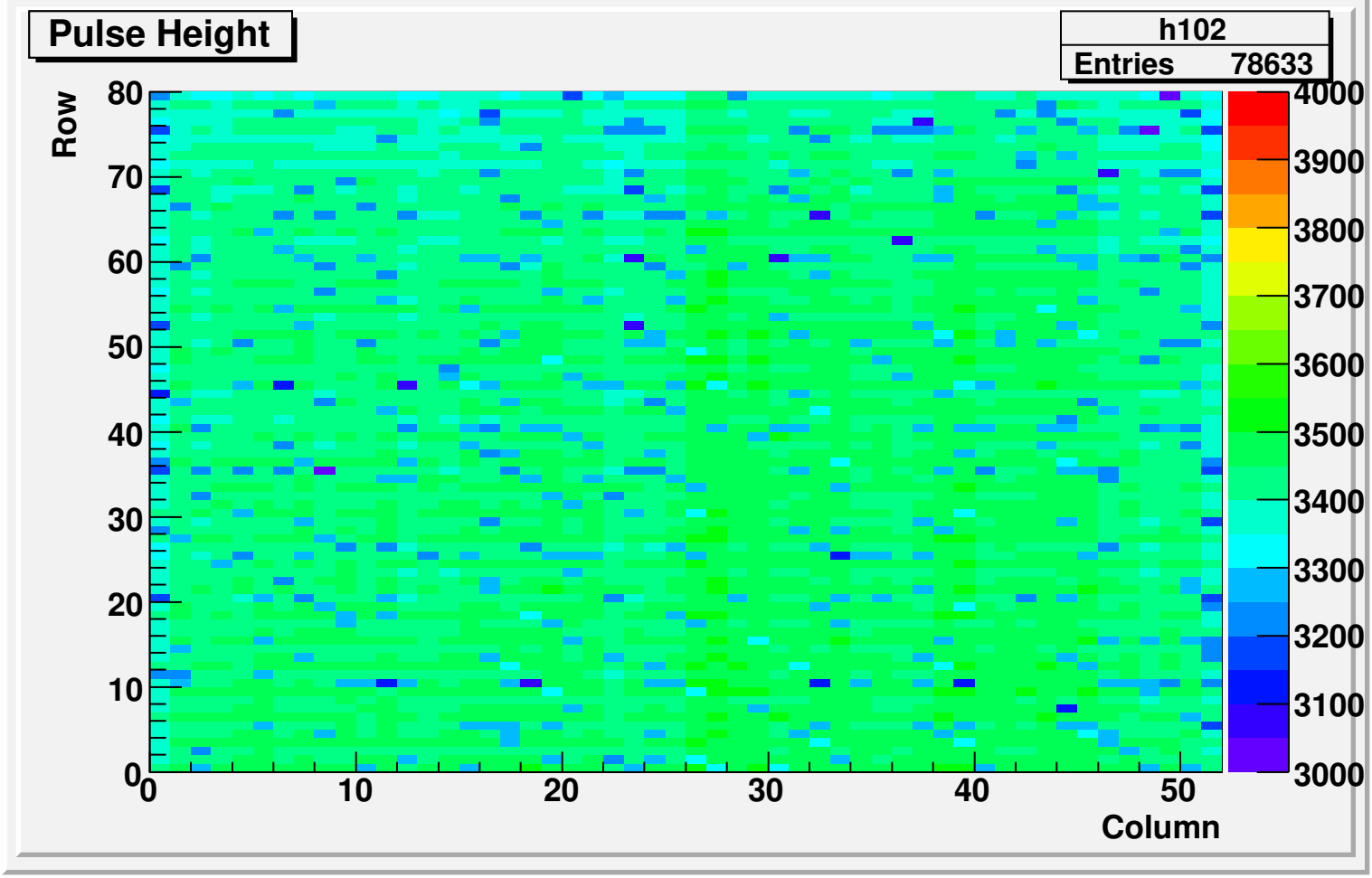
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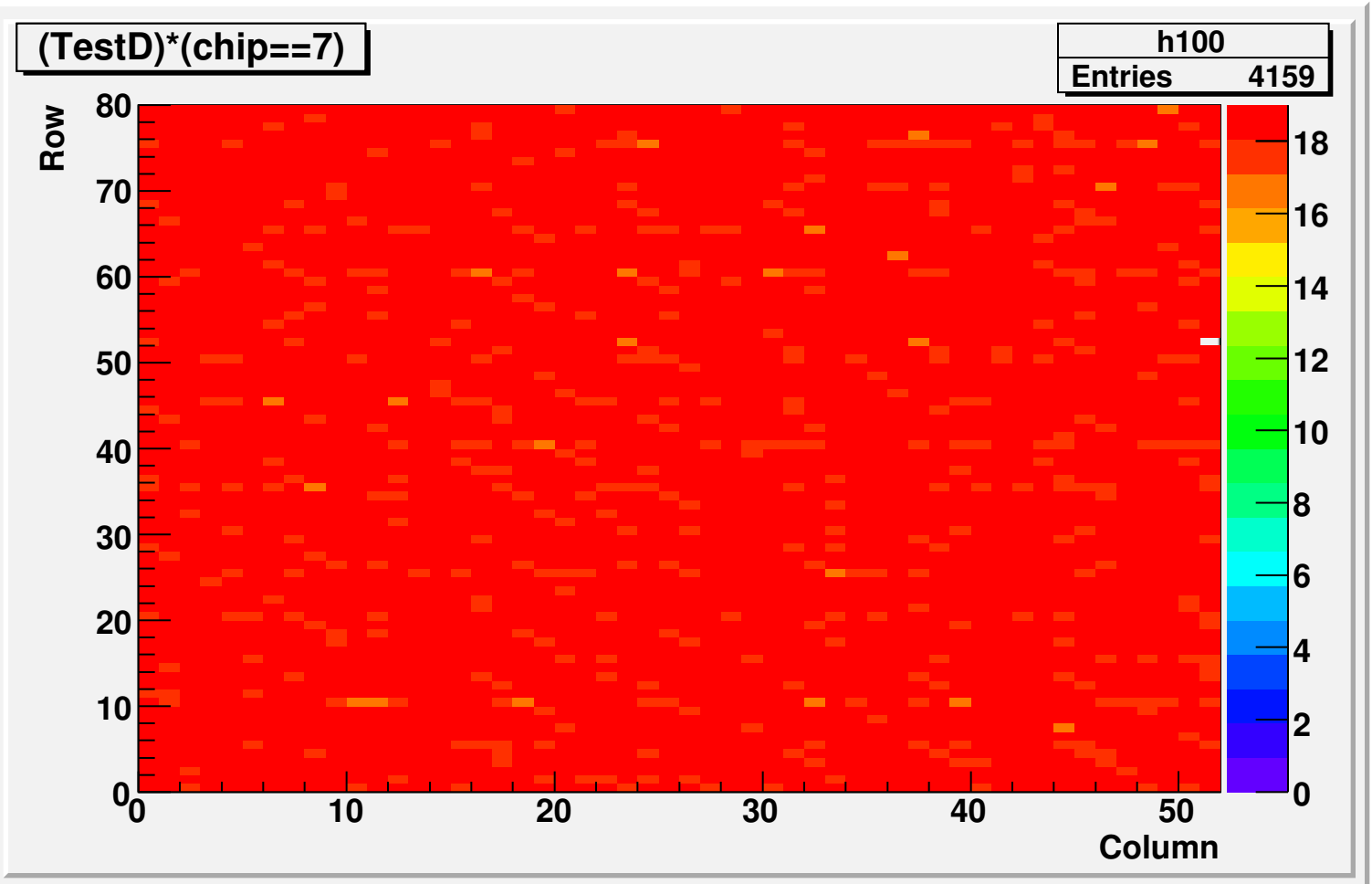
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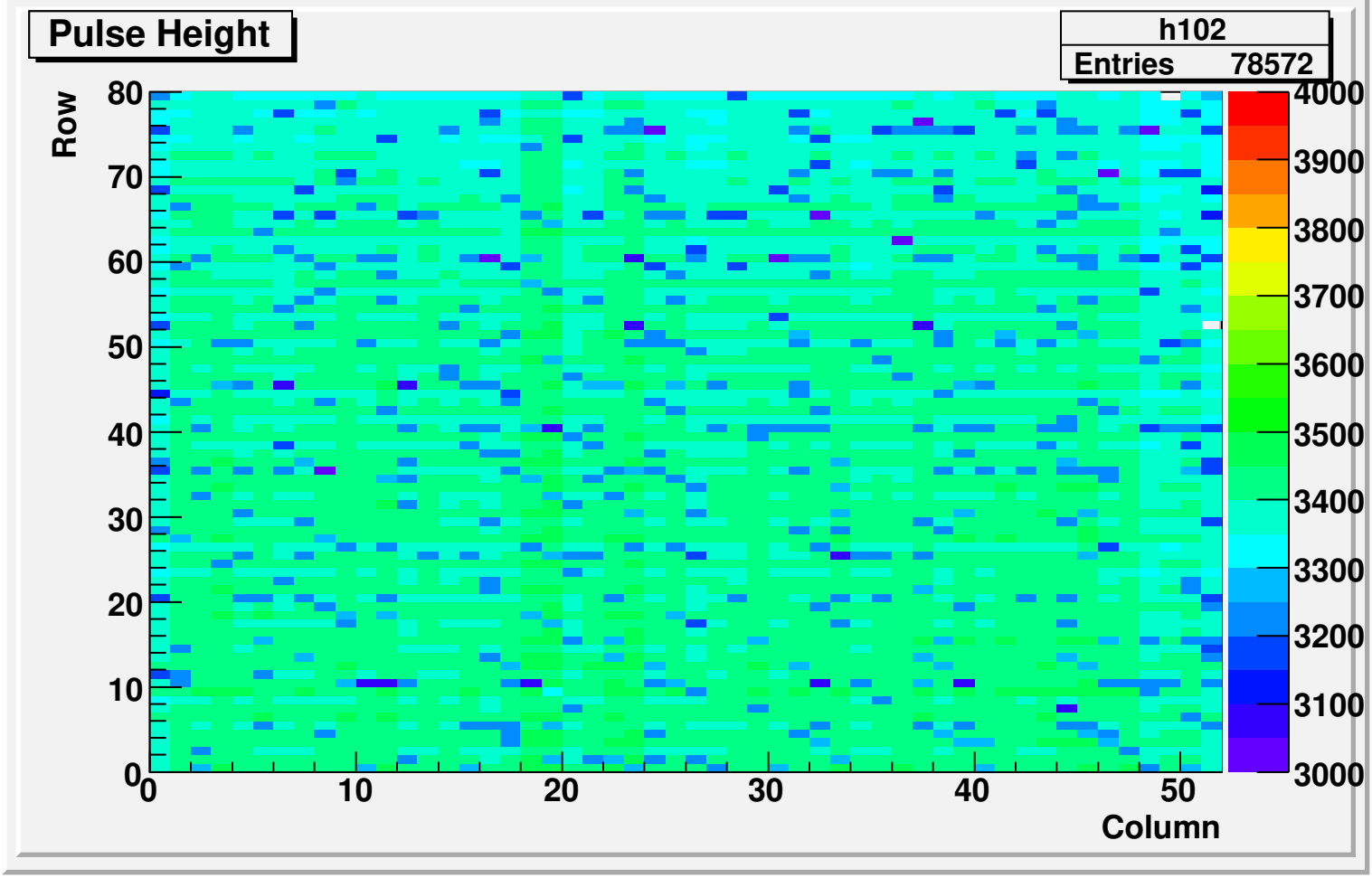
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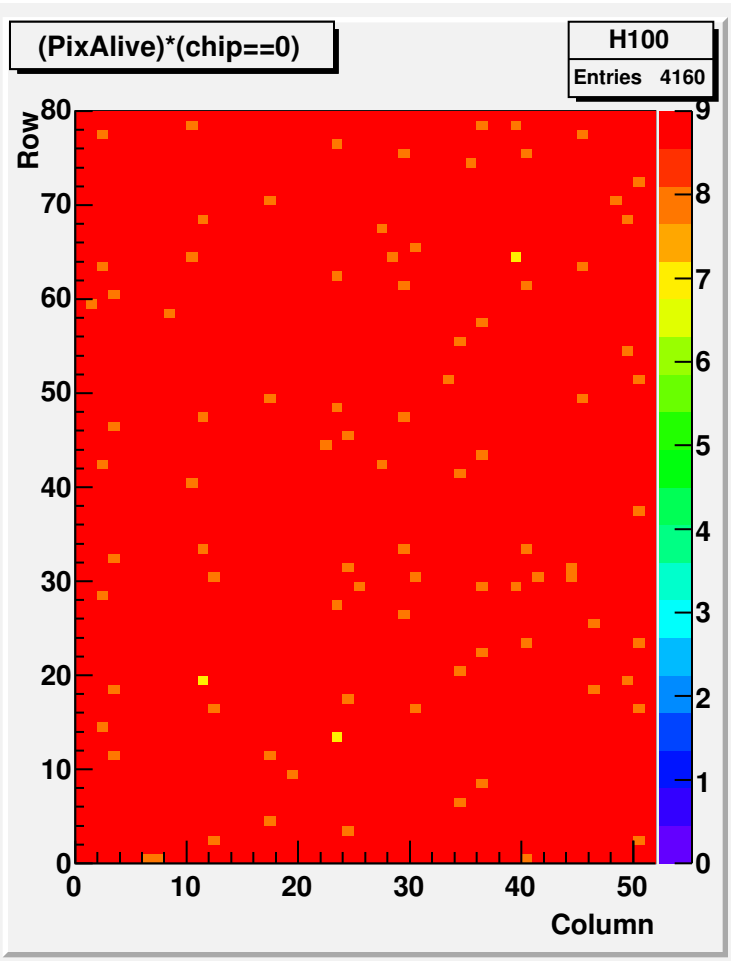
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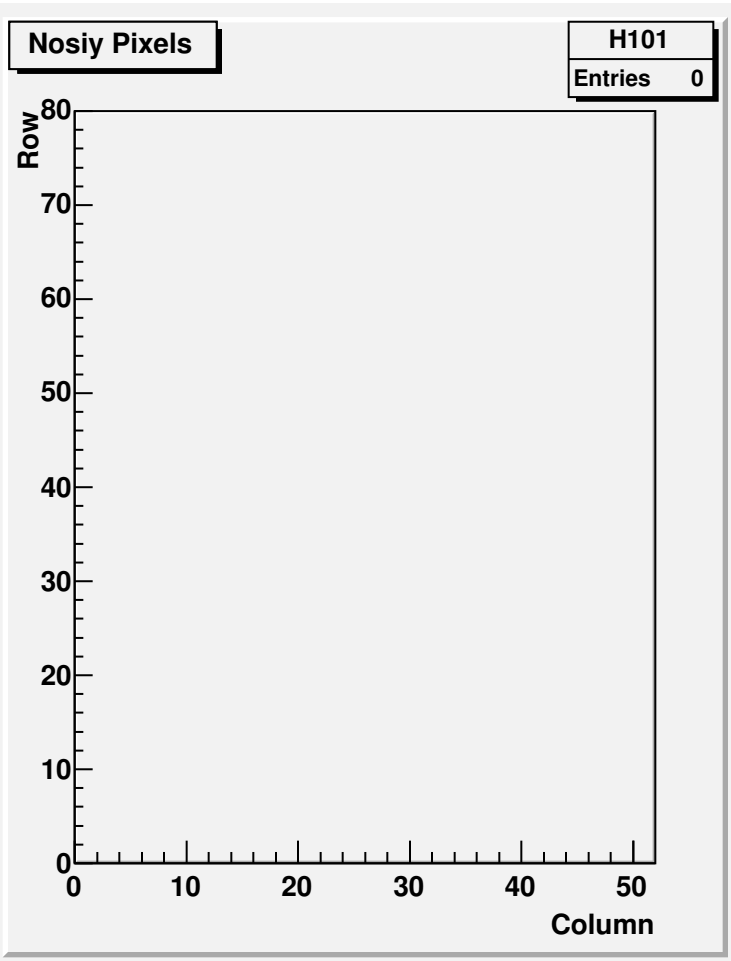
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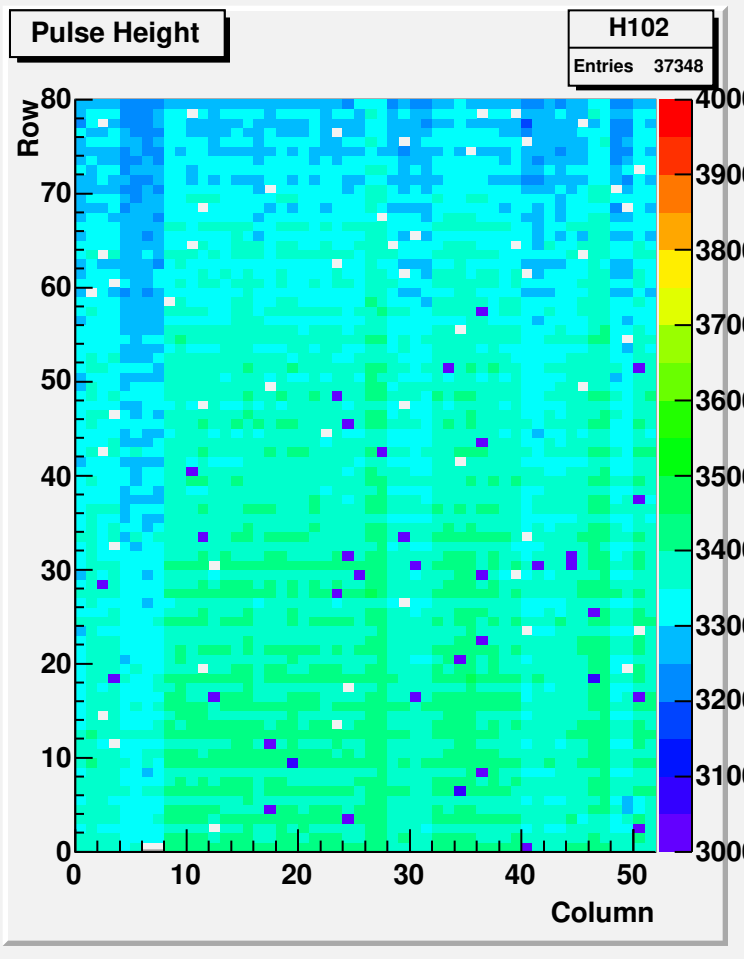
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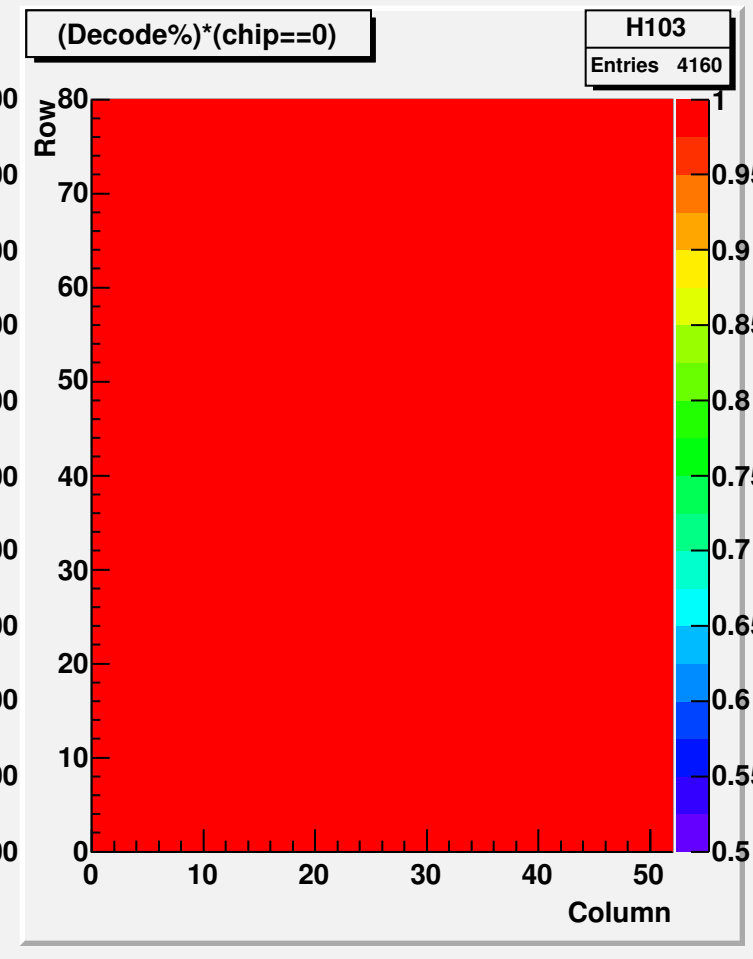
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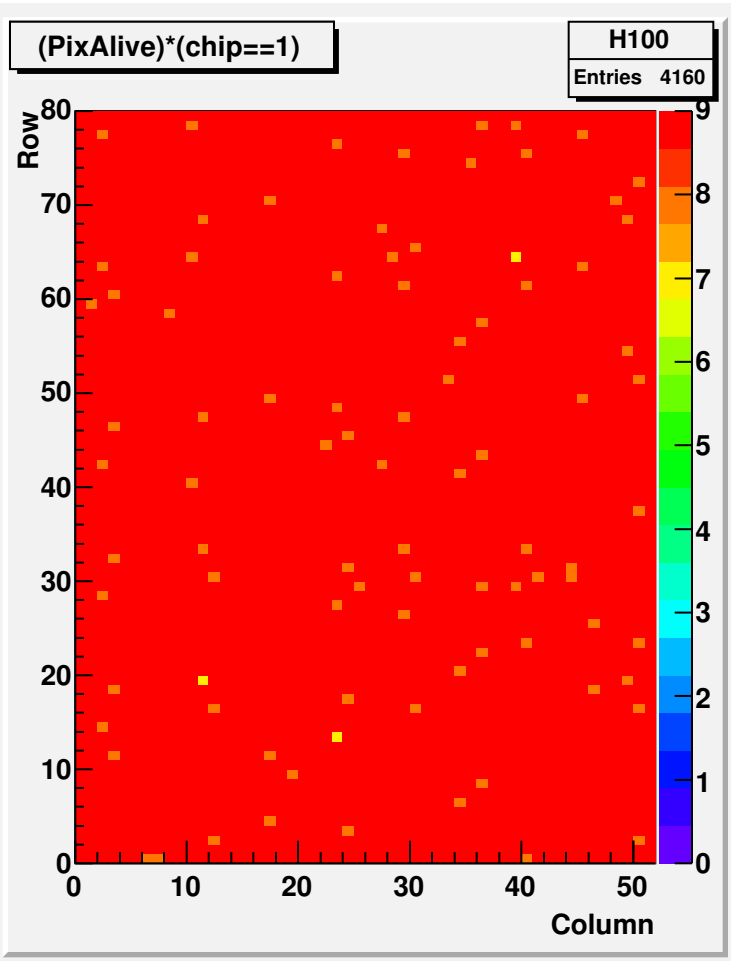
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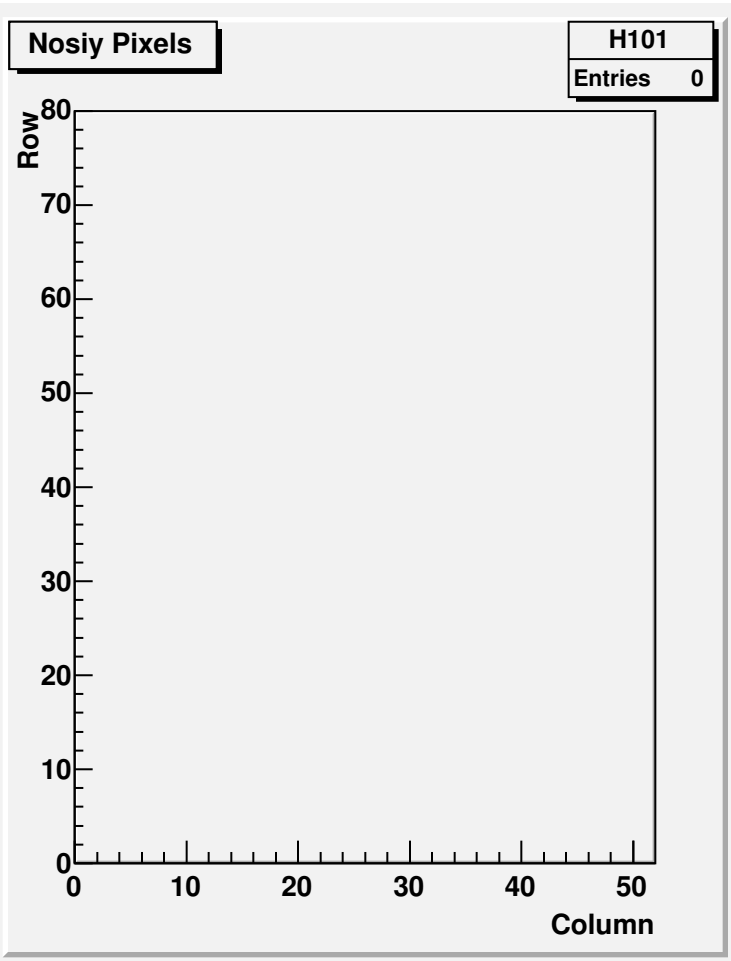
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H101
Entries 0
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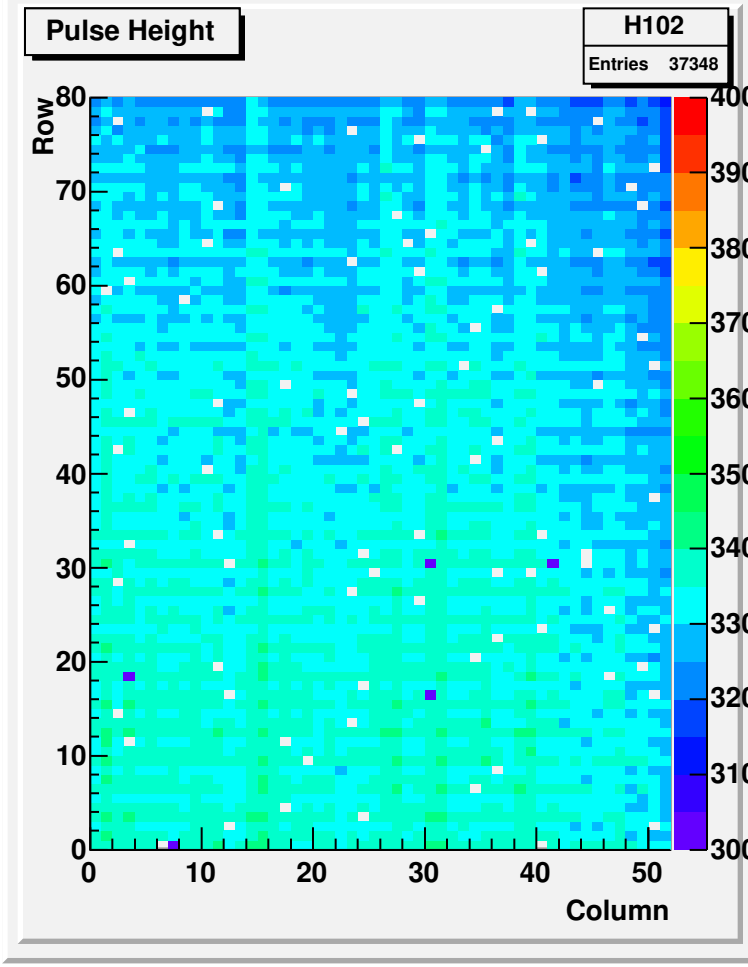
Column


Pulse Height
H102
Entries 37348
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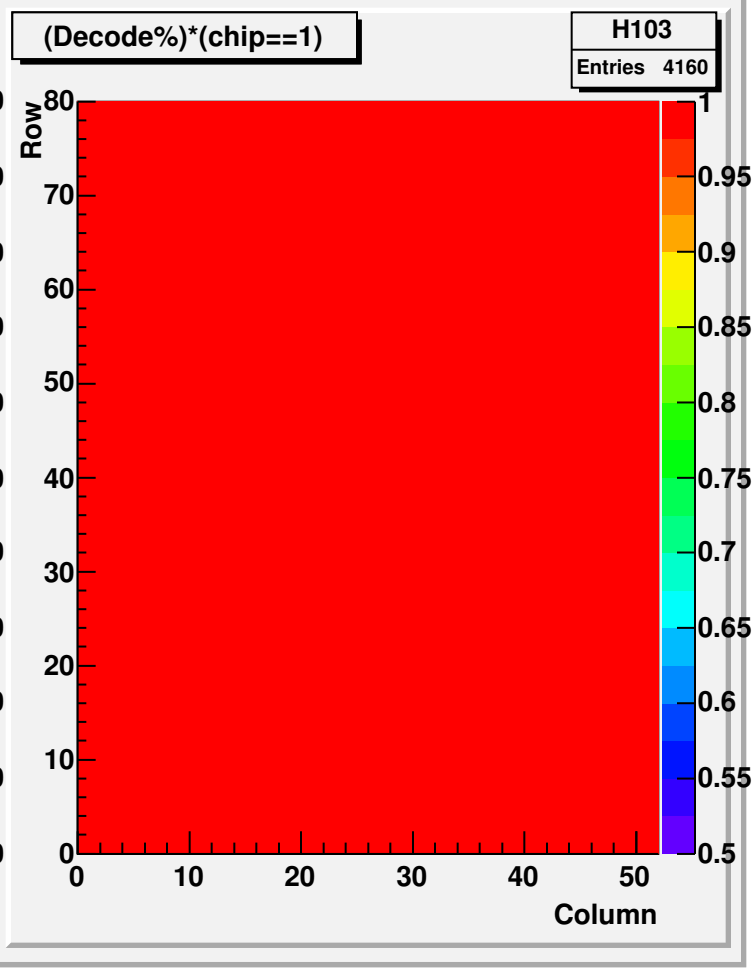
Column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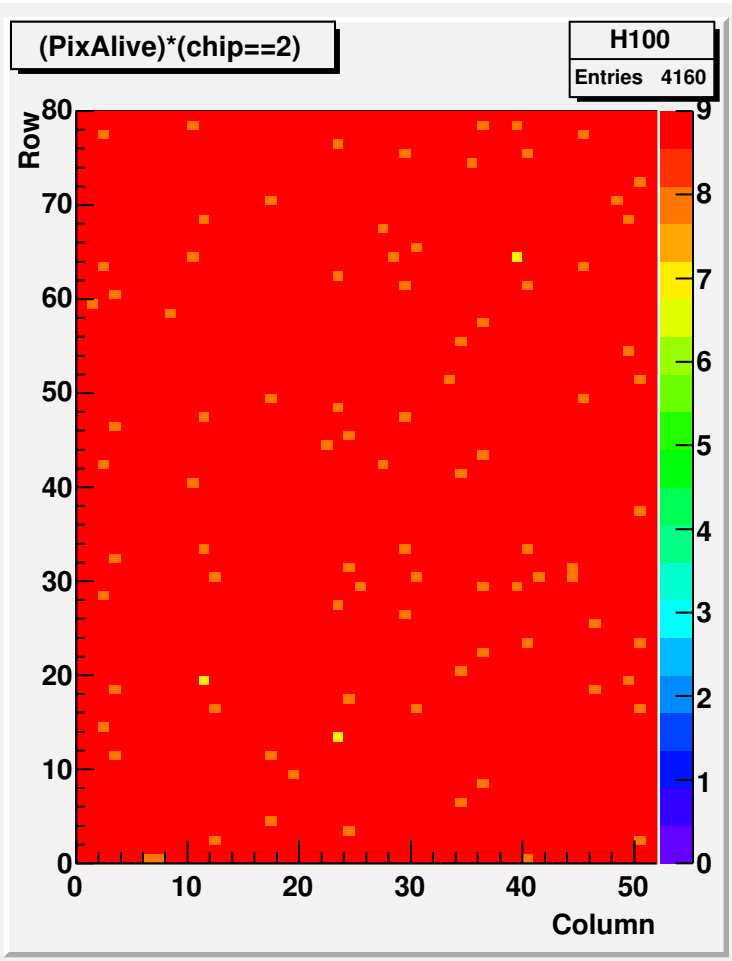
H103
Entries 4160
Row
Column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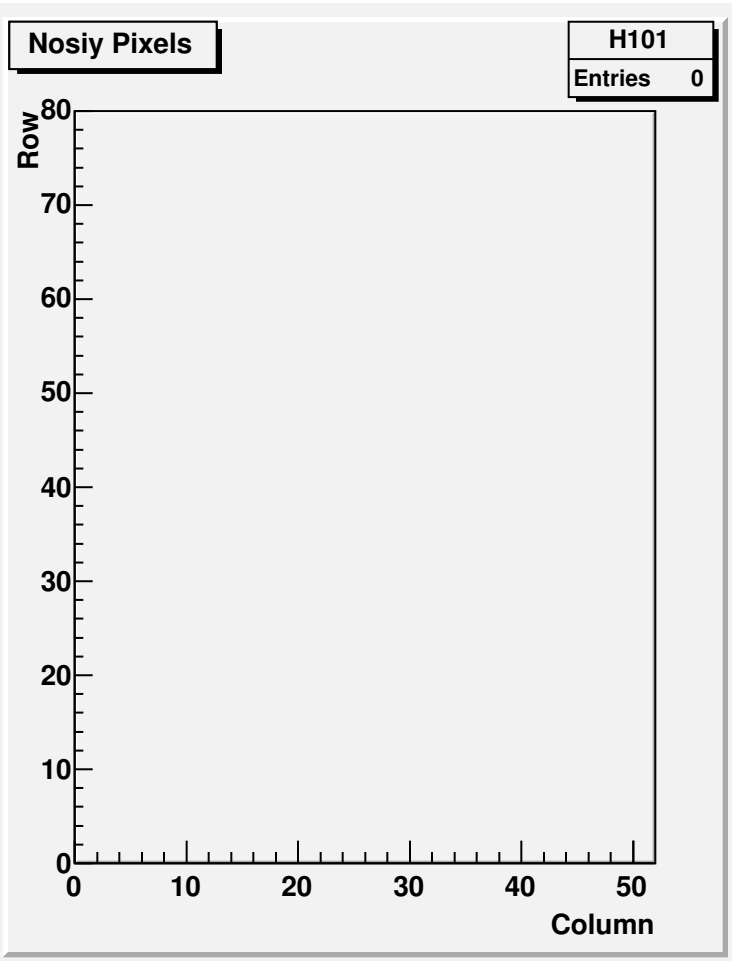
H100
Entries 4160
Row
Column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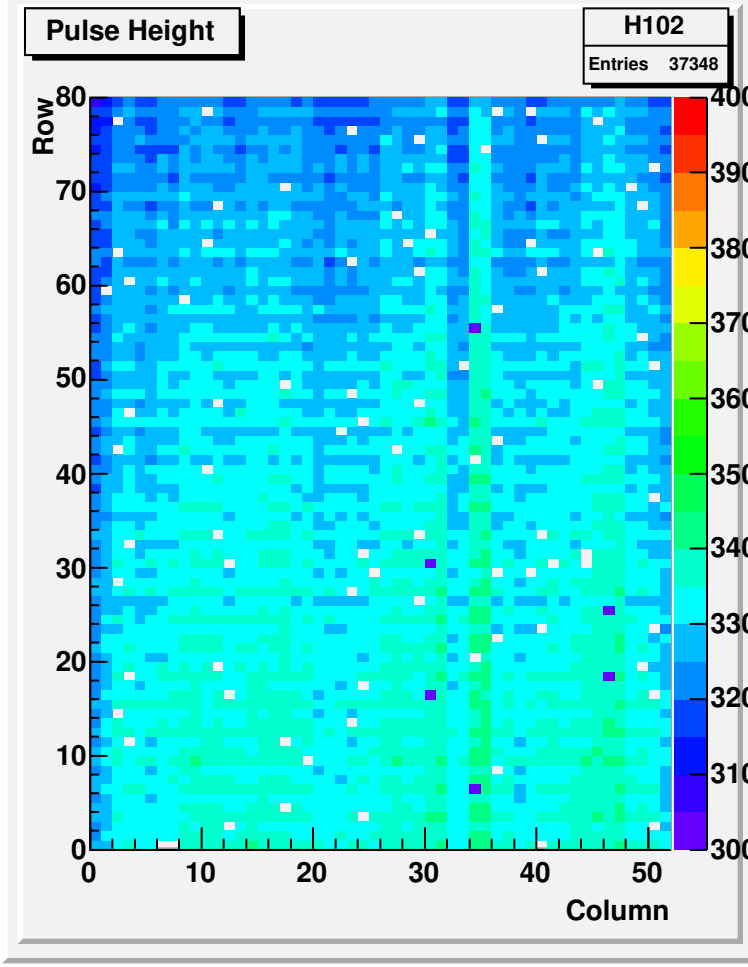
H101
Entries 0
Row
Column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H102
Entries 37348
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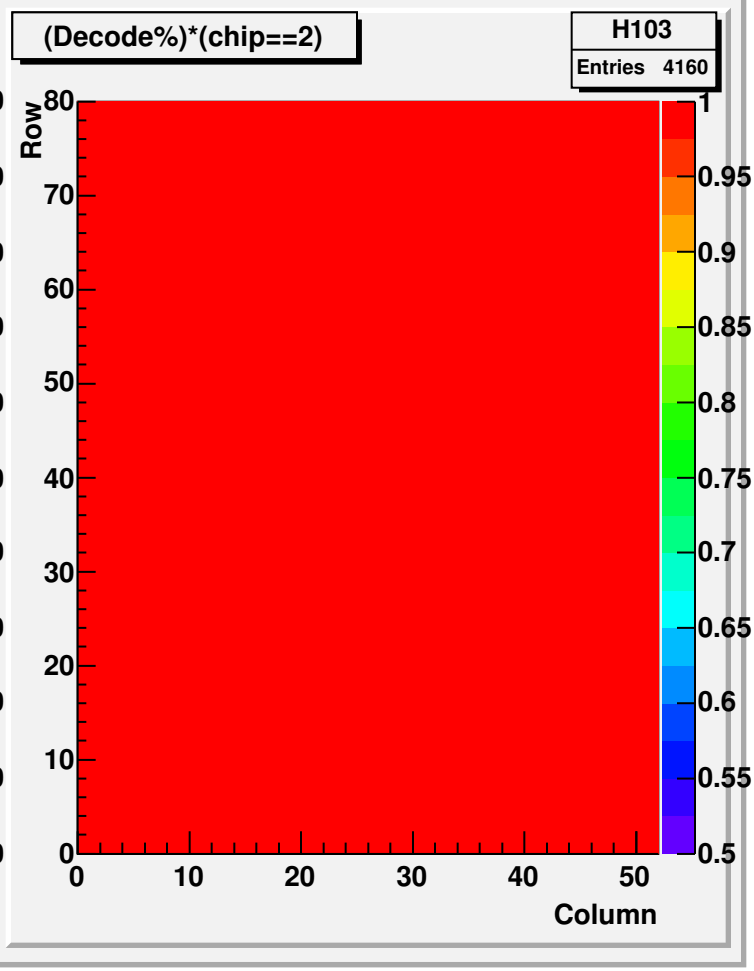
Column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H103
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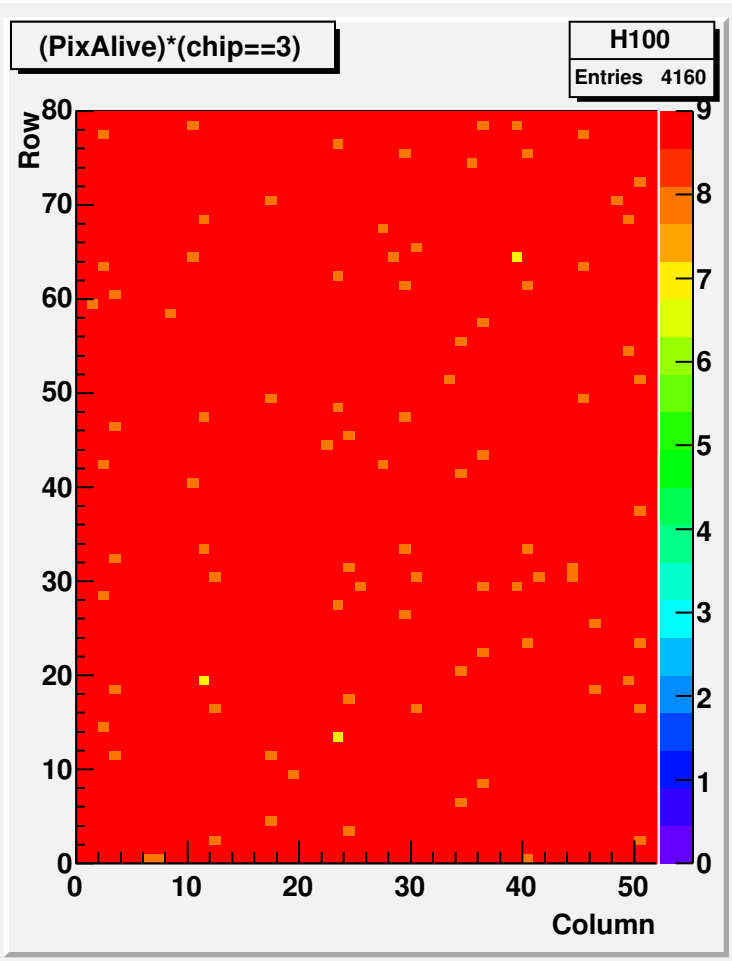
Column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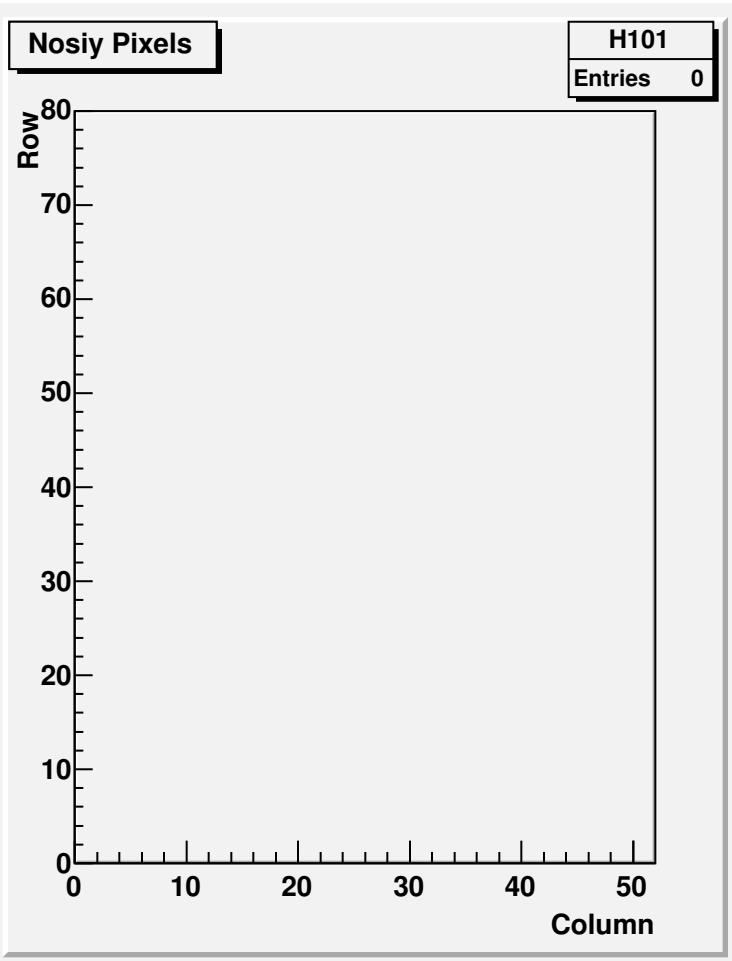
H100
Entries 4160
Row
Column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H101
Entries 0
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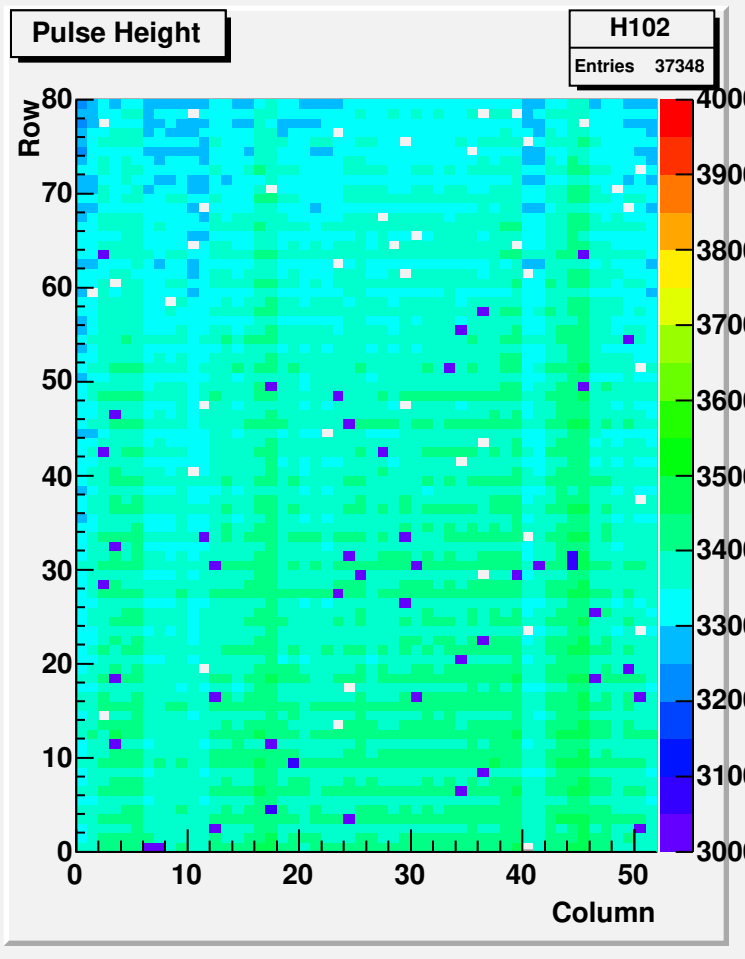
Column


Pulse Height
H102
Entries 37348
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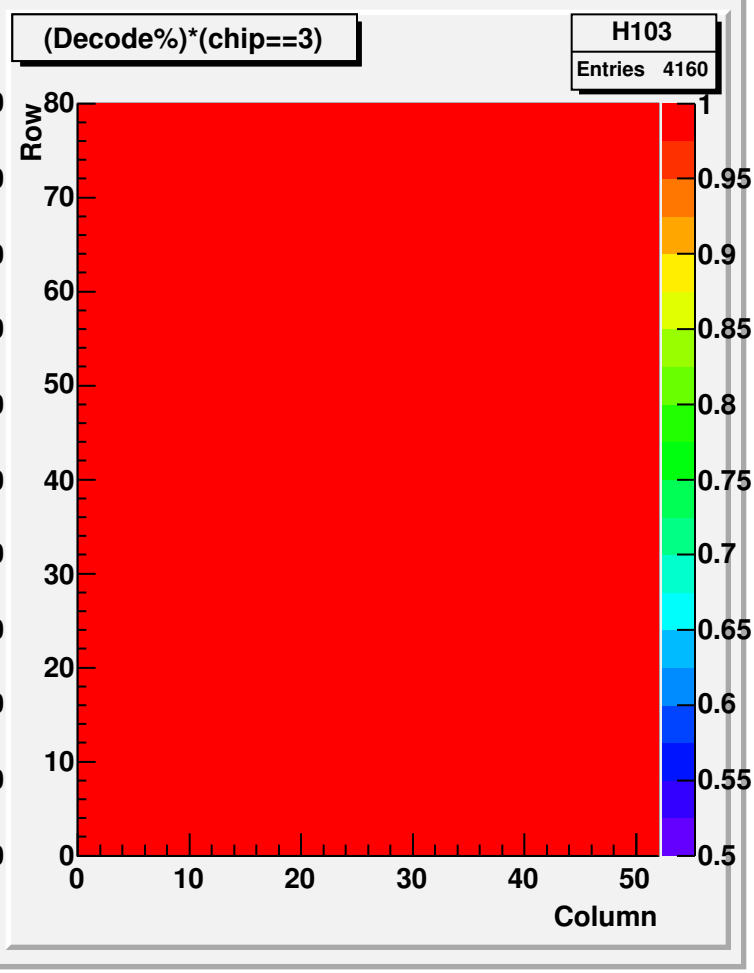
Column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H103
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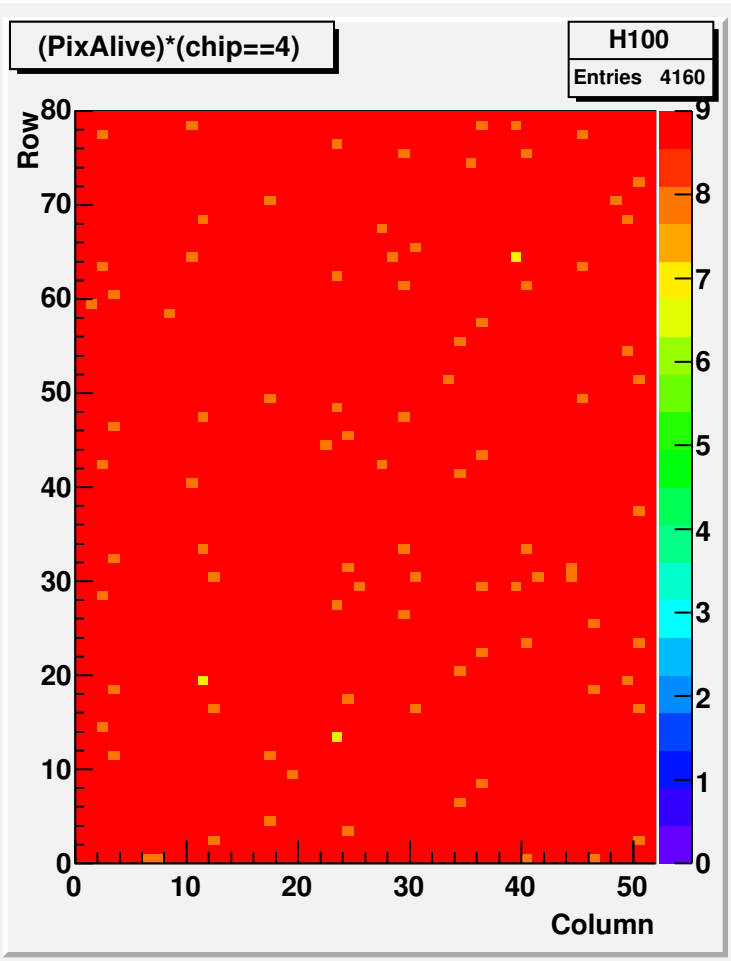
Row
Column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H100
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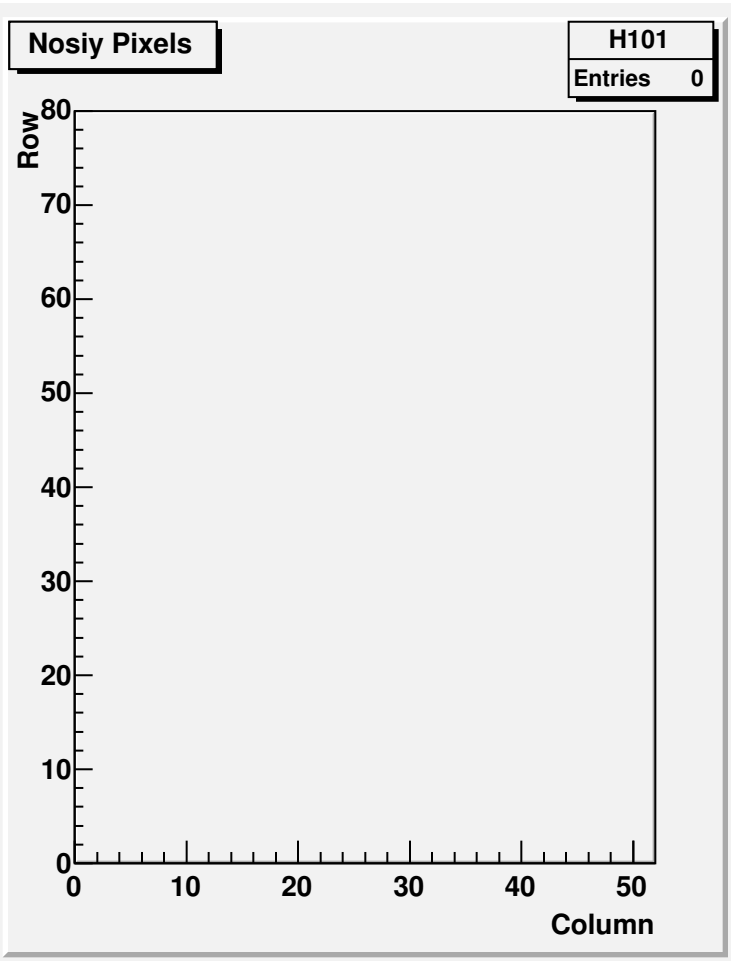
Column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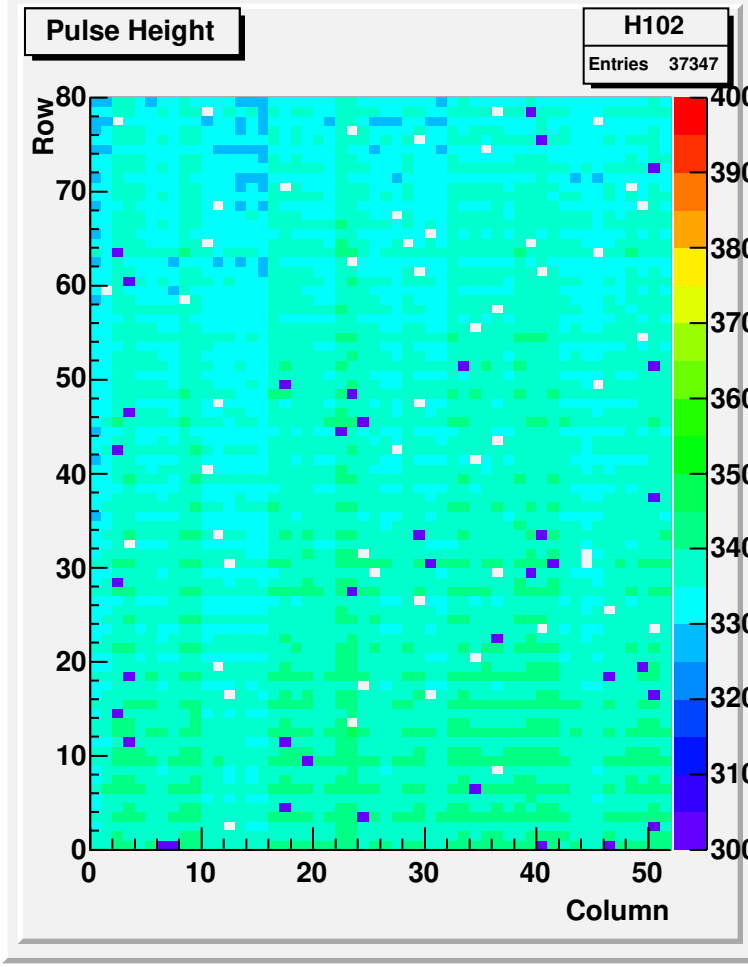
H101
Entries 0
Row
Column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H102
Entries 37347
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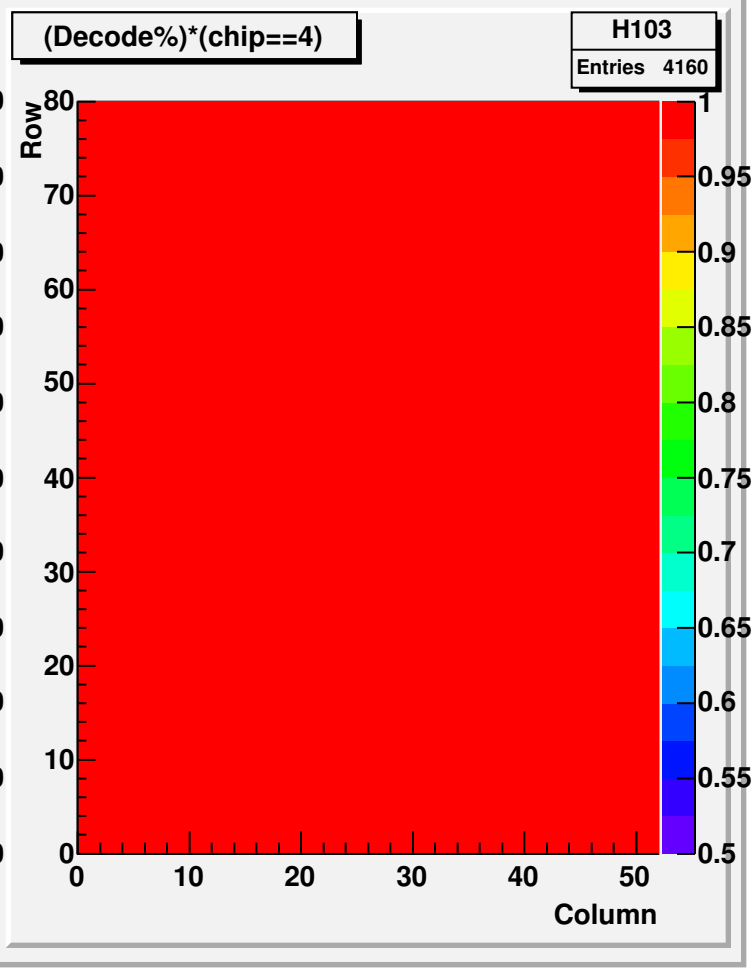
Column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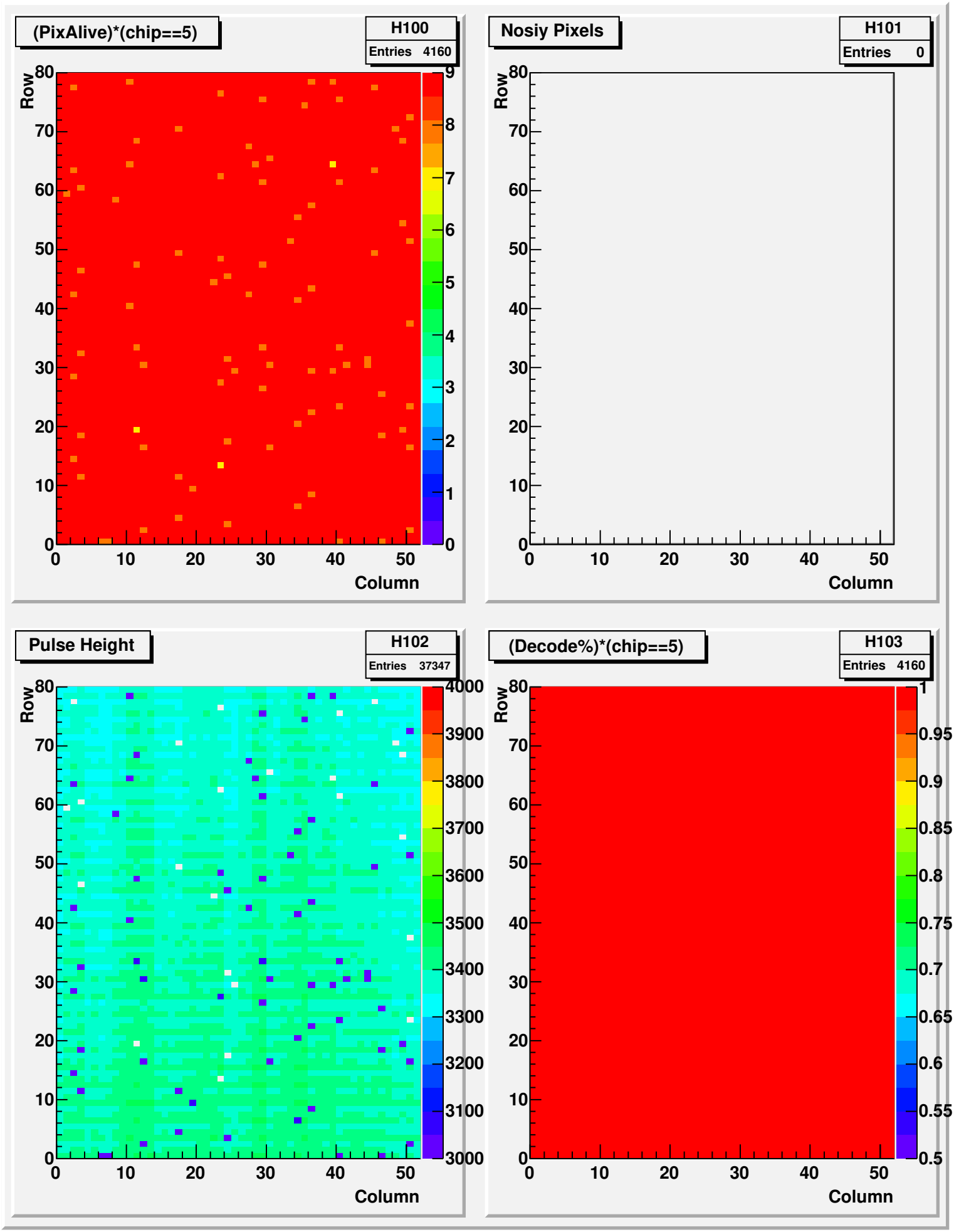
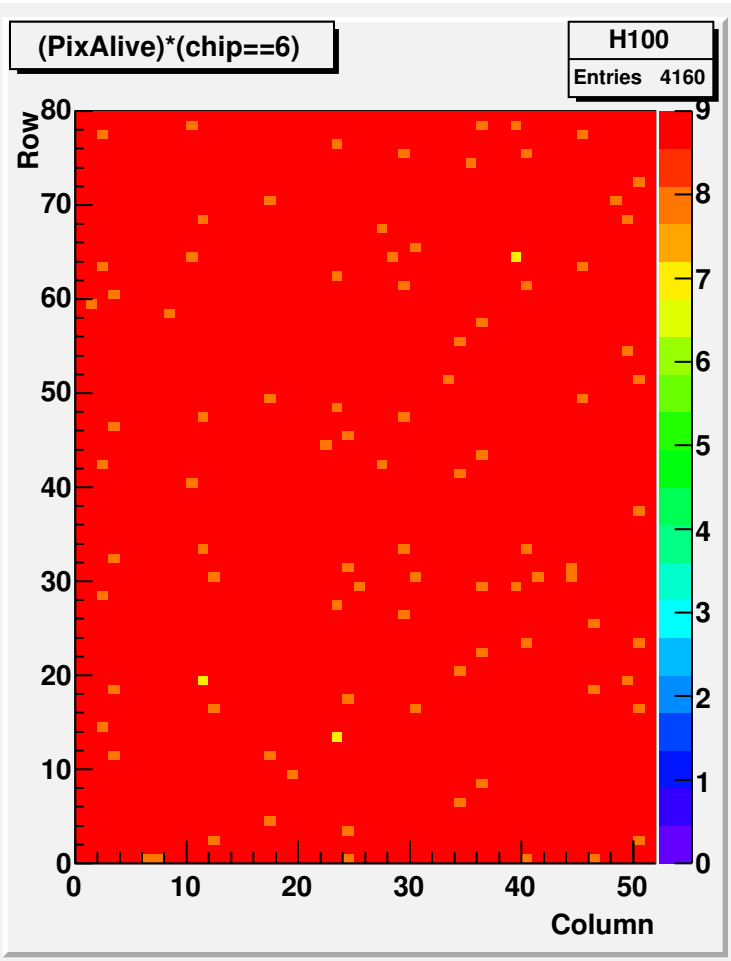
H103
Entries 4160
Row
Column


(PixAlive)*(chip==5)
H100
Entries 4160
Row
Column
Nosiy Pixels
H101
Entries 0
Row
Column
Pulse Height
H102
Entries 37347
Row
Column
(Decode%)*(chip==5)
H103
Entries 4160
Row
Column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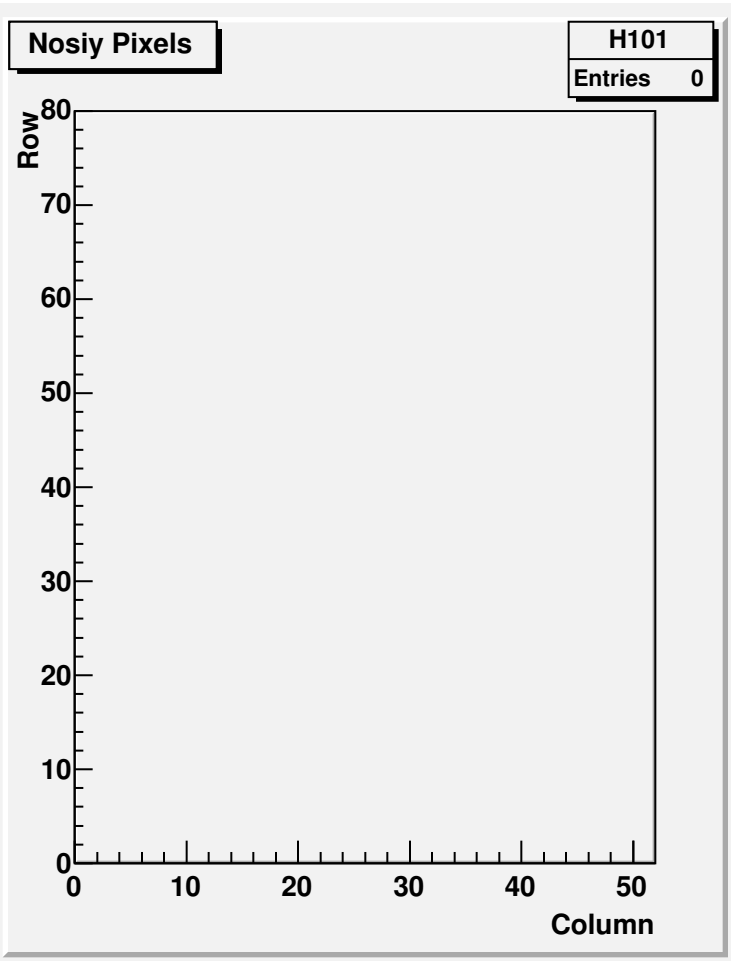
H100
Entries 4160
Row
Column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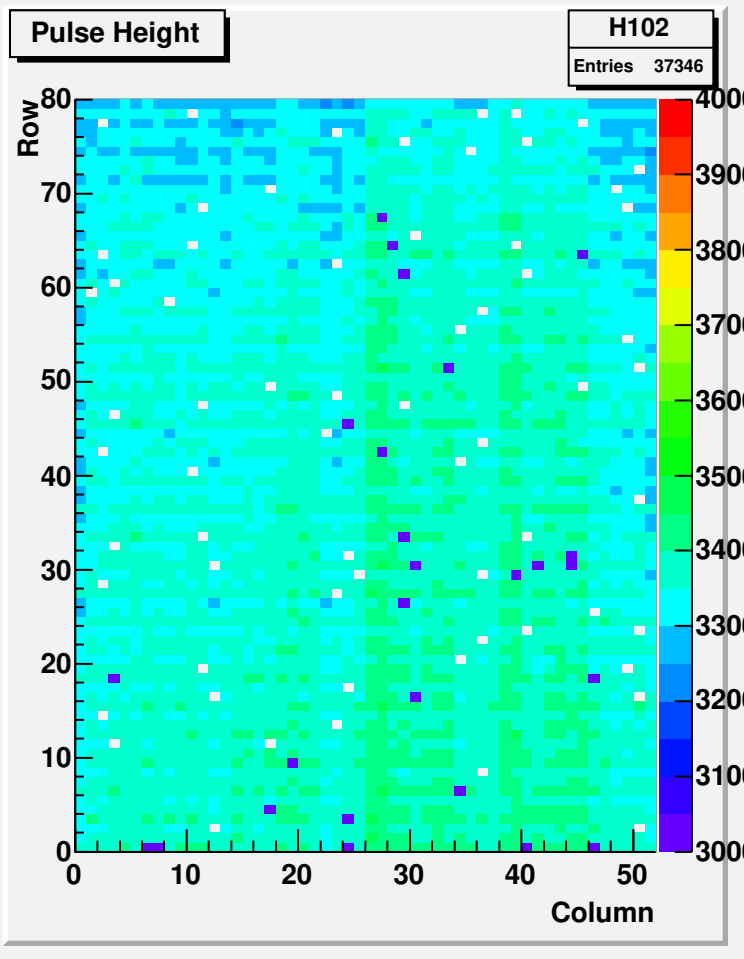
Column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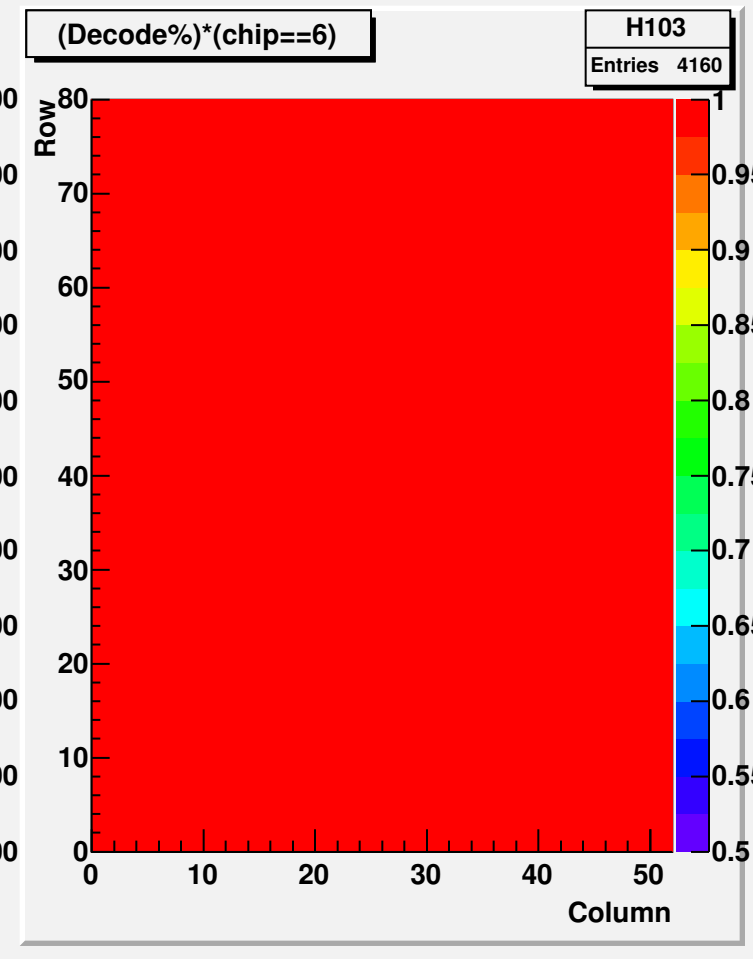
Column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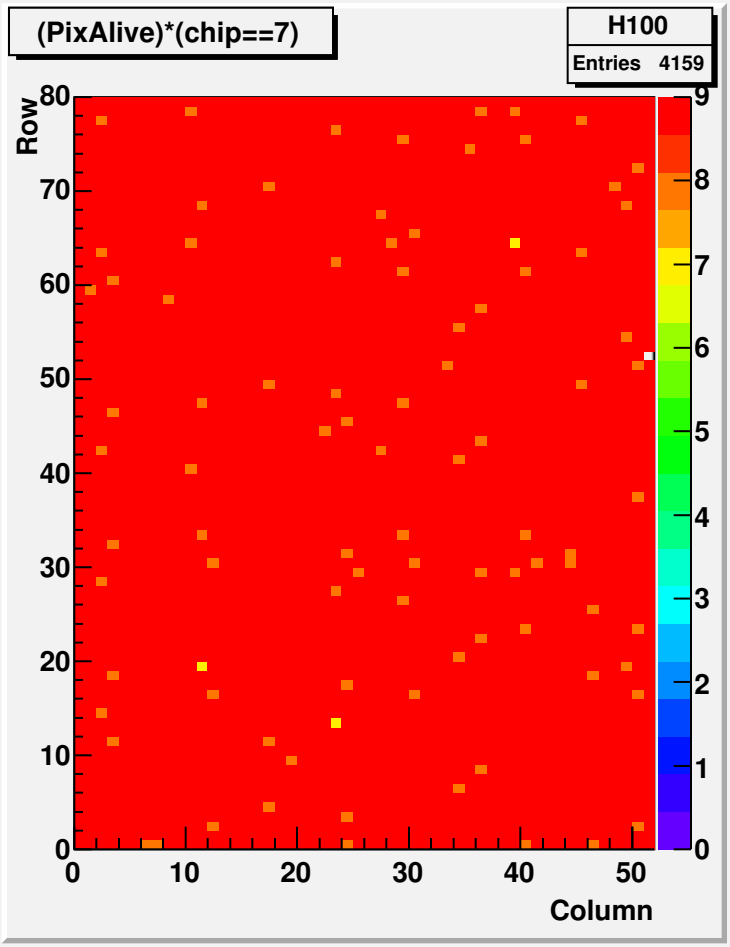
Row
Column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H100
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Row
Column
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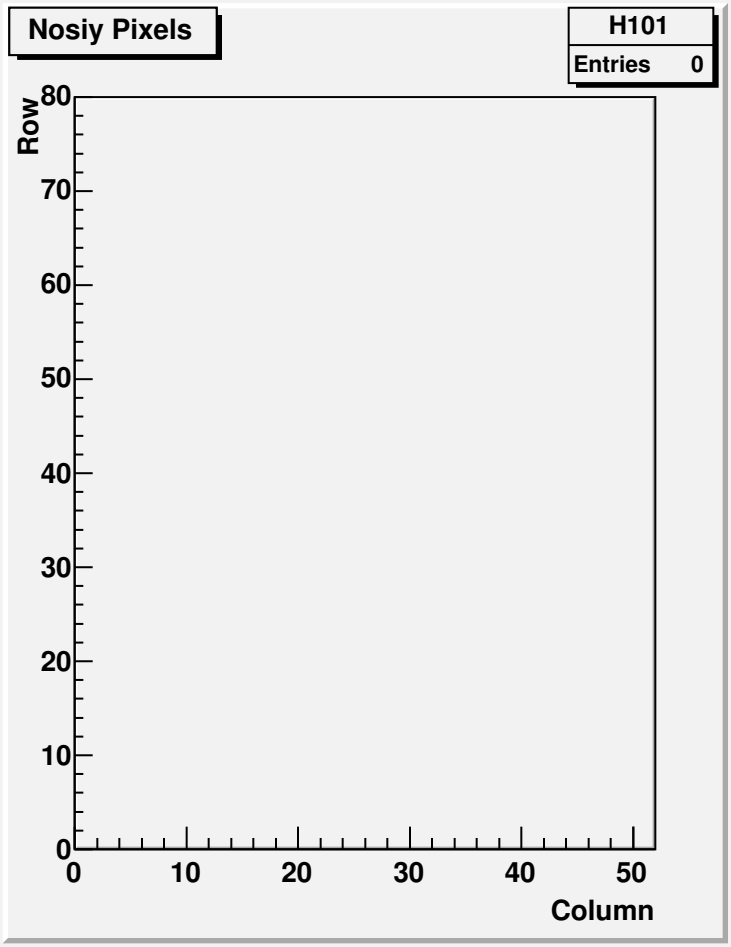
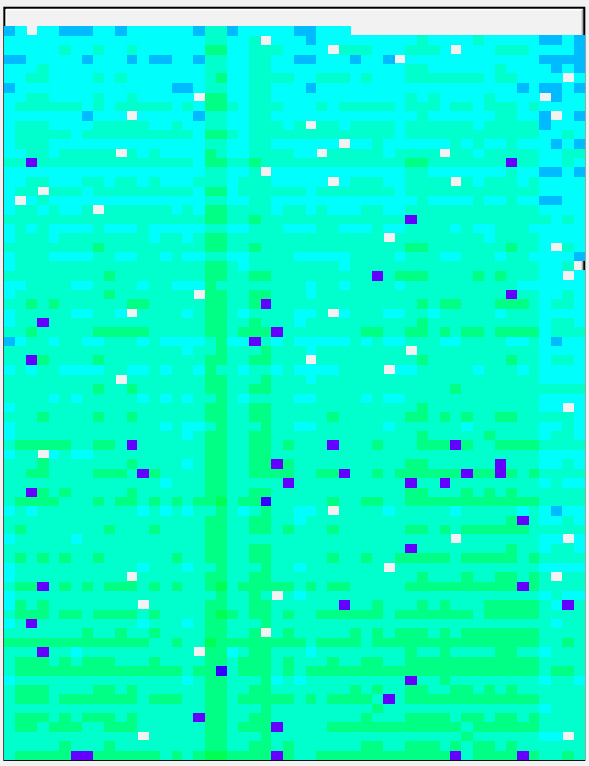
6
7
8
9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H101
Entries 0
Row
Column
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70
80