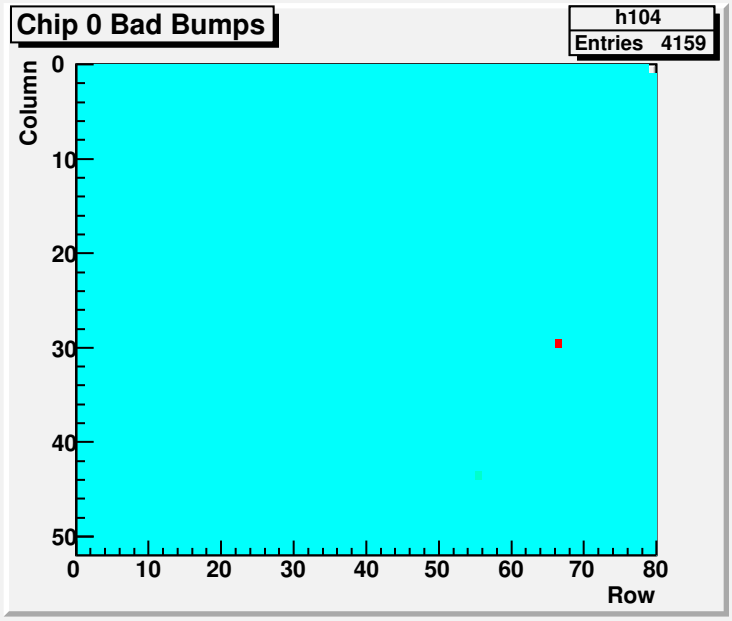
Chip 0 Bad Bumps
h104
Entries 4159
Column
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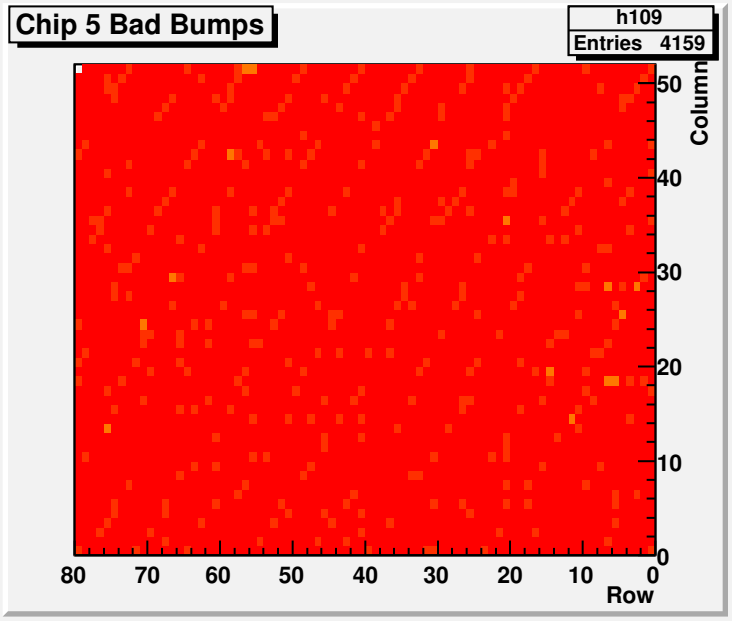
Chip 5 Bad Bumps
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Entries 4159
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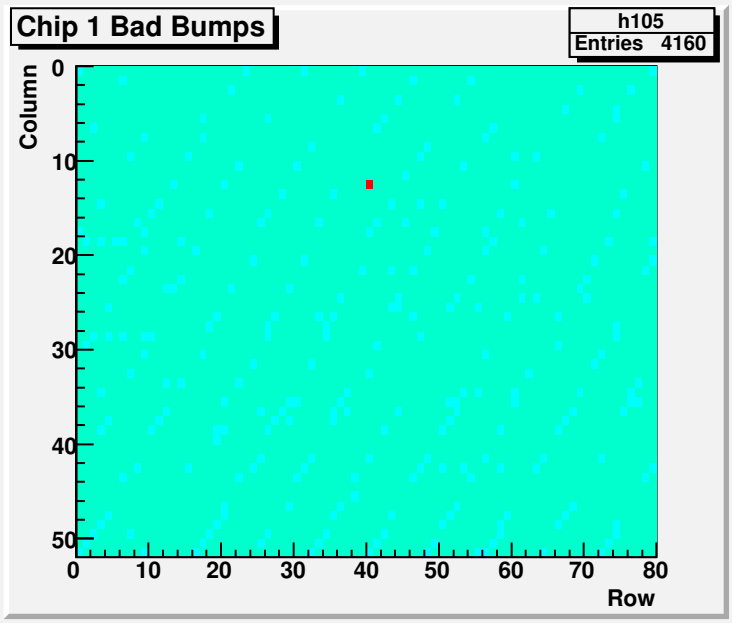
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Entries 4160
Column
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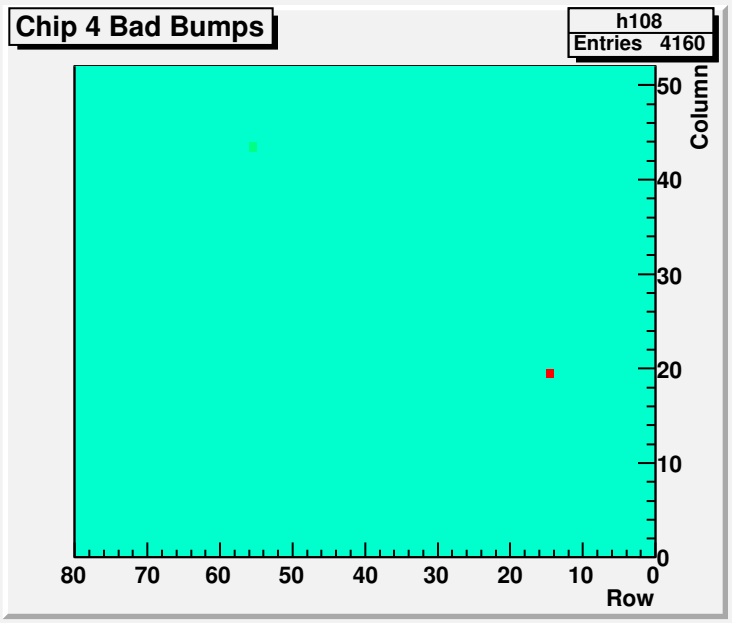
Chip 4 Bad Bumps
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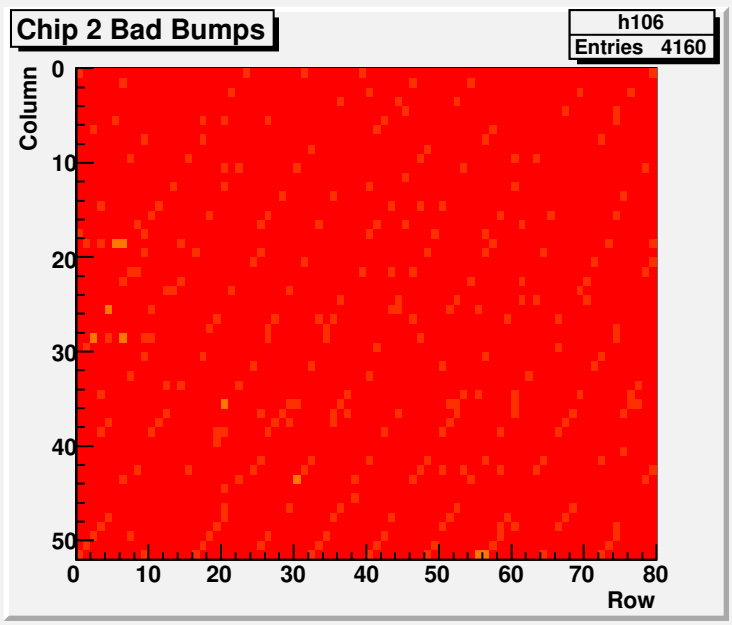
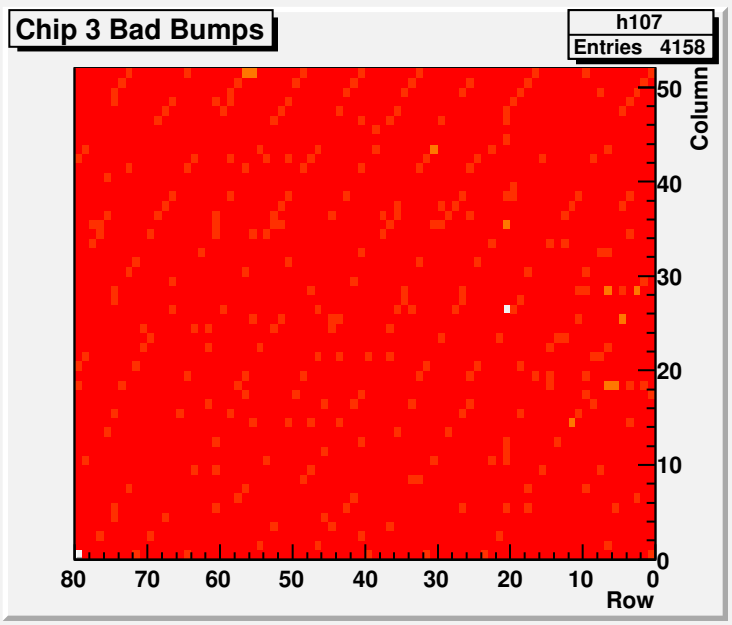
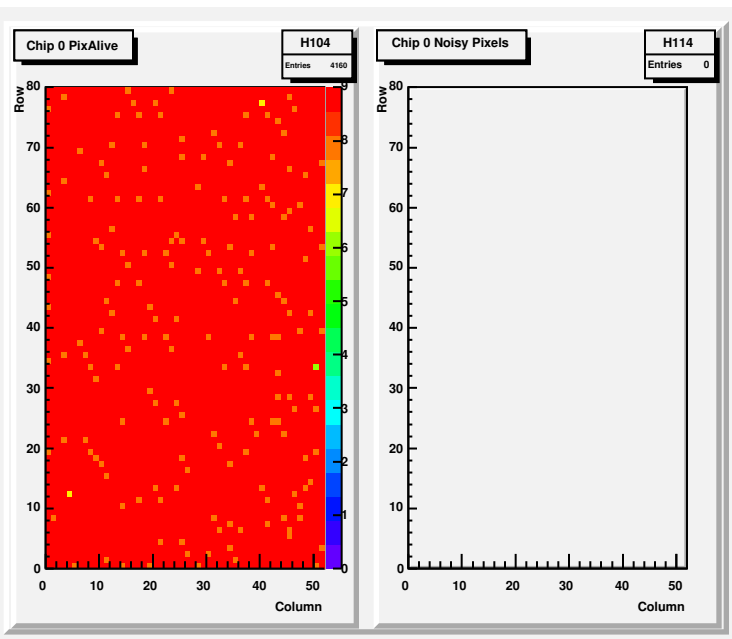
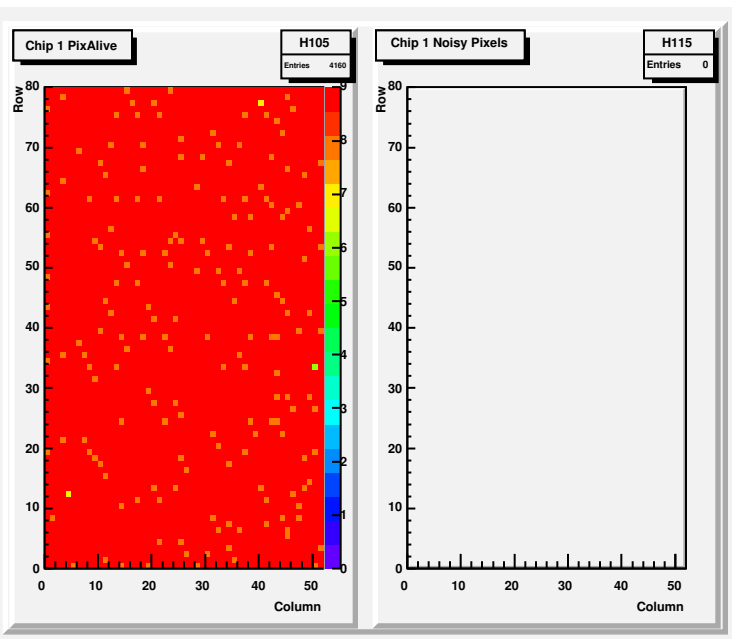
Entries 4160
Column
Row


Chip 2 Bad Bumps
h106
Entries 4160
Column
Row


Chip 3 Bad Bumps
h107
Entries 4158
Column
Row


Chip 0 PixAlive
H104
Entries 4160
Row
Column

Chip 0 Noisy Pixels
H114
Entries 0
Row
Column


Chip 1 PixAlive
H105
Entries 4160
Chip 1 Noisy Pixels
H115
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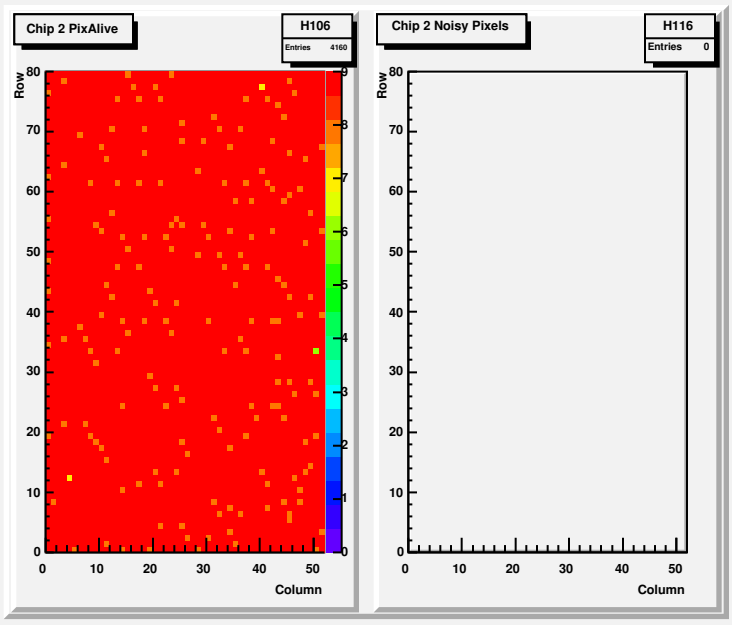
Column


Chip 2 PixAlive
H106
Entries 4160
Chip 2 Noisy Pixels
H116
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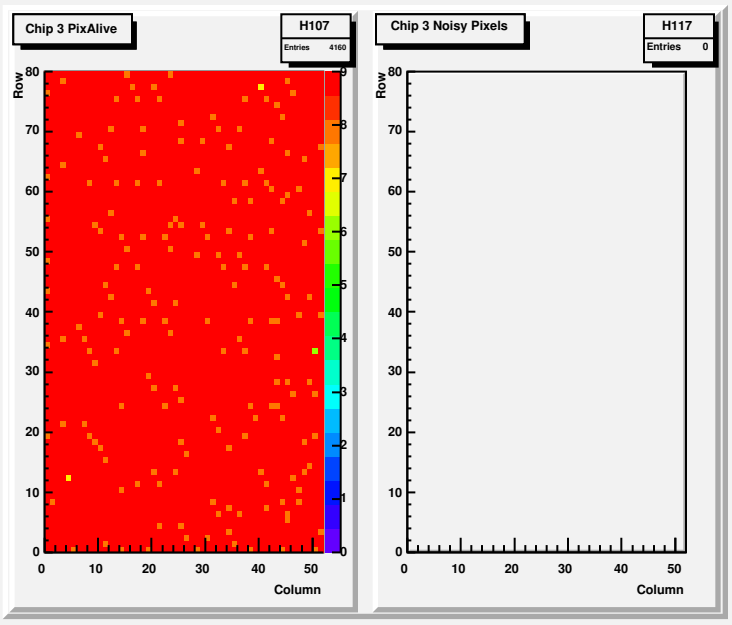
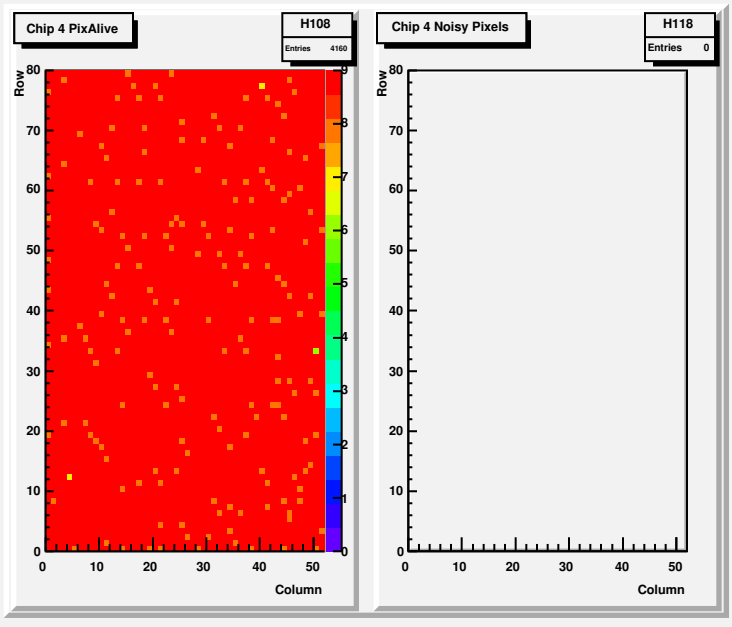
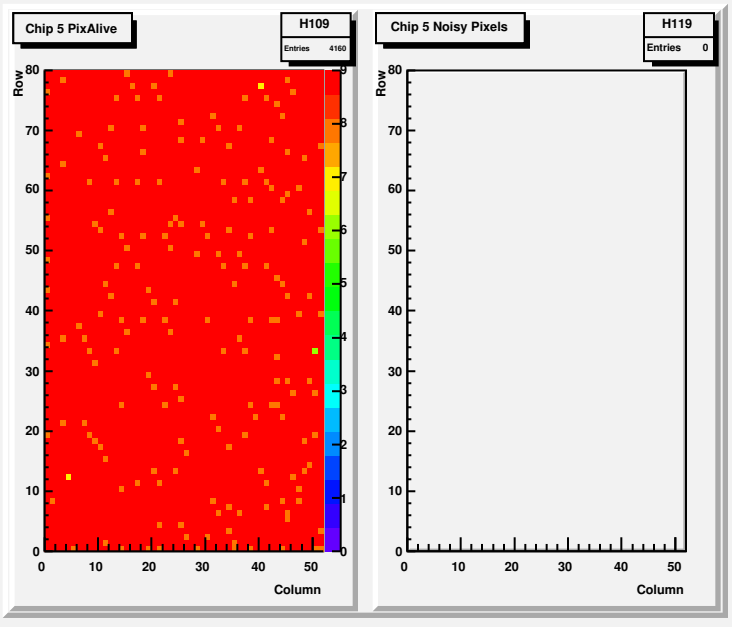
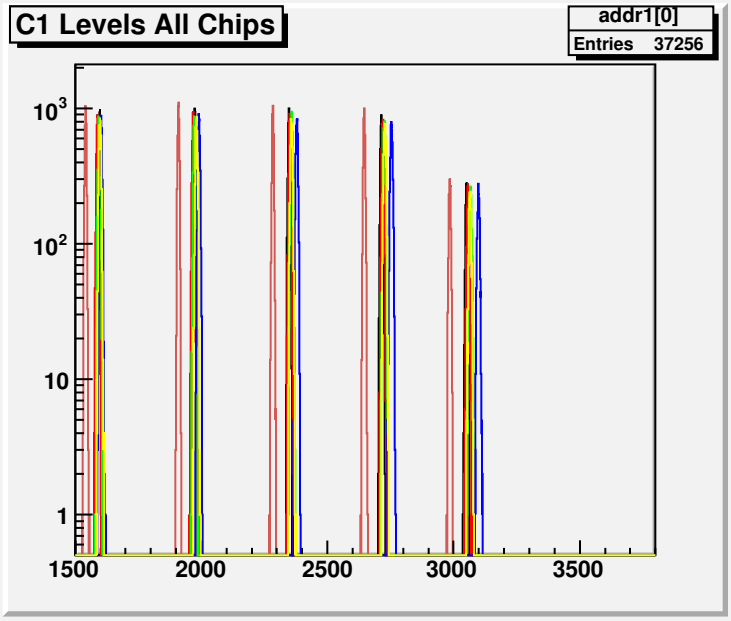
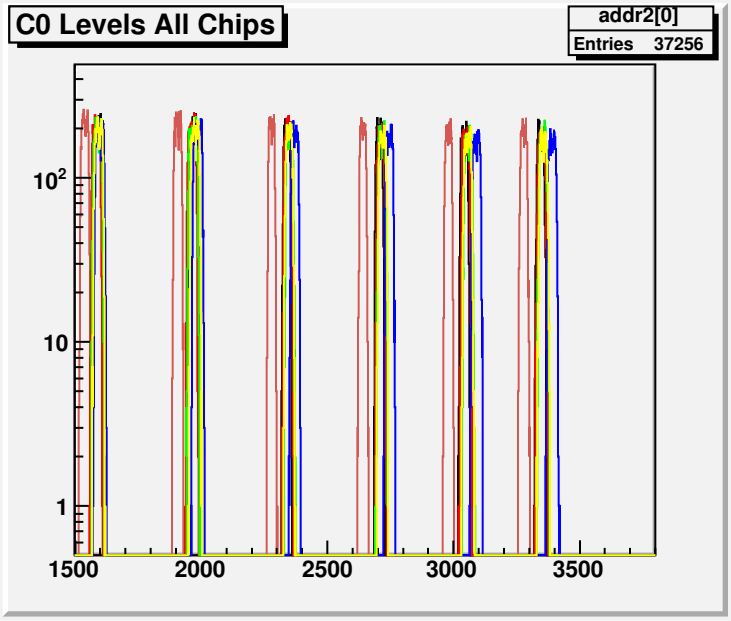
Row
Column

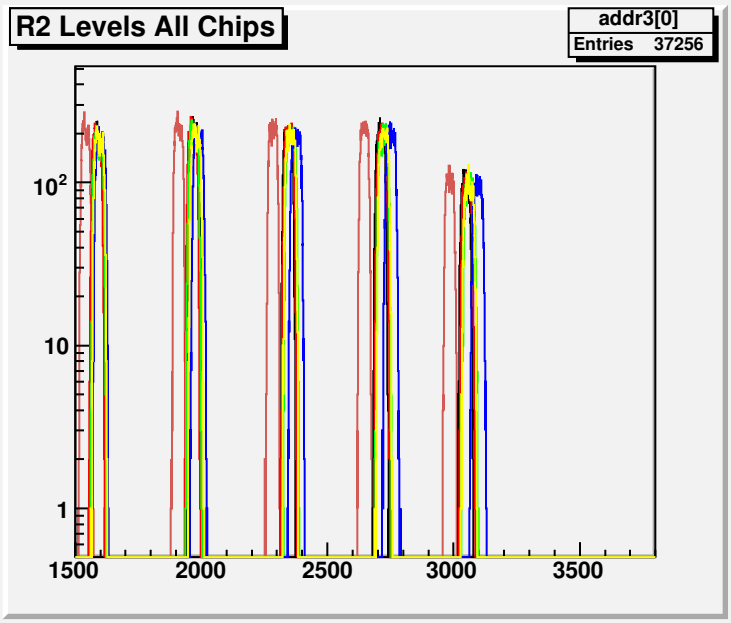

Chip 3 PixAlive
H107
Entries 4160
Chip 3 Noisy Pixels
H117
Entries 0
Row
Column

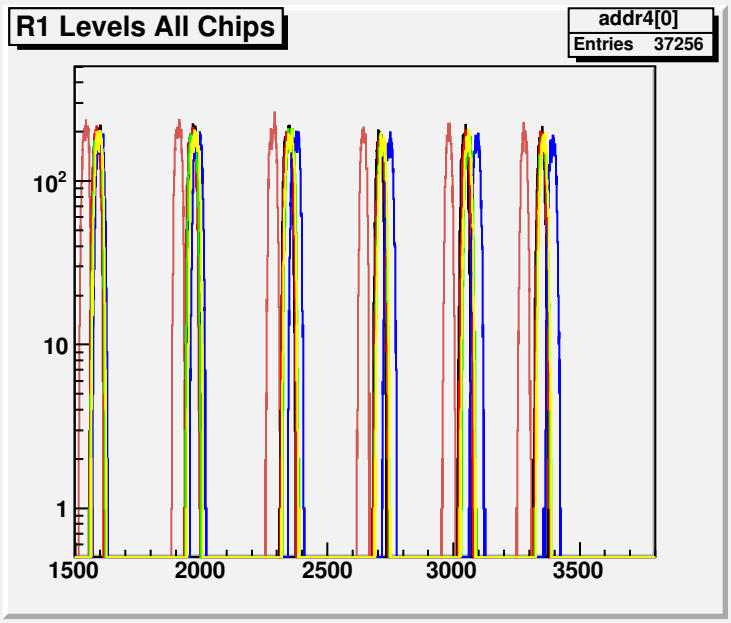

Chip 4 PixAlive
H108
Entries 4160
Chip 4 Noisy Pixels
H118
Entries 0
Row
Column

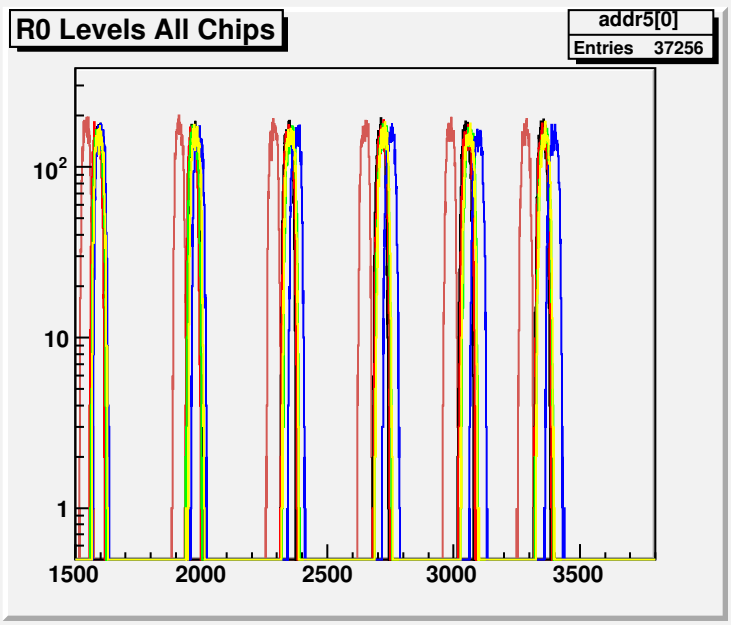

Chip 5 PixAlive
H109
Entries 4160
Chip 5 Noisy Pixels
H119
Entries 0
Row
Column

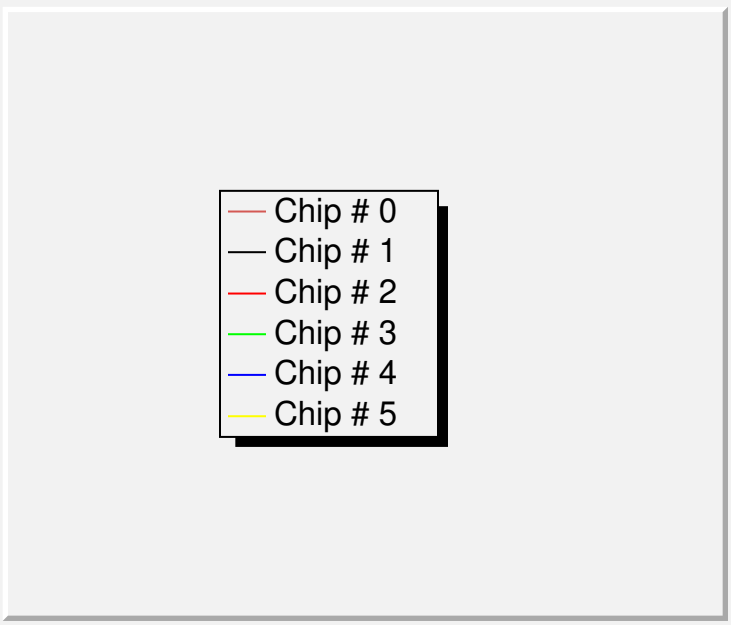

C1 Levels All Chips
addr1[0]
Entries 37256
10^3
10^2
10
1
1500
2000
2500
3000
3500

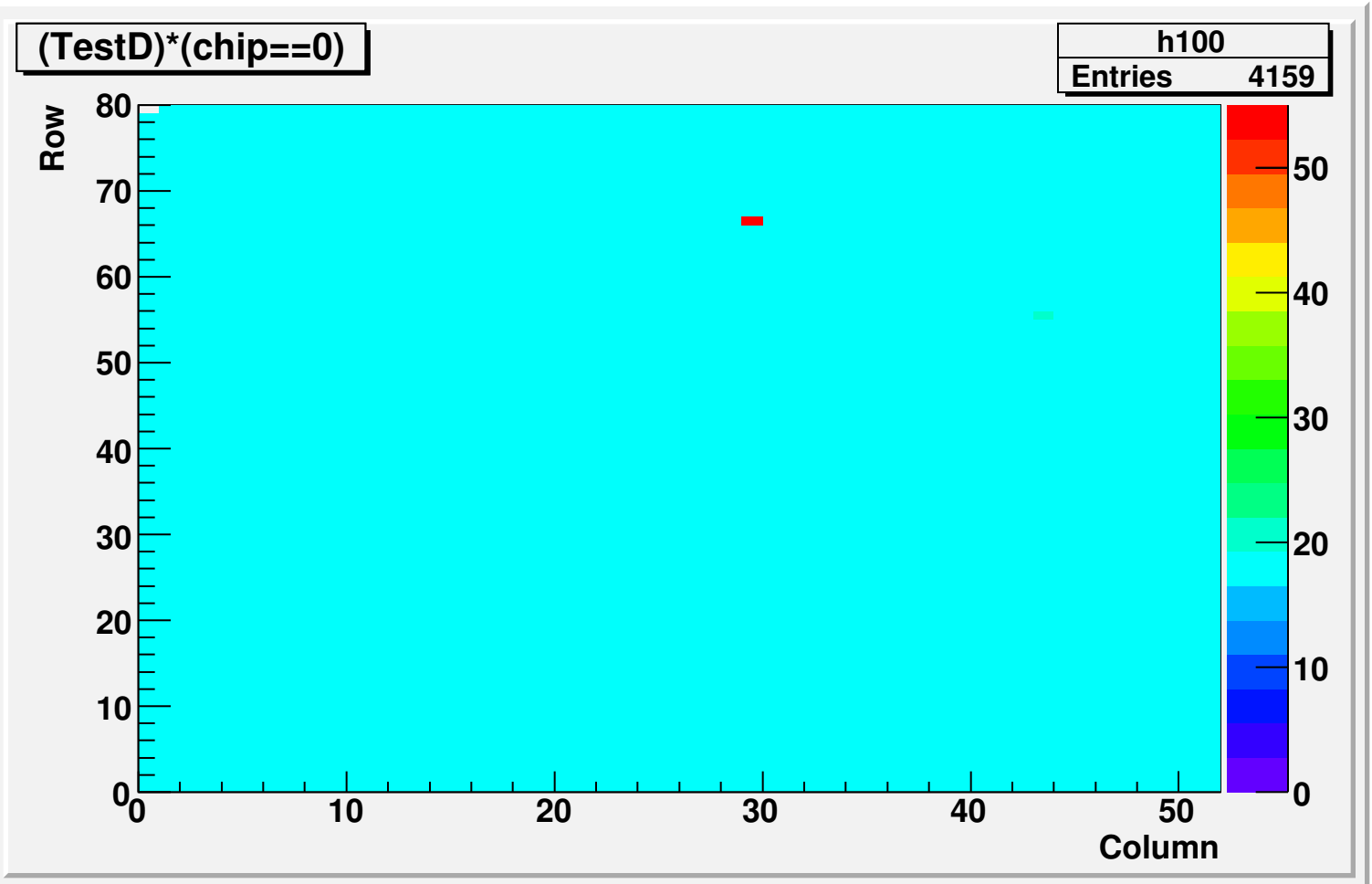

C0 Levels All Chips
addr2[0]
Entries 37256
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37256
10^2
10
1
1500
2000
2500
3000
3500


R1 Levels All Chips
addr4[0]
Entries 37256
10^2
10
1
1500
2000
2500
3000
3500


R0 Levels All Chips
addr5[0]
Entries 37256
10^2
10
1
1500
2000
2500
3000
3500


Chip # 0
Chip # 1
Chip # 2
Chip # 3
Chip # 4
Chip # 5


(TestD)*(chip==0)
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Entries 4159
Row
Column
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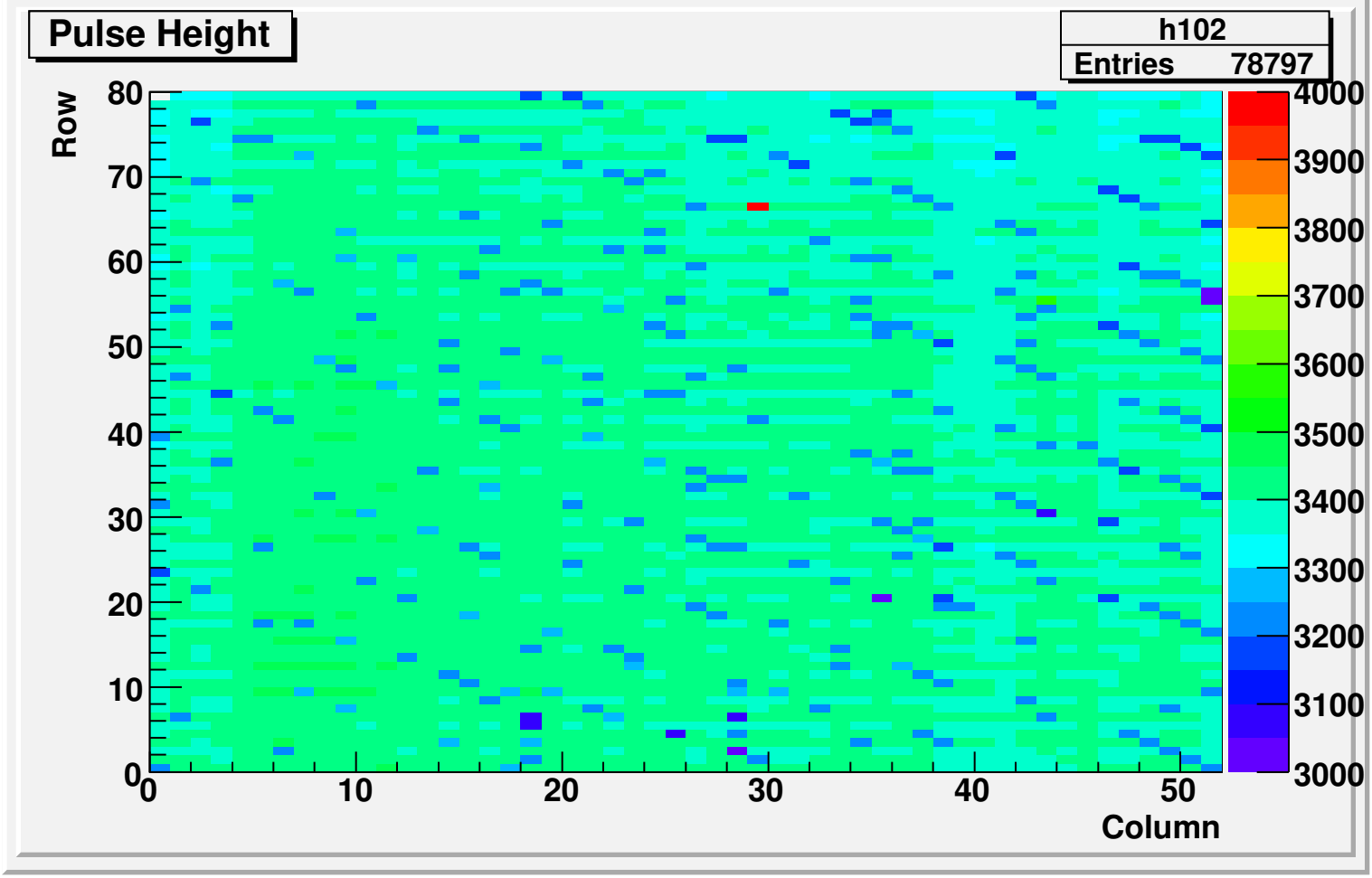
60
70
80


Pulse Height
h102
Entries 78797
Row
Column
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3100
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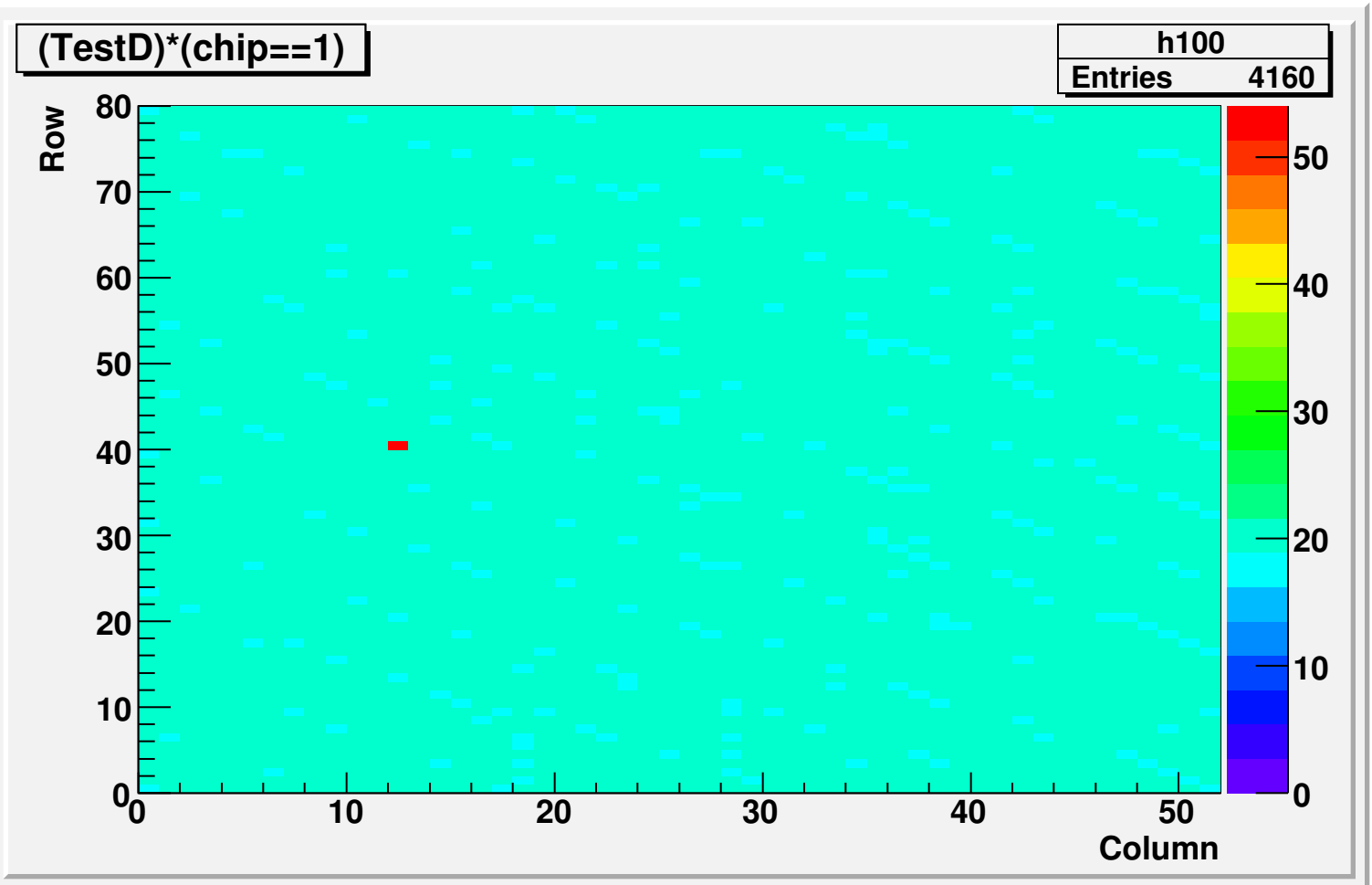
3800
3900
4000


(TestD)*(chip==1)
h100
Entries 4160
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Column
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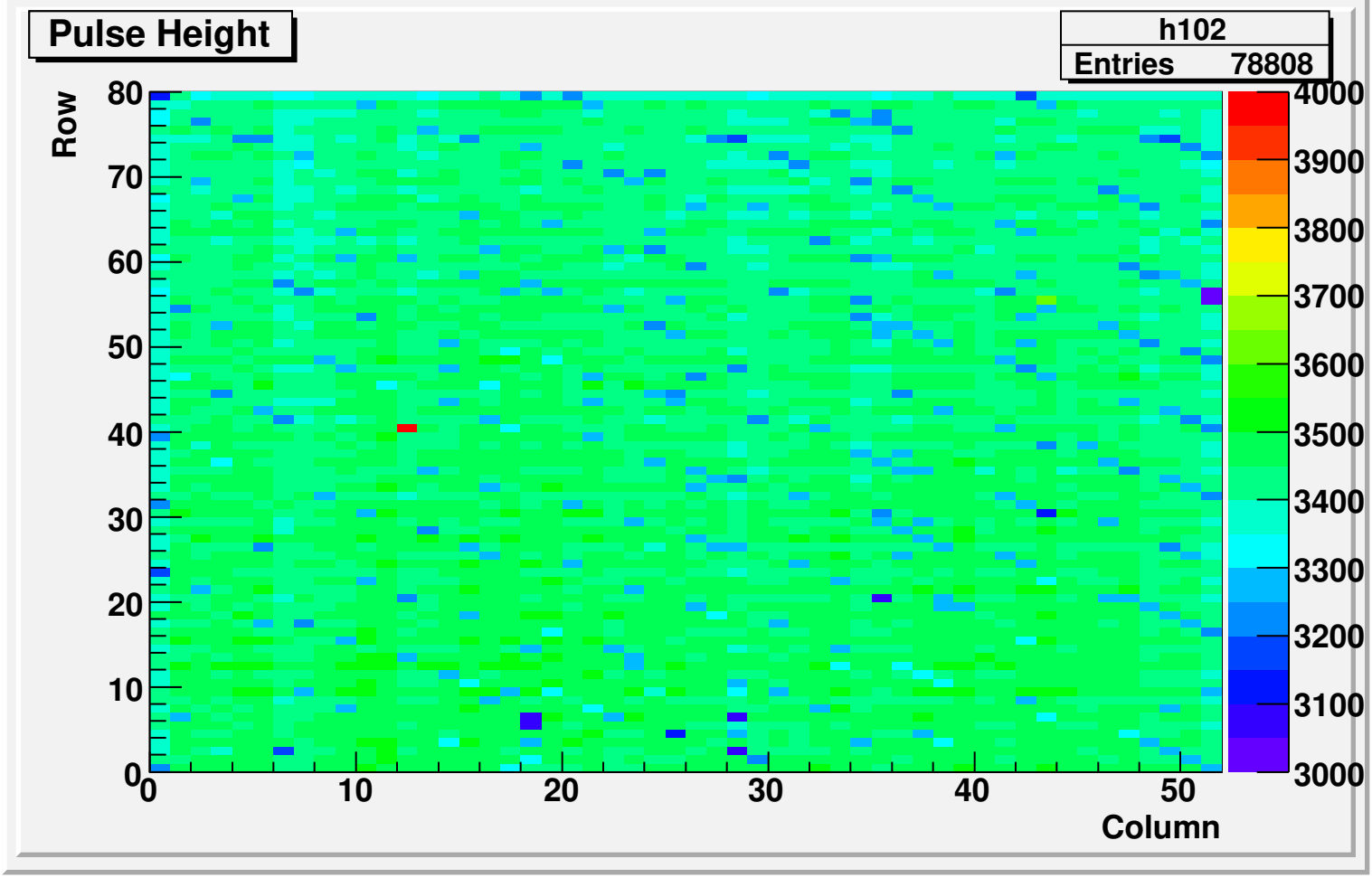
Pulse Height
h102
Entries 78808
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Column
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3900
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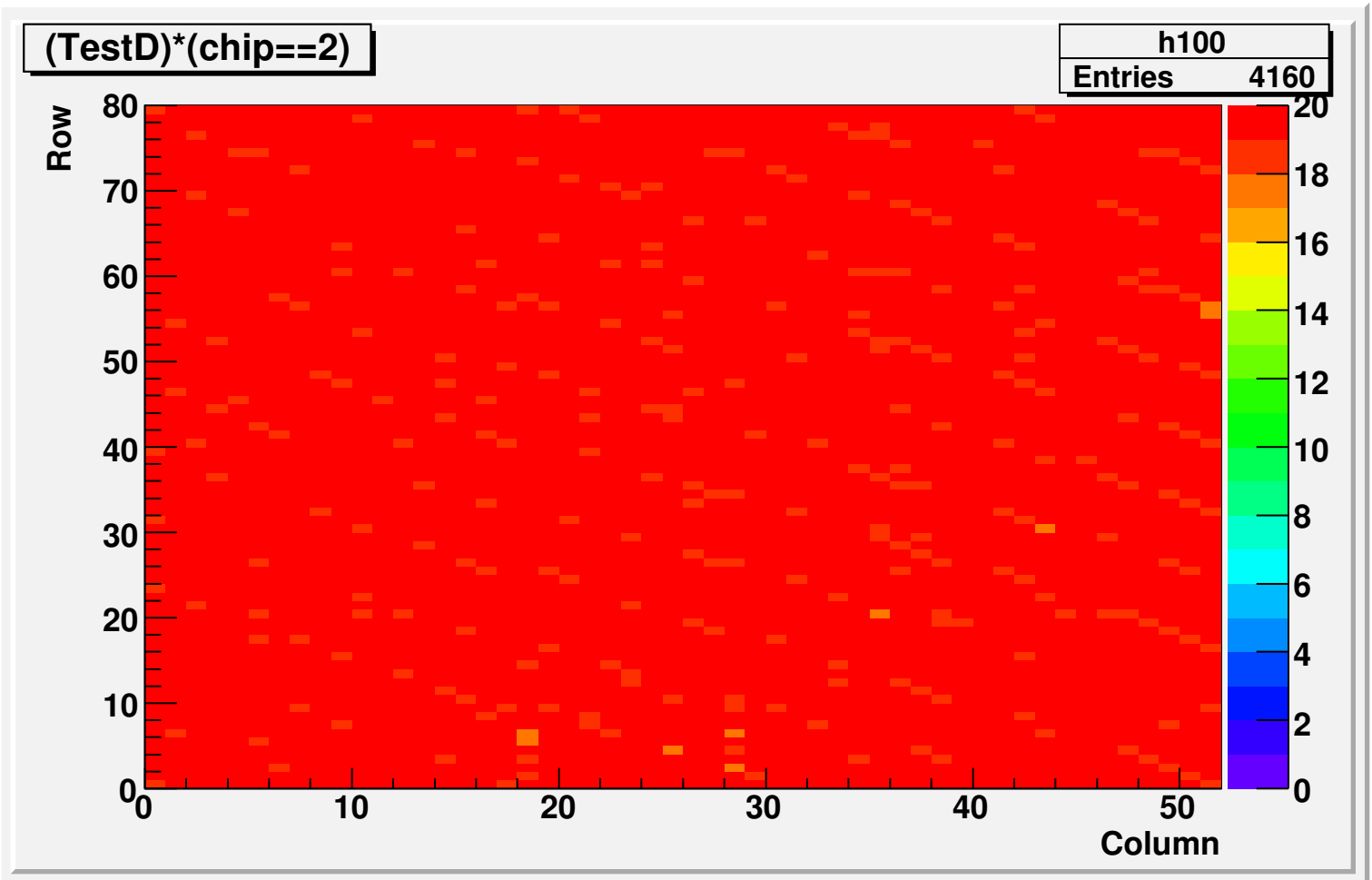
(TestD)*(chip==2)
h100
Entries 4160
Row
Column
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80
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10
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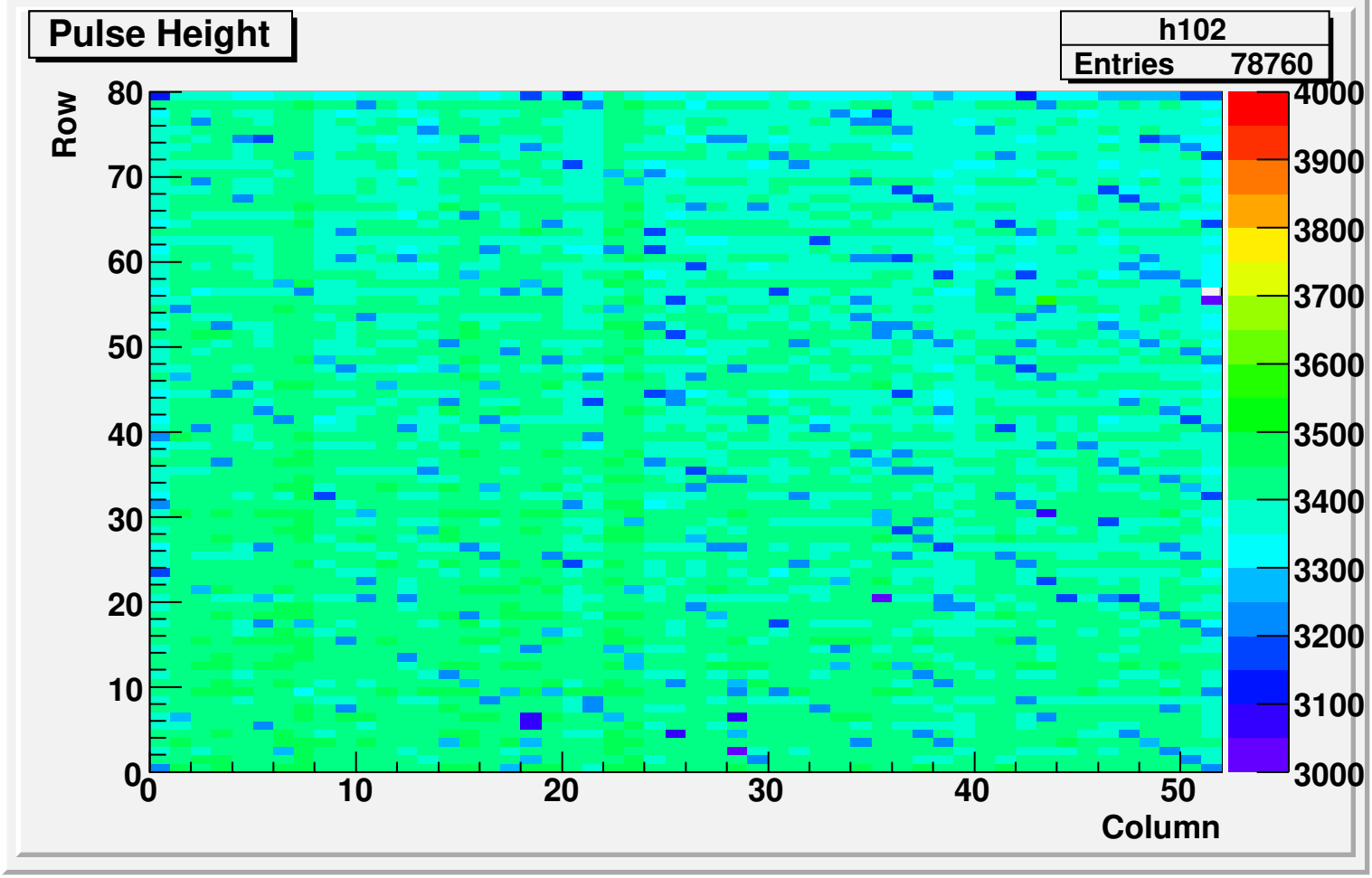
14
16
18
20


Pulse Height
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Entries 78760
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Column
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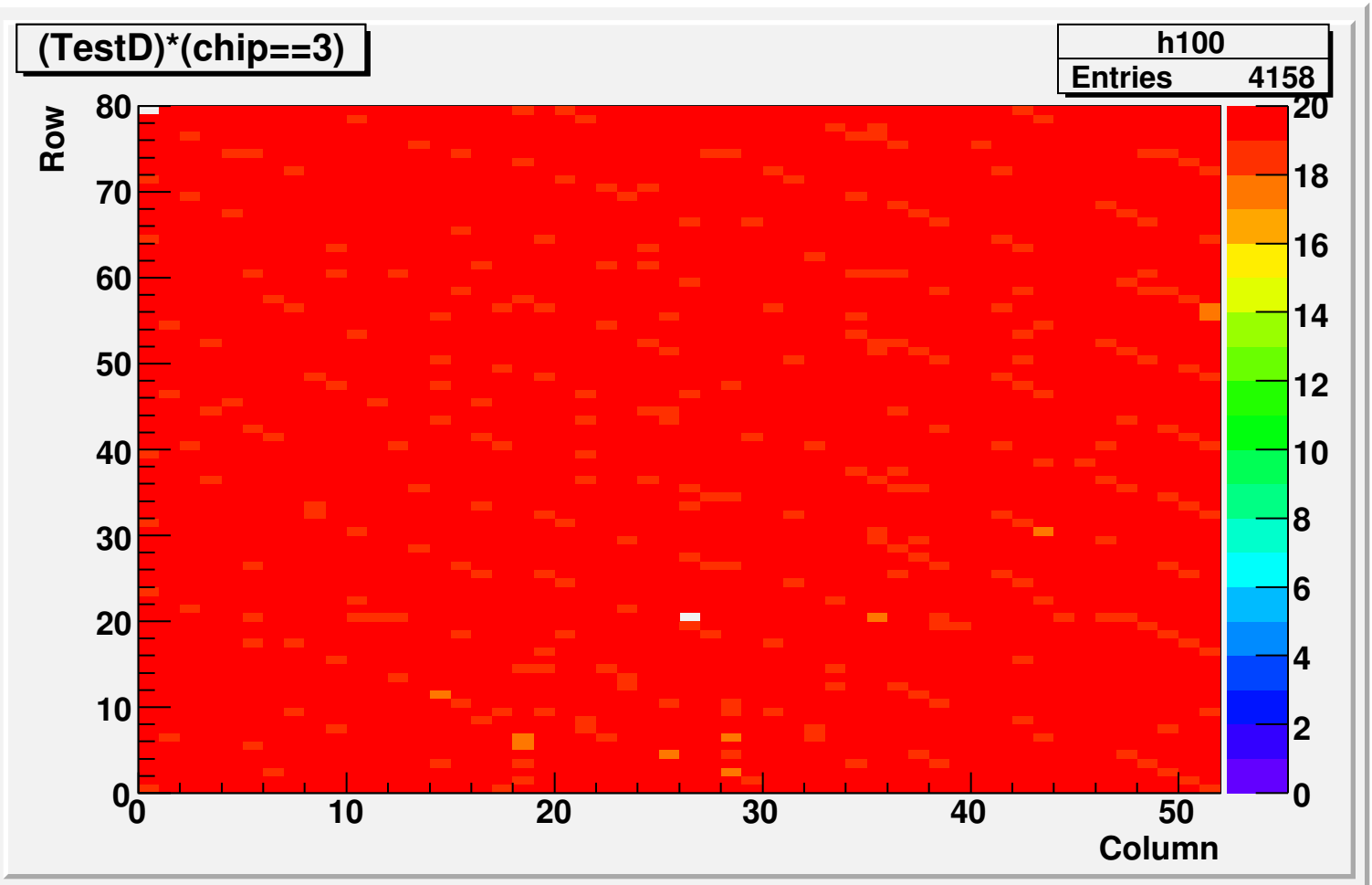
(TestD)*(chip==3)
h100
Entries 4158
Row
Column
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80
0
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14
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18
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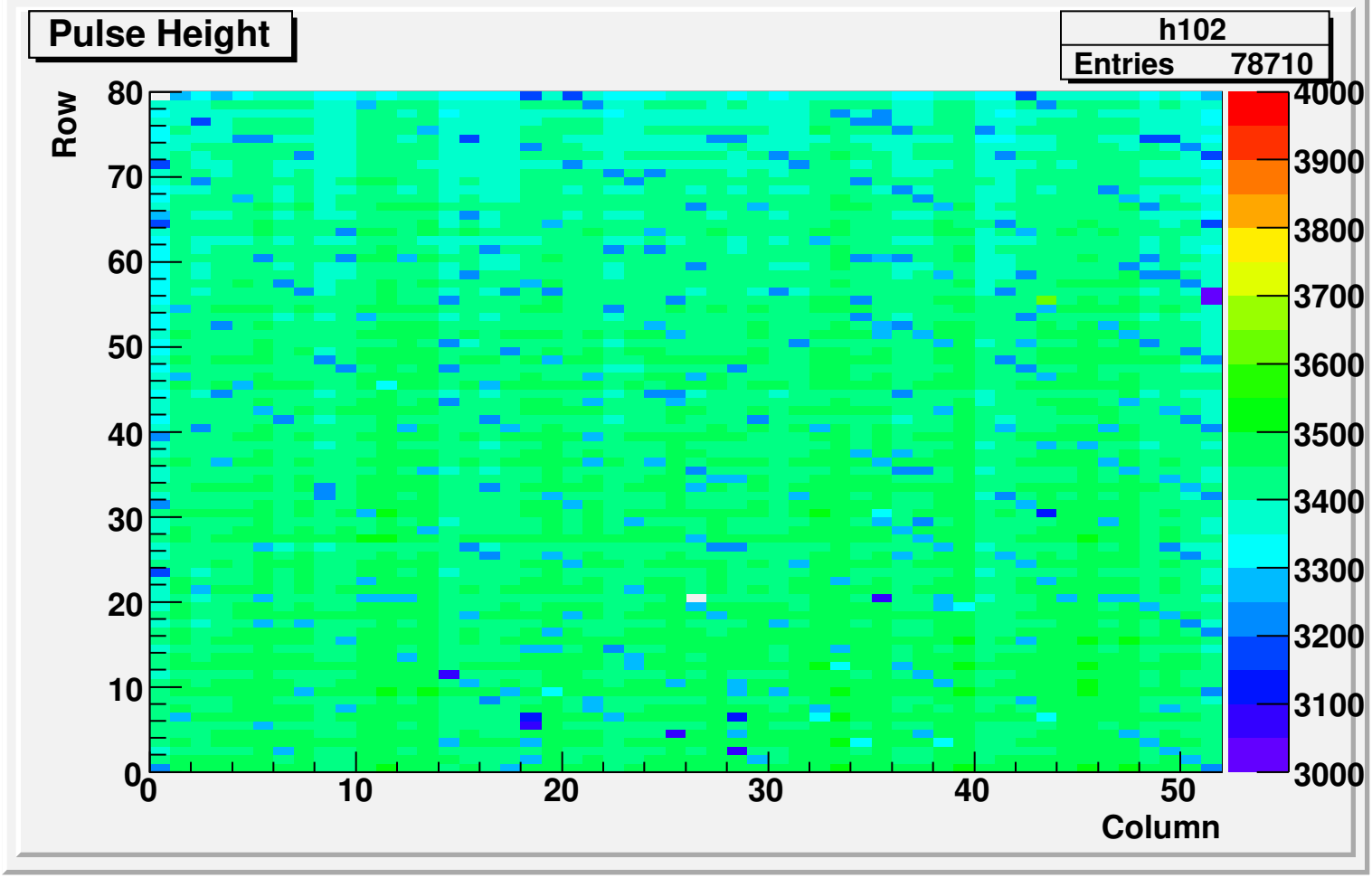
Pulse Height
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Entries 78710
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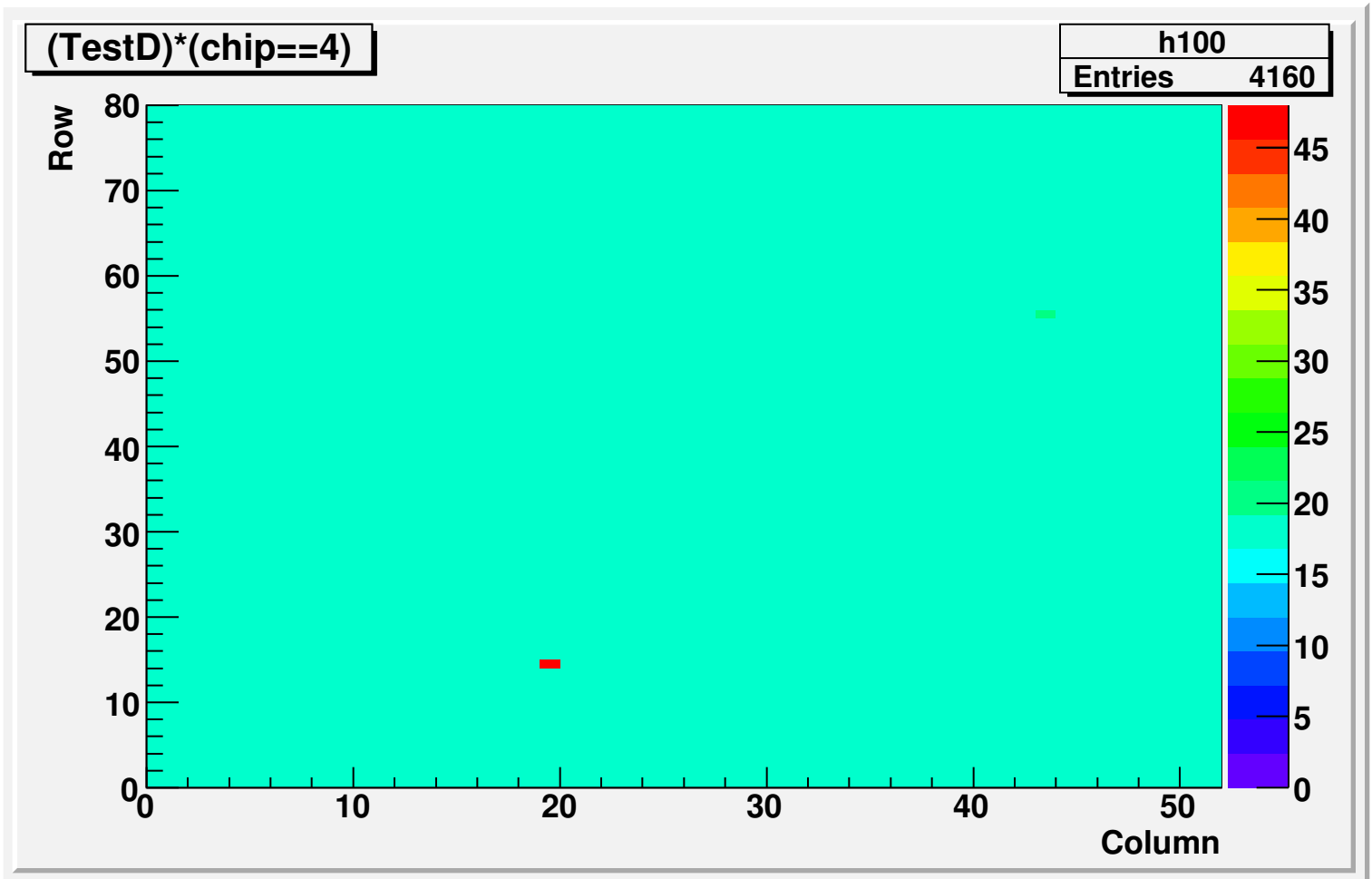
(TestD)*(chip==4)
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Entries 4160
Row
Column
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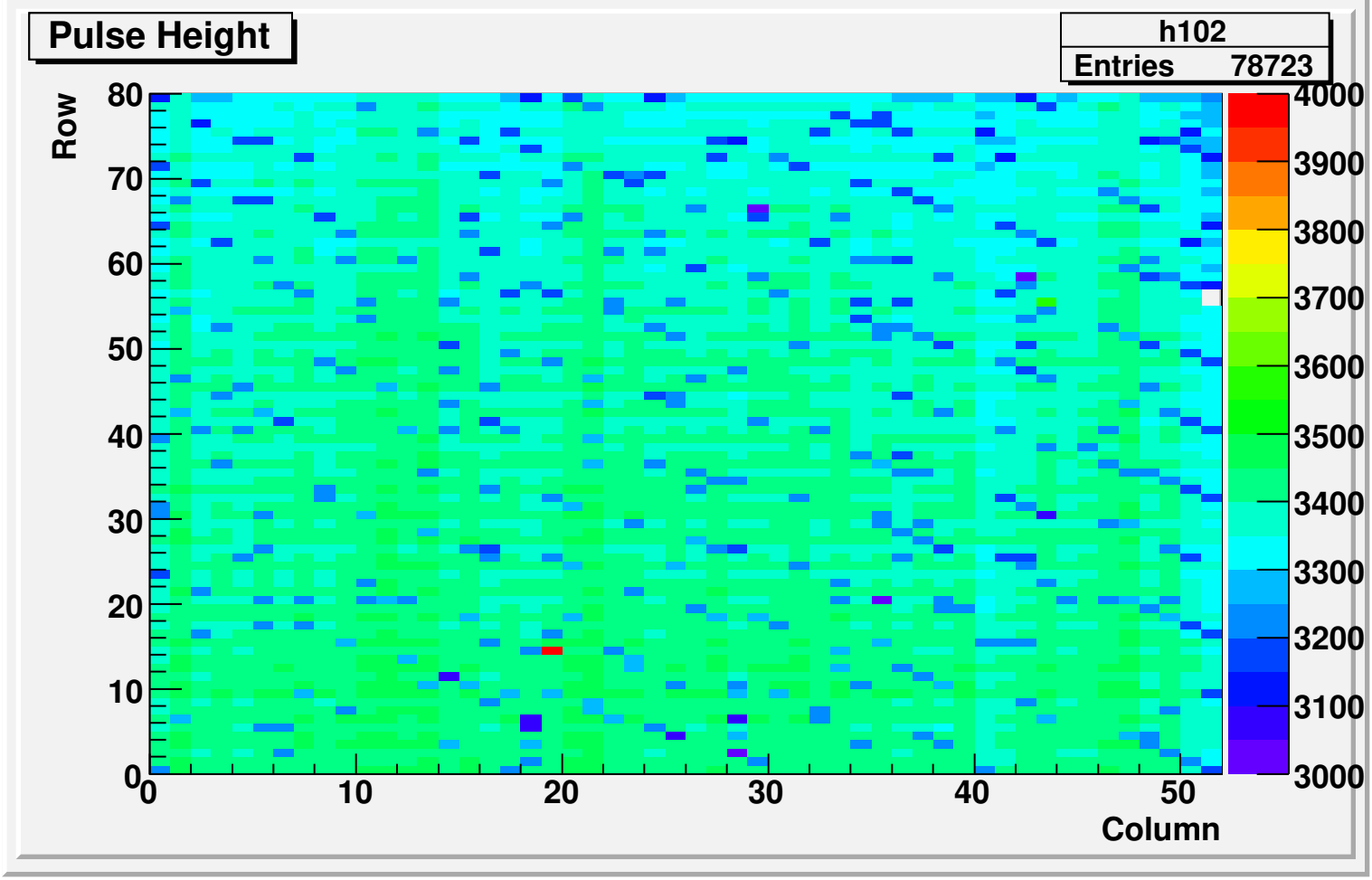
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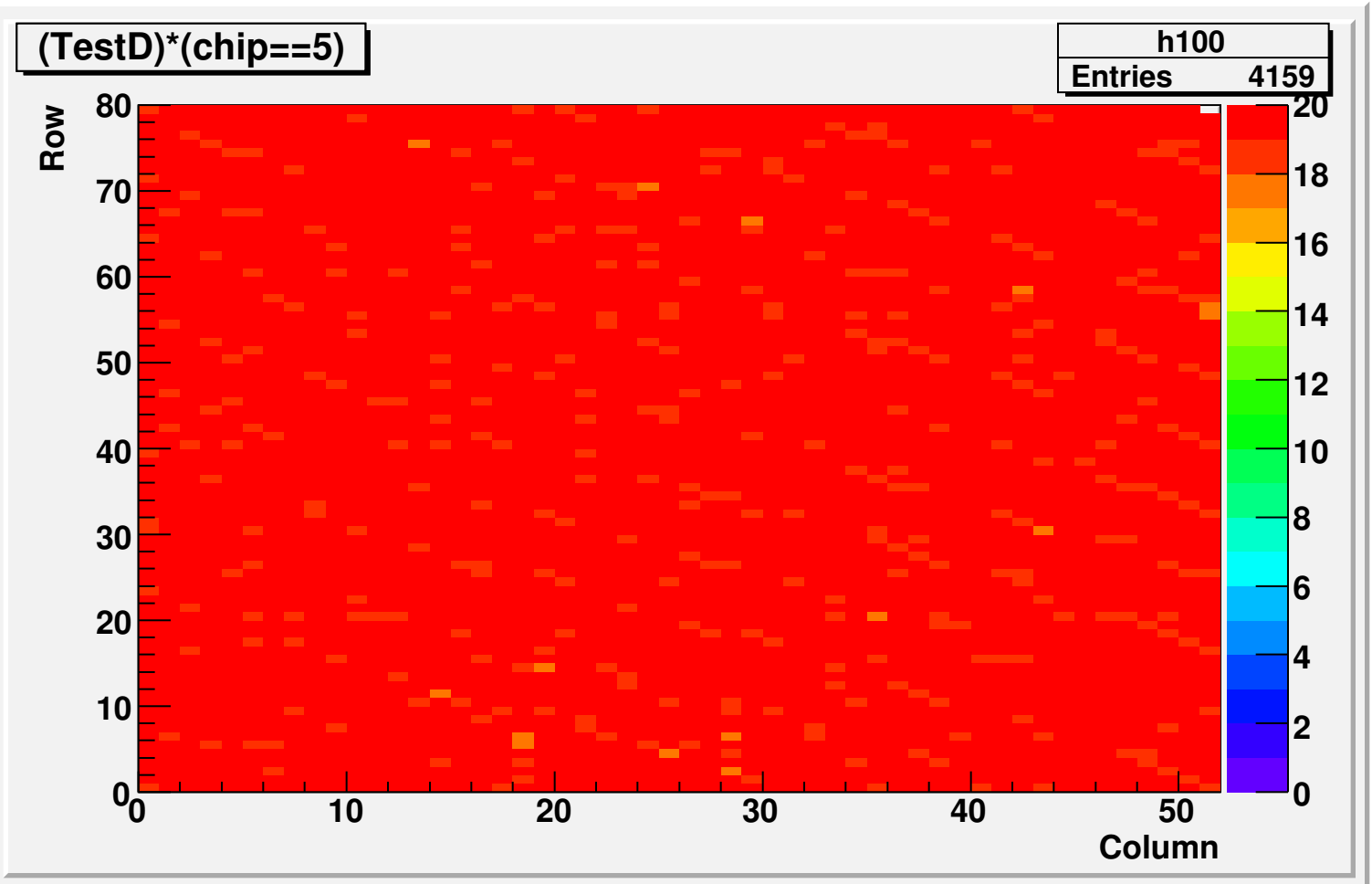
(TestD)*(chip==5)
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Entries 4159
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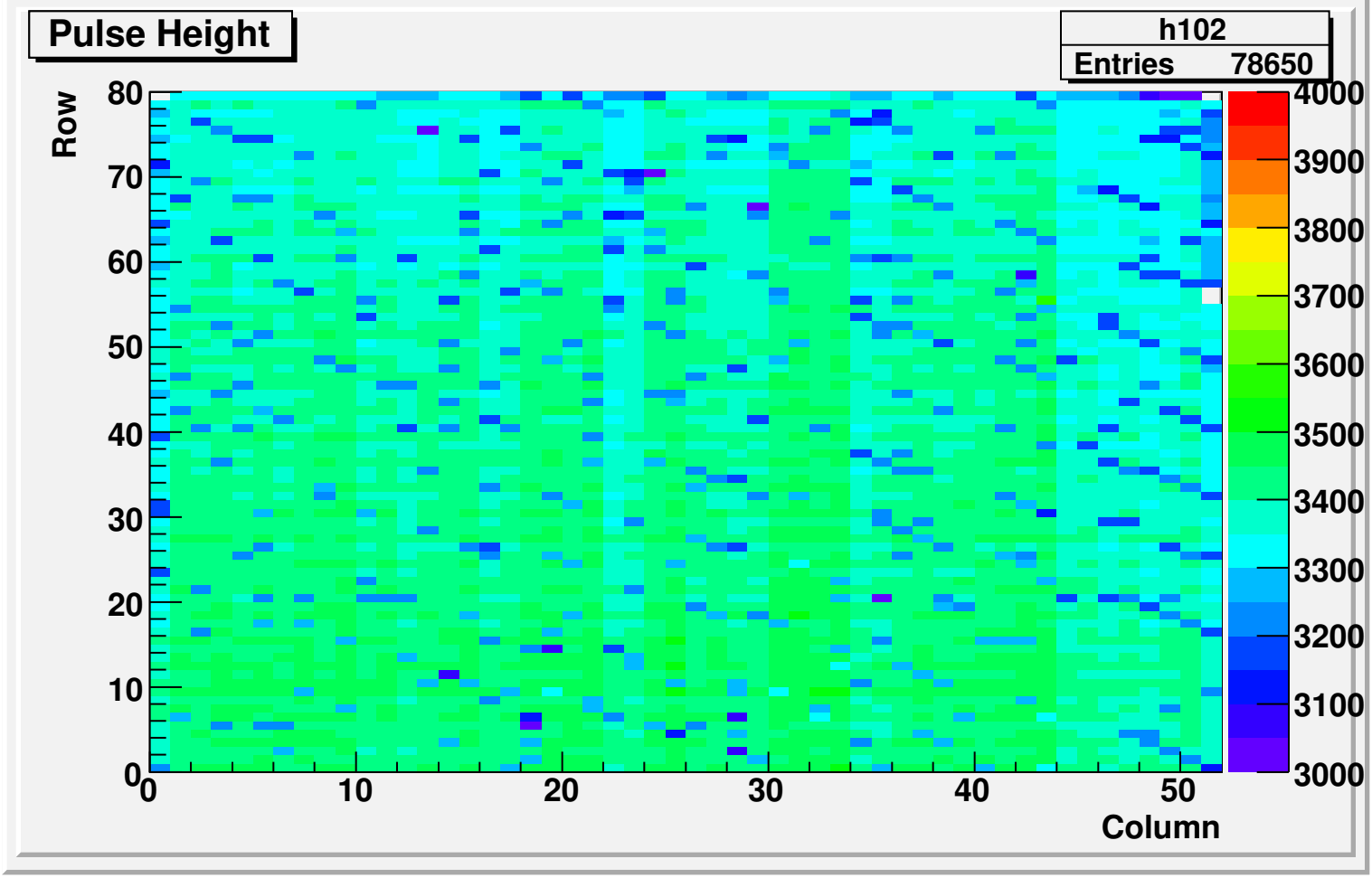
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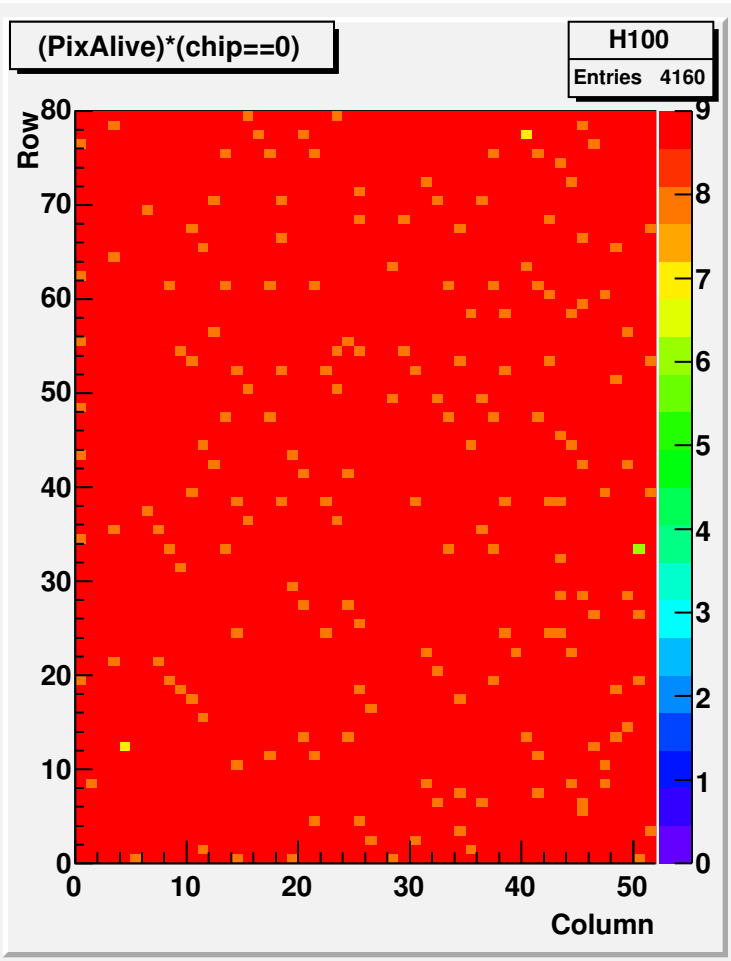
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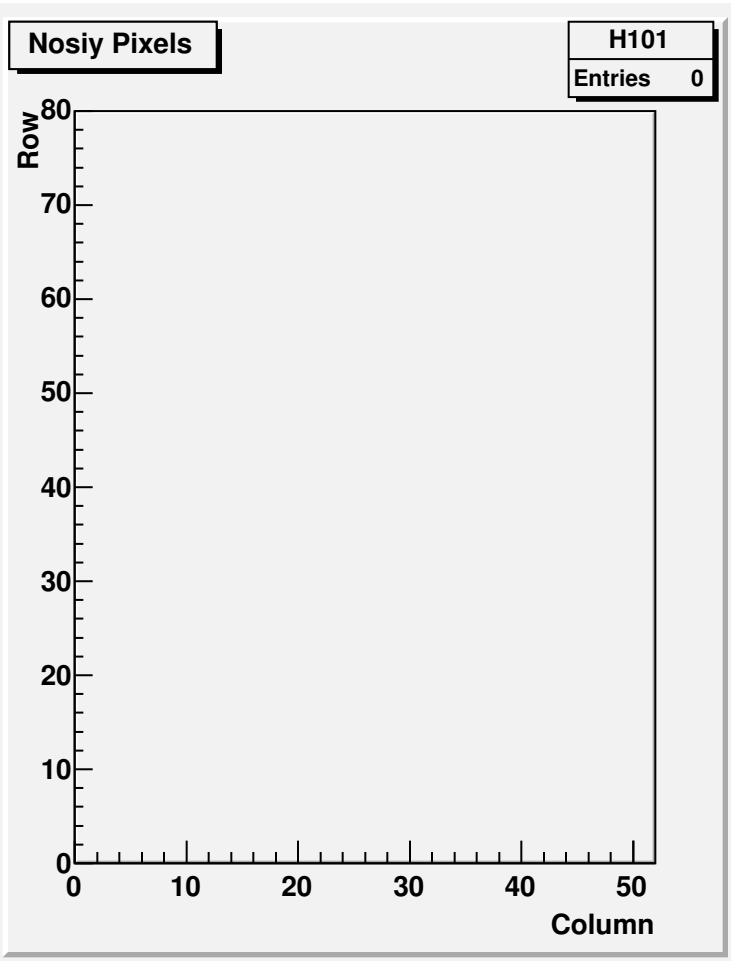
Column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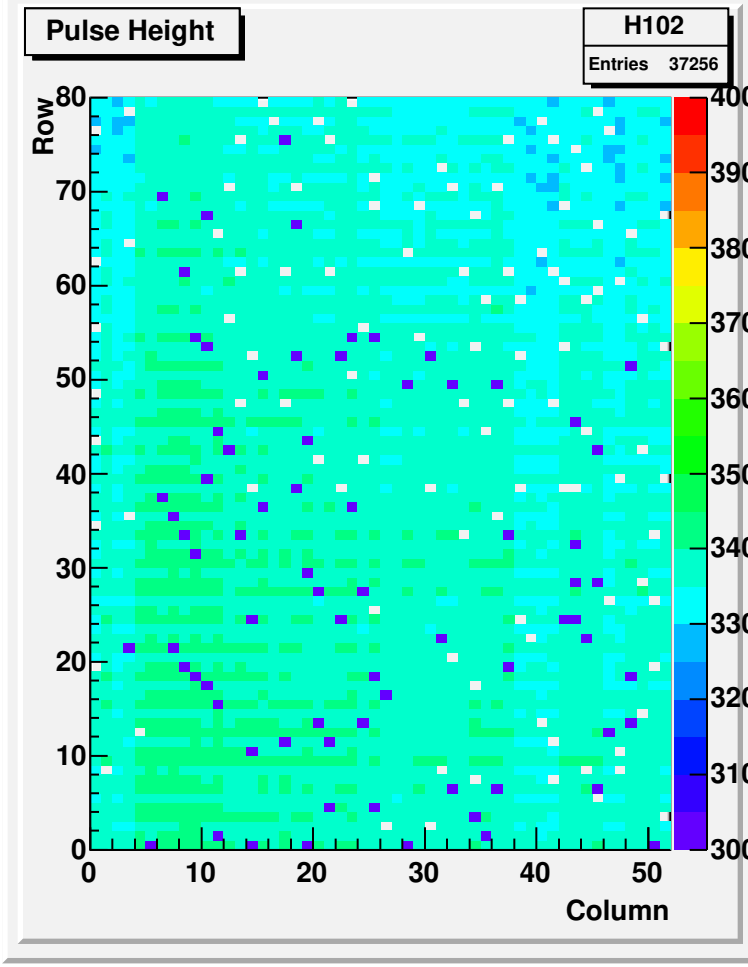
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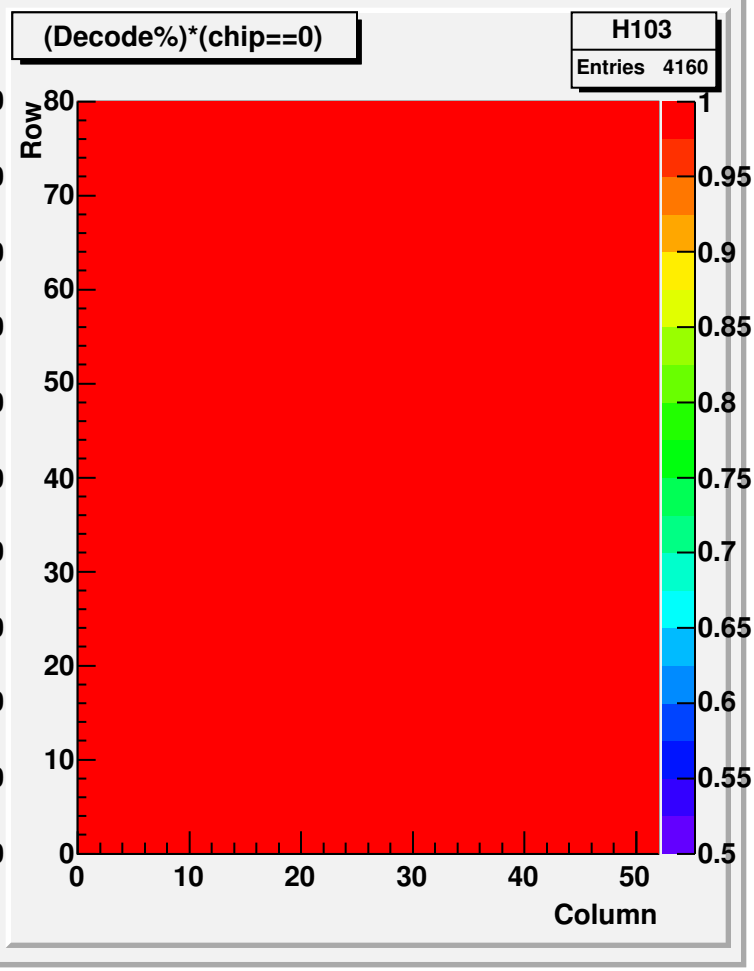
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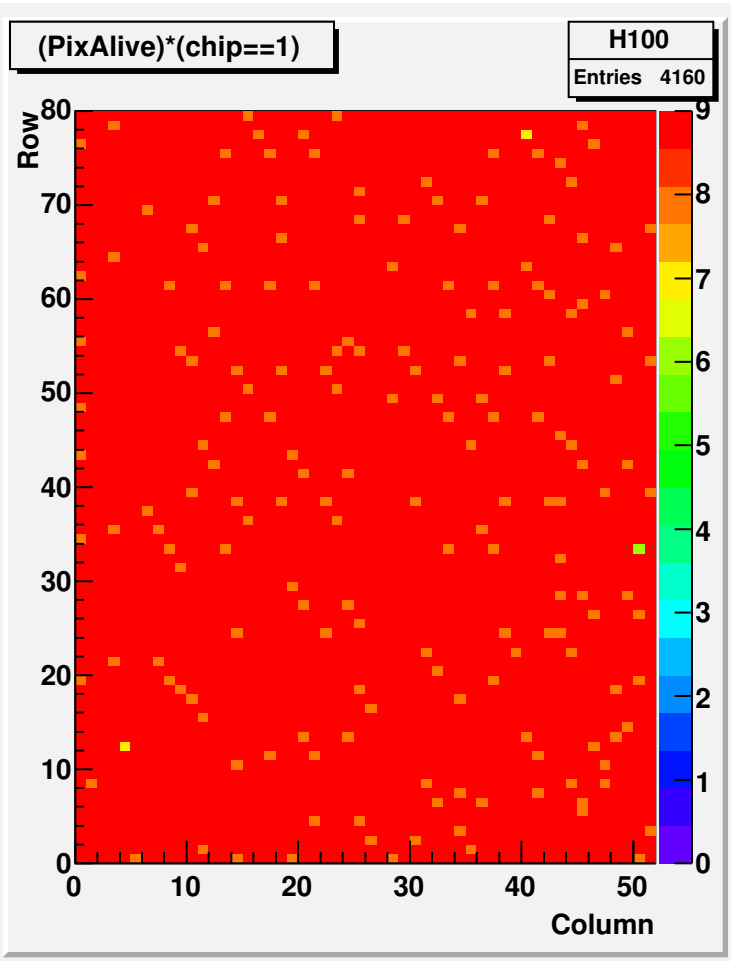
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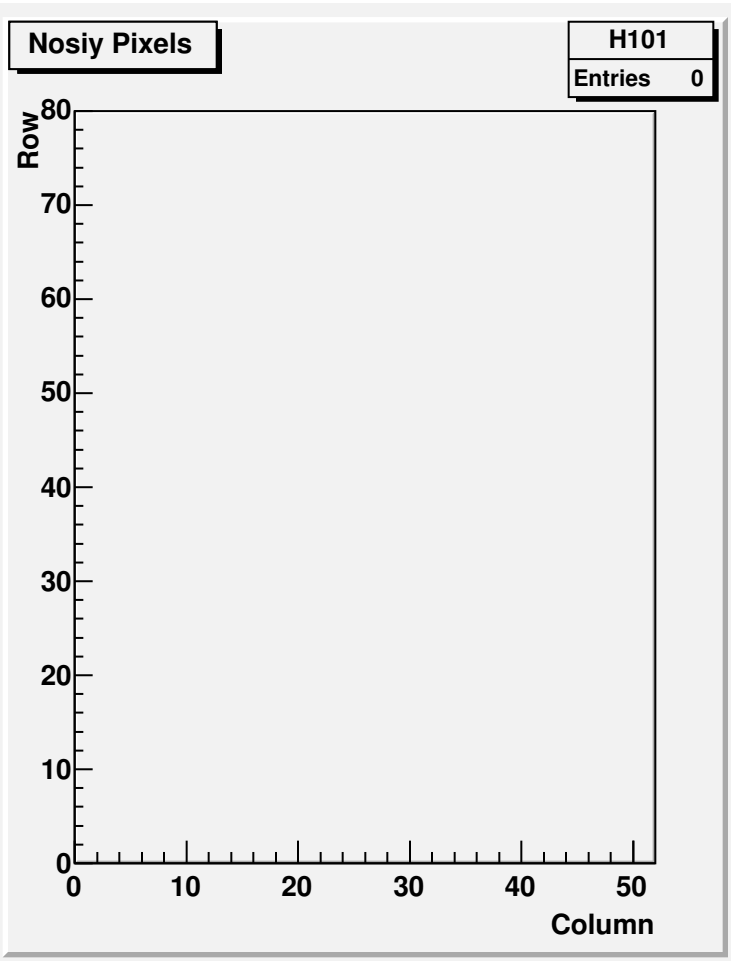
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H101
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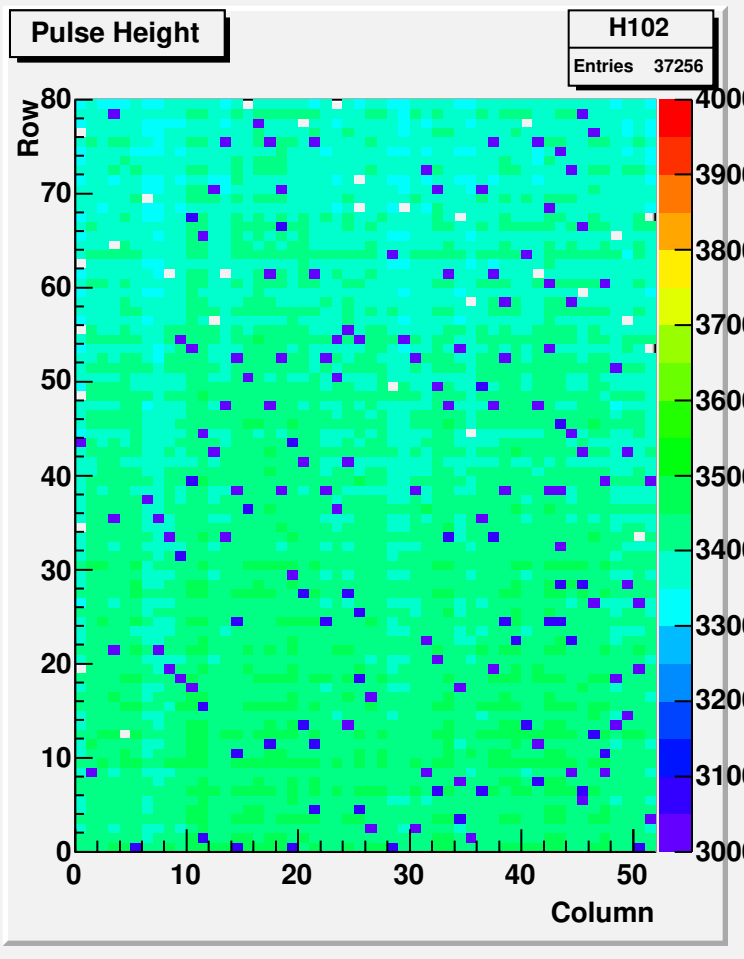
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H102
Entries 37256
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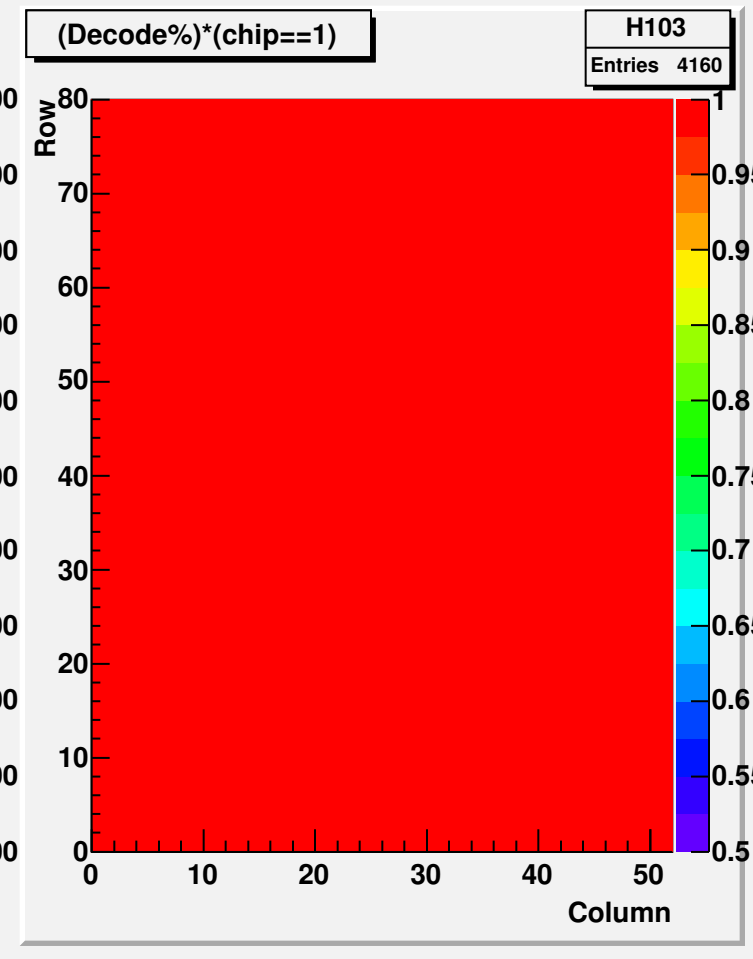
Column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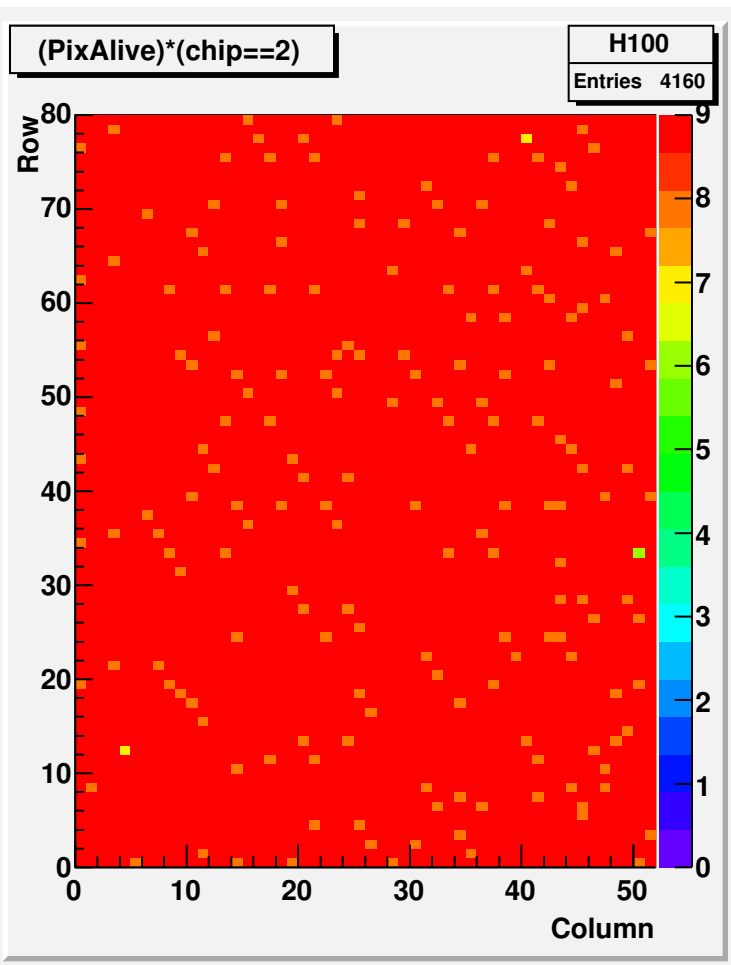
Column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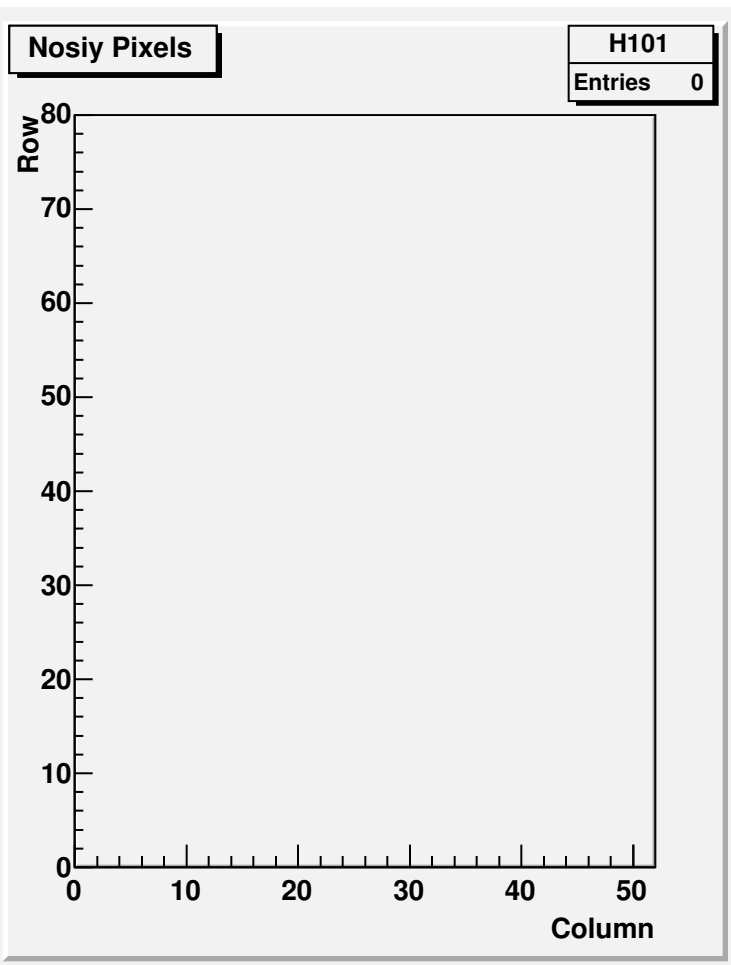
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H101
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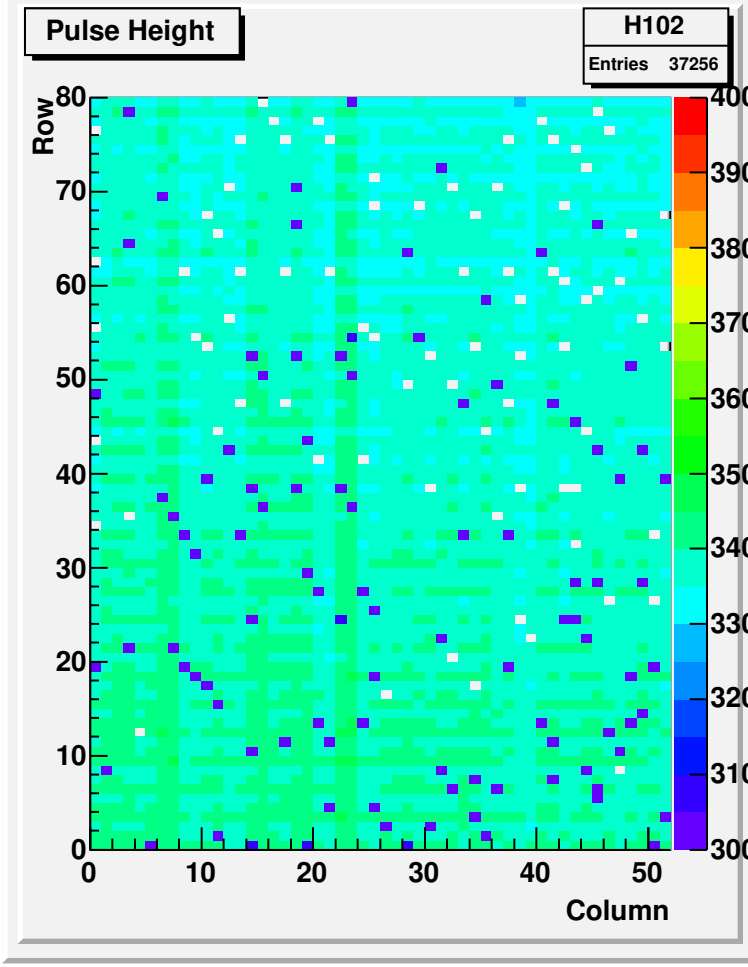
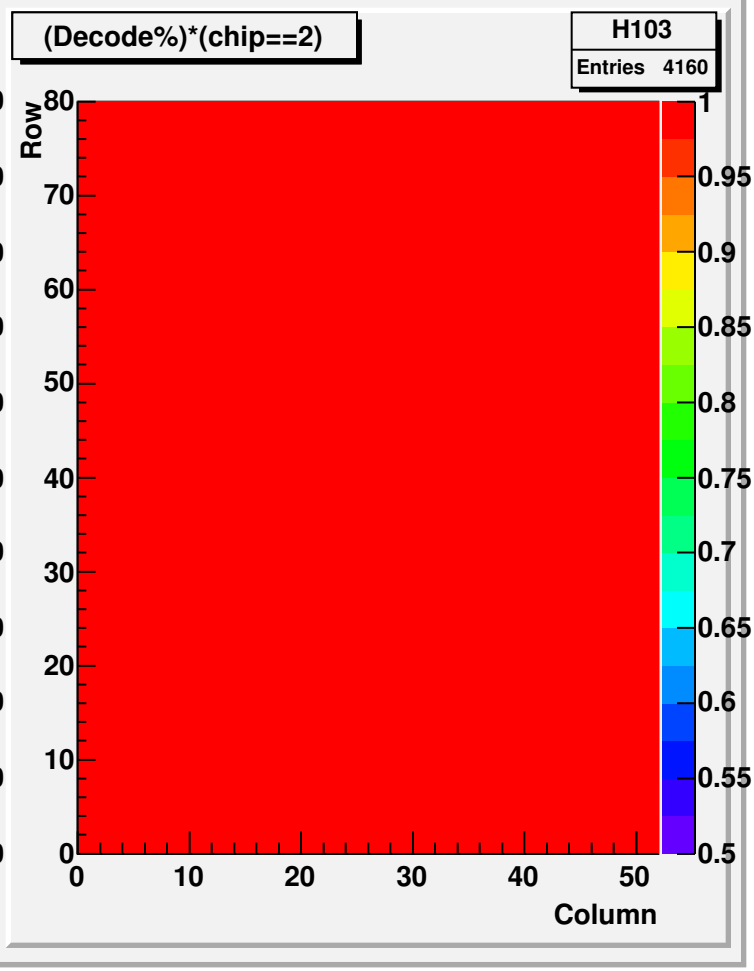

Pulse Height
H102
Entries 37256
Row
Column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H103
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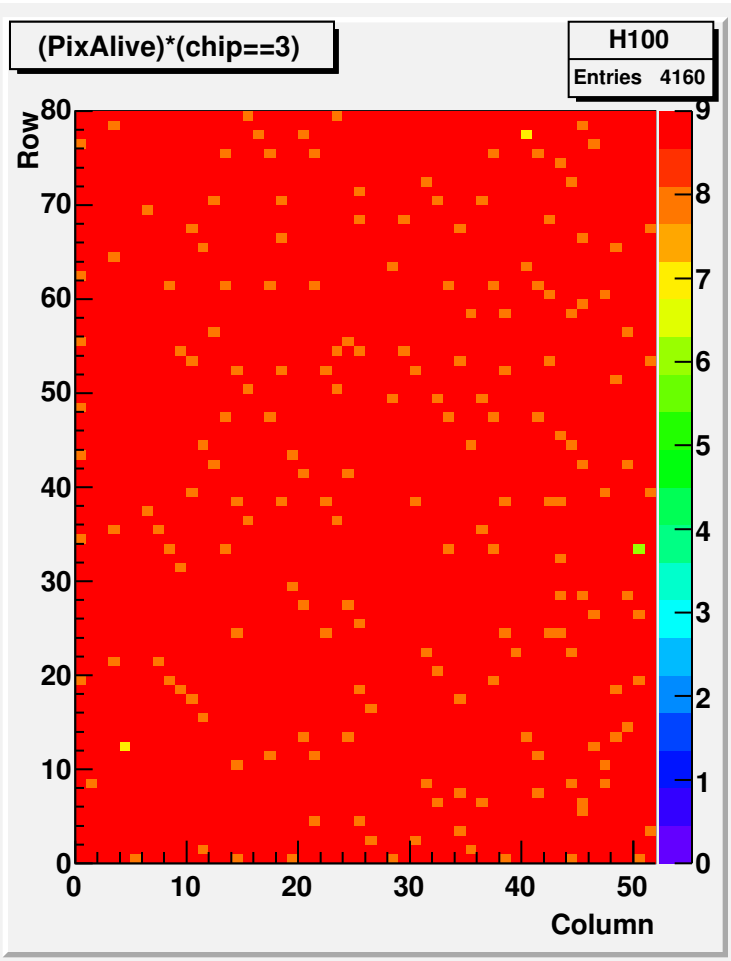
Row
Column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Entries 4160
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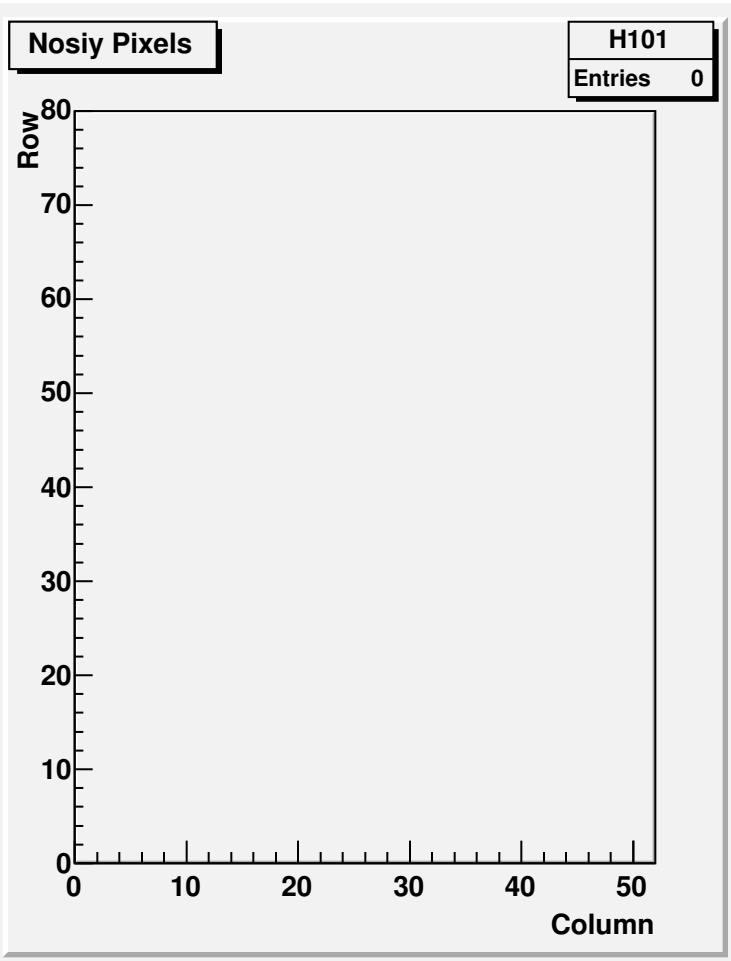
Column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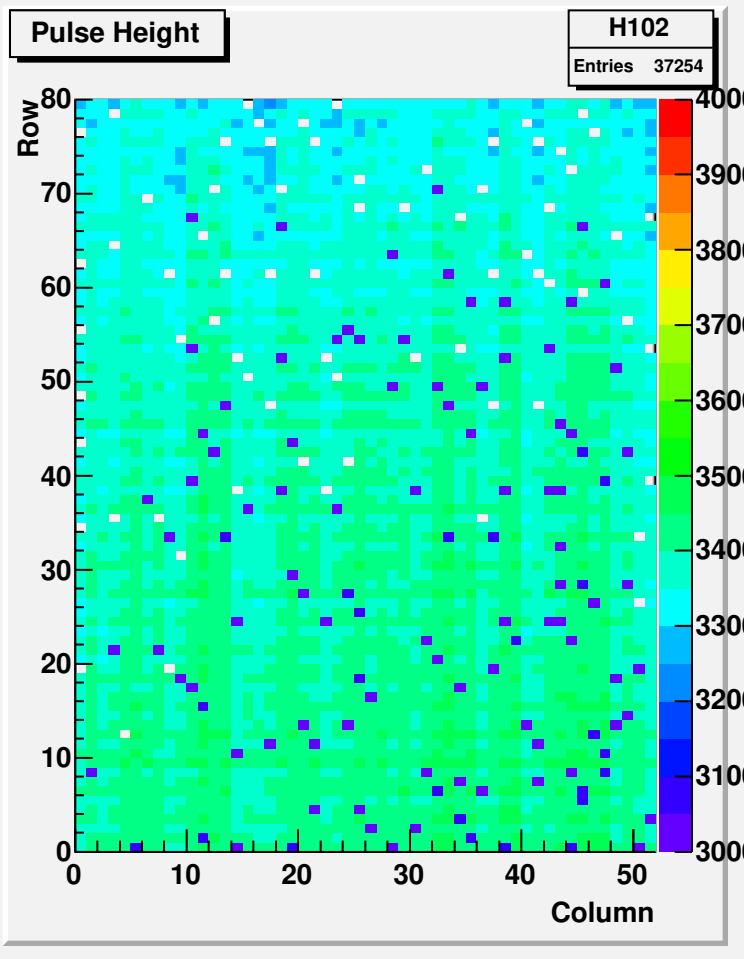
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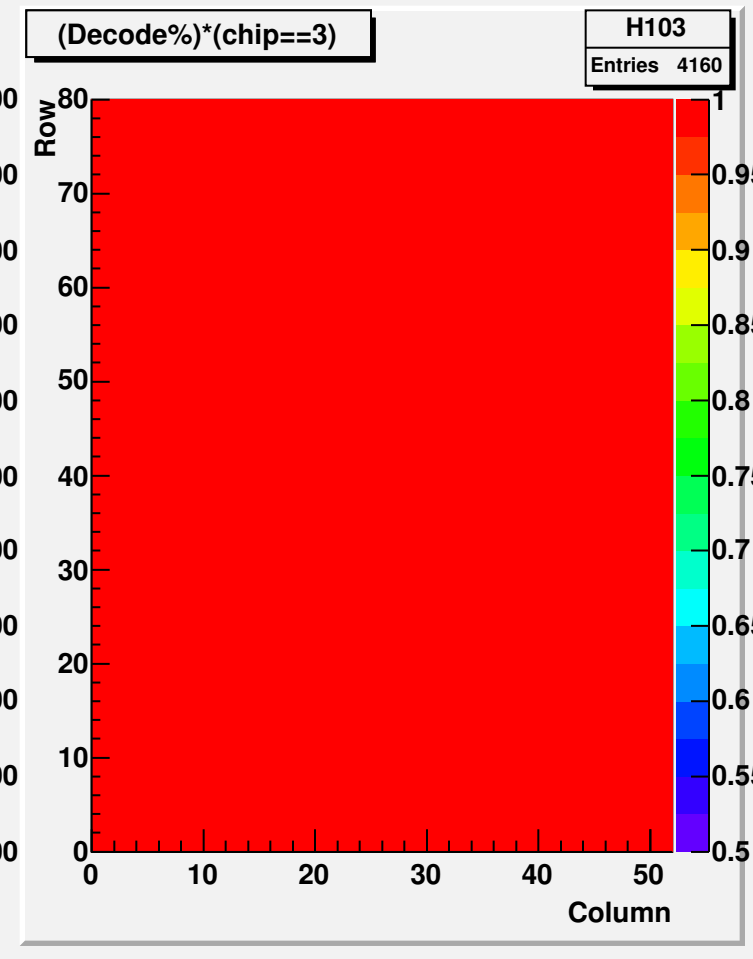
H102
Entries 37254
Row
Column


(Decode%)*(chip==3)
H103
Entries 4160
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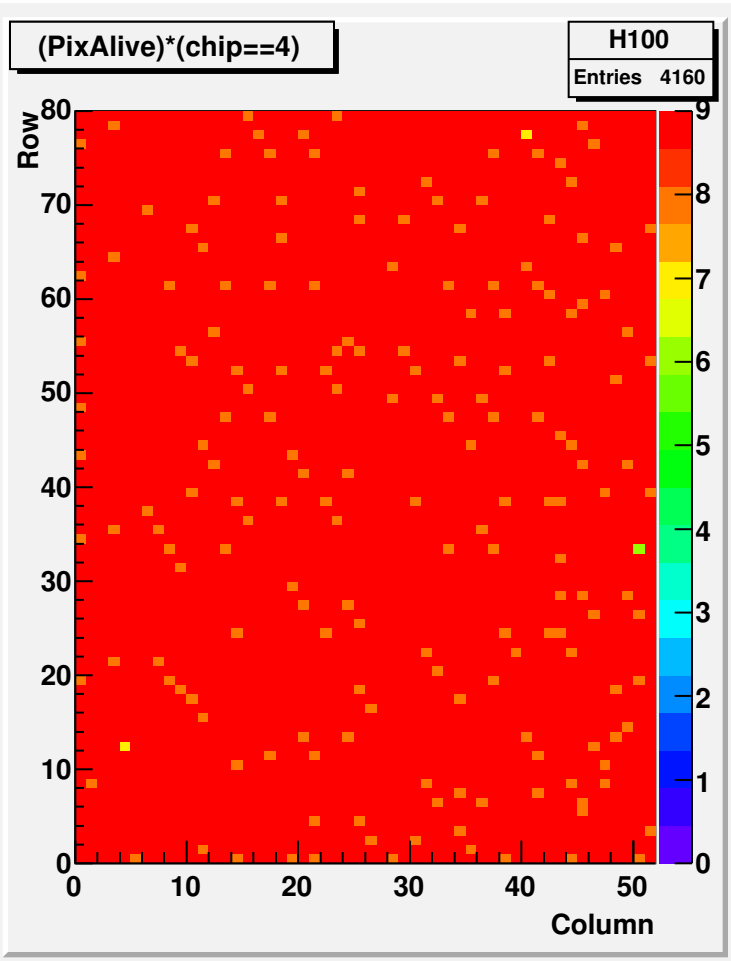
Column


(PixAlive)*(chip==4)
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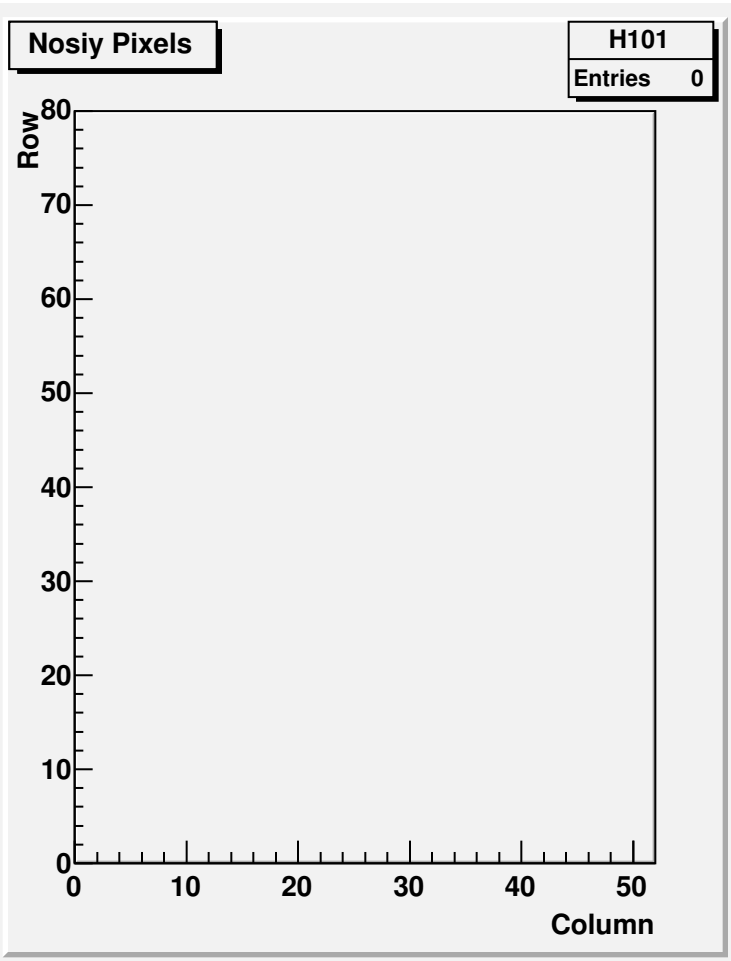
Column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H101
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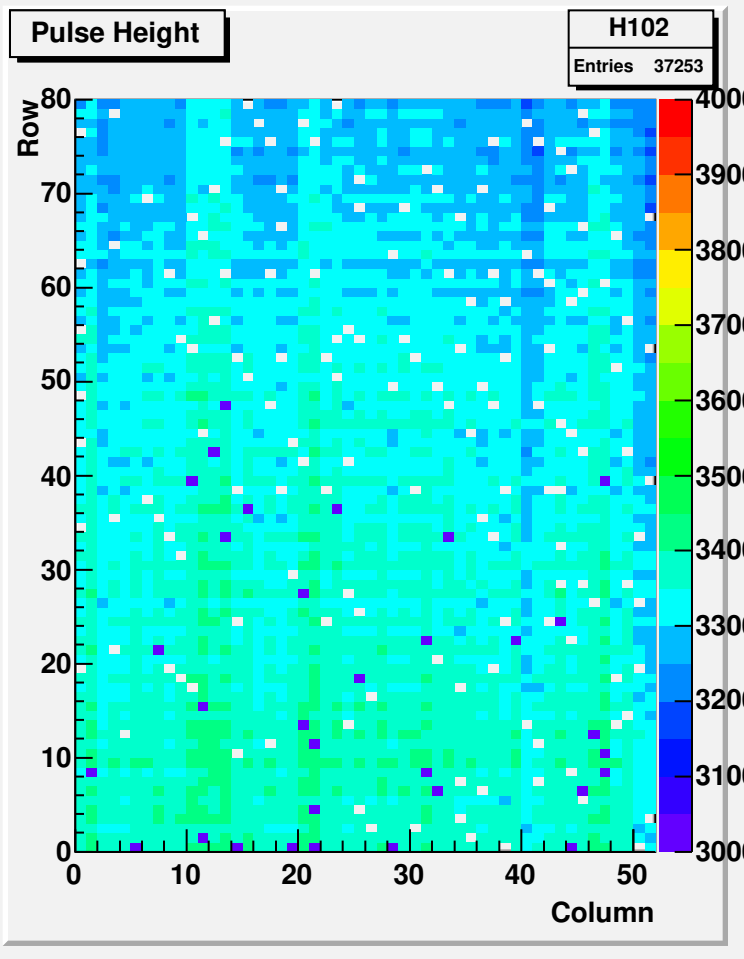
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H102
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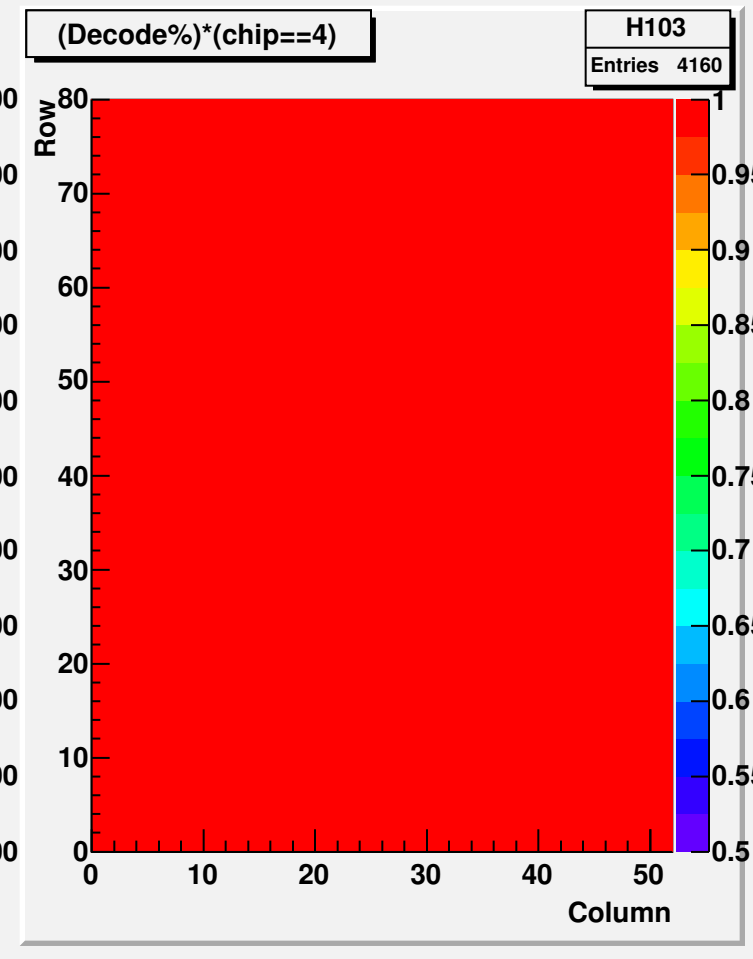
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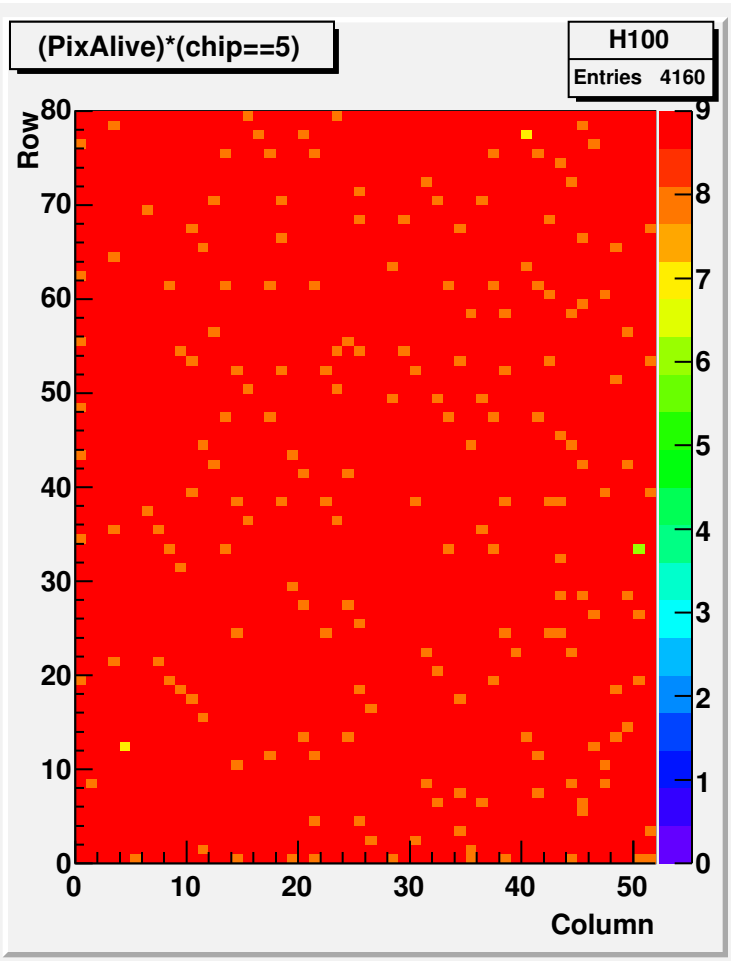
Column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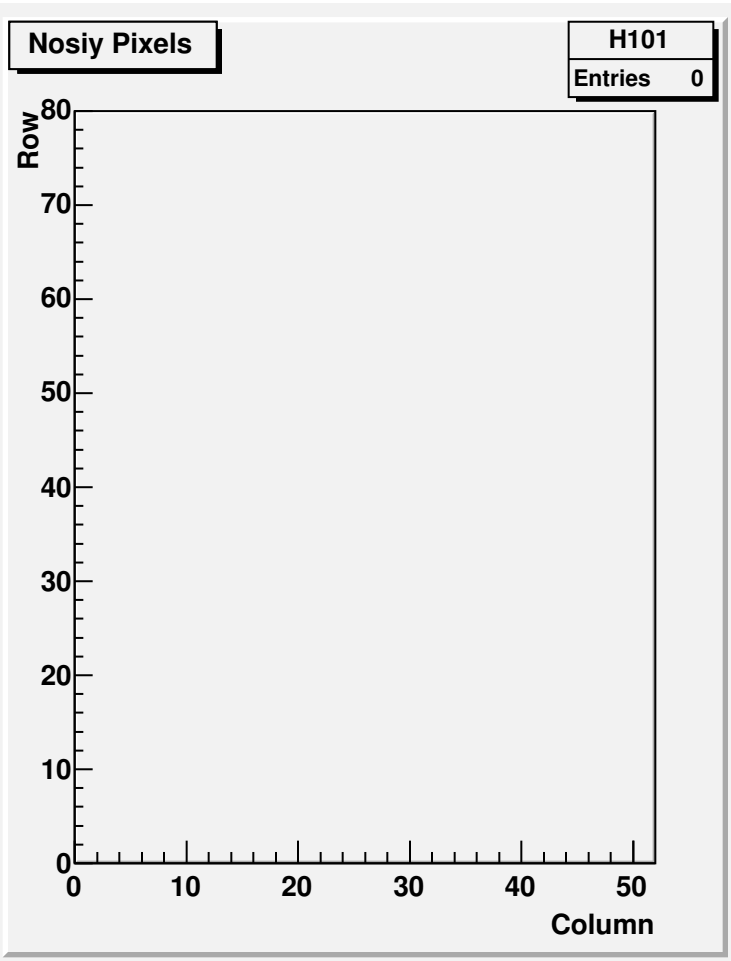
Column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H101
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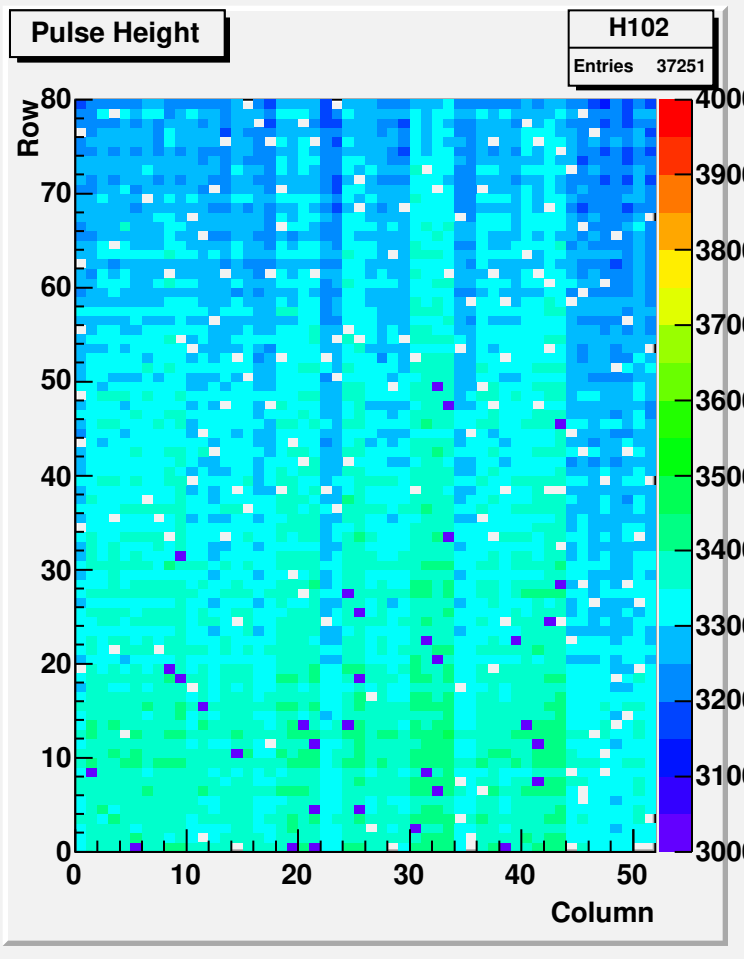
Row
Column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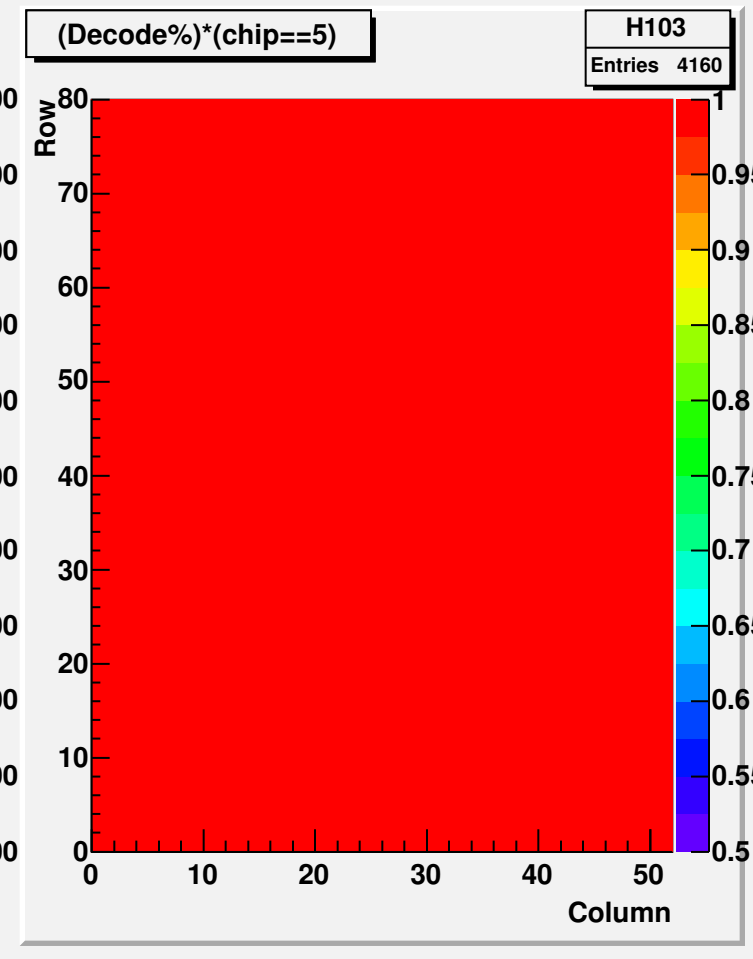
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Entries 4160
Row
Column