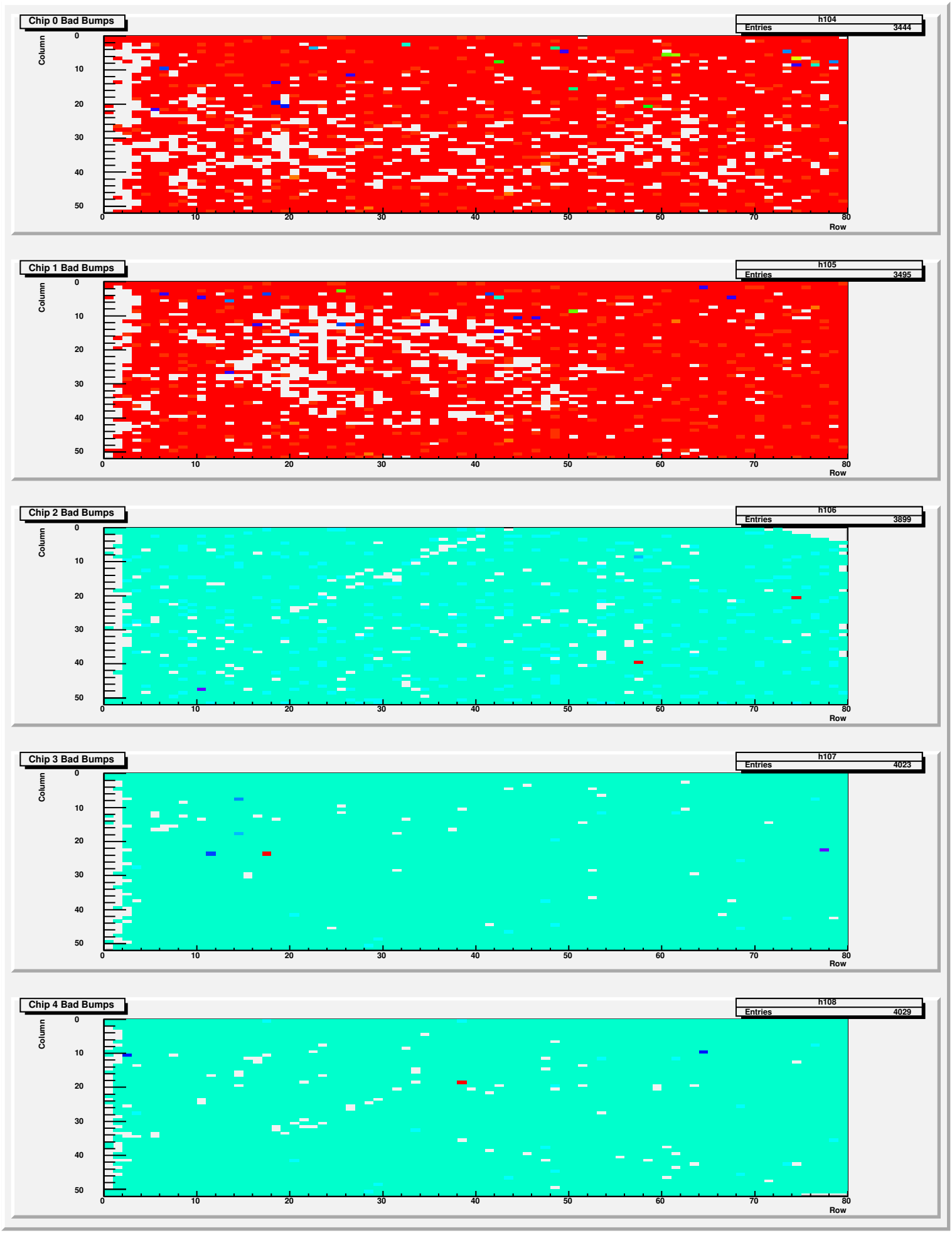
Chip 0 Bad Bumps
h104
Entries 3444
Chip 1 Bad Bumps
h105
Entries 3495
Chip 2 Bad Bumps
h106
Entries 3899
Chip 3 Bad Bumps
h107
Entries 4023
Chip 4 Bad Bumps
h108
Entries 4029
Column
Row
0
10
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70
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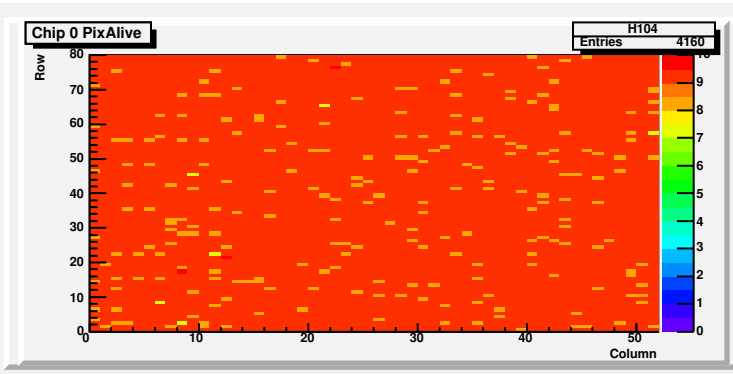
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H104
Entries 4160
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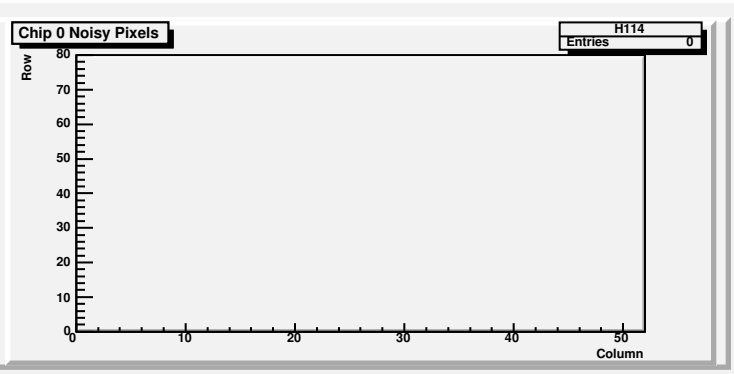
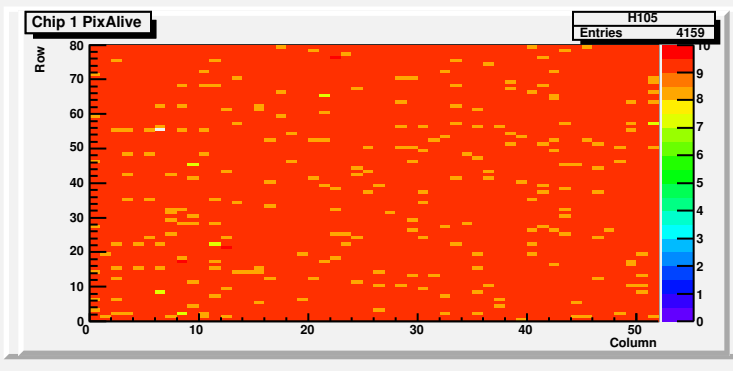
Column


Chip 0 Noisy Pixels
H114
Entries 0
Row
Column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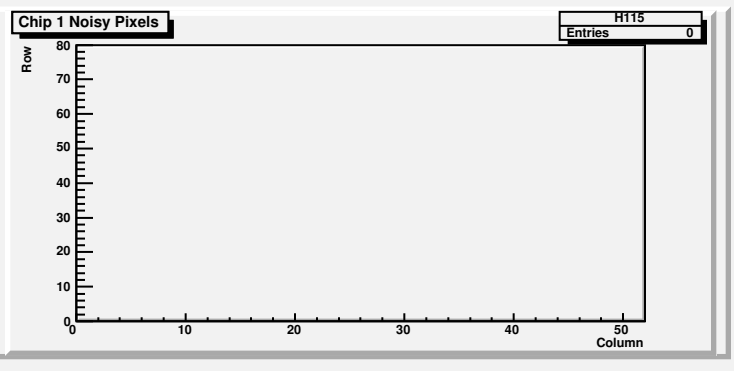
H105
Entries 4159
Row
Column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Entries 0
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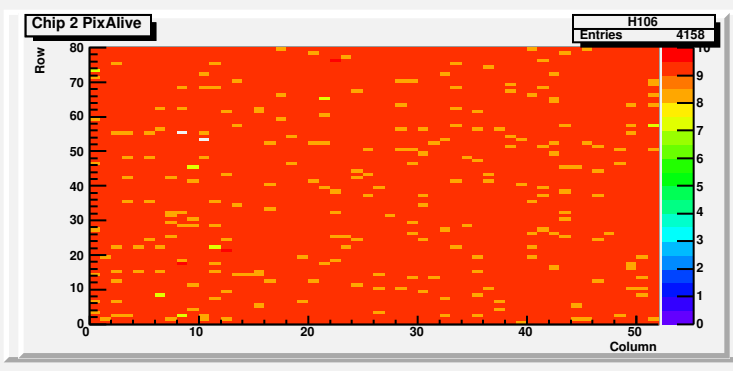
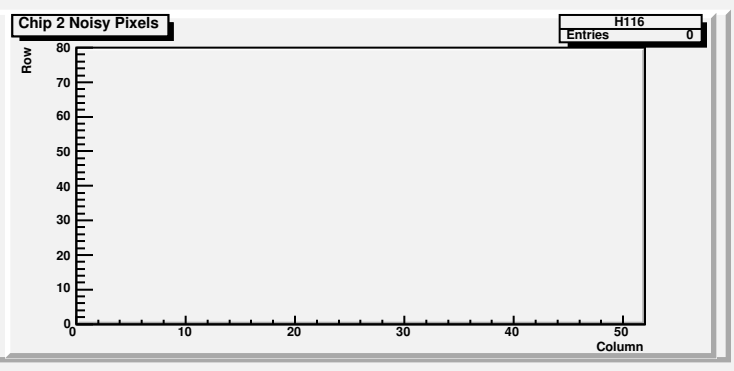

Chip 2 PixAlive
H106
Entries 4158
Row
Column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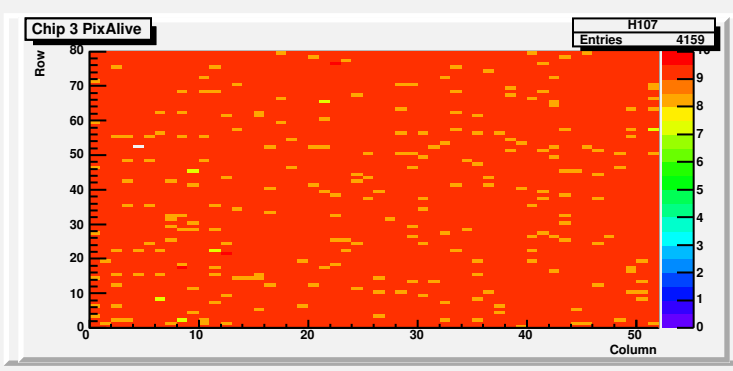
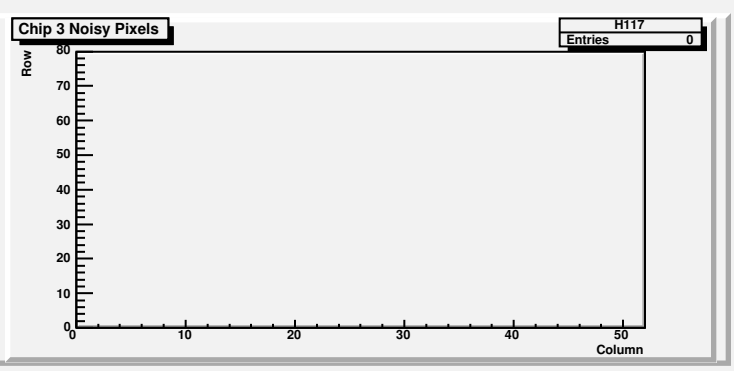
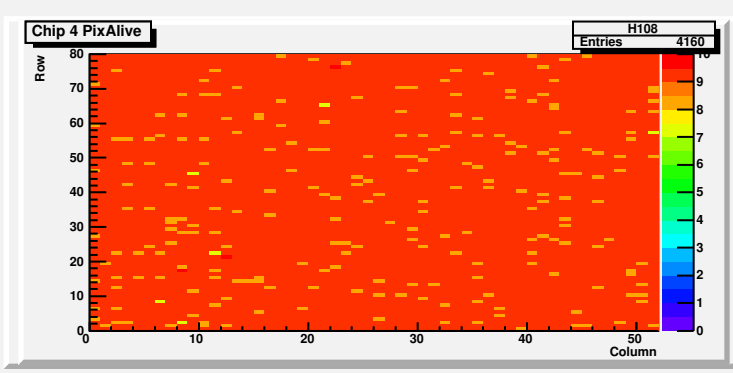
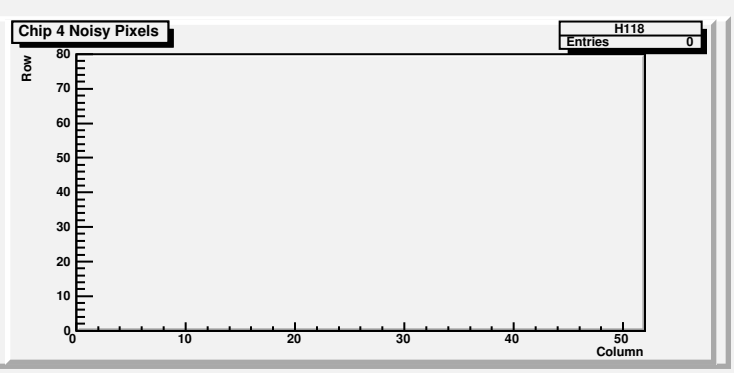
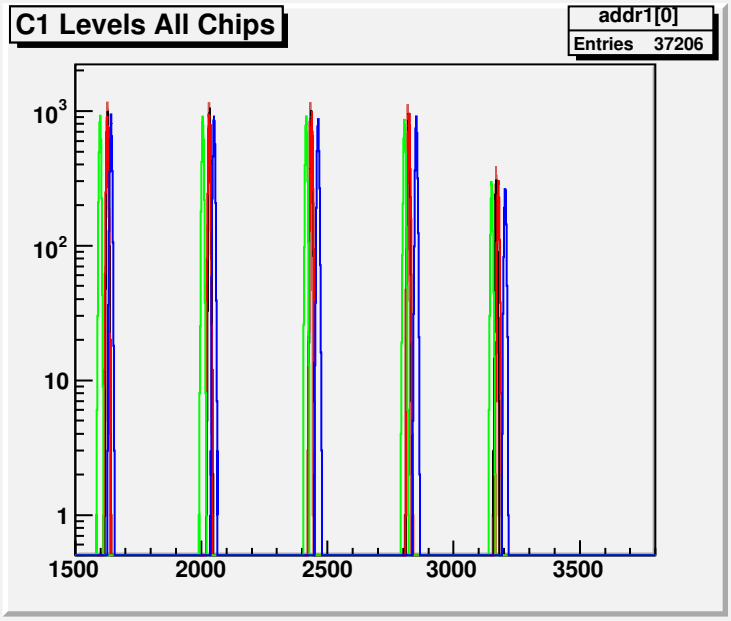
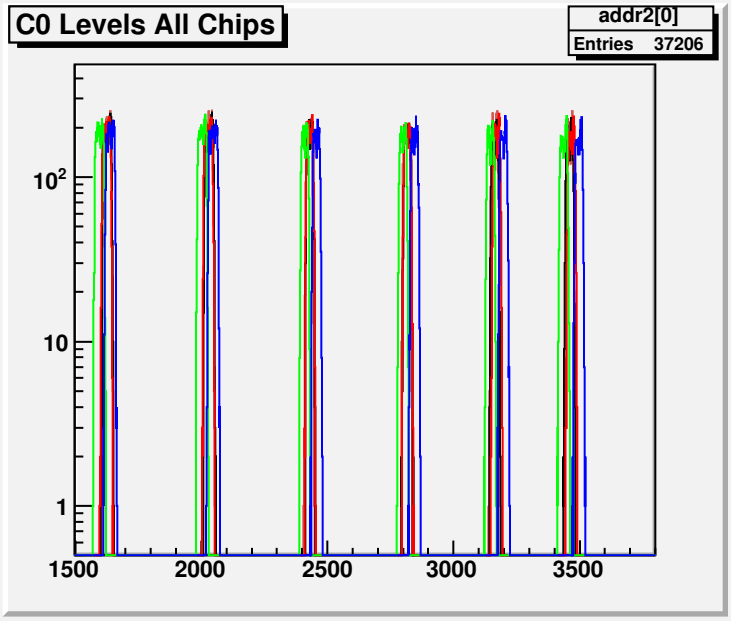

Chip 3 PixAlive
H107
Entries 4159
Row
Column

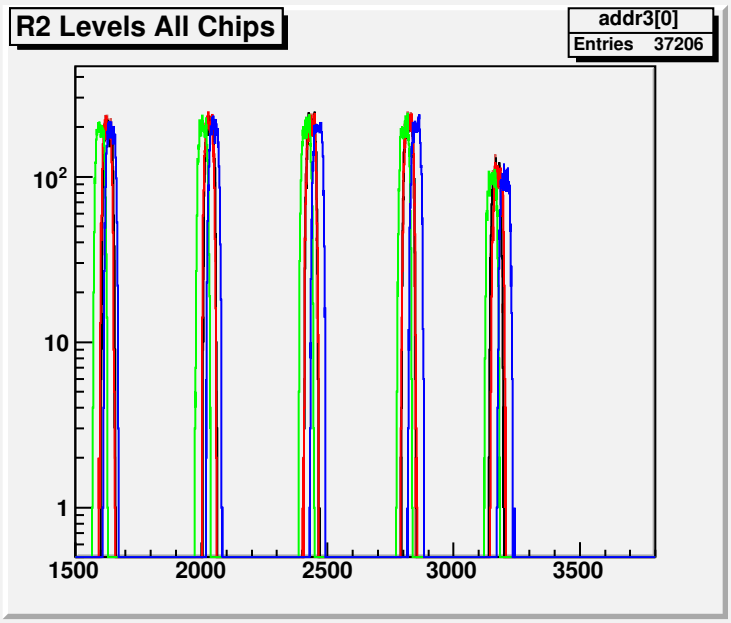

Chip 3 Noisy Pixels
H117
Entries 0
Row
Column

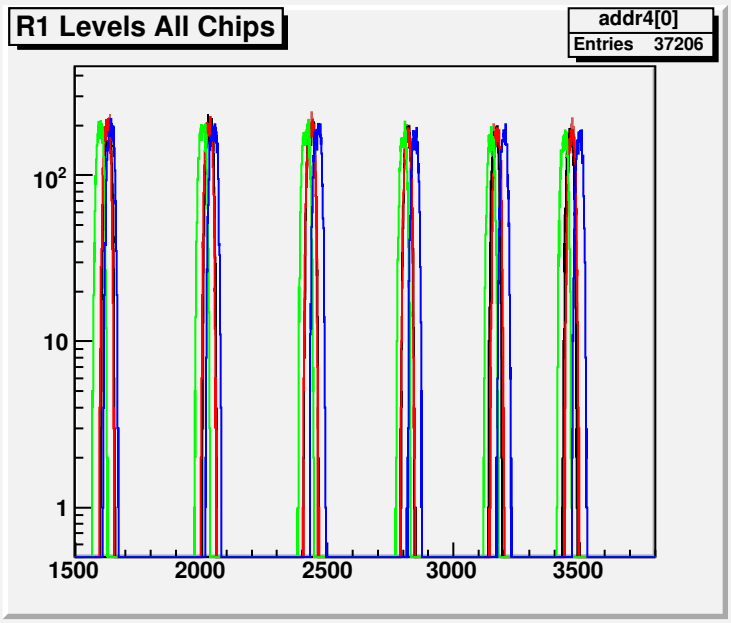

Chip 4 PixAlive
H108
Entries 4160
Row
Column


Chip 4 Noisy Pixels
H118
Entries 0
Row
Column


C1 Levels All Chips
addr1[0]
Entries 37206
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37206
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37206
10^2
10
1
1500
2000
2500
3000
3500


R1 Levels All Chips
addr4[0]
Entries 37206
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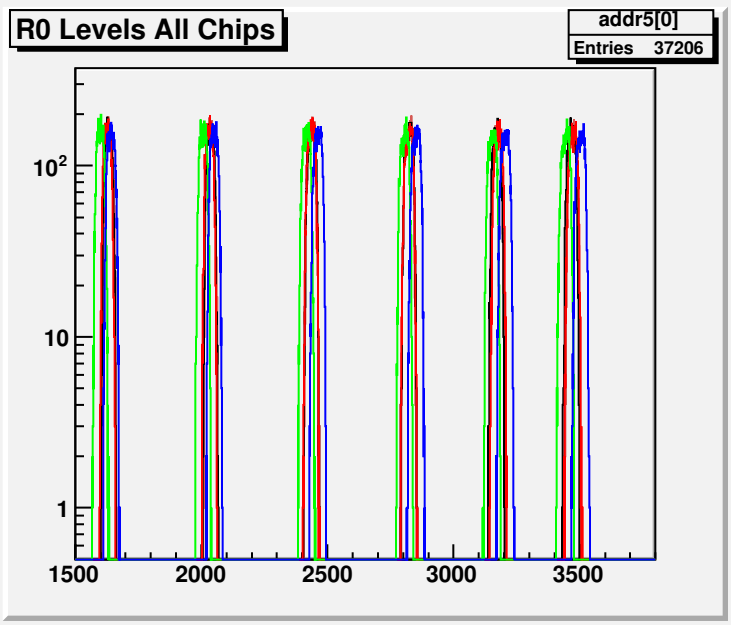
10
1
1500
2000
2500
3000
3500


R0 Levels All Chips
addr5[0]
Entries 37206
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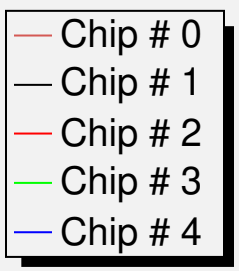
10
1
1500
2000
2500
3000
3500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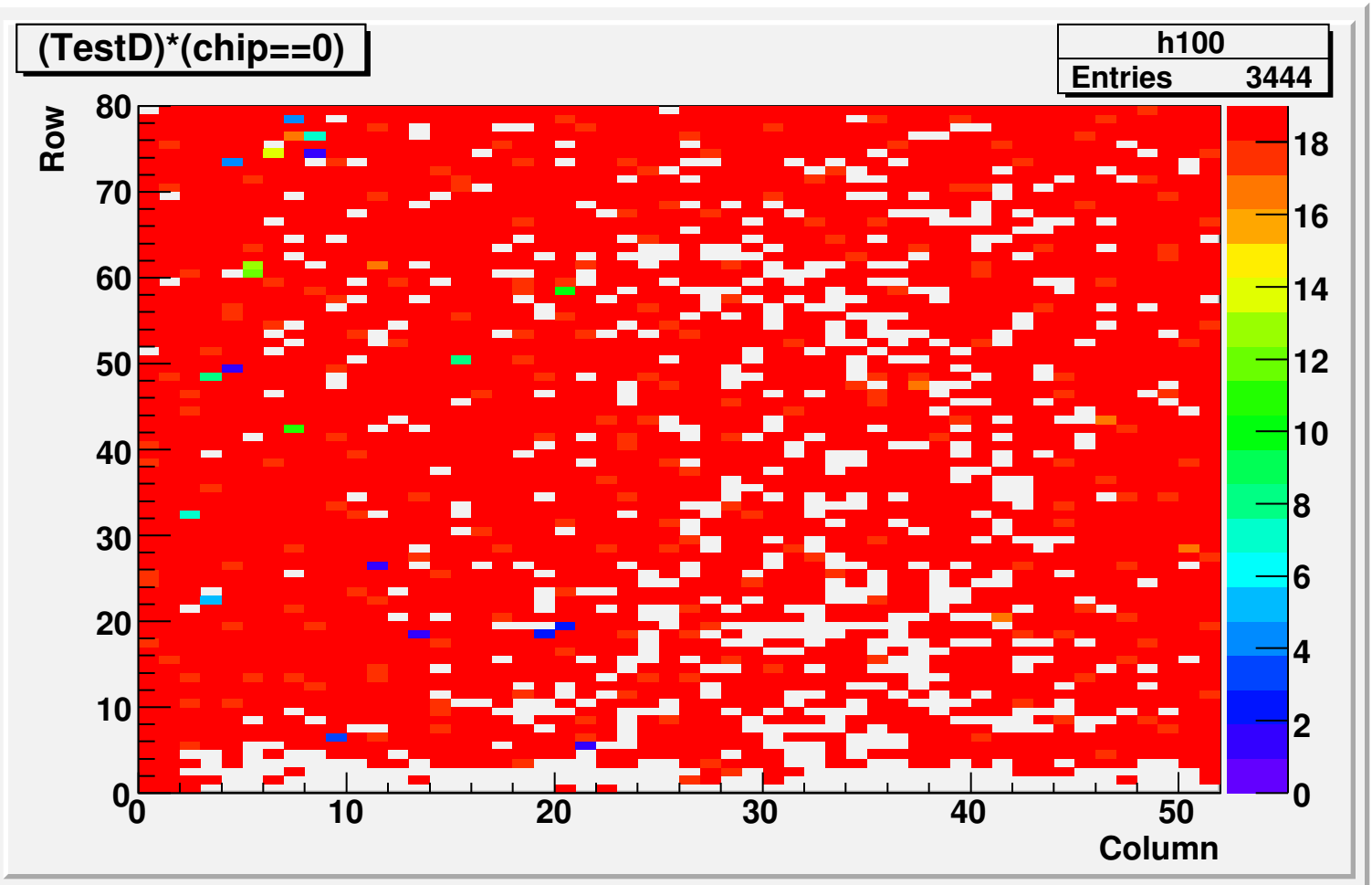
Chip # 1
Chip # 2
Chip # 3
Chip # 4


(TestD)*(chip==0)
h100
Entries 3444
Row
Column
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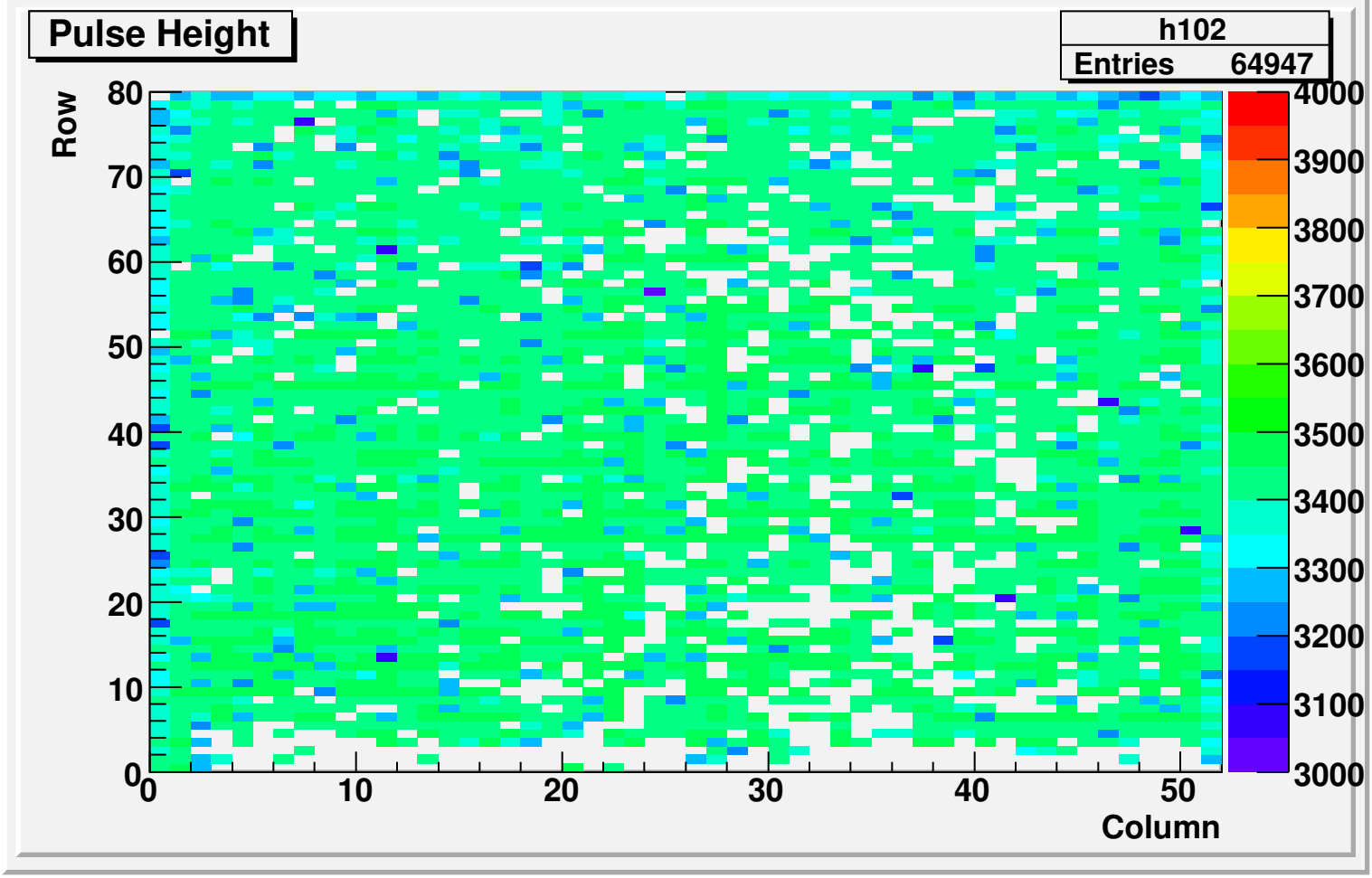
14
16
18


Pulse Height
h102
Entries 64947
Row
Column
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3100
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3900
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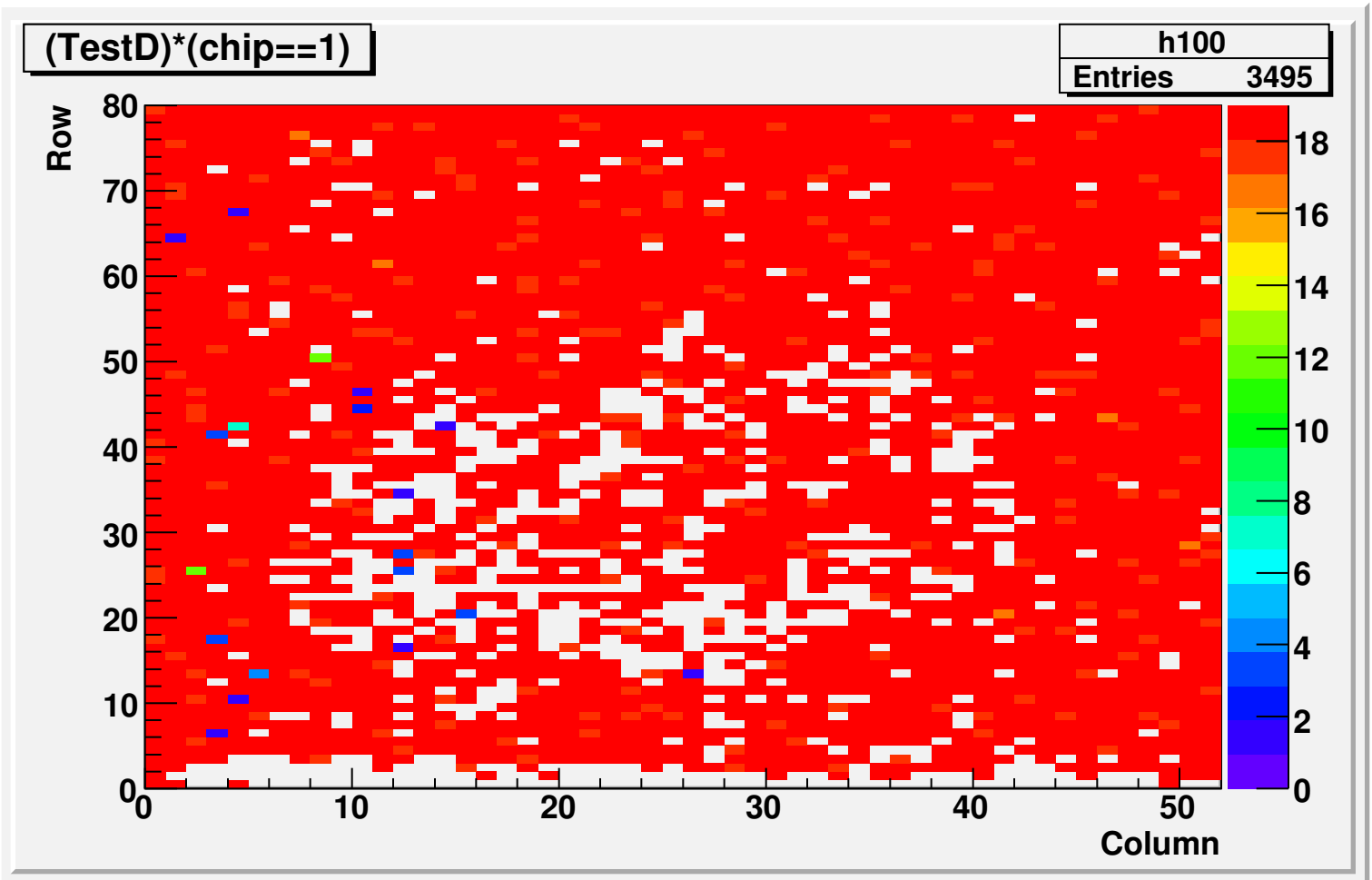
(TestD)*(chip==1)
h100
Entries 3495
Row
Column
0
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80
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12
14
16
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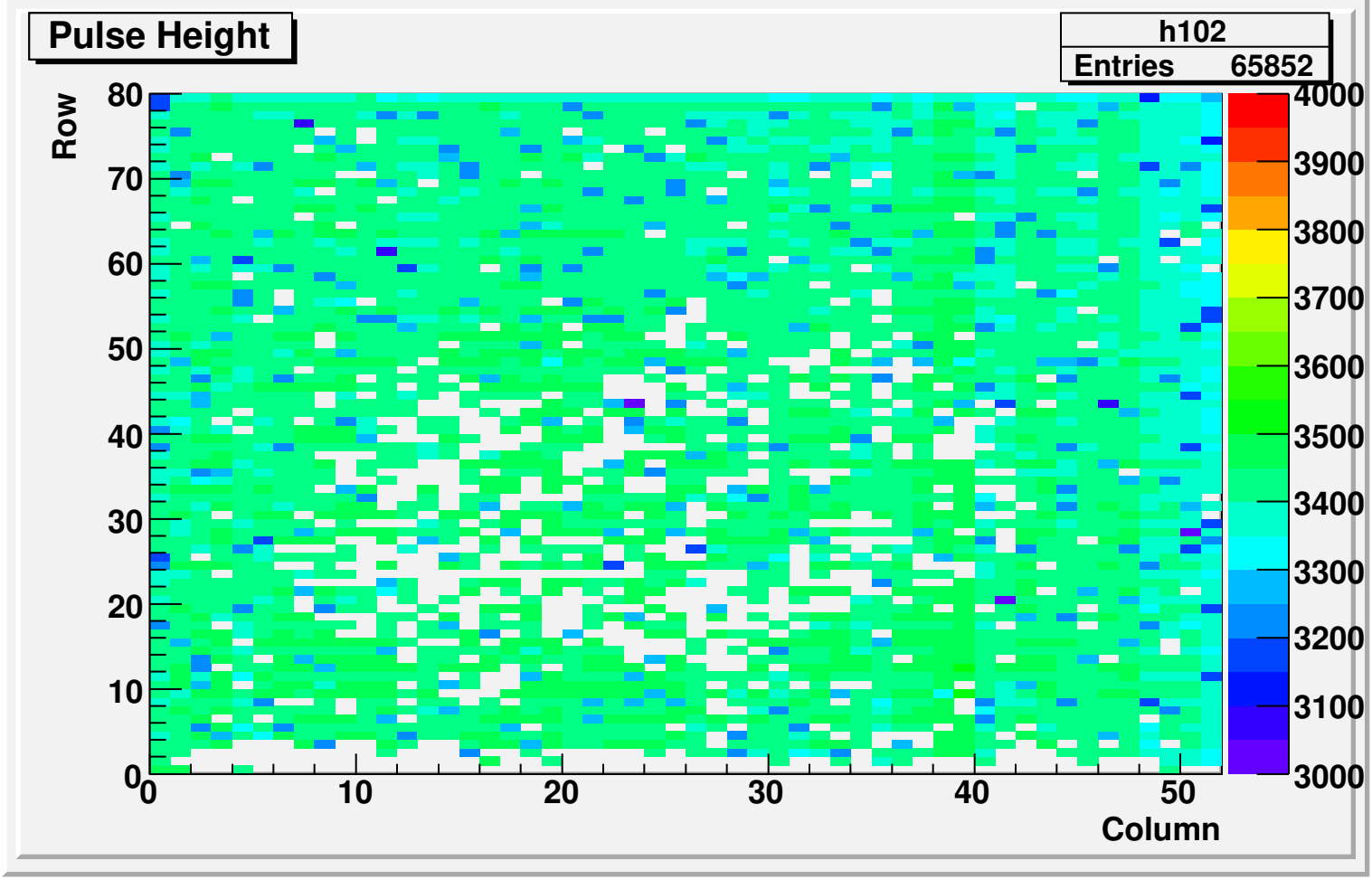
Pulse Height
h102
Entries 65852
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Column
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3900
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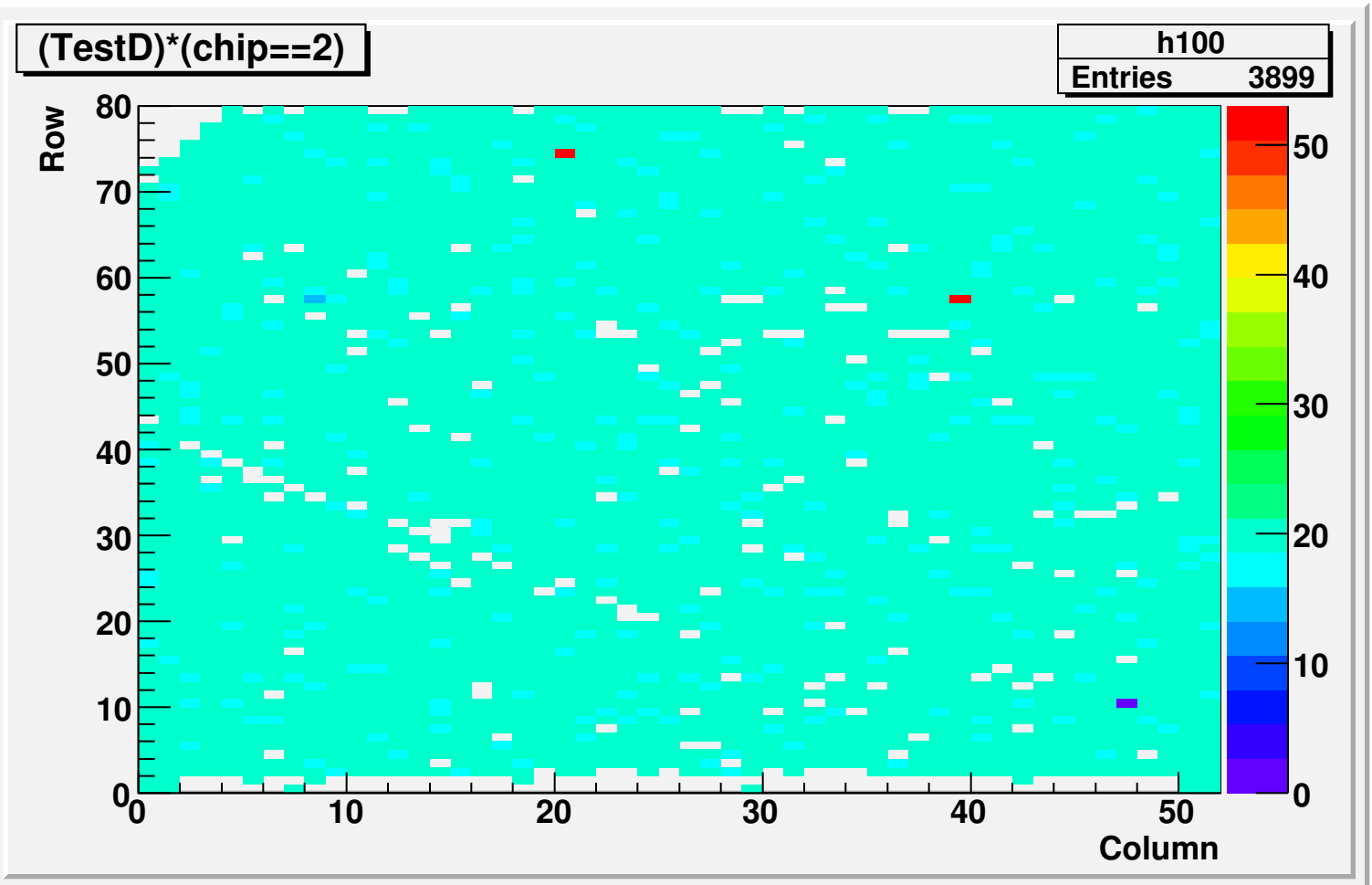
(TestD)*(chip==2)
h100
Entries 3899
Row
Column
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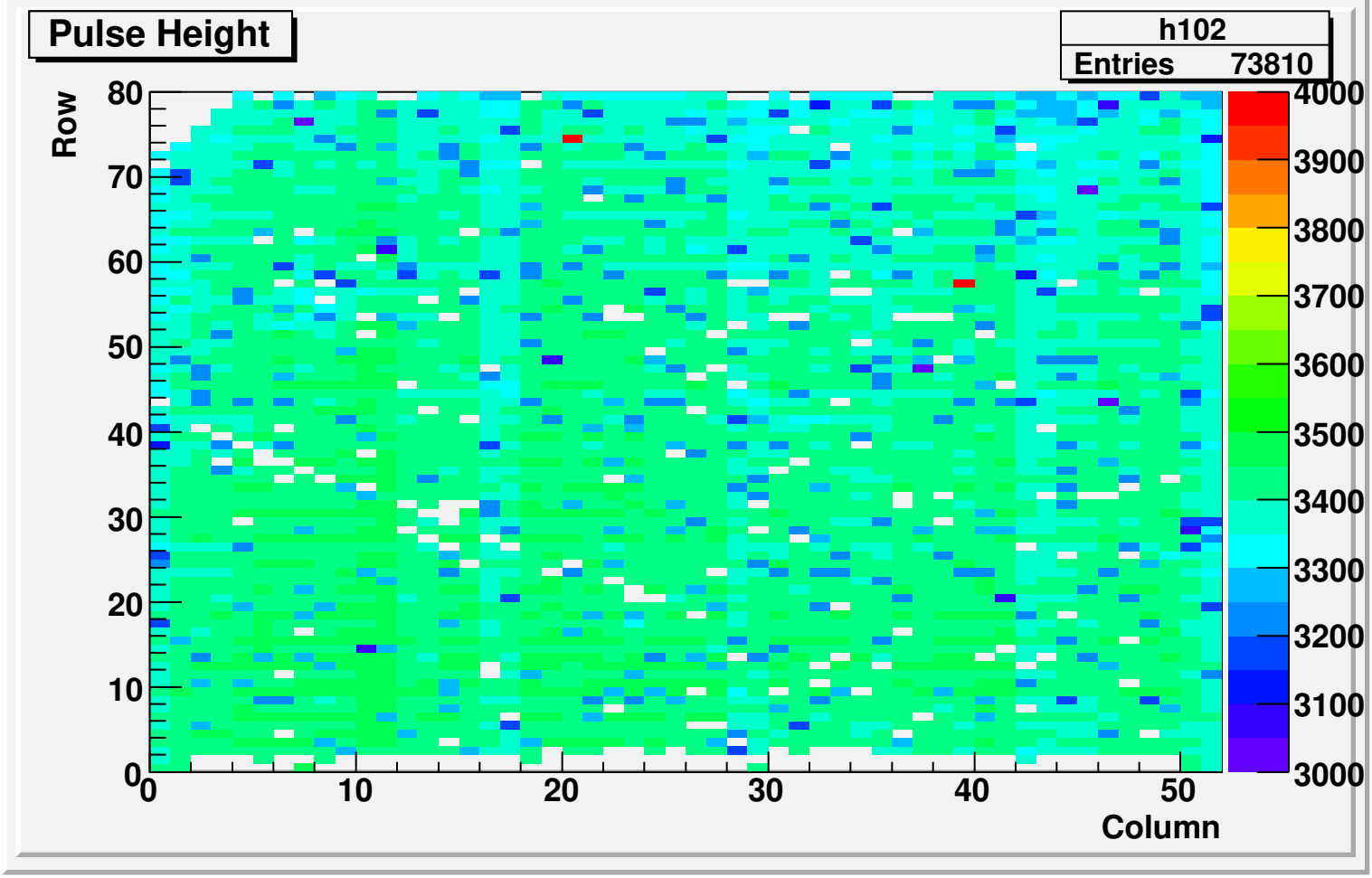
Pulse Height
h102
Entries 73810
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Column
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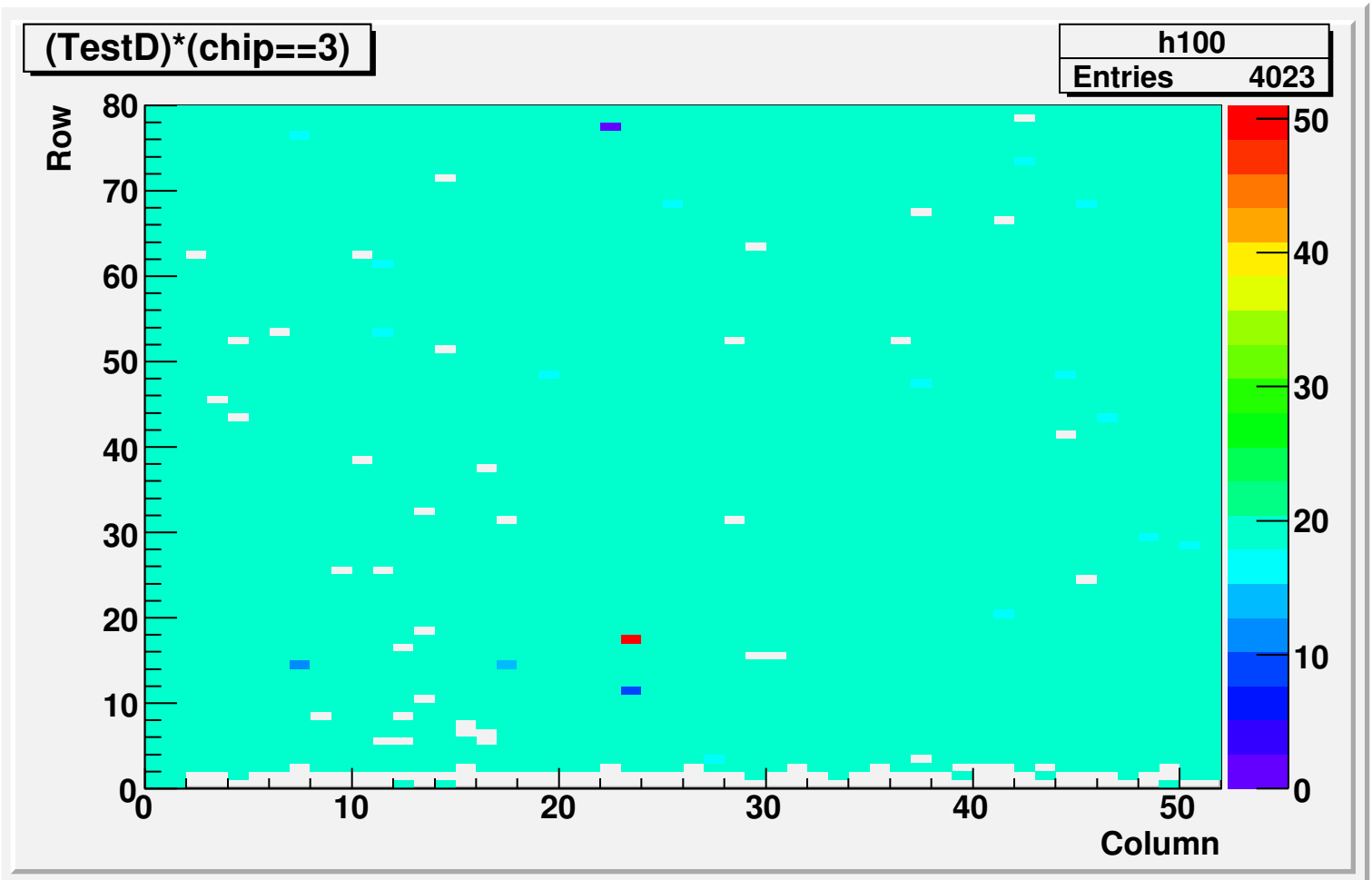
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h100
Entries 4023
Row
Column
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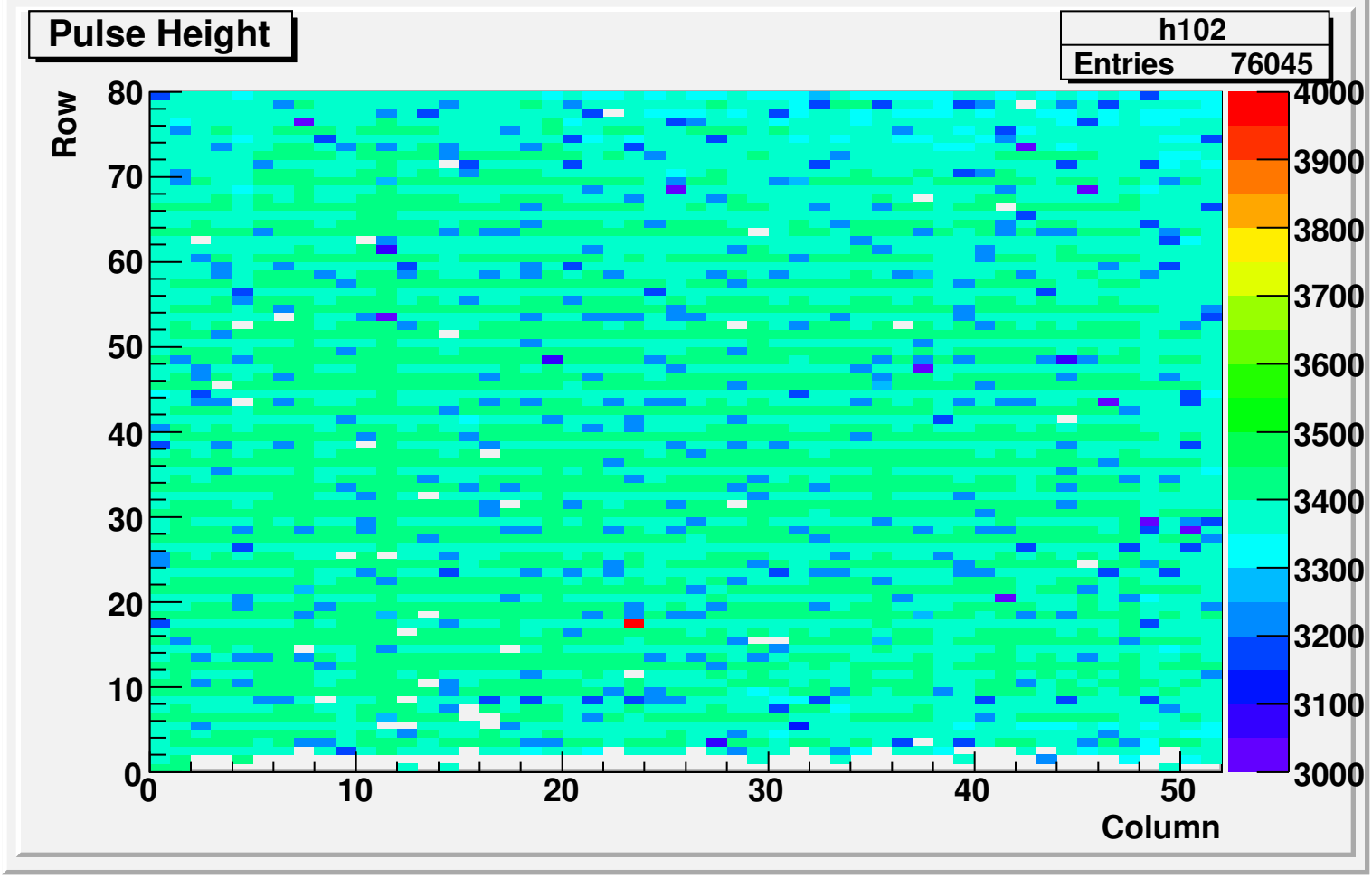
Pulse Height
h102
Entries 76045
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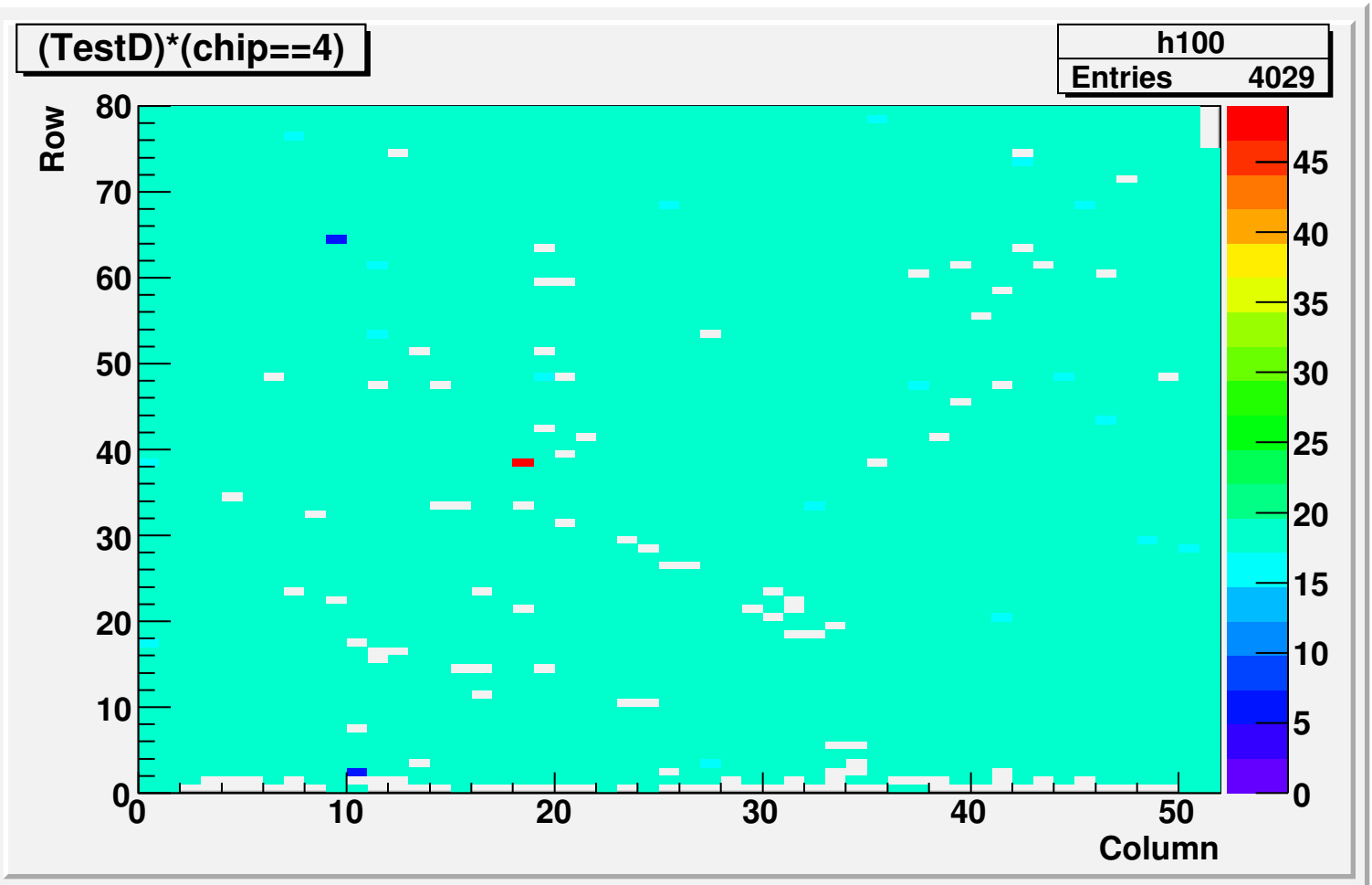
(TestD)*(chip==4)
h100
Entries 4029
Row
Column
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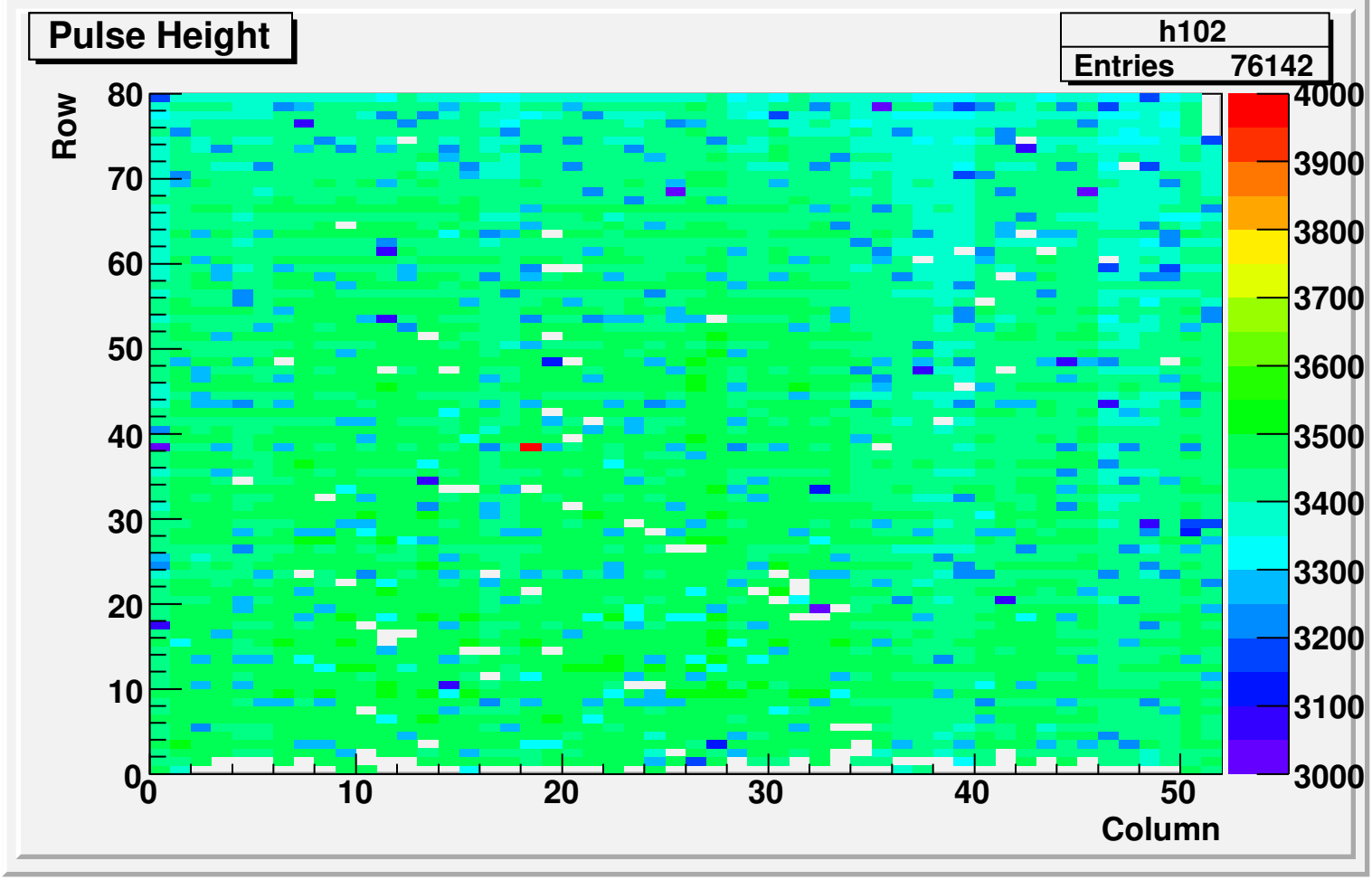
Pulse Height
h102
Entries 76142
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Column
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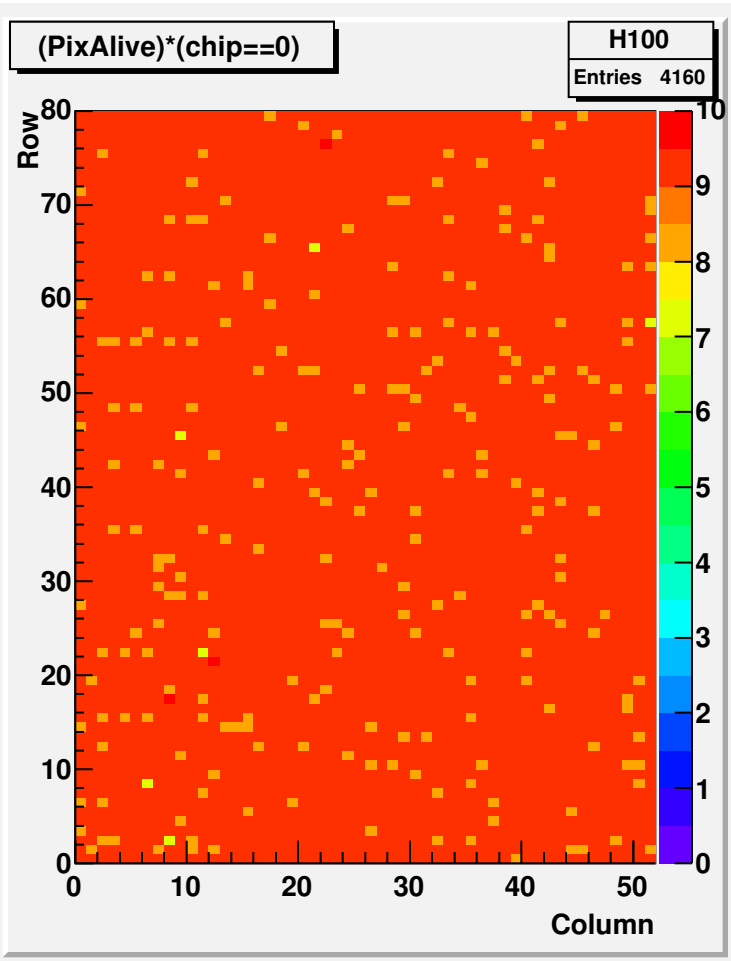
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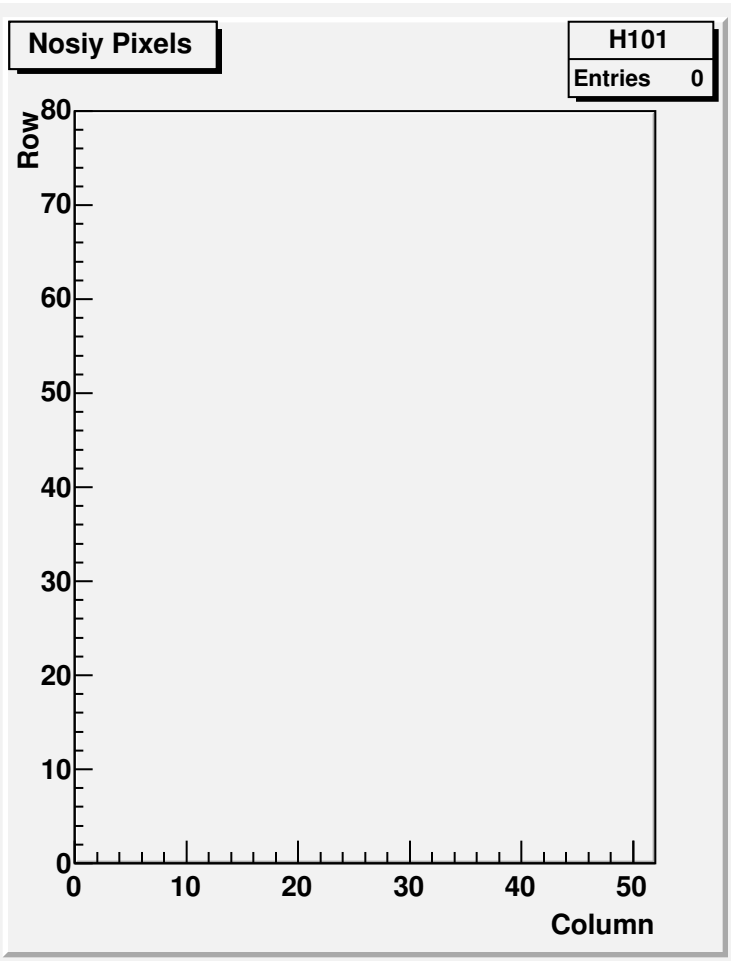
Column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H101
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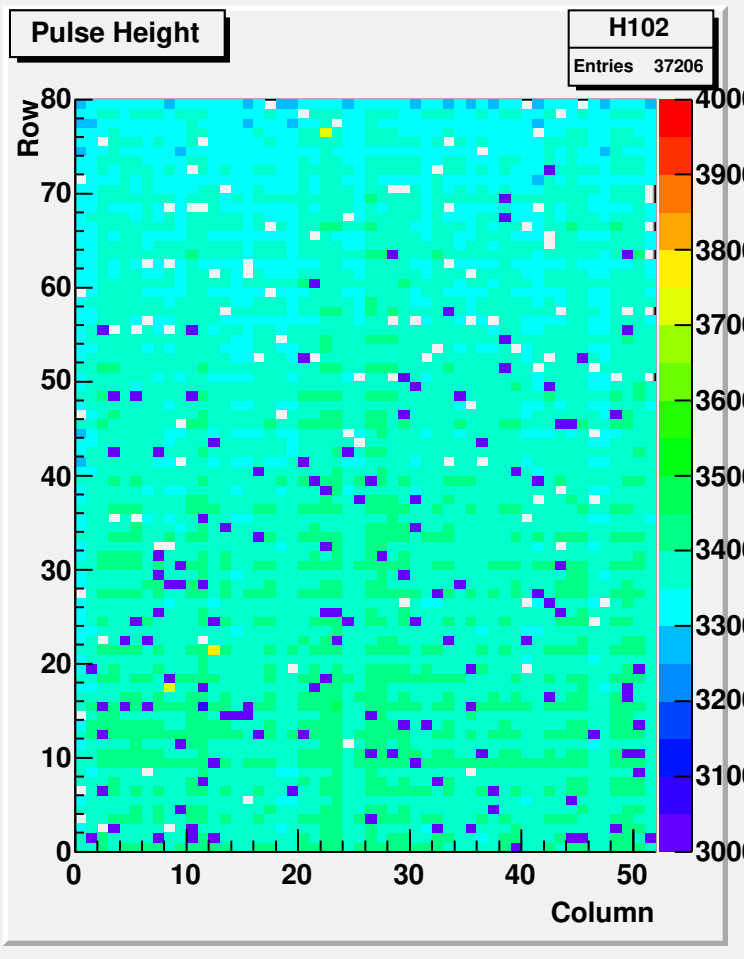
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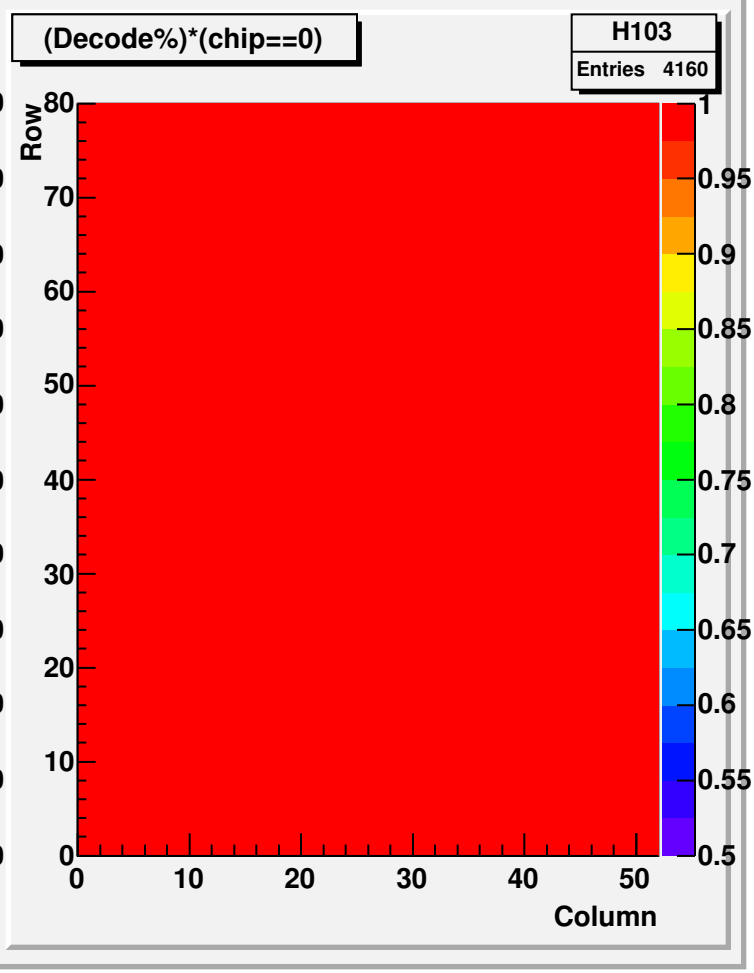
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H103
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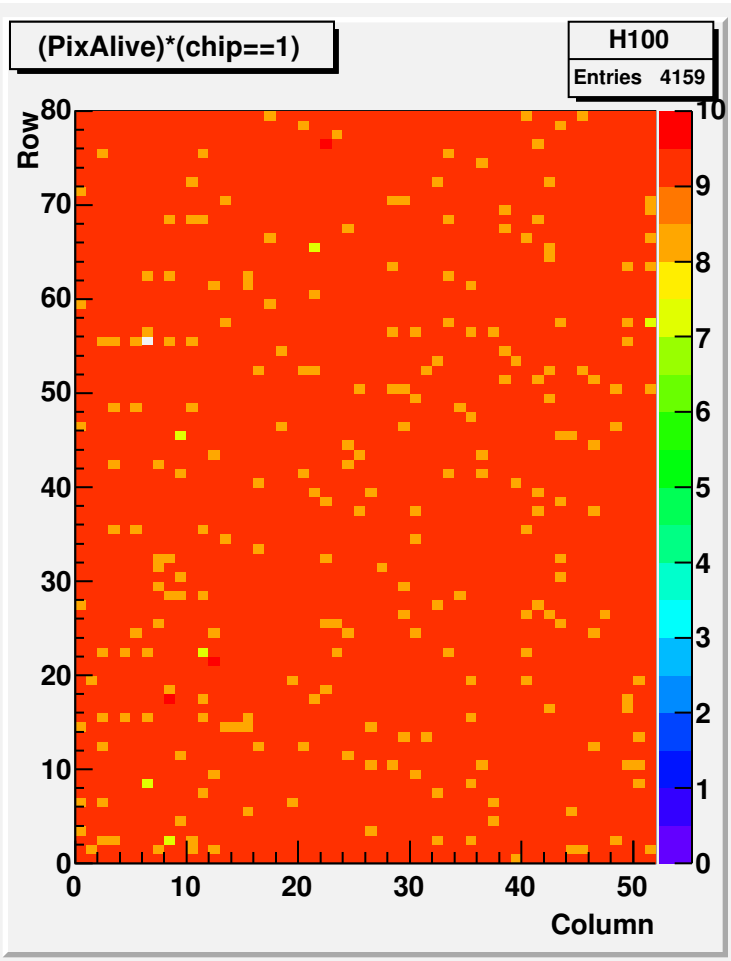
Column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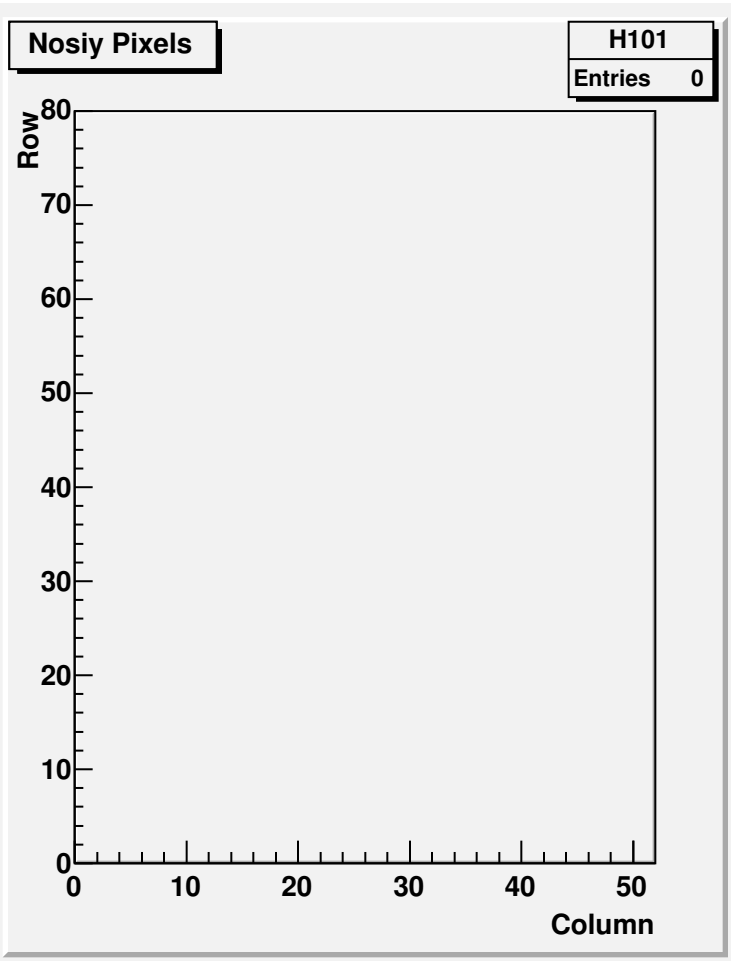
Column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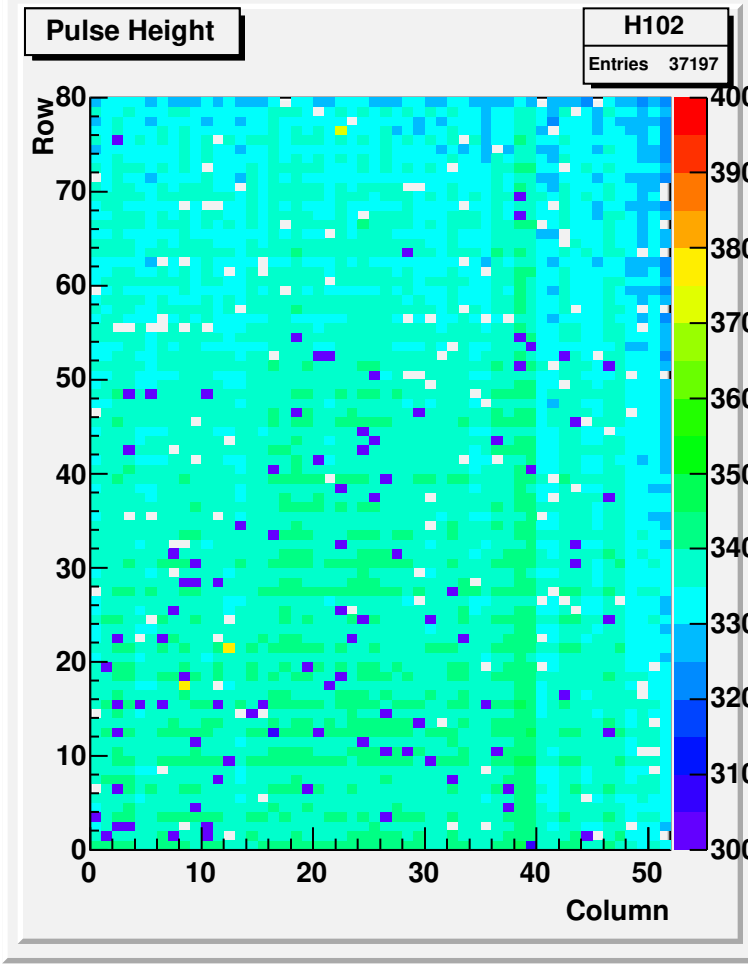
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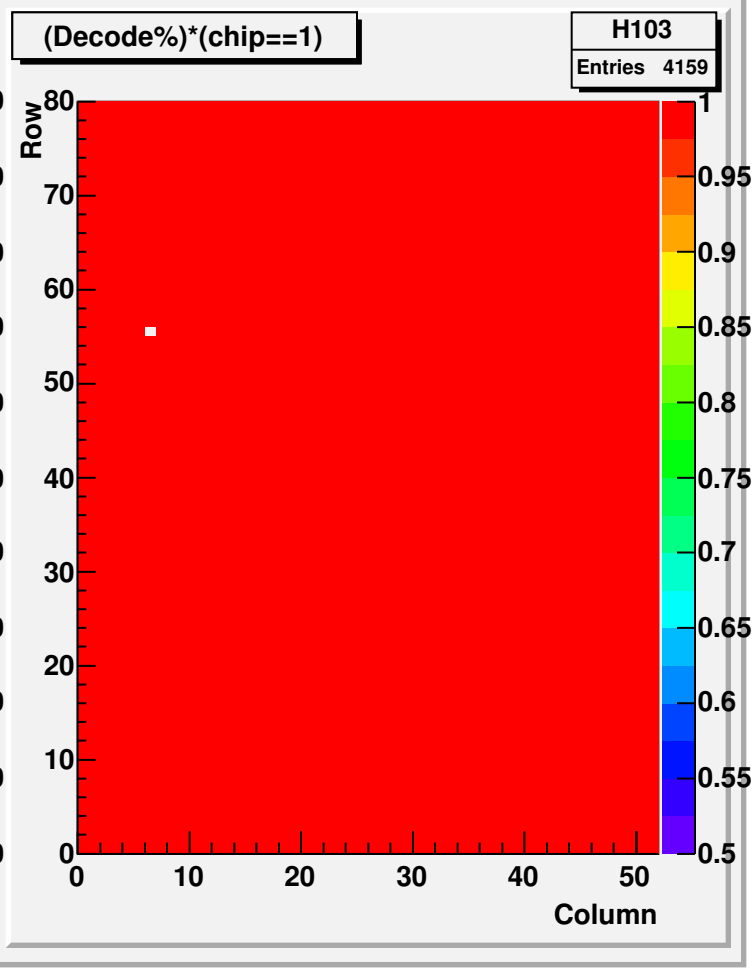
Column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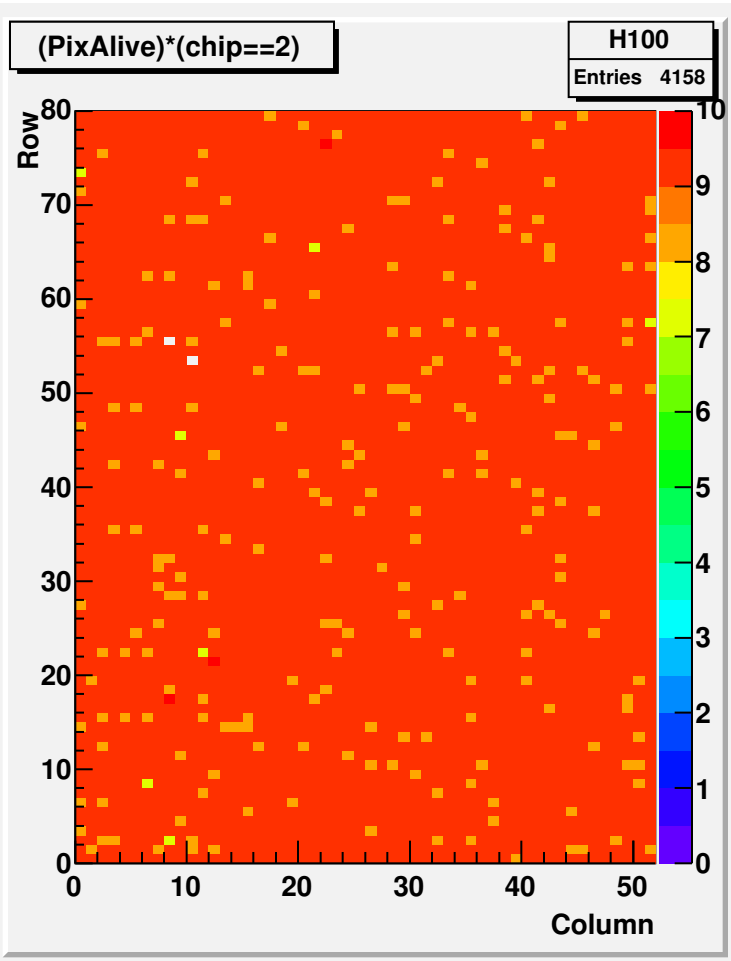
Column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H100
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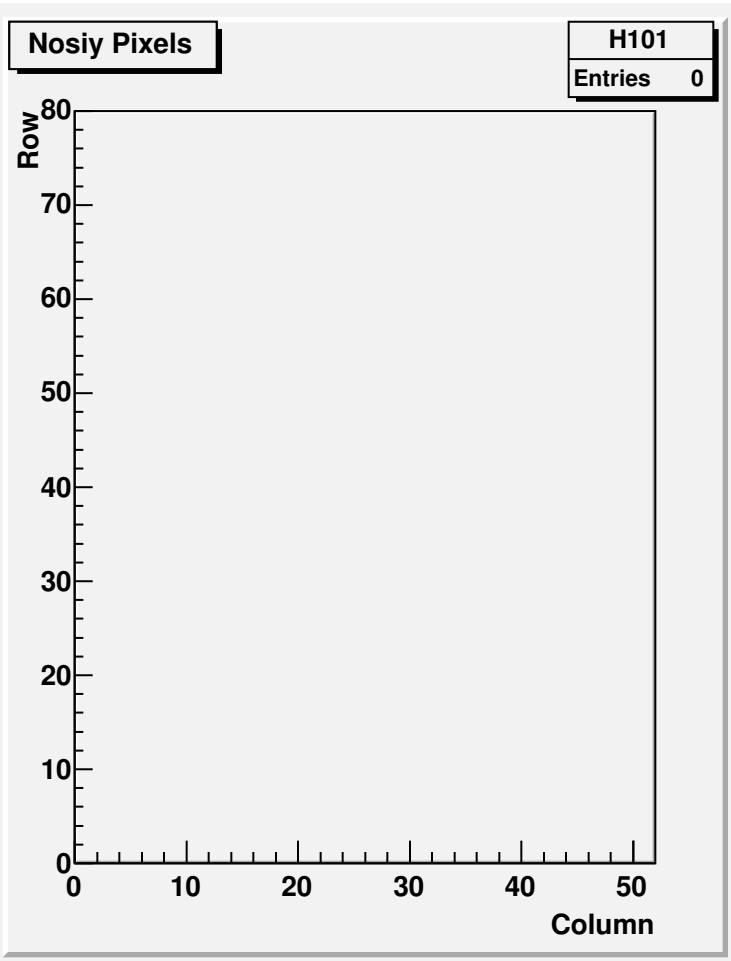
Column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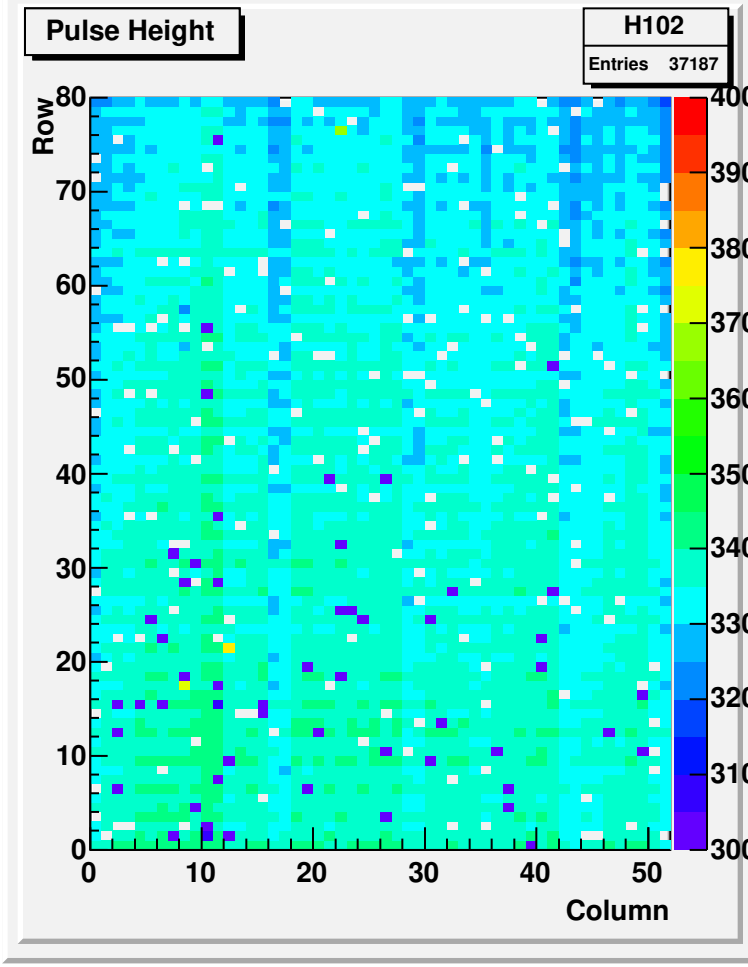
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H102
Entries 37187
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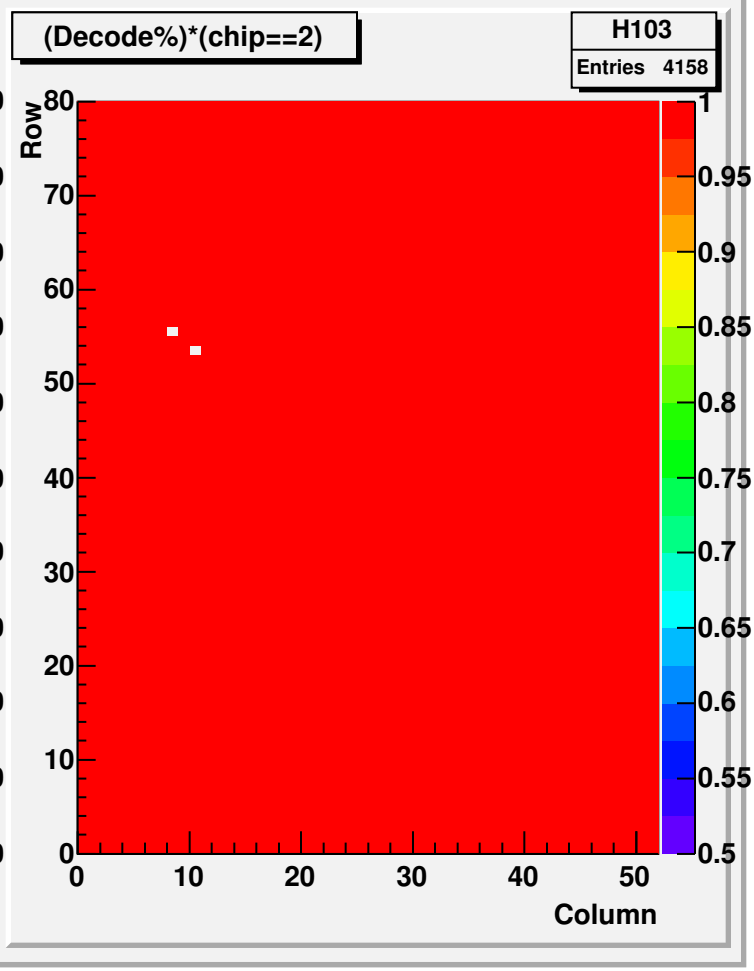
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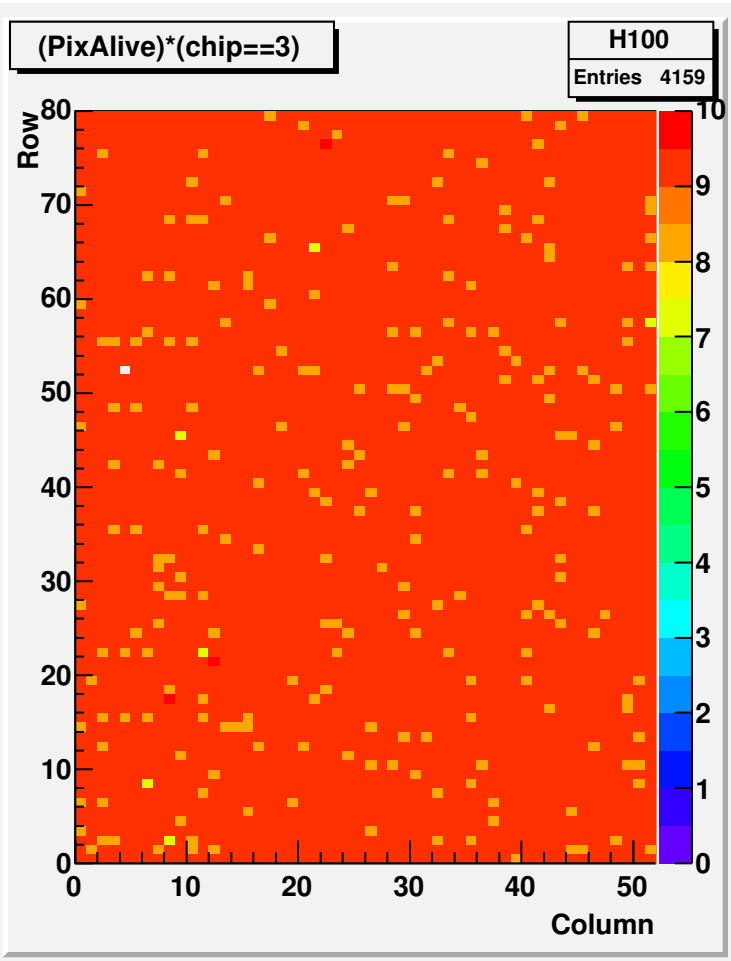
Column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Entries 4159
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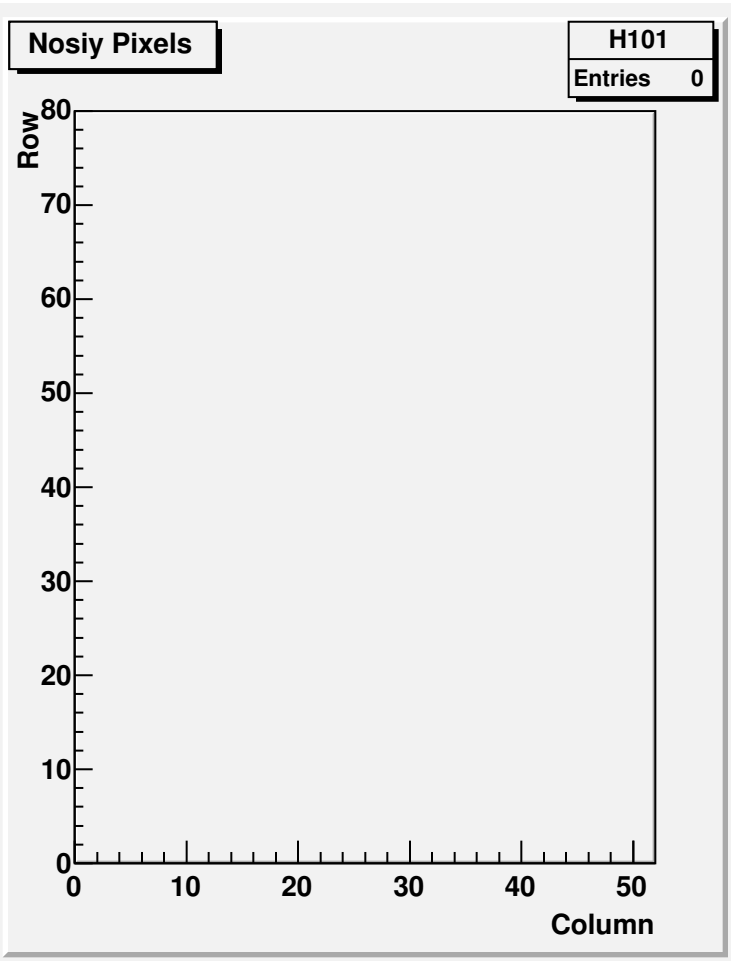
Column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H101
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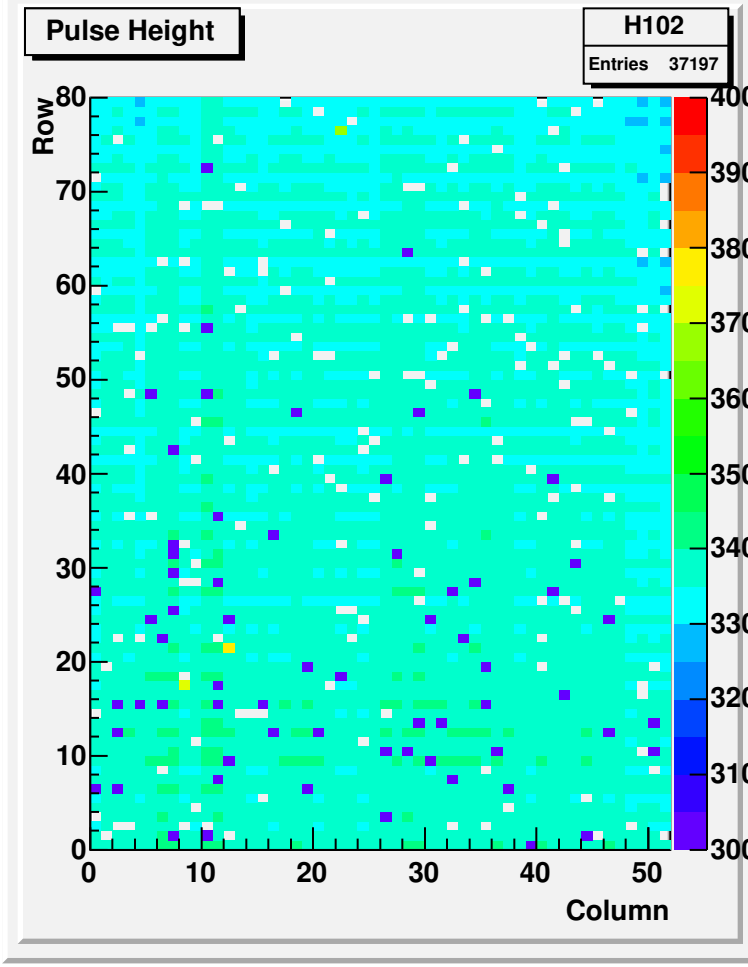
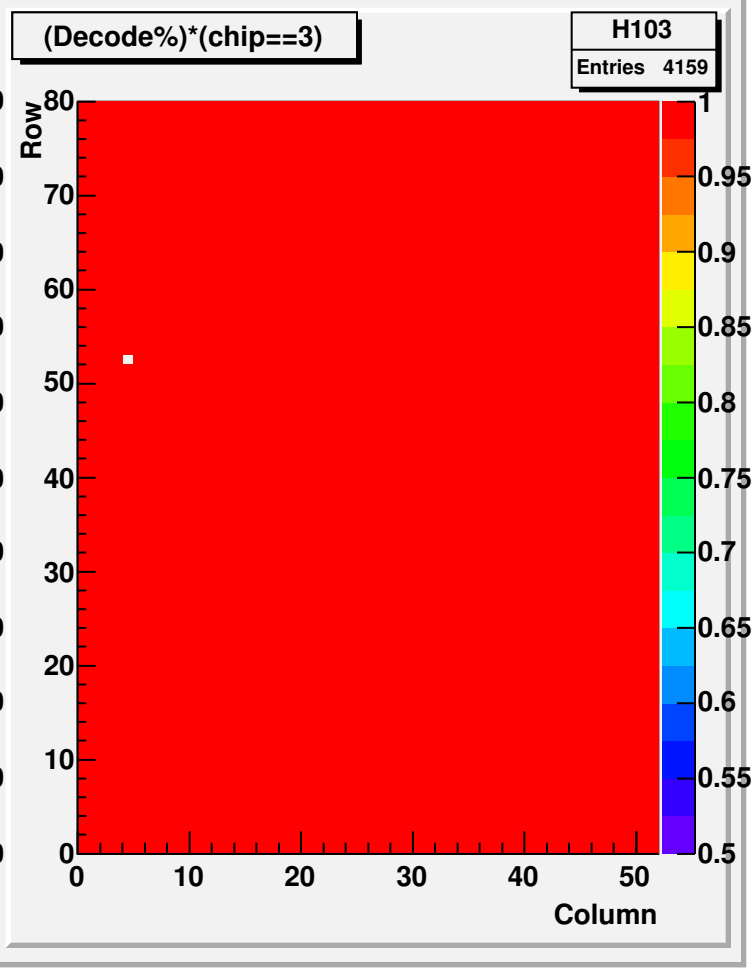

Pulse Height
H102
Entries 37197
Row
Column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Entries 4159
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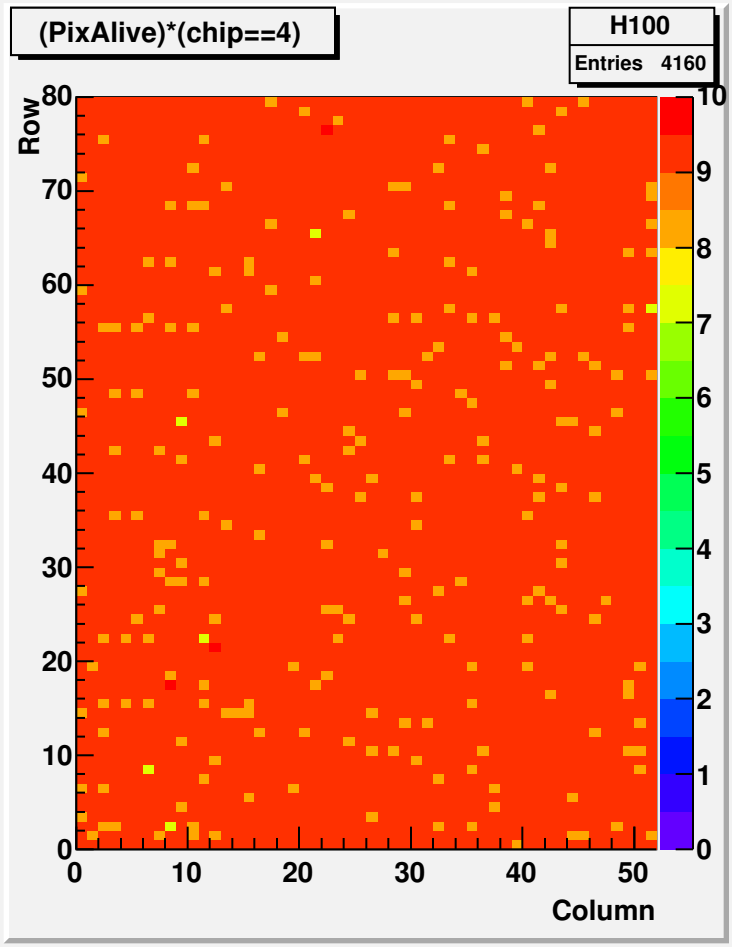
Column


(PixAlive)*(chip==4)
H100
Entries 4160
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Column
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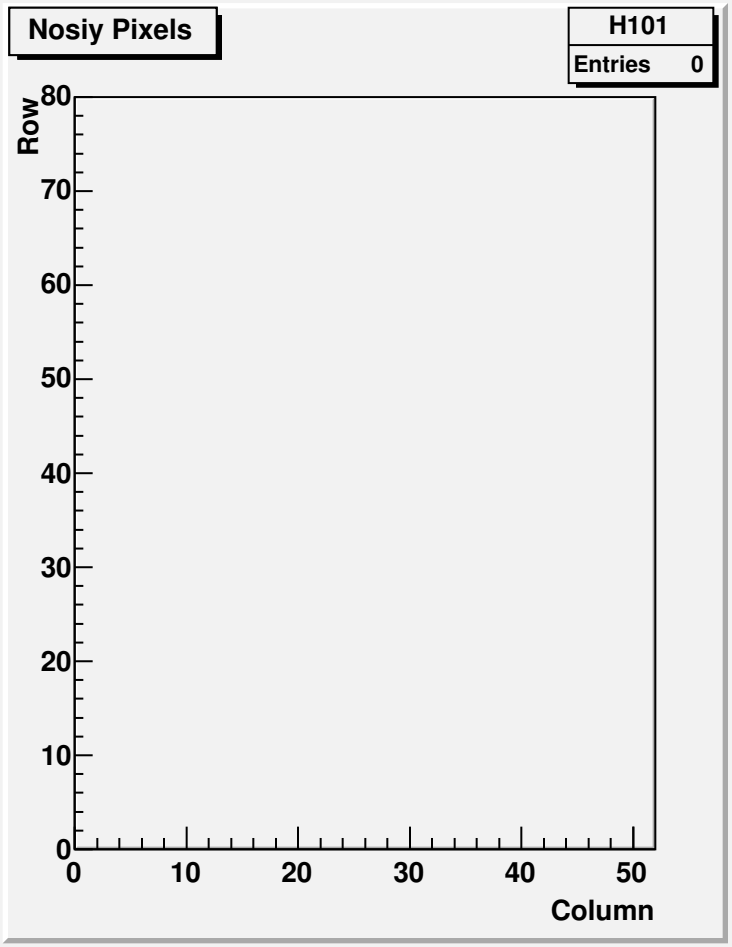
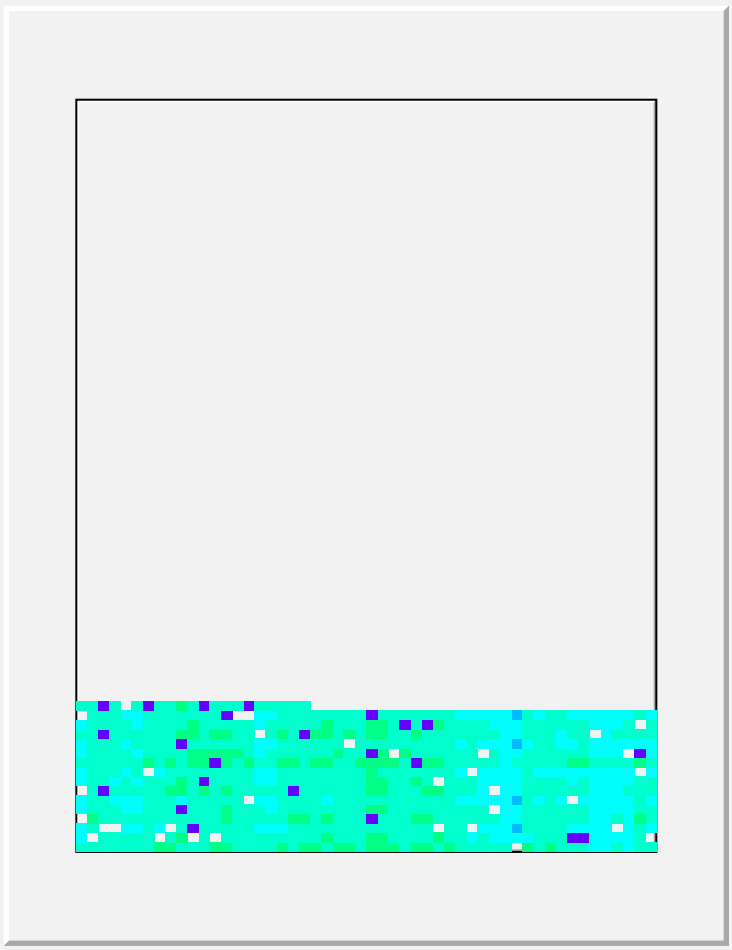
9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H101
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