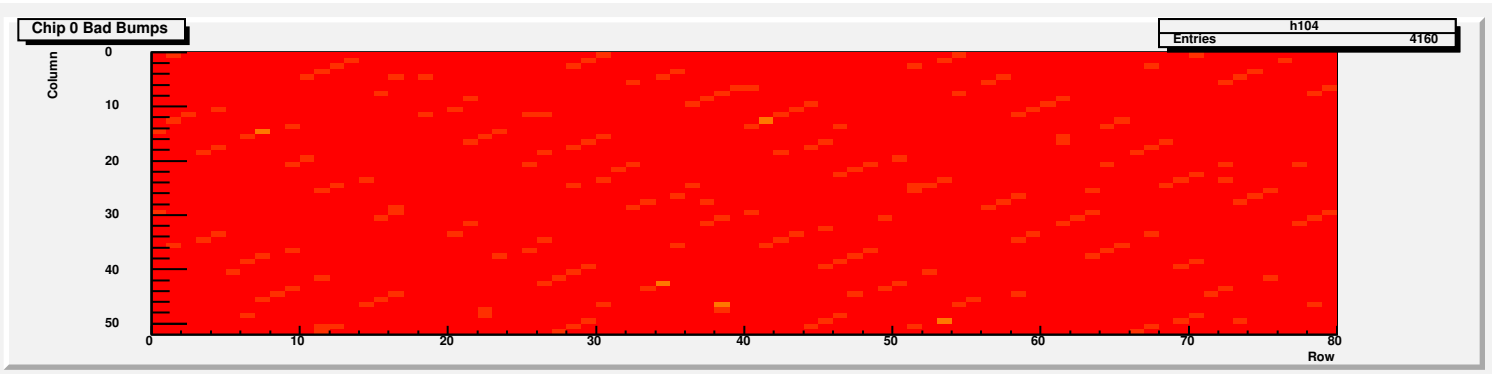
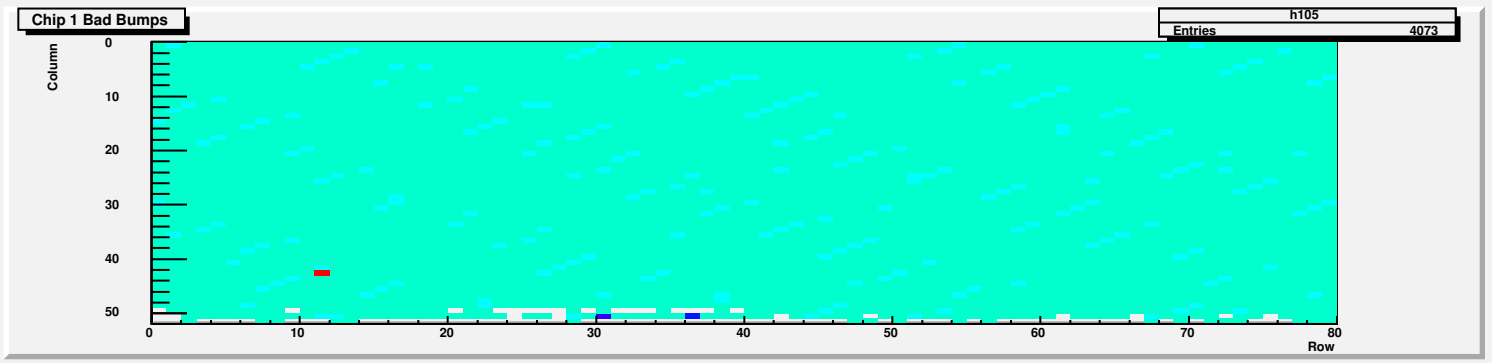
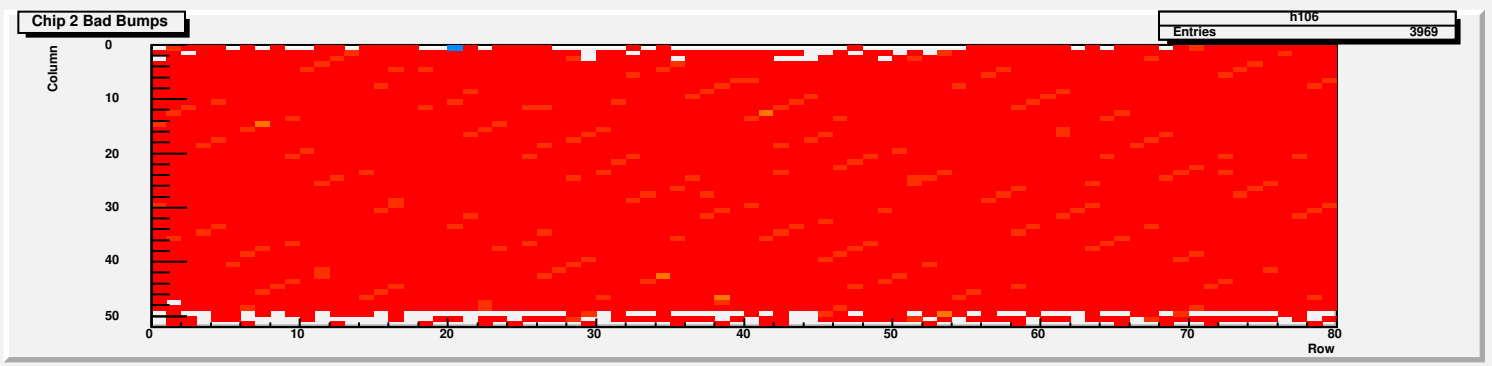
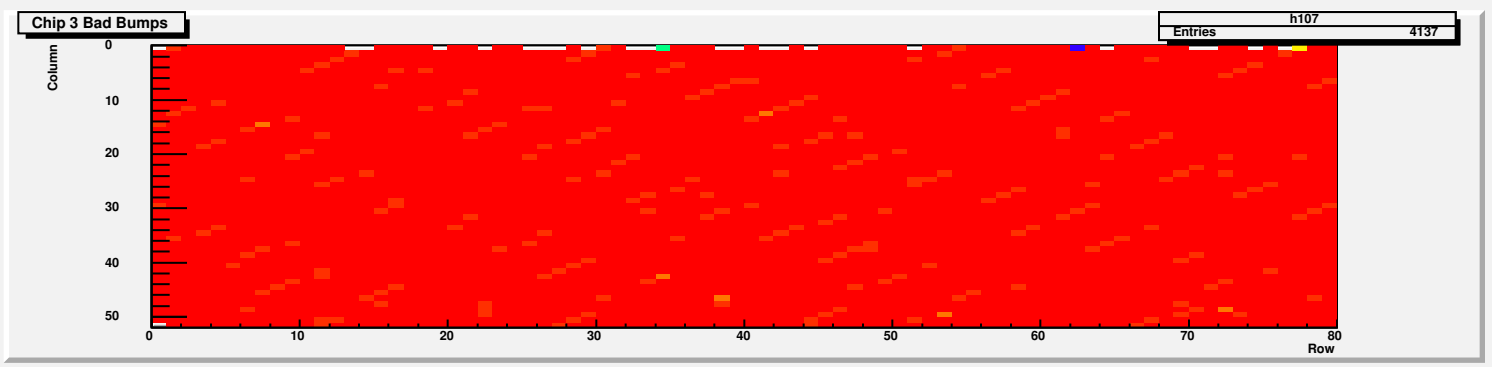
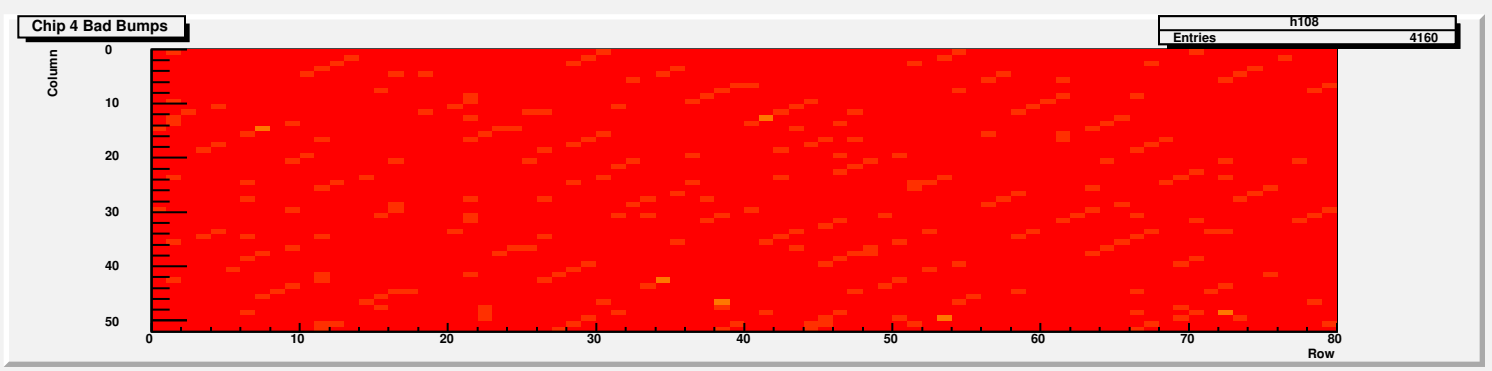
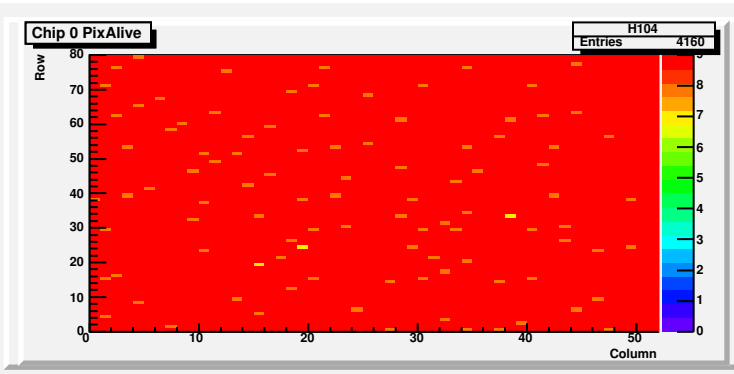

Chip 0 Bad Bumps
h104
Entries 4160
Column
Row

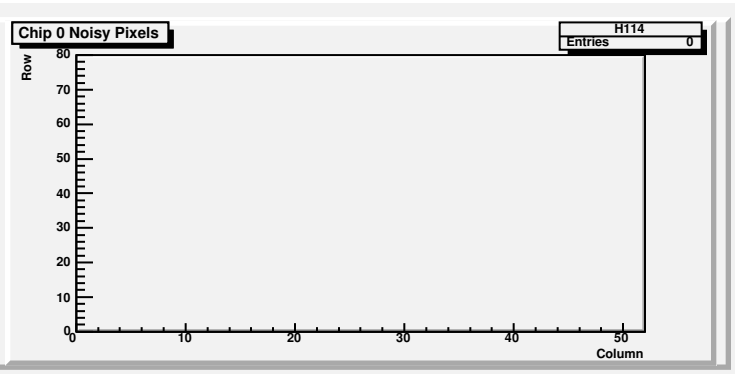

Chip 1 Bad Bumps
h105
Entries 4073
Column
Row

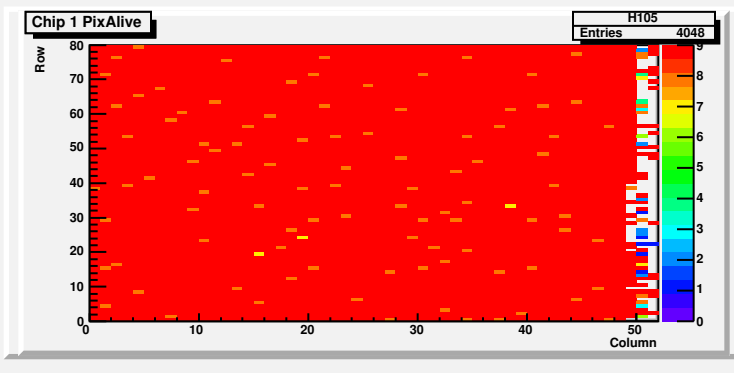

Chip 2 Bad Bumps
h106
Entries 3969
Column
Row

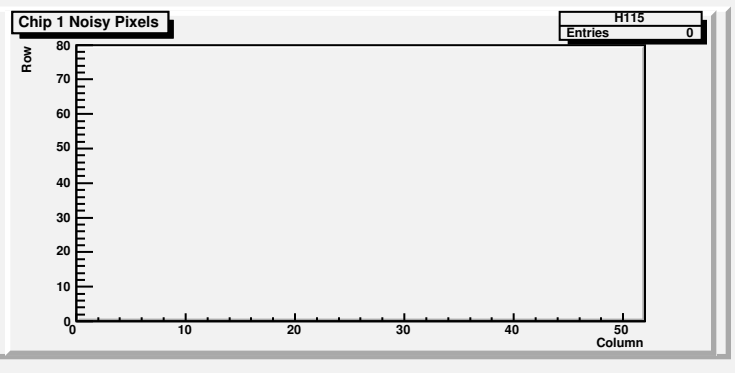

Chip 3 Bad Bumps
h107
Entries 4137
Column
Row

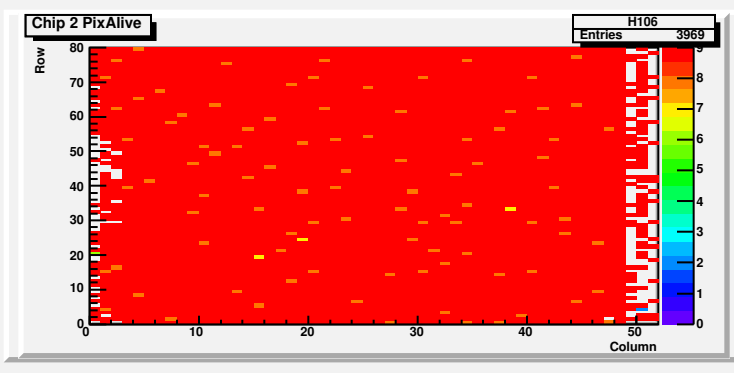

Chip 4 Bad Bumps
h108
Entries 4160
Column
Row

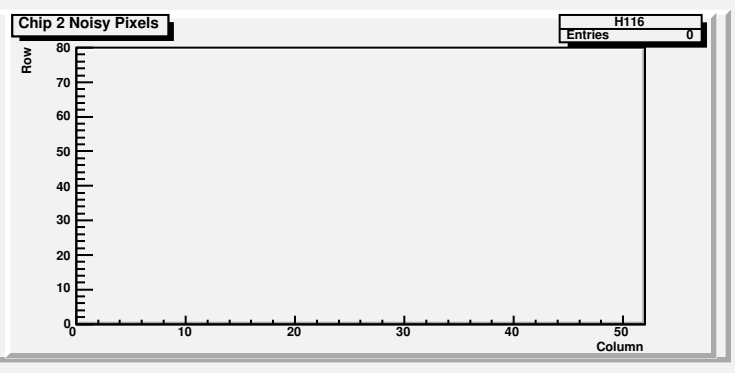

Chip 0 PixAlive
H104
Entries 4160
Row
Column

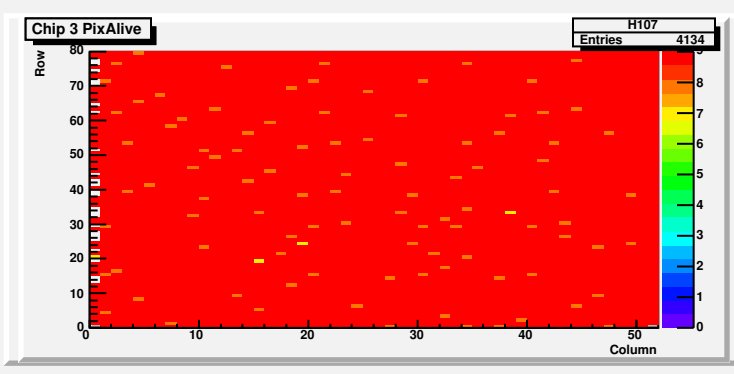

Chip 0 Noisy Pixels
H114
Entries 0
Row
Column

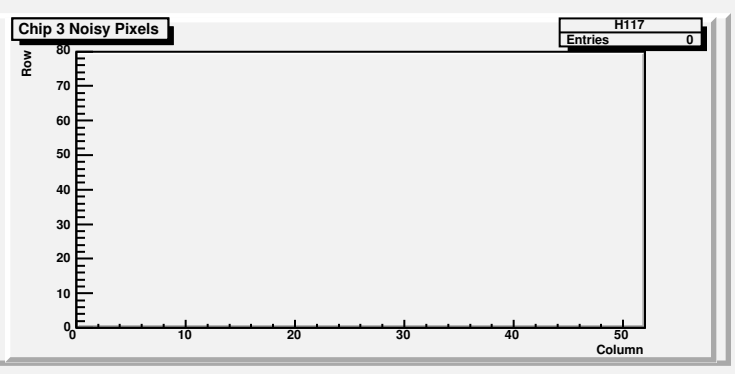

Chip 1 PixAlive
H105
Entries 4048
Row
Column

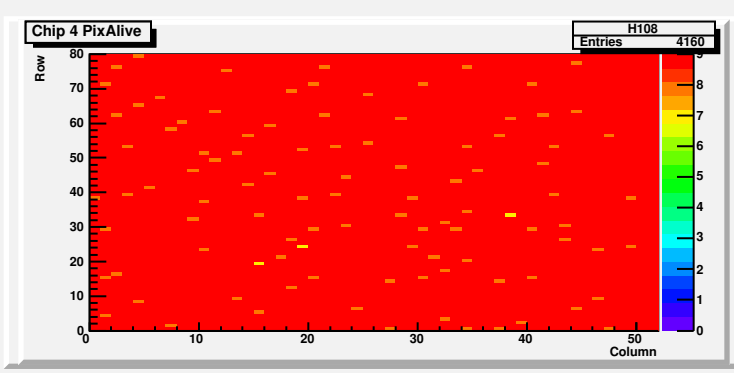

Chip 1 Noisy Pixels
H115
Entries 0
Row
Column

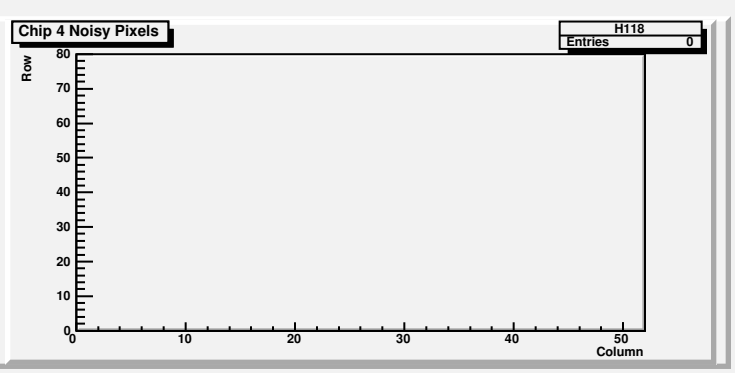

Chip 2 PixAlive
H106
Entries 3969
Row
Column

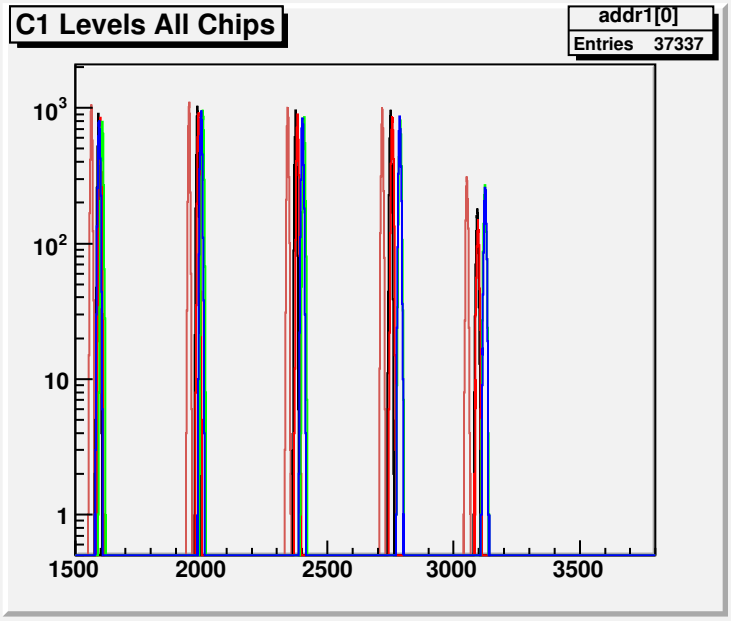

Chip 2 Noisy Pixels
H116
Entries 0
Row
Column

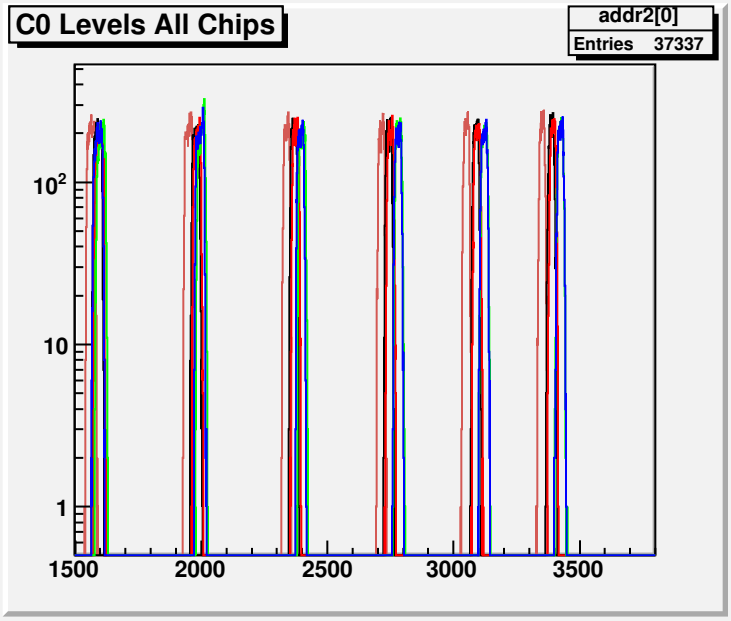

Chip 3 PixAlive
H107
Entries 4134
Row
Column

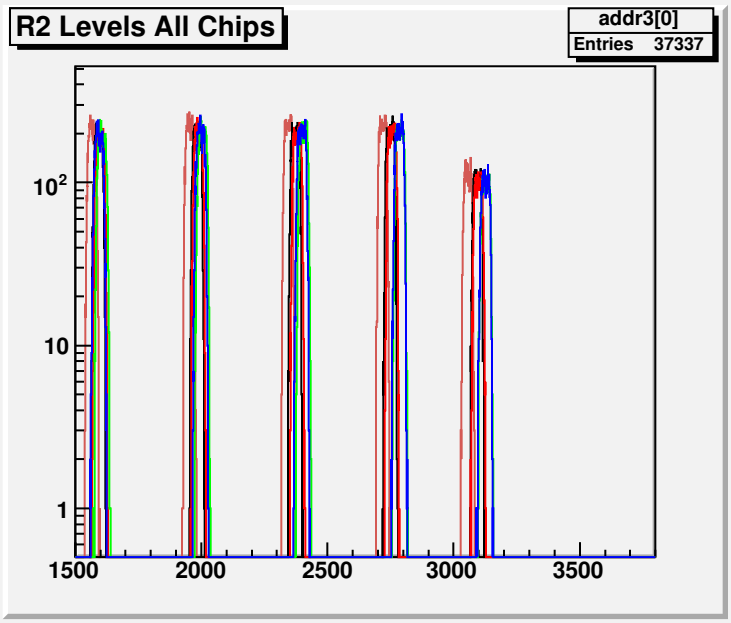

Chip 3 Noisy Pixels
H117
Entries 0
Row
Column

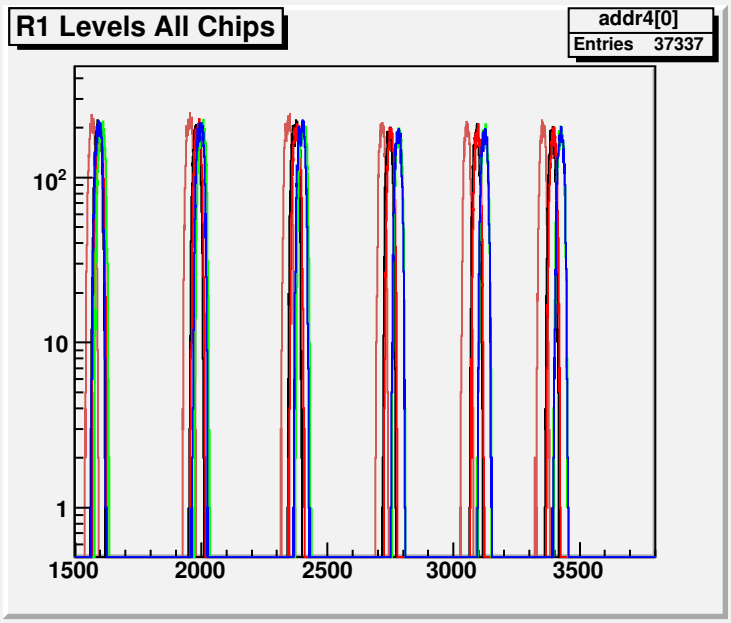

Chip 4 PixAlive
H108
Entries 4160
Row
Column

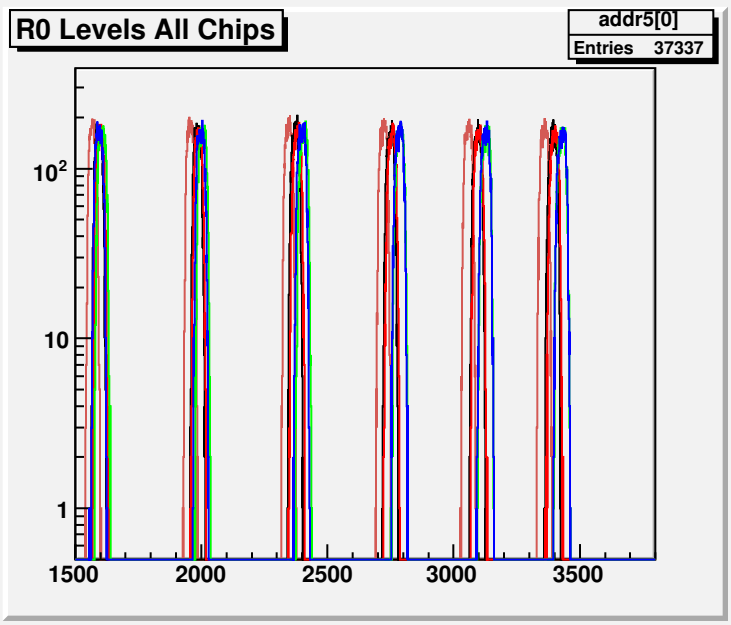

Chip 4 Noisy Pixels
H118
Entries 0
Row
Column

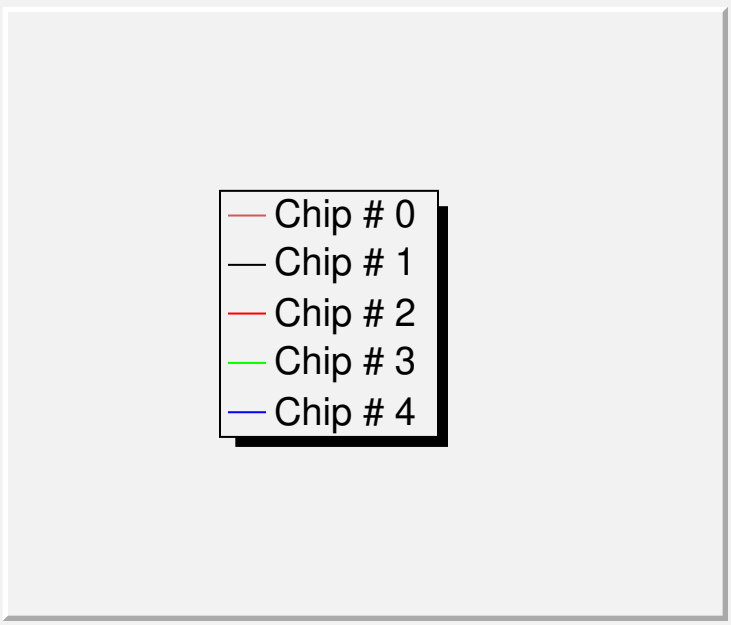

C1 Levels All Chips
addr1[0]
Entries 37337
10^3
10^2
10
1
1500
2000
2500
3000
3500

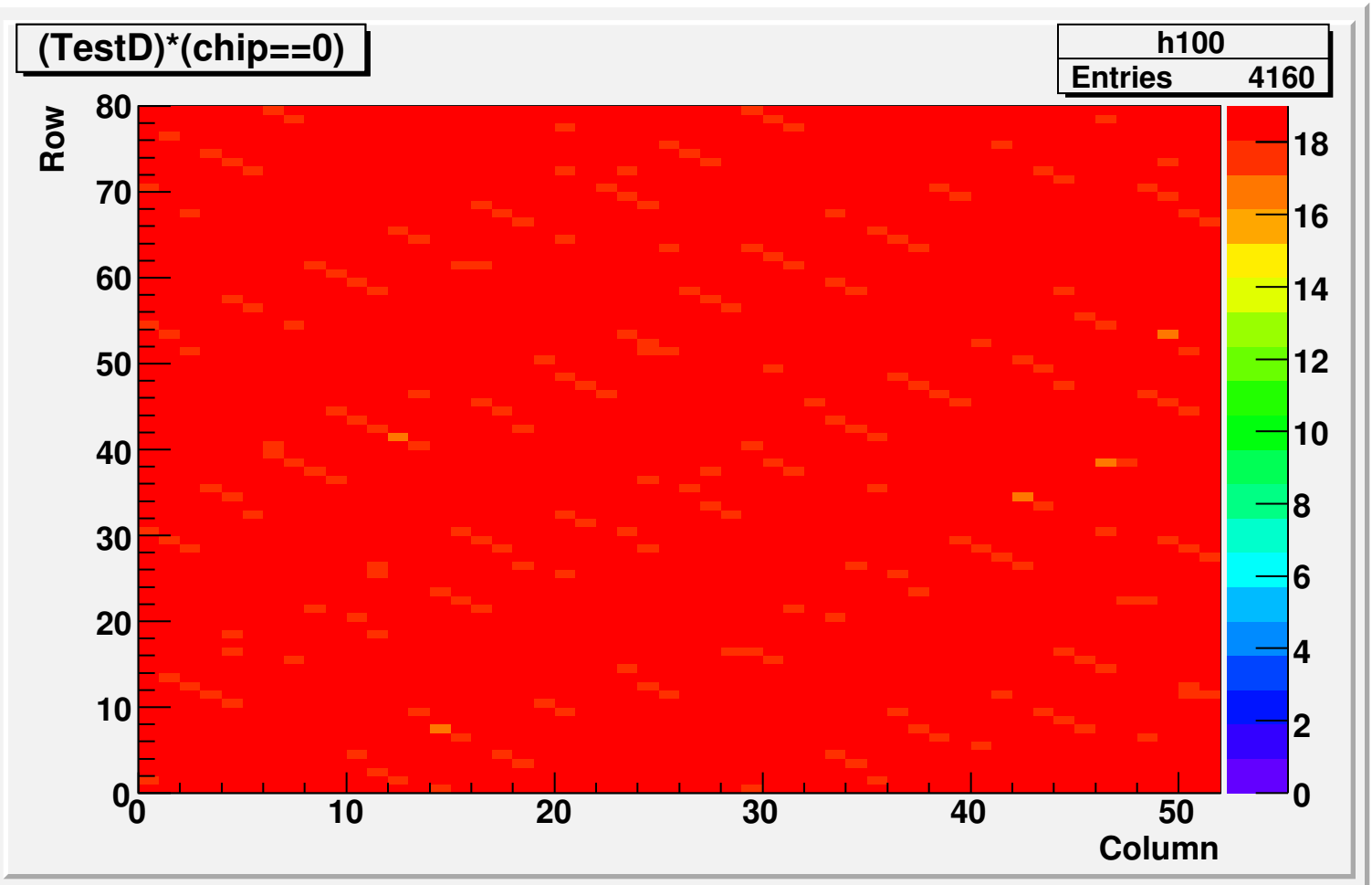

C0 Levels All Chips
addr2[0]
Entries 37337
10^2
10
1
1500
2000
2500
3000
3500

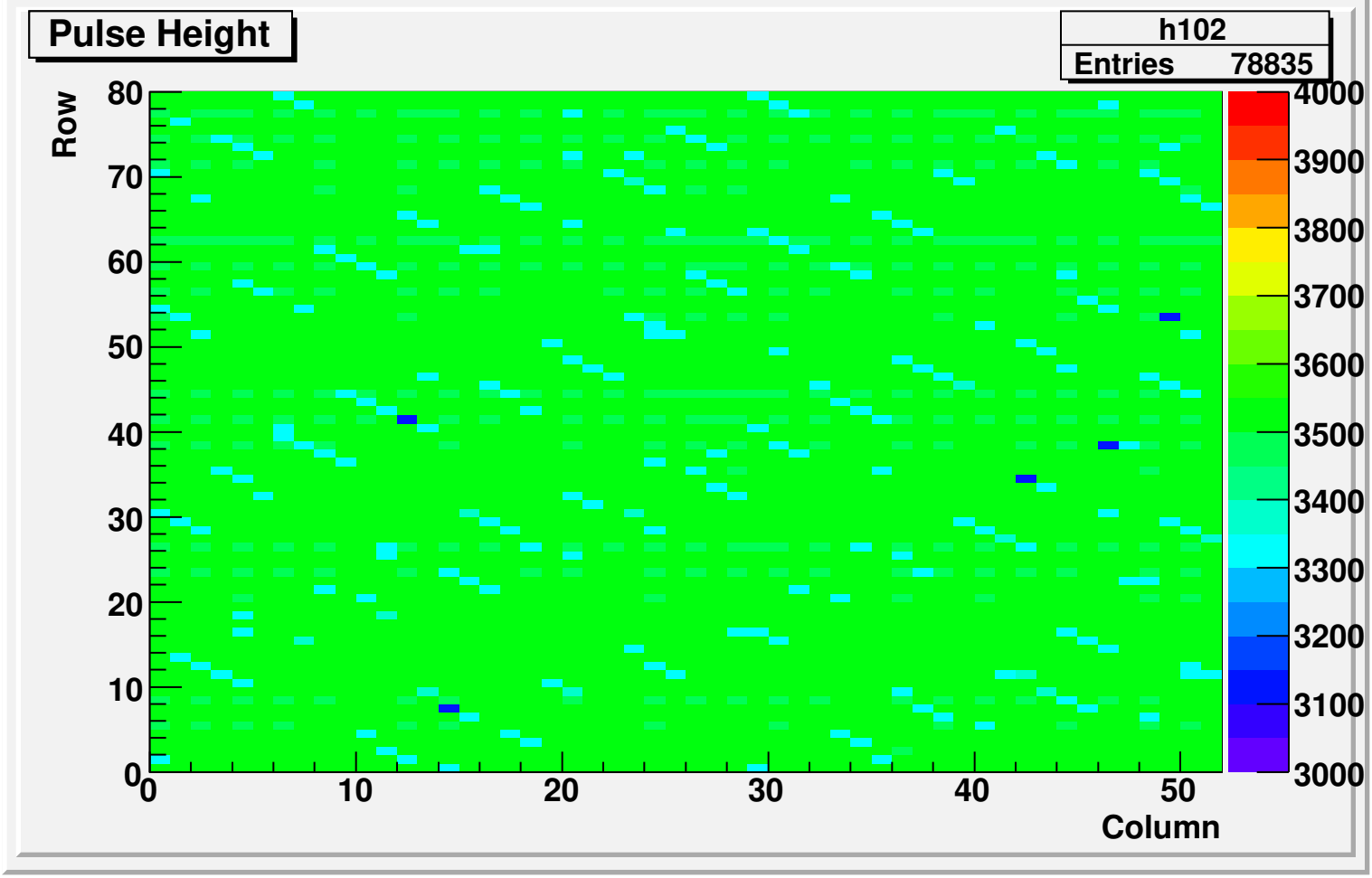

R2 Levels All Chips
addr3[0]
Entries 37337
10^2
10
1
1500
2000
2500
3000
3500

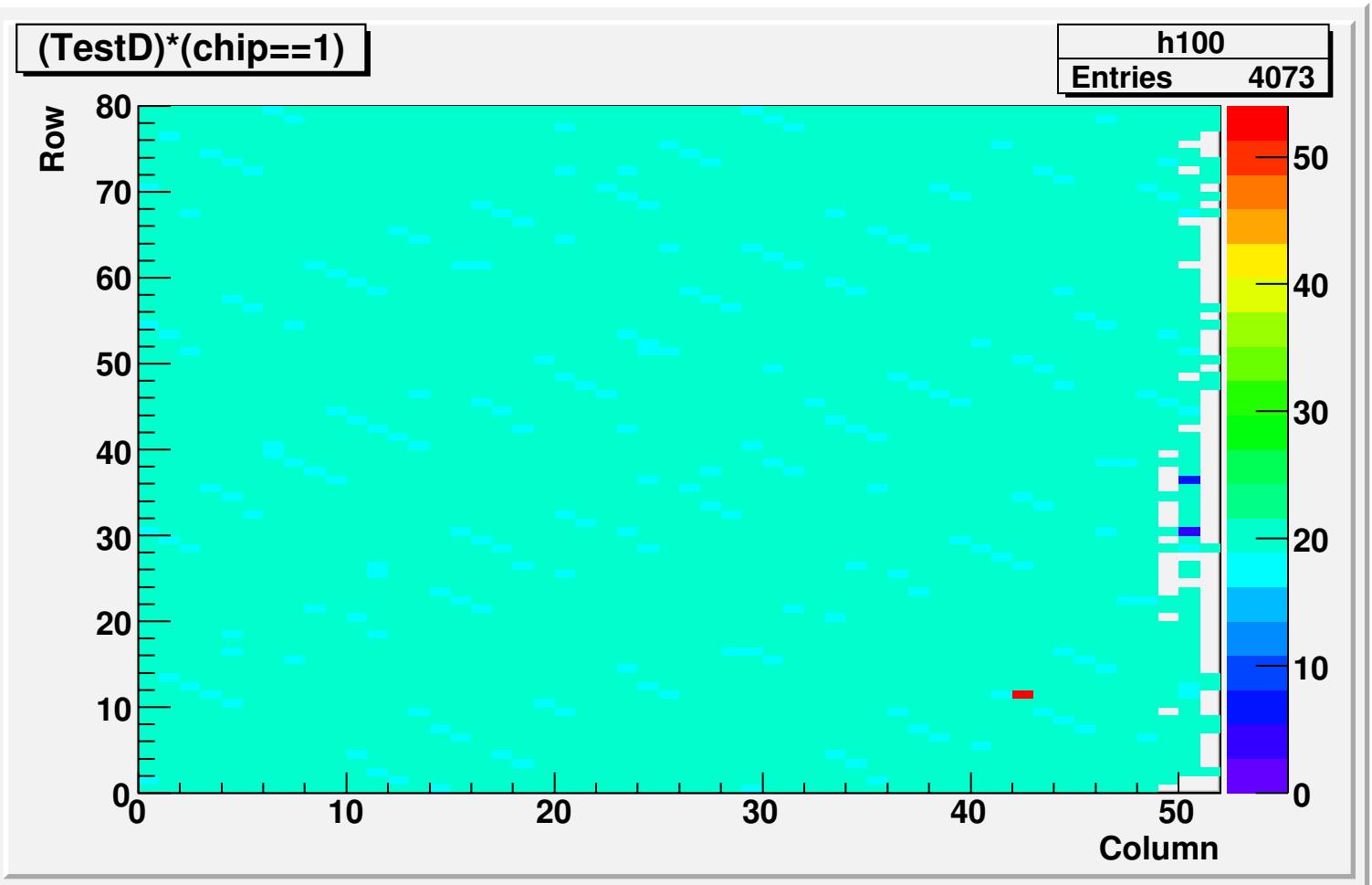

R1 Levels All Chips
addr4[0]
Entries 37337
10^2
10
1
1500
2000
2500
3000
3500

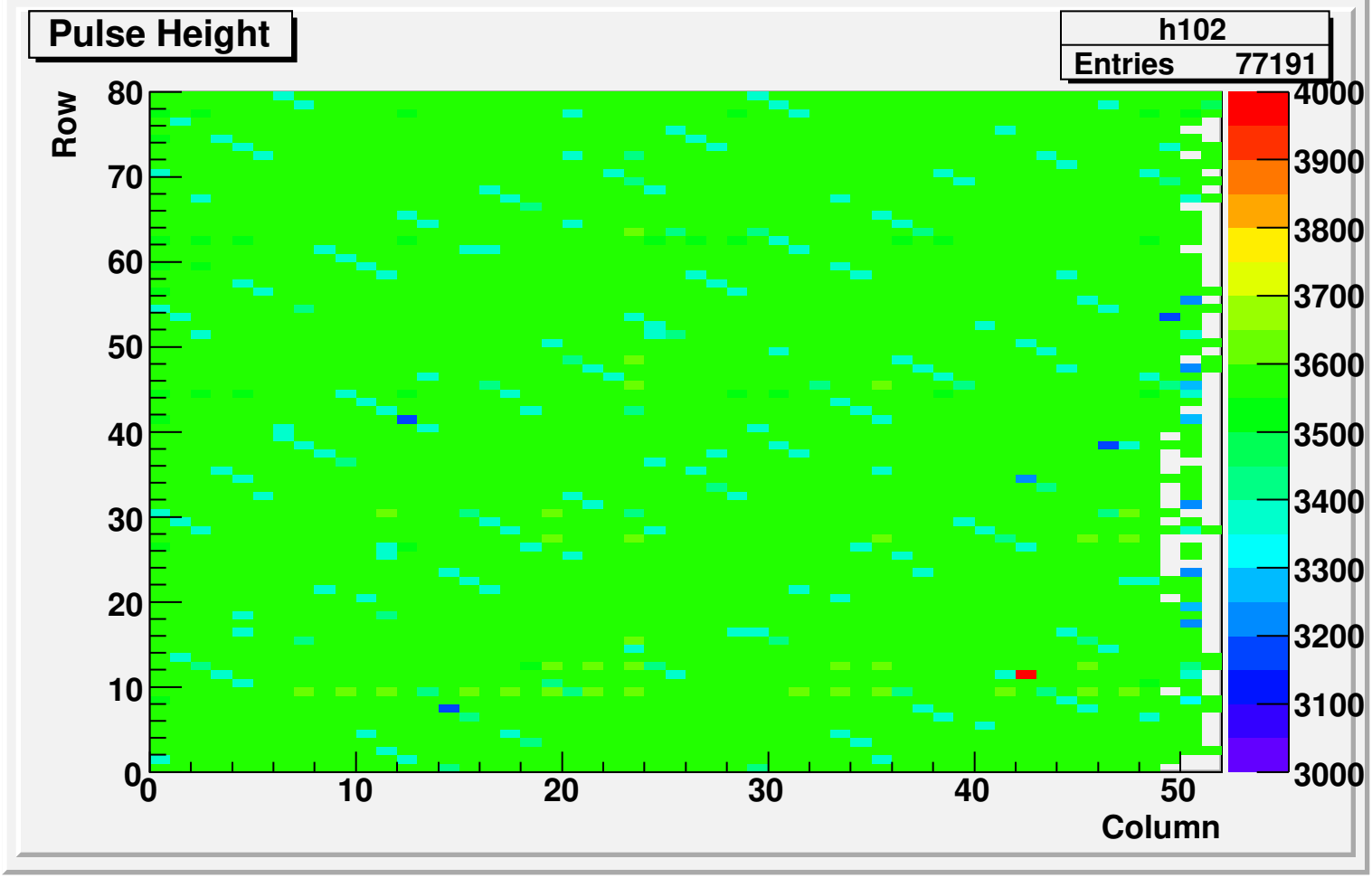

R0 Levels All Chips
addr5[0]
Entries 37337
10^2
10
1
1500
2000
2500
3000
3500

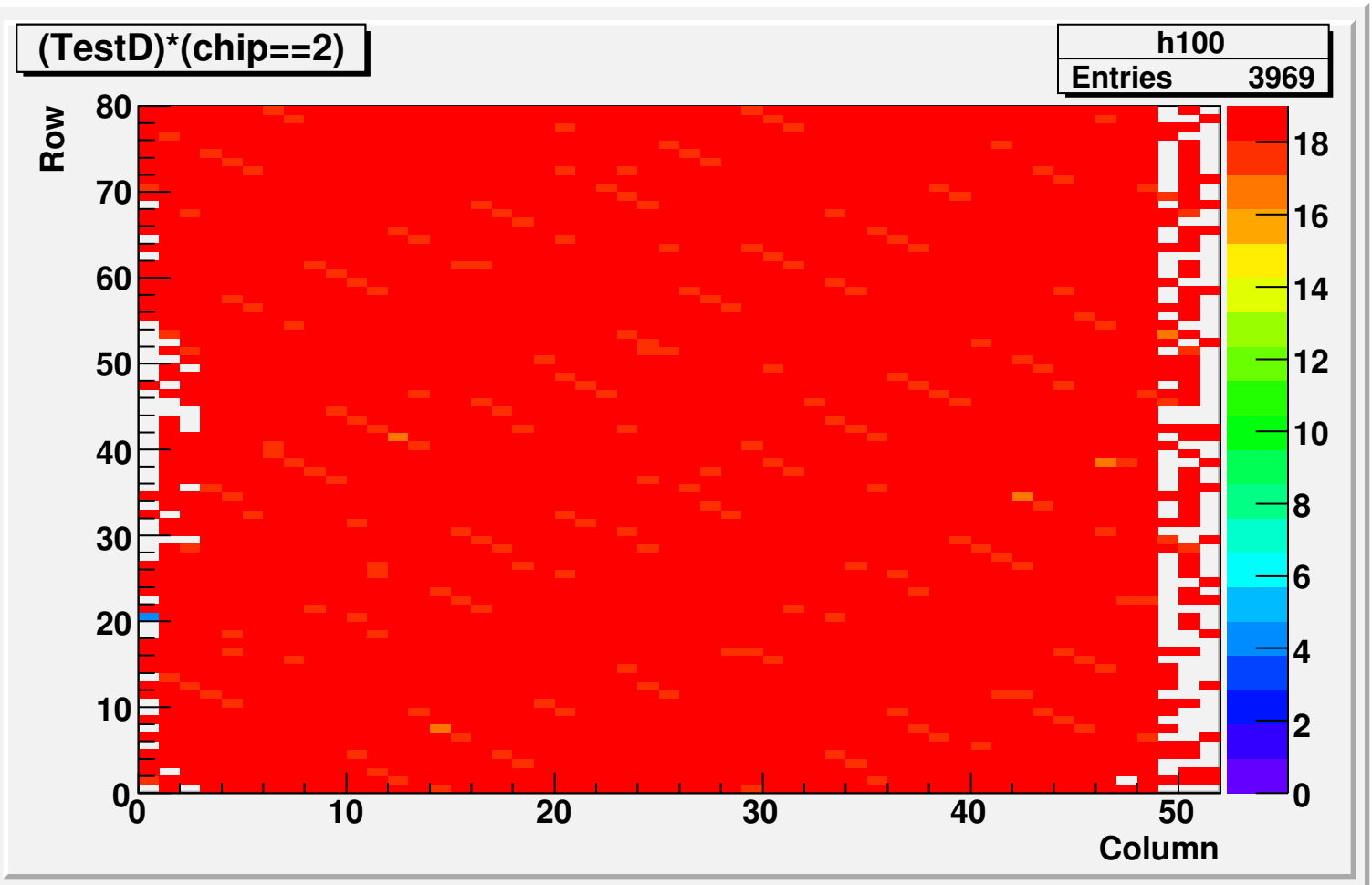

Chip # 0
Chip # 1
Chip # 2
Chip # 3
Chip # 4

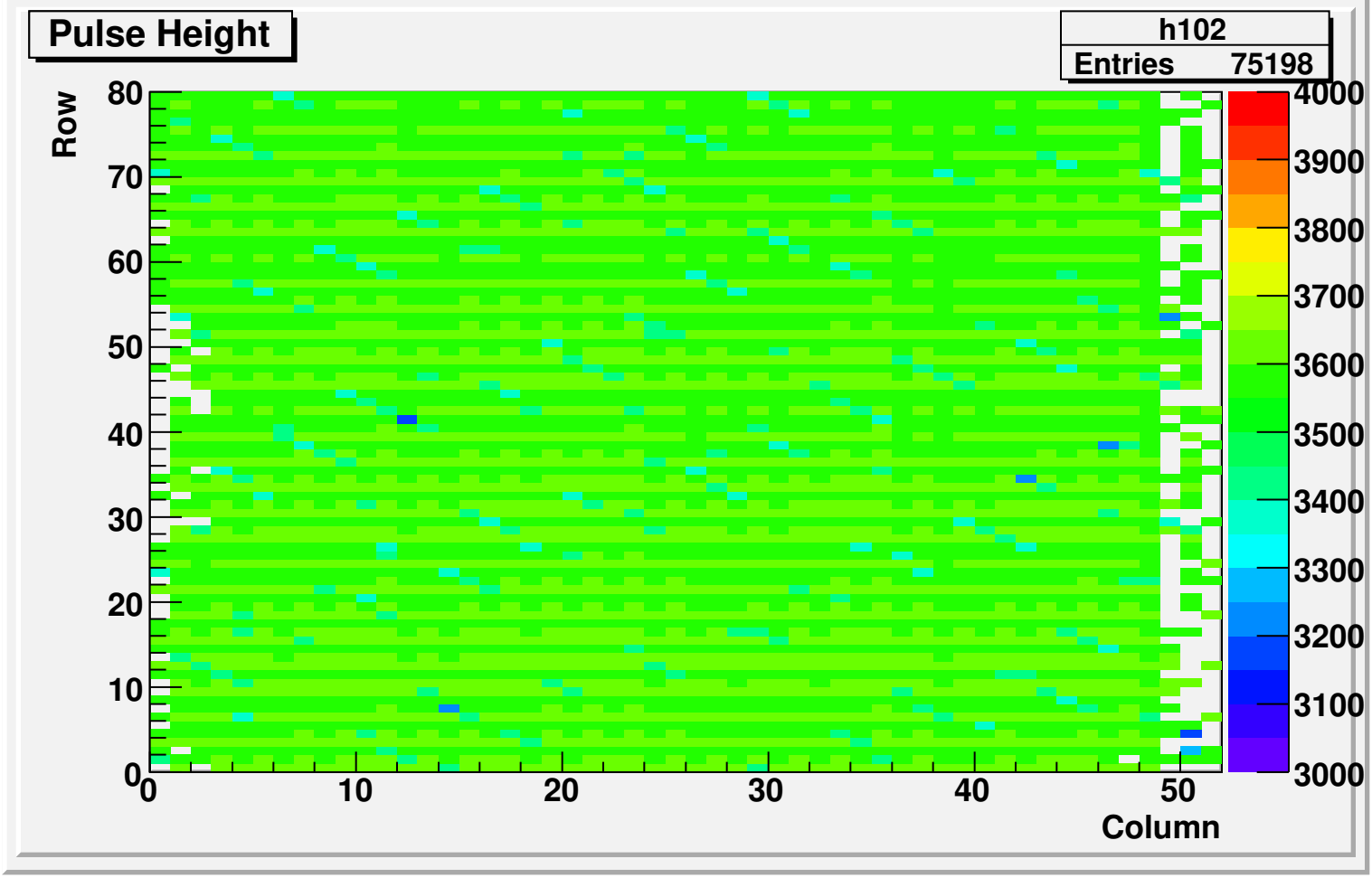

(TestD)*(chip==0)
h100
Entries 4160
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

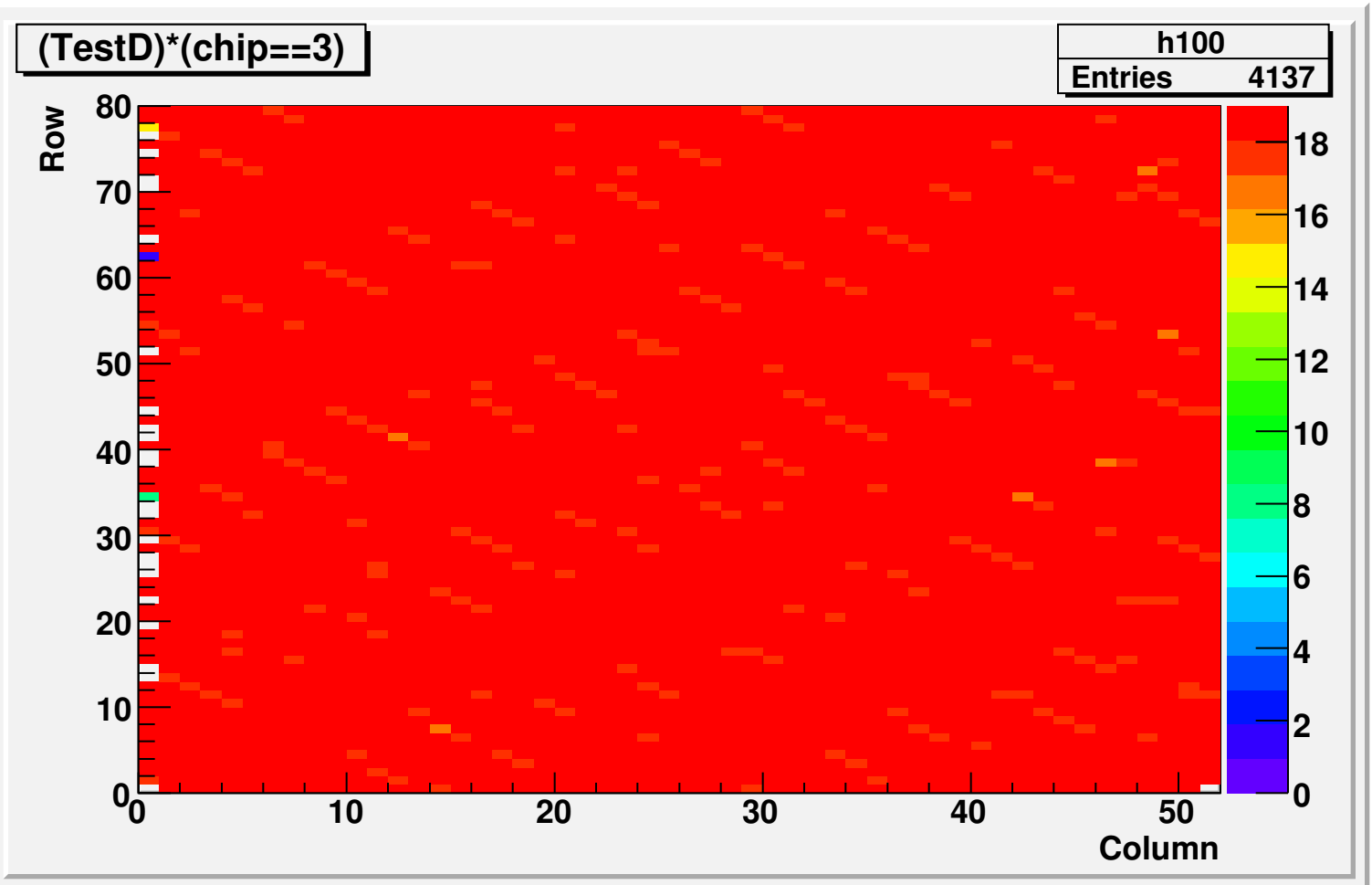

Pulse Height
h102
Entries 78835
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

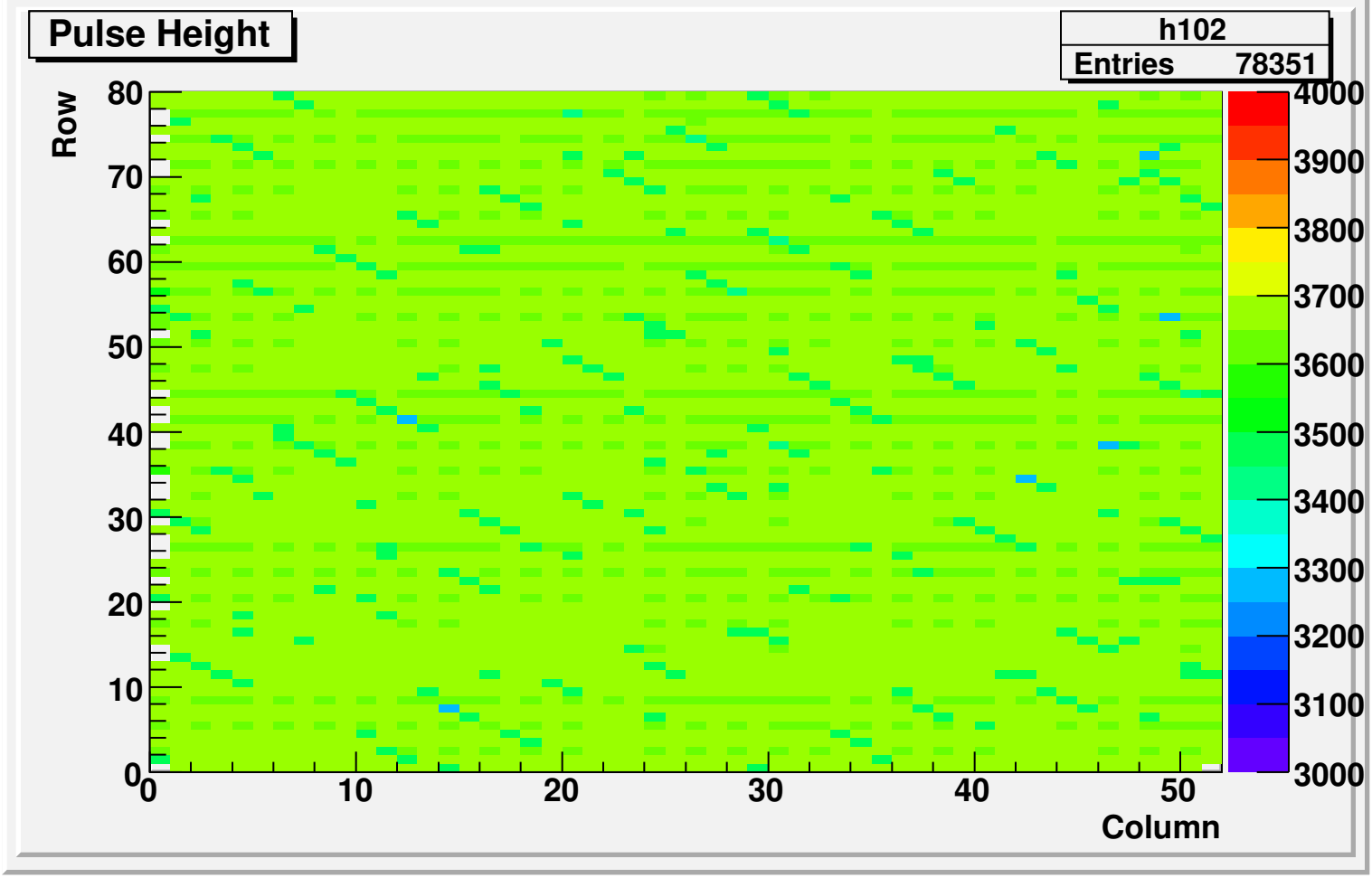

(TestD)*(chip==1)
h100
Entries 4073
Row
Column
0
10
20
30
40
50
60
70
80
0
10
20
30
40
50

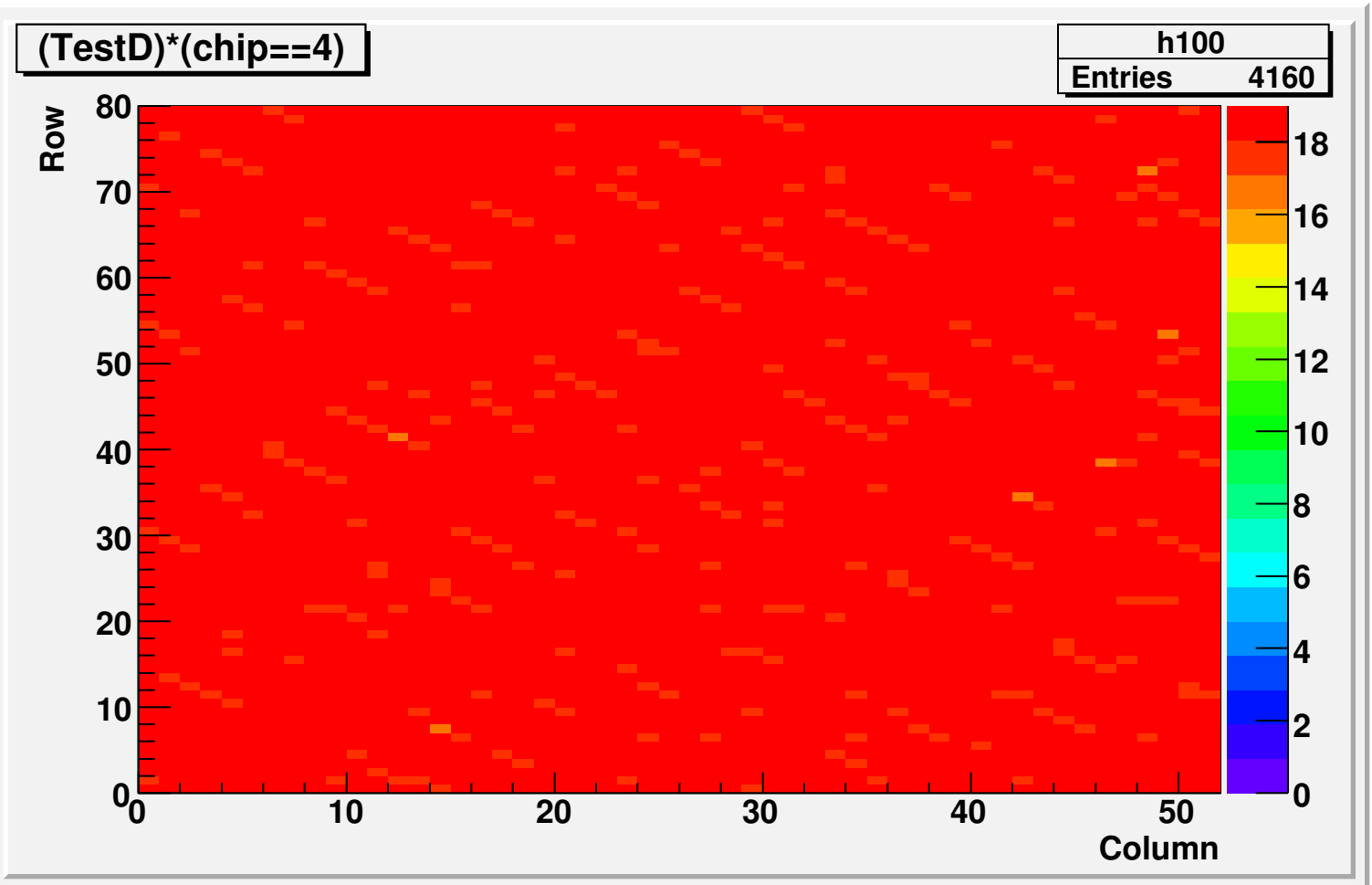

Pulse Height
h102
Entries 77191
Row
Column
0
10
20
30
40
50
60
70
80
0
10
20
30
40
50
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

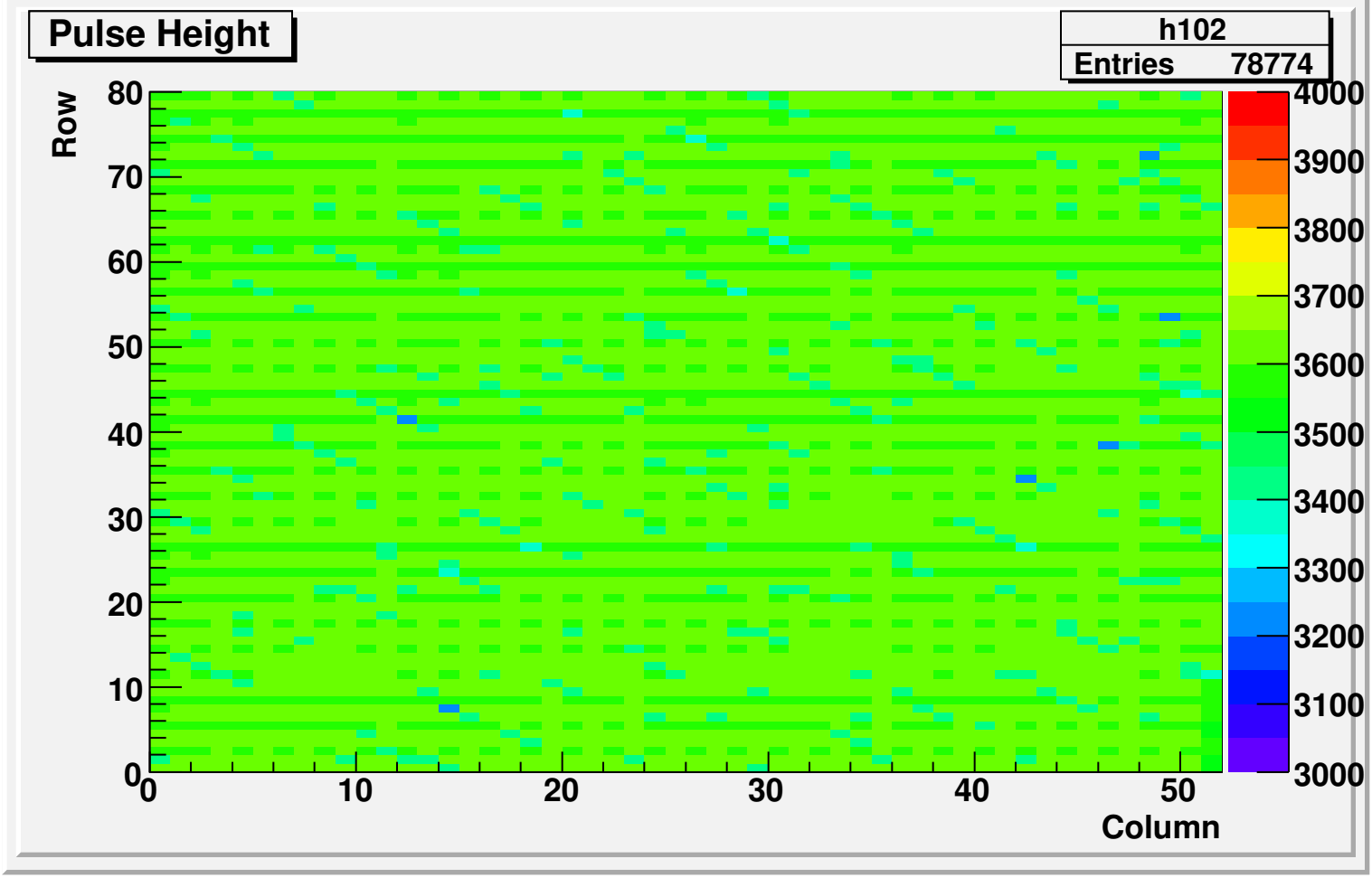

(TestD)*(chip==2)
h100
Entries 3969
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

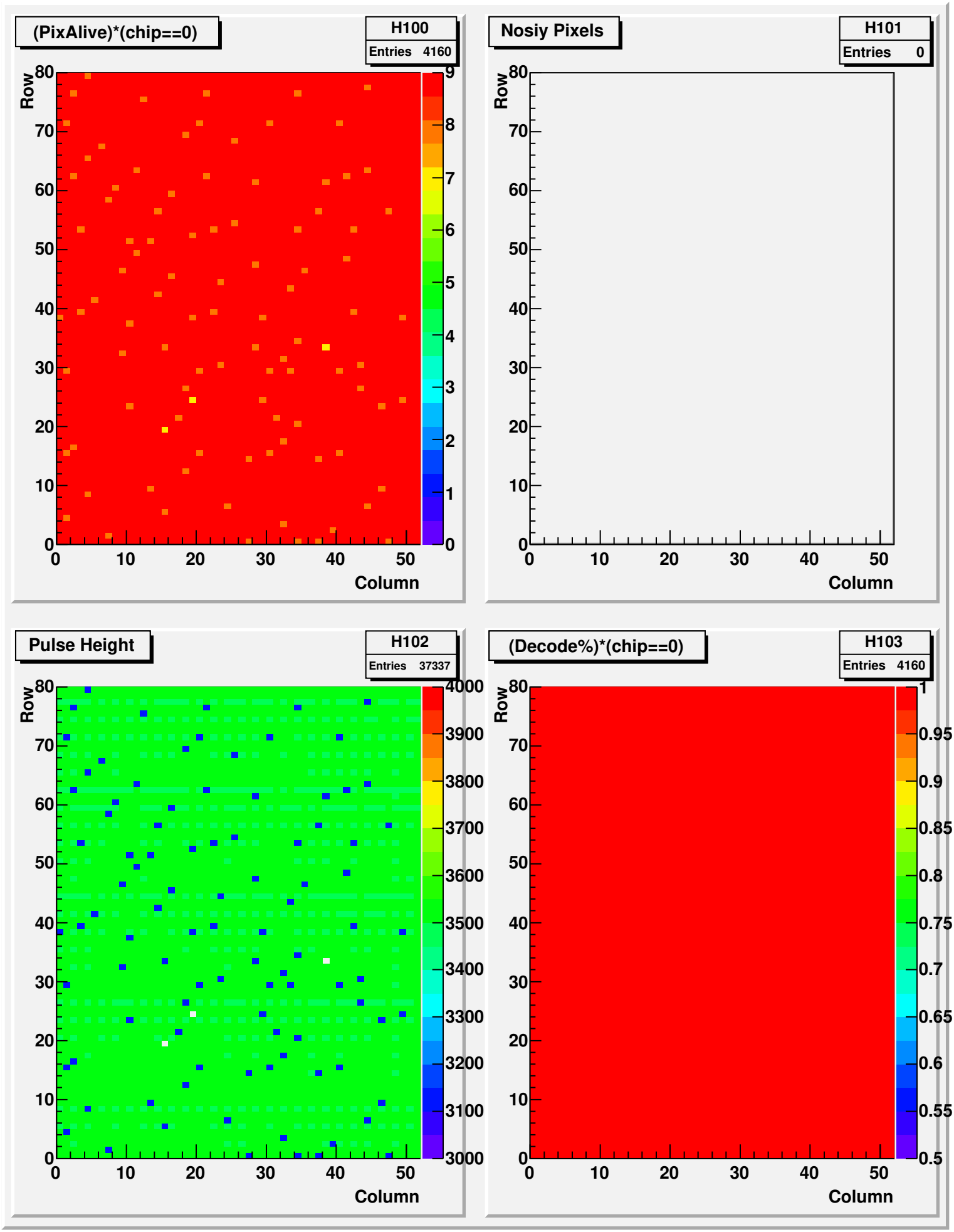

Pulse Height
h102
Entries 75198
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

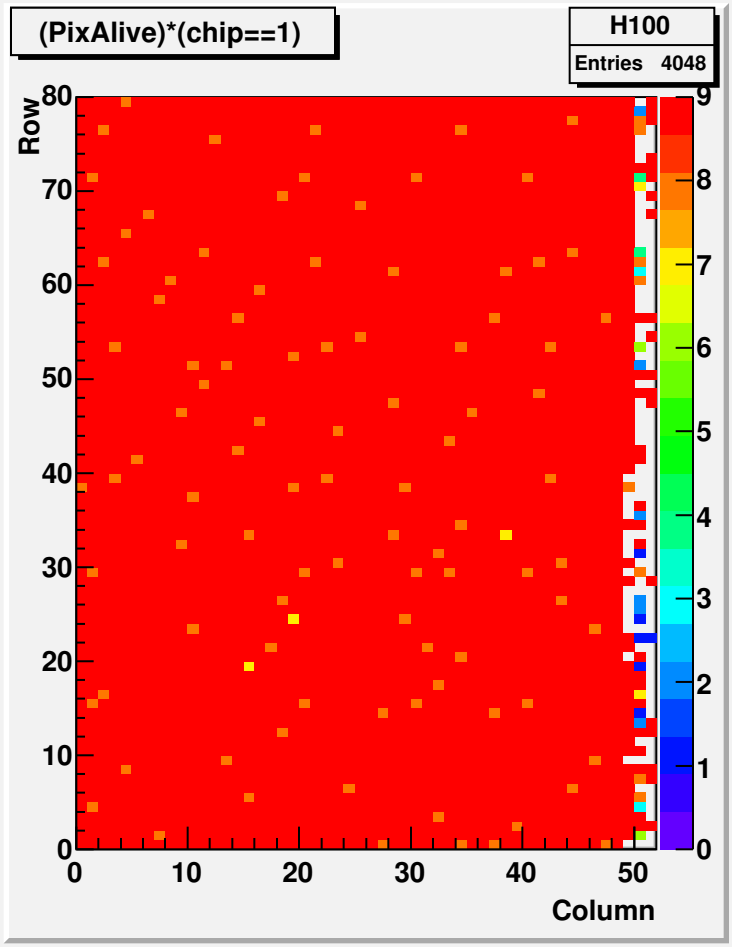

(TestD)*(chip==3)
h100
Entries 4137
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

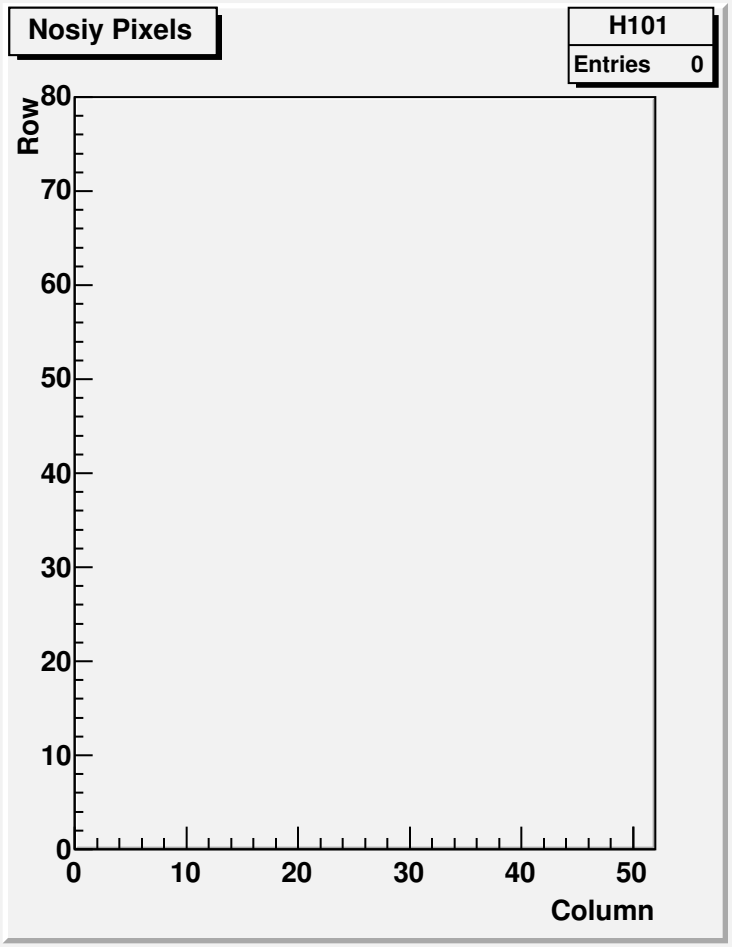

Pulse Height
h102
Entries 78351
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

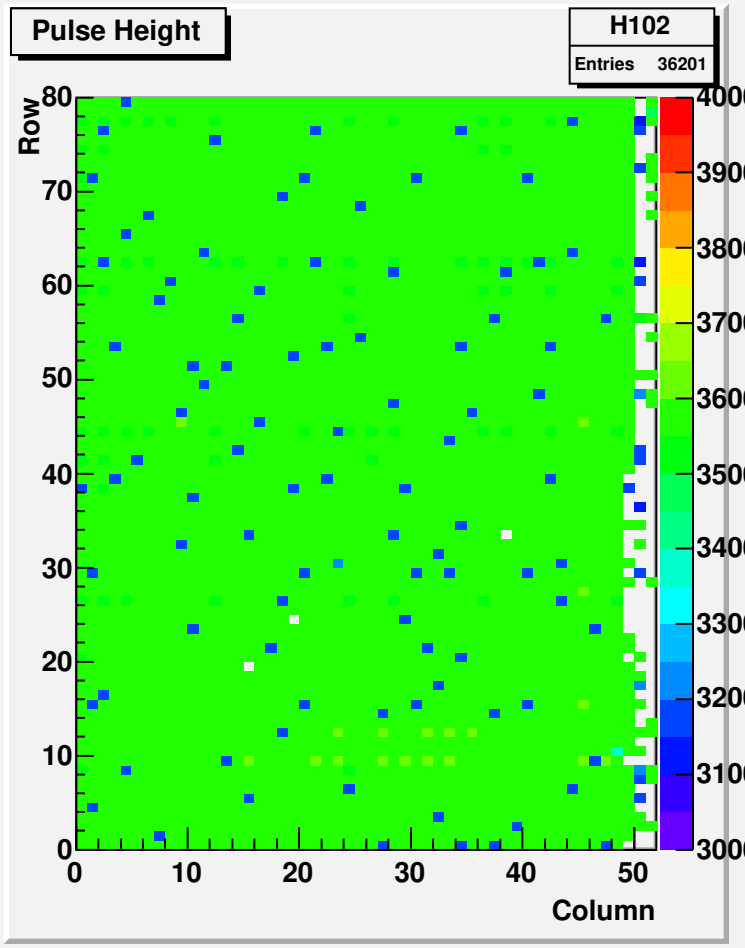

(TestD)*(chip==4)
h100
Entries 4160
Row
Column
0
10
20
30
40
50
60
70
80
0
2
4
6
8
10
12
14
16
18

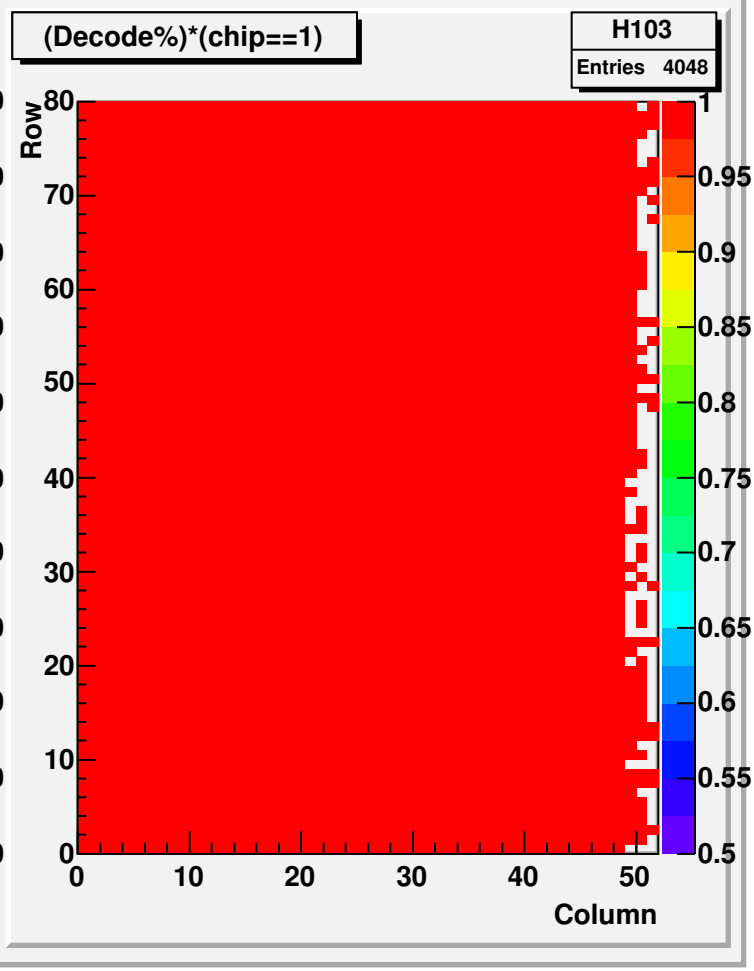

Pulse Height
h102
Entries 78774
Row
Column
0
10
20
30
40
50
60
70
80
3000
3100
3200
3300
3400
3500
3600
3700
3800
3900
4000

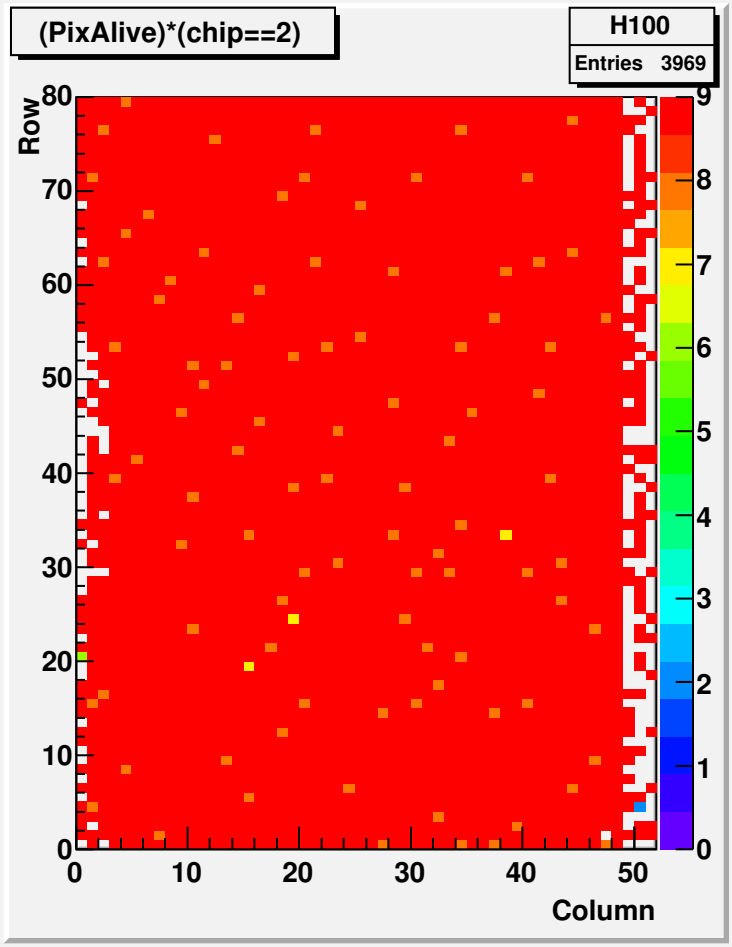

(PixAlive)*(chip==0)
H100
Entries 4160
Row
Column
Nosiy Pixels
H101
Entries 0
Row
Column
Pulse Height
H102
Entries 37337
Row
Column
(Decode%)*(chip==0)
H103
Entries 4160
Row
Column

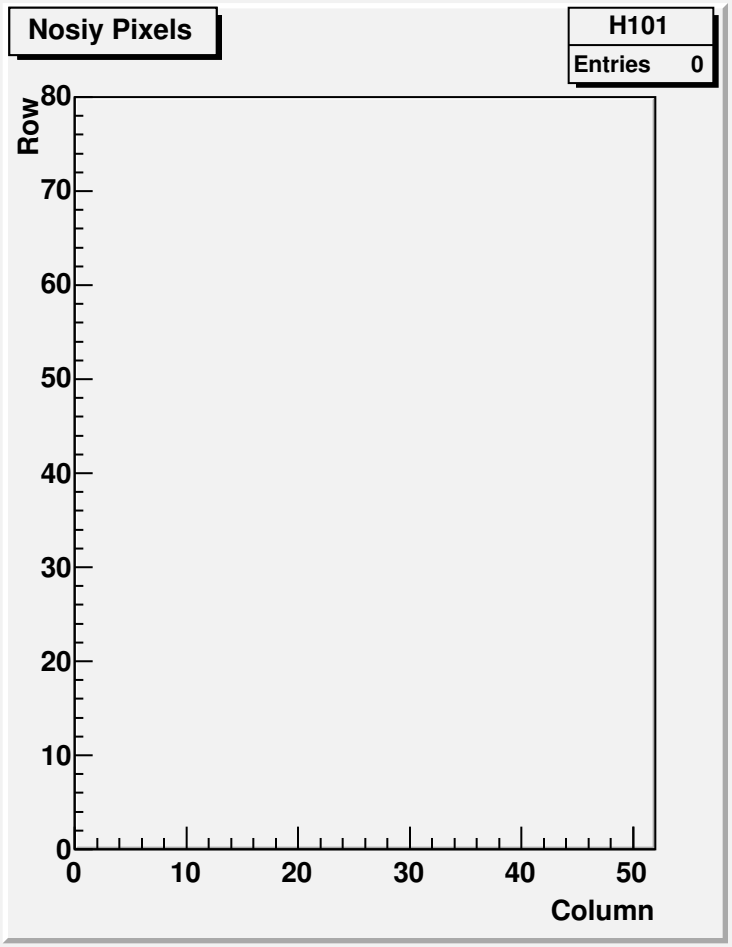

(PixAlive)*(chip==1)
H100
Entries 4048
Row
Column

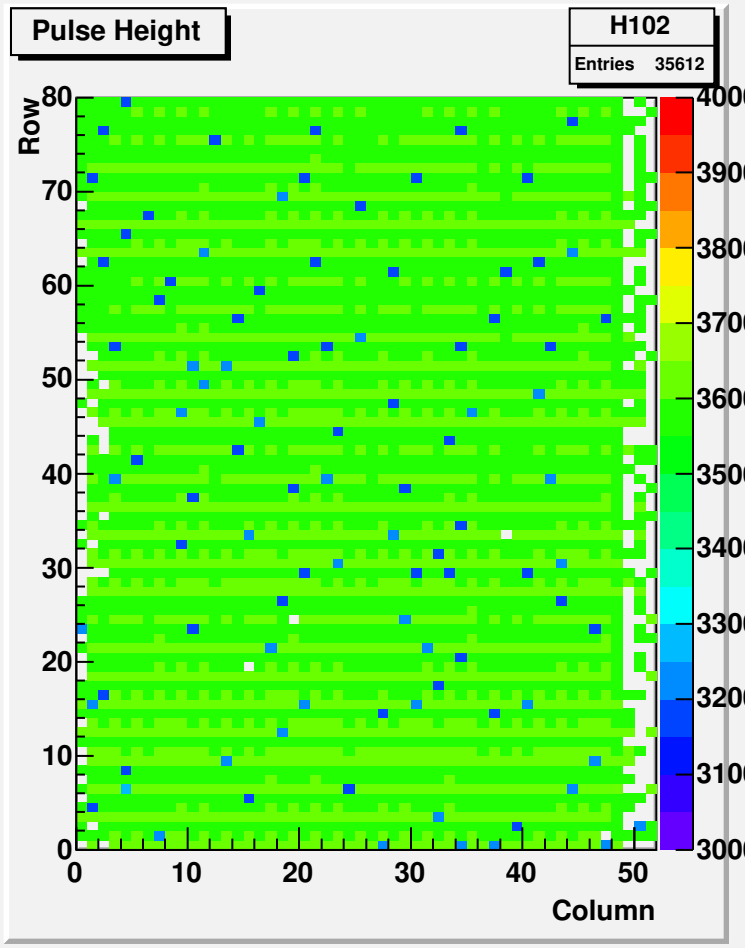

Nosiy Pixels
H101
Entries 0
Row
Column

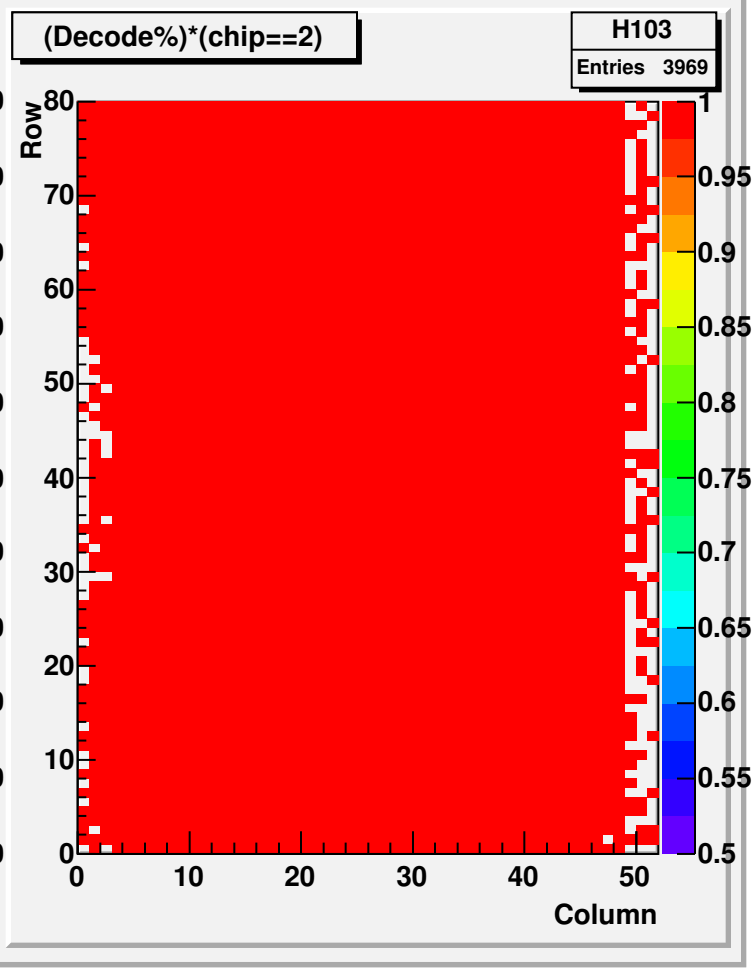

Pulse Height
H102
Entries 36201
Row
Column

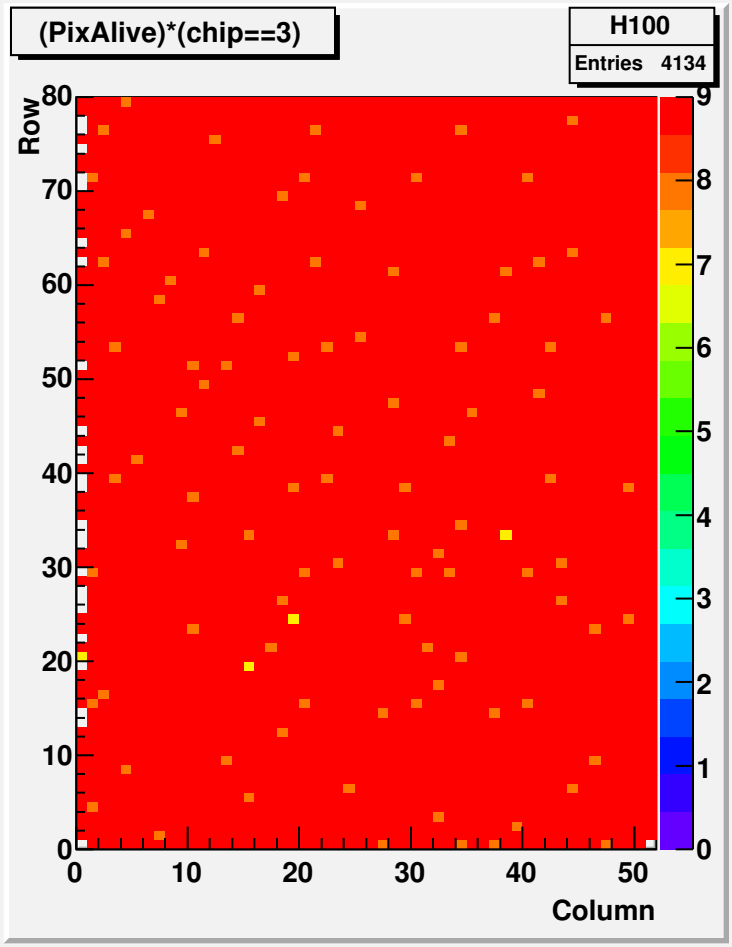

(Decode%)*(chip==1)
H103
Entries 4048
Row
Column

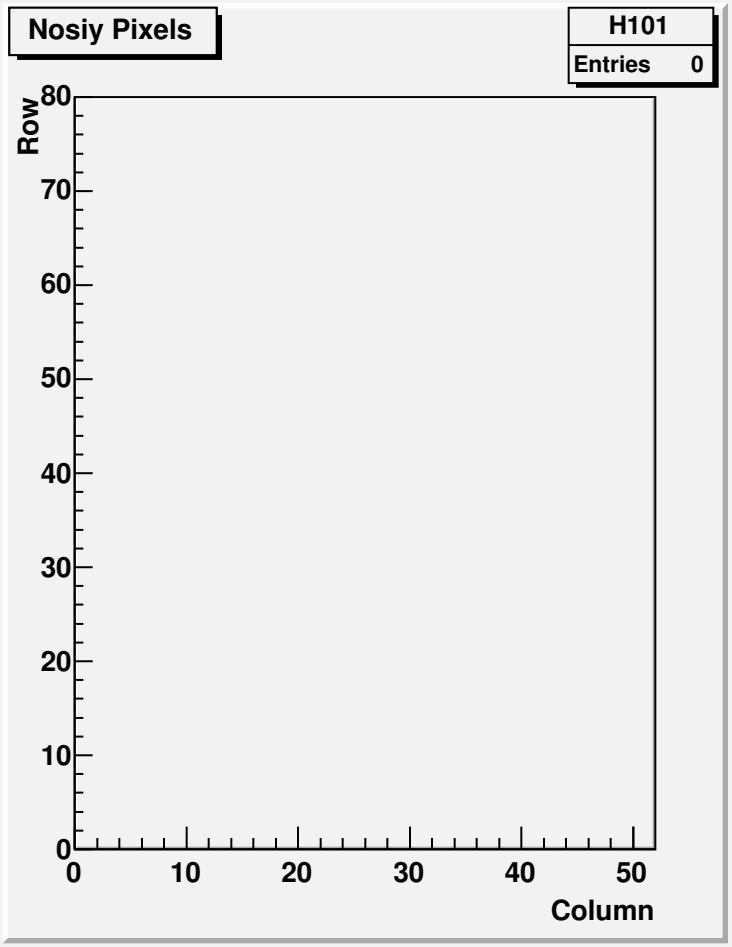

(PixAlive)*(chip==2)
H100
Entries 3969
Row
Column

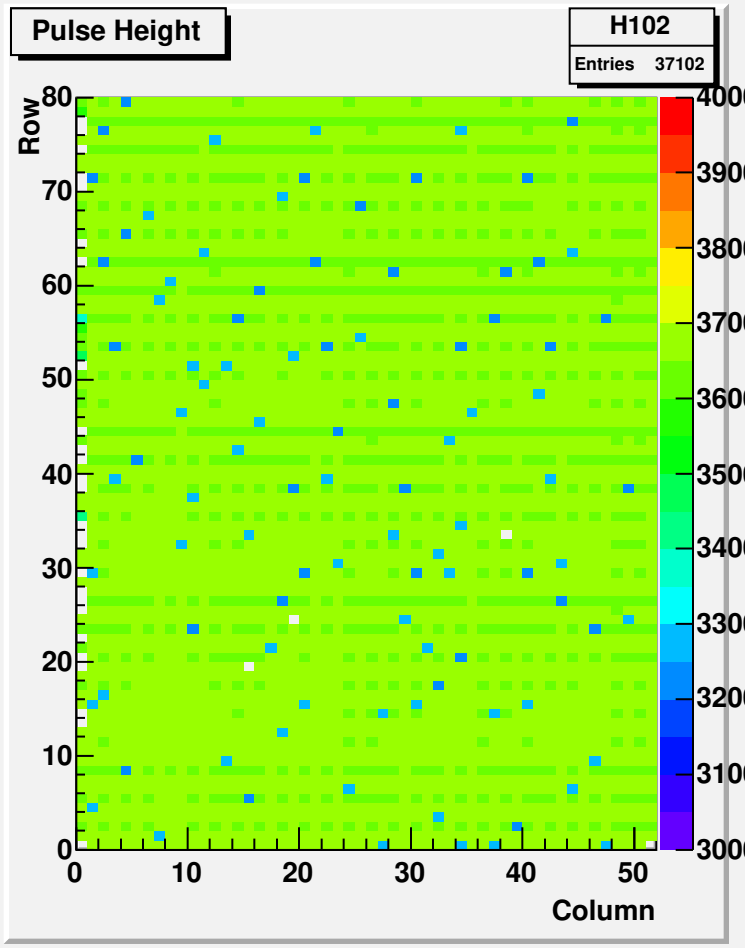

Nosiy Pixels
H101
Entries 0
Row
Column

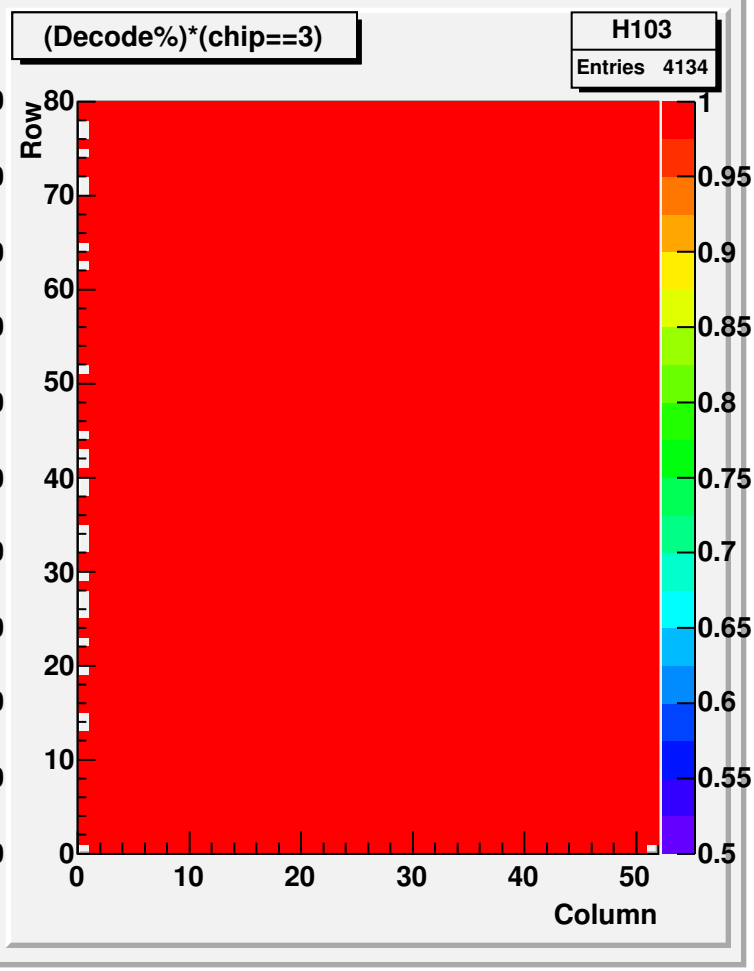

Pulse Height
H102
Entries 35612
Row
Column

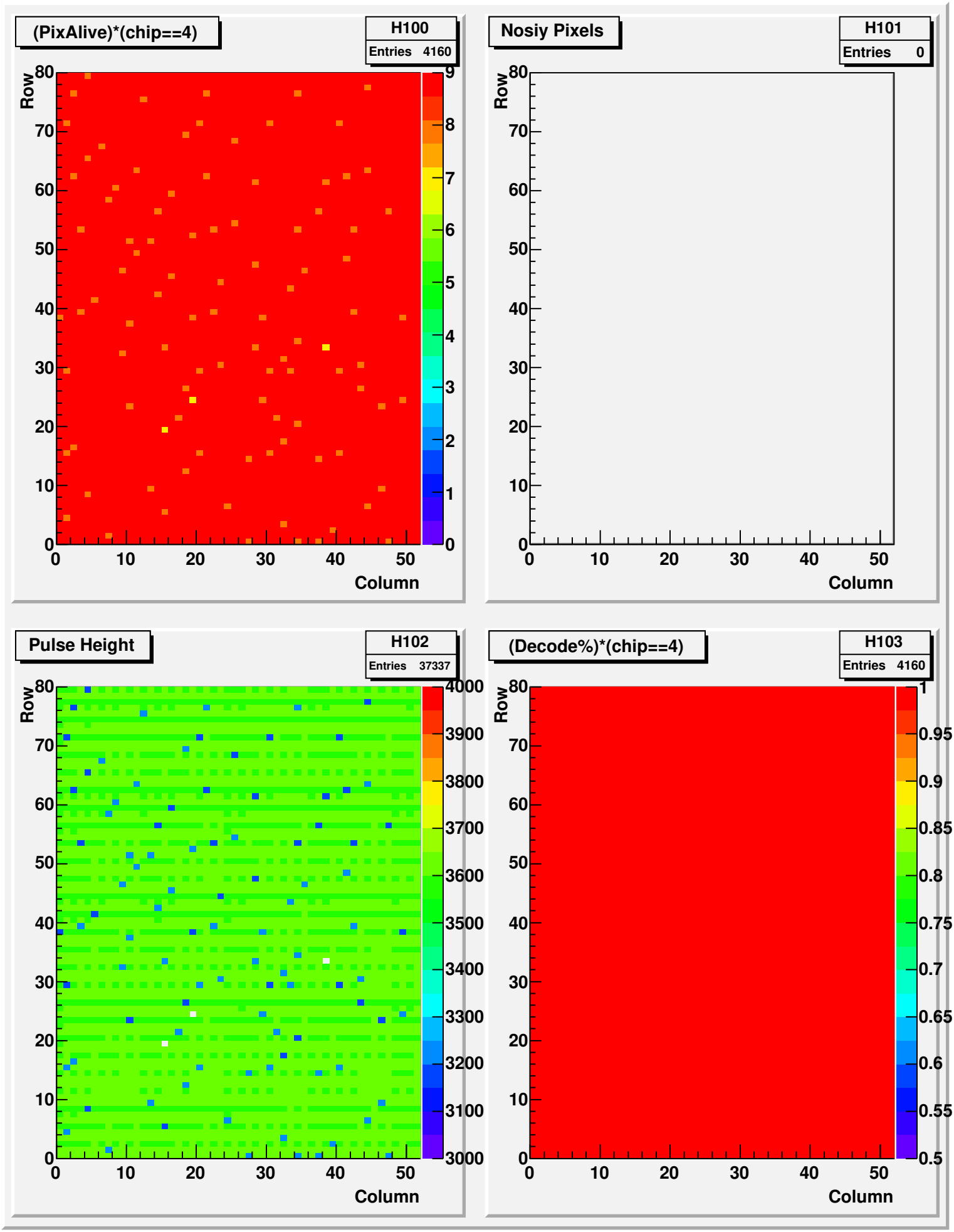

(Decode%)*(chip==2)
H103
Entries 3969
Row
Column


(PixAlive)*(chip==3)
H100
Entries 4134
Row
Column


Nosiy Pixels
H101
Entries 0
Row
Column


Pulse Height
H102
Entries 37102
Row
Column


(Decode%)*(chip==3)
H103
Entries 4134
Row
Column


(PixAlive)*(chip==4)
H100
Entries 4160
Row
Column
Nosiy Pixels
H101
Entries 0
Row
Column
Pulse Height
H102
Entries 37337
Row
Column
(Decode%)*(chip==4)
H103
Entries 4160
Row
Column