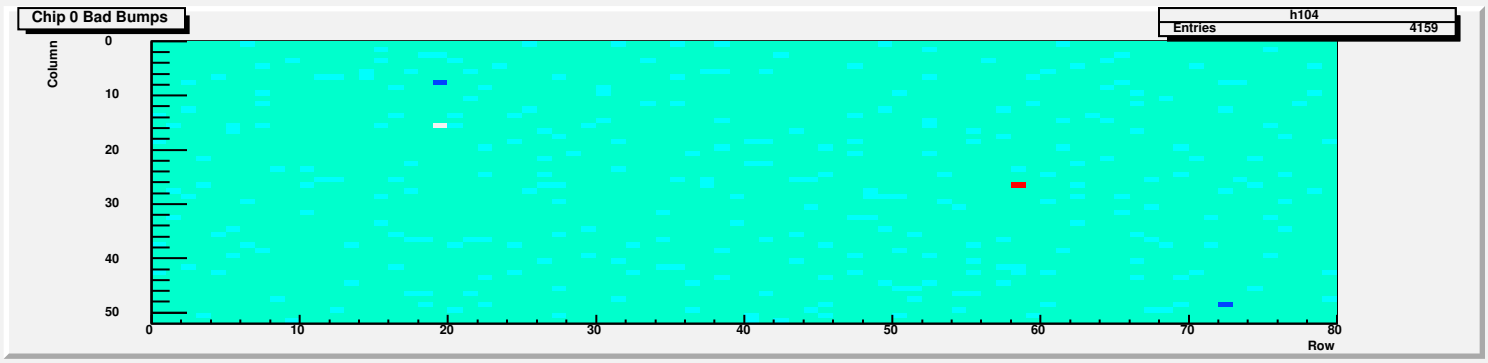
Chip 0 Bad Bumps
h104
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Column
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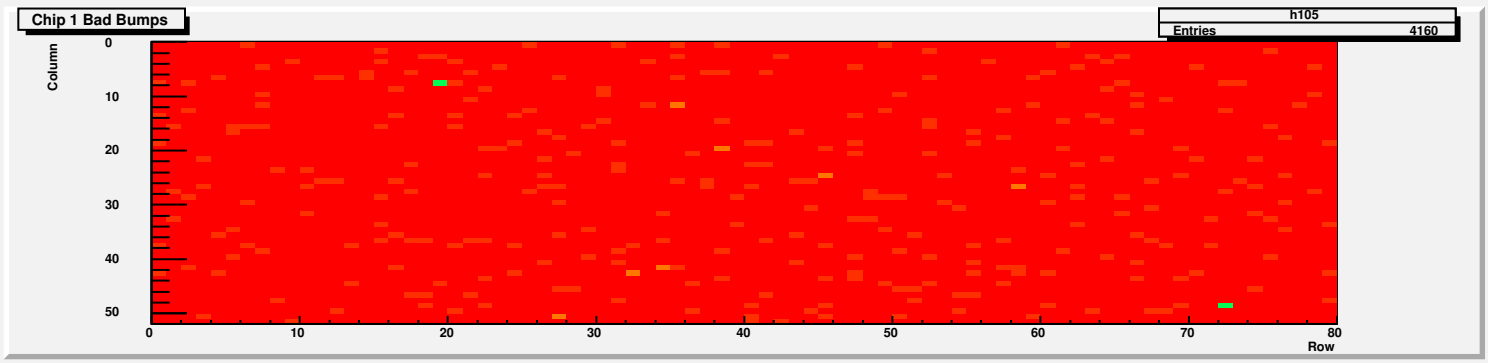
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h105
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Column
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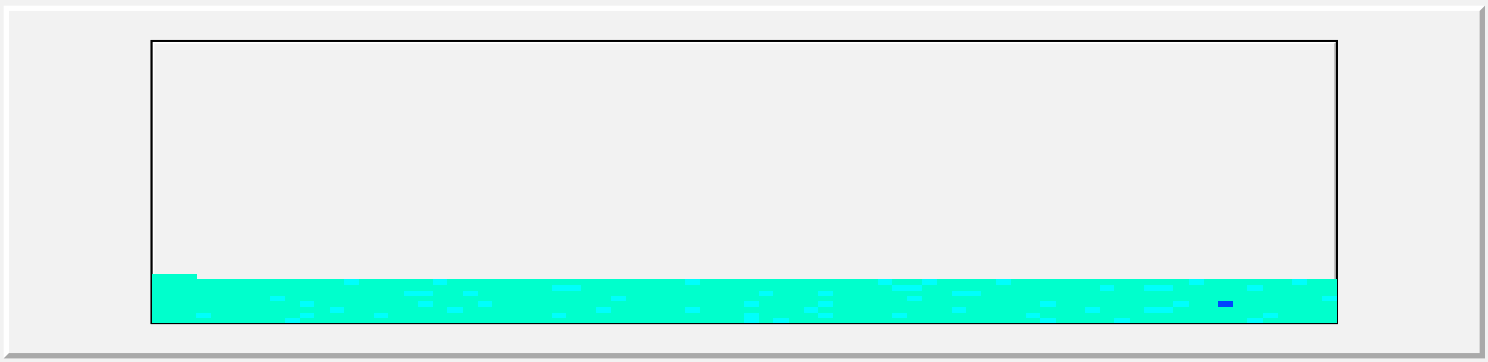

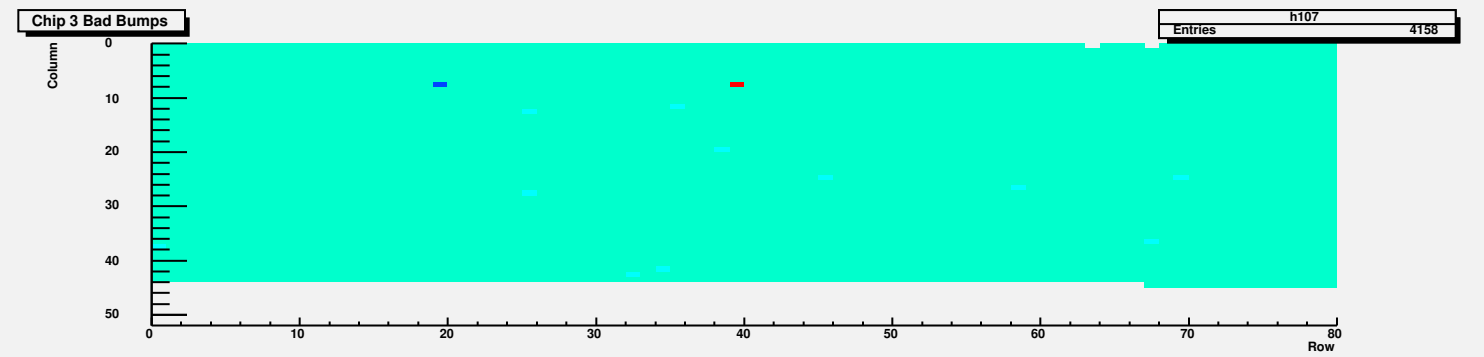
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h107
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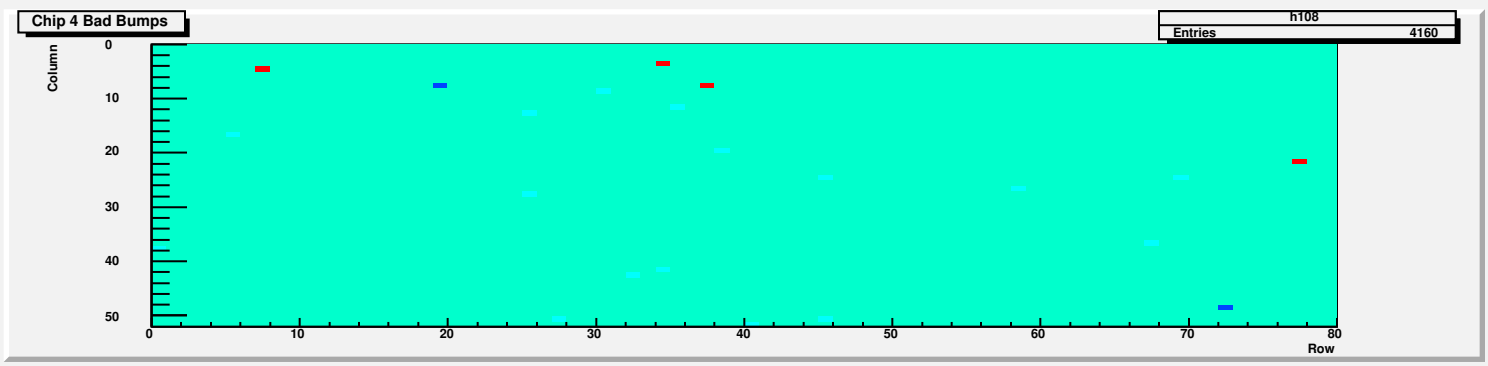
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Entries 4160
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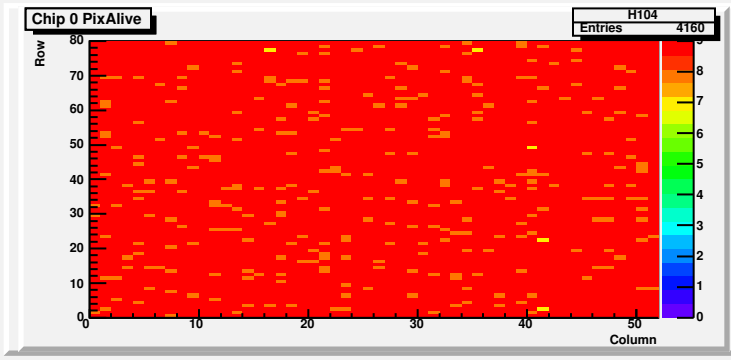
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H104
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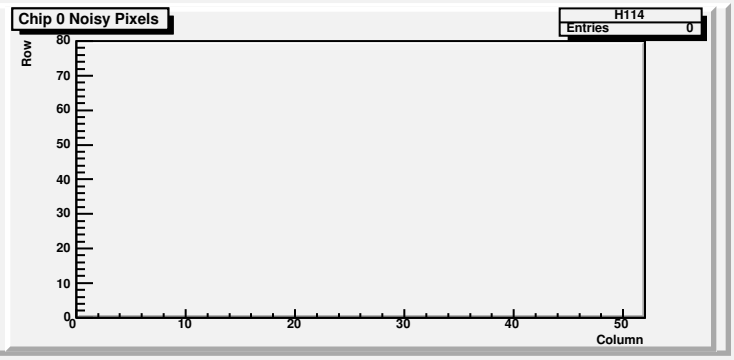
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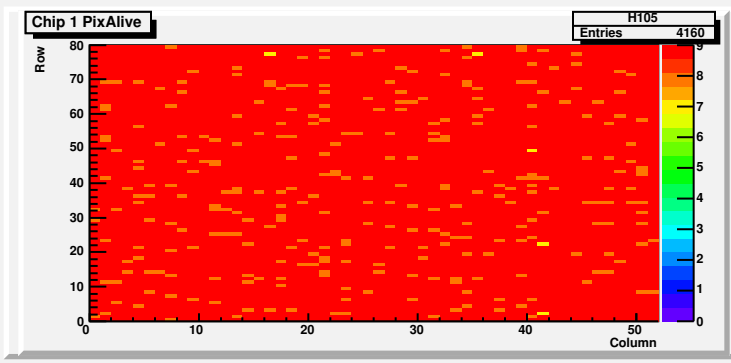
Column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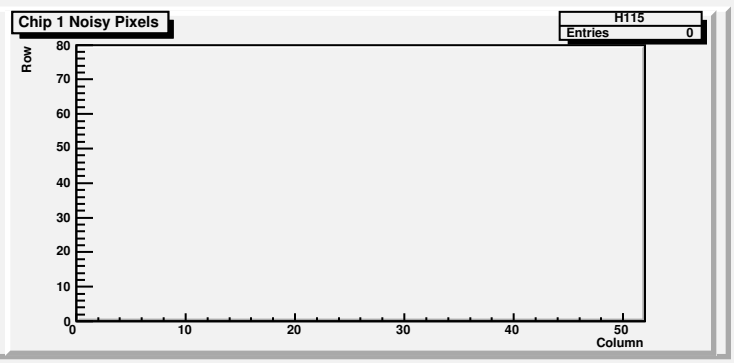
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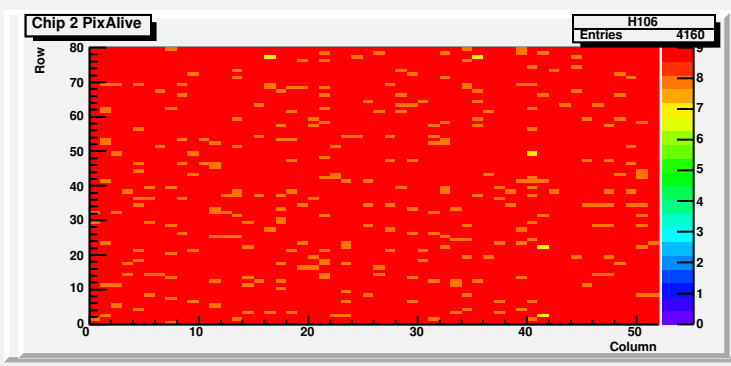
Chip 2 PixAlive
H106
Entries 4160
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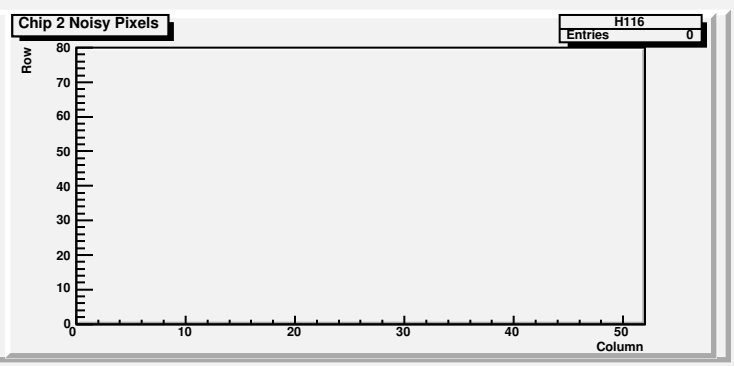
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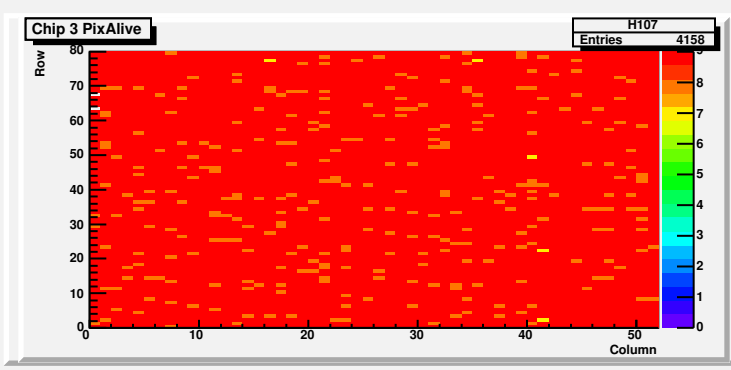
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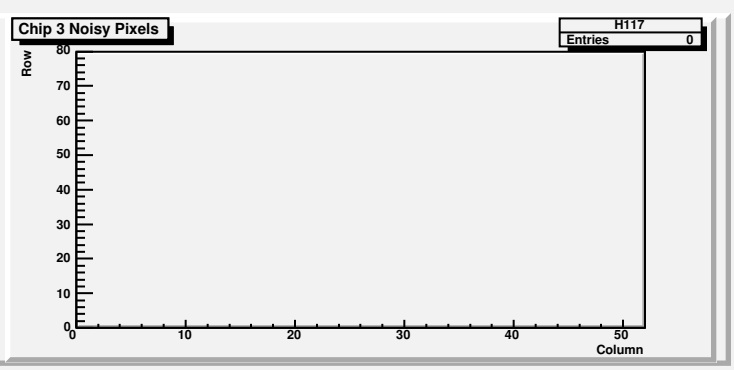
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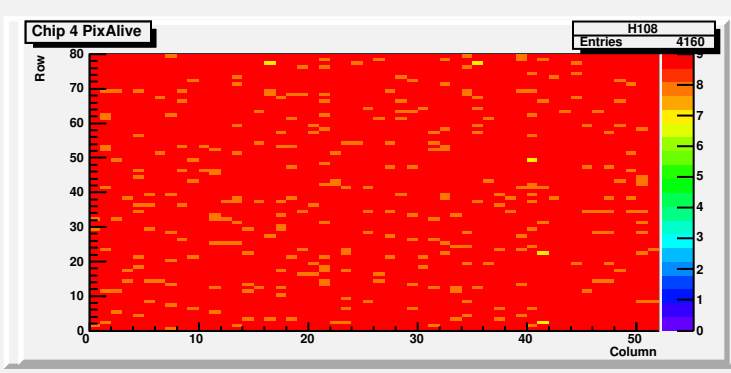
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Entries 4160
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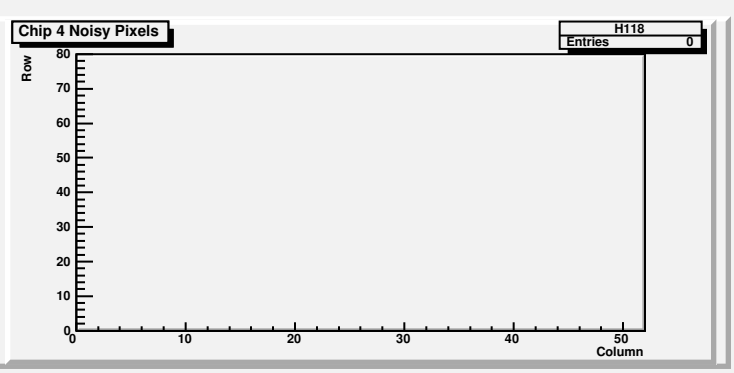
Column


Chip 4 Noisy Pixels
H118
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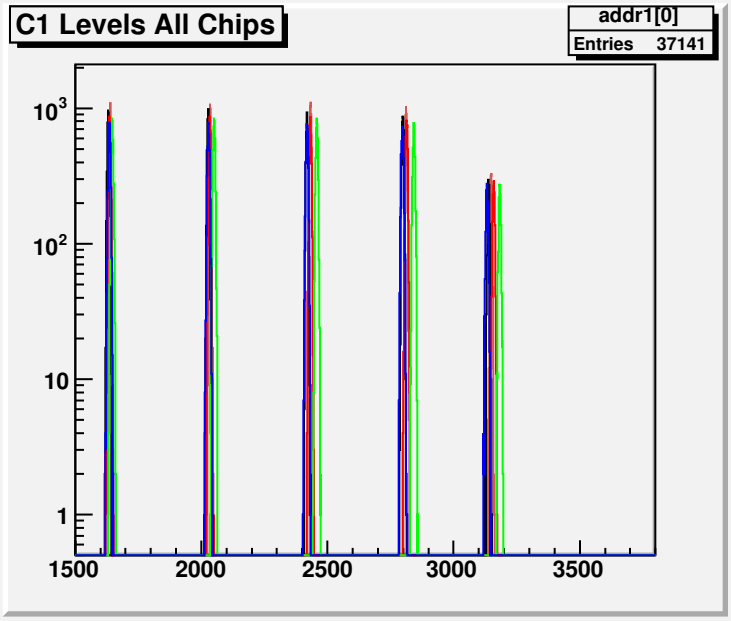
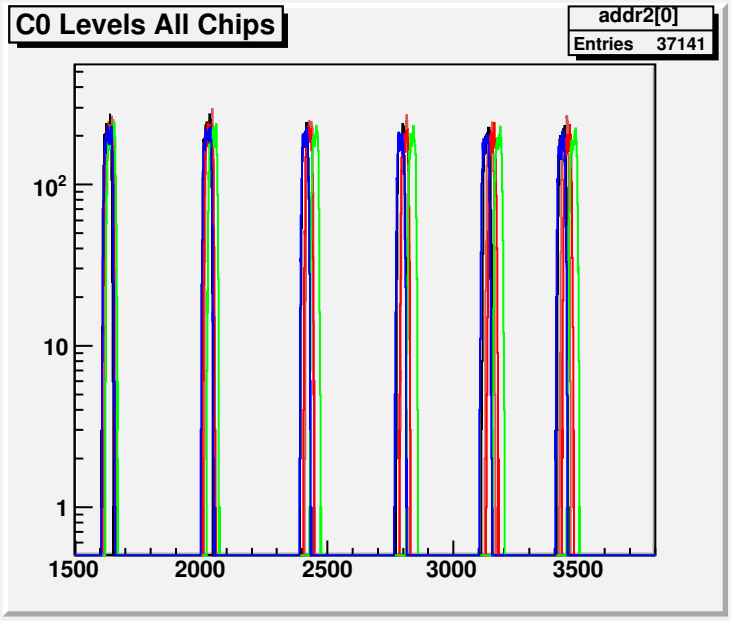
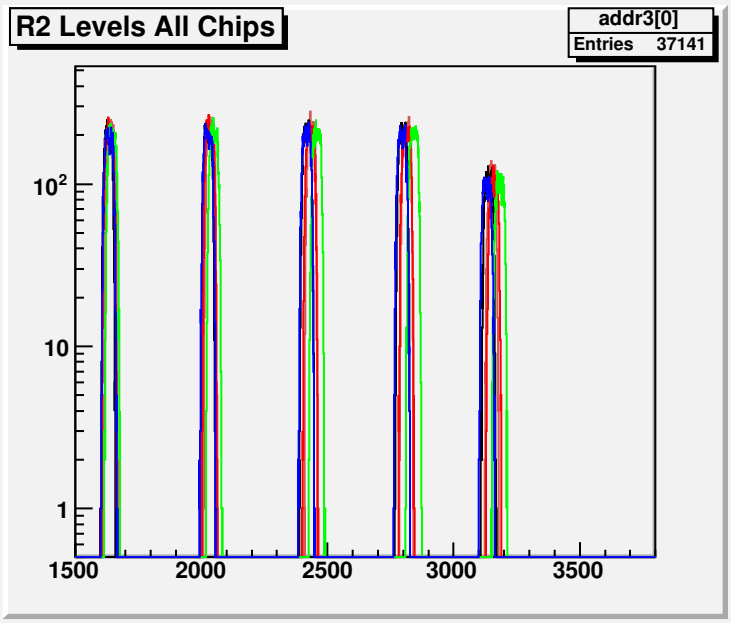

C1 Levels All Chips
addr1[0]
Entries 37141
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37141
10^2
10
1
1500
2000
2500
3000
3500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addr3[0]
Entries 37141
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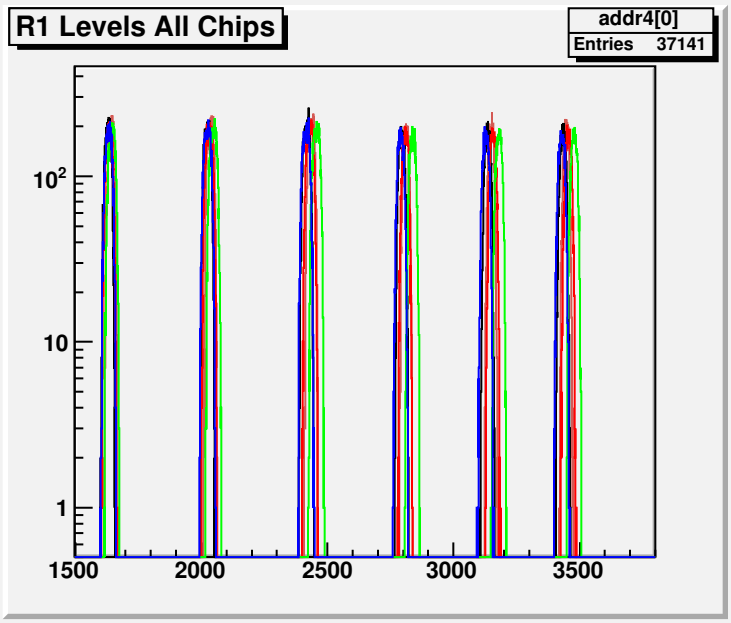
10
1
1500
2000
2500
3000
3500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1500
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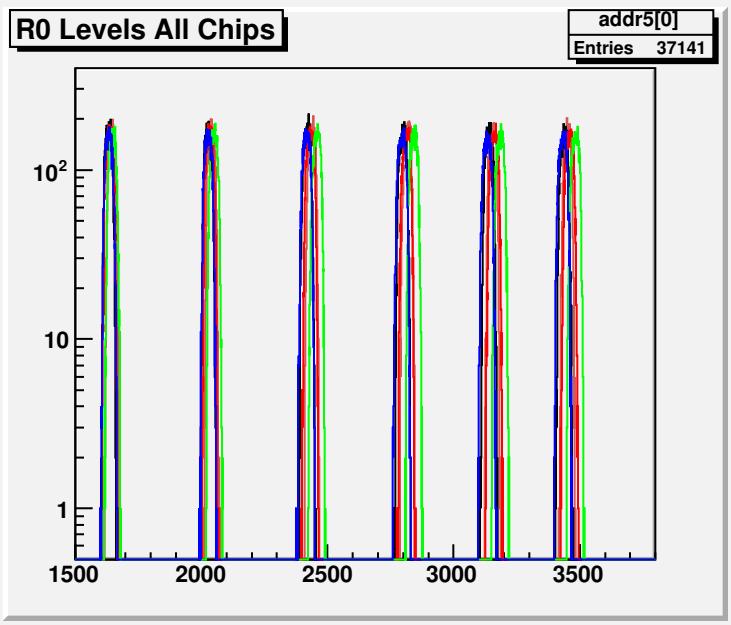
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Entries 37141
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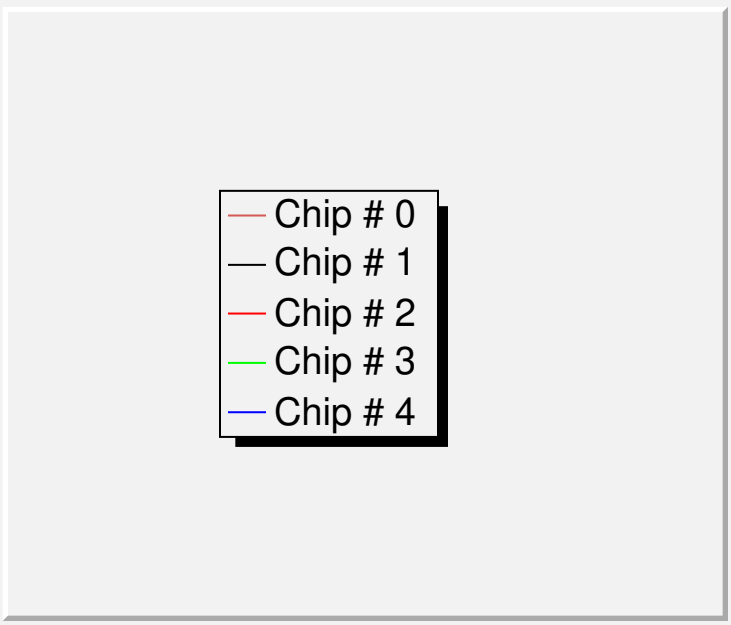
2000
2500
3000
3500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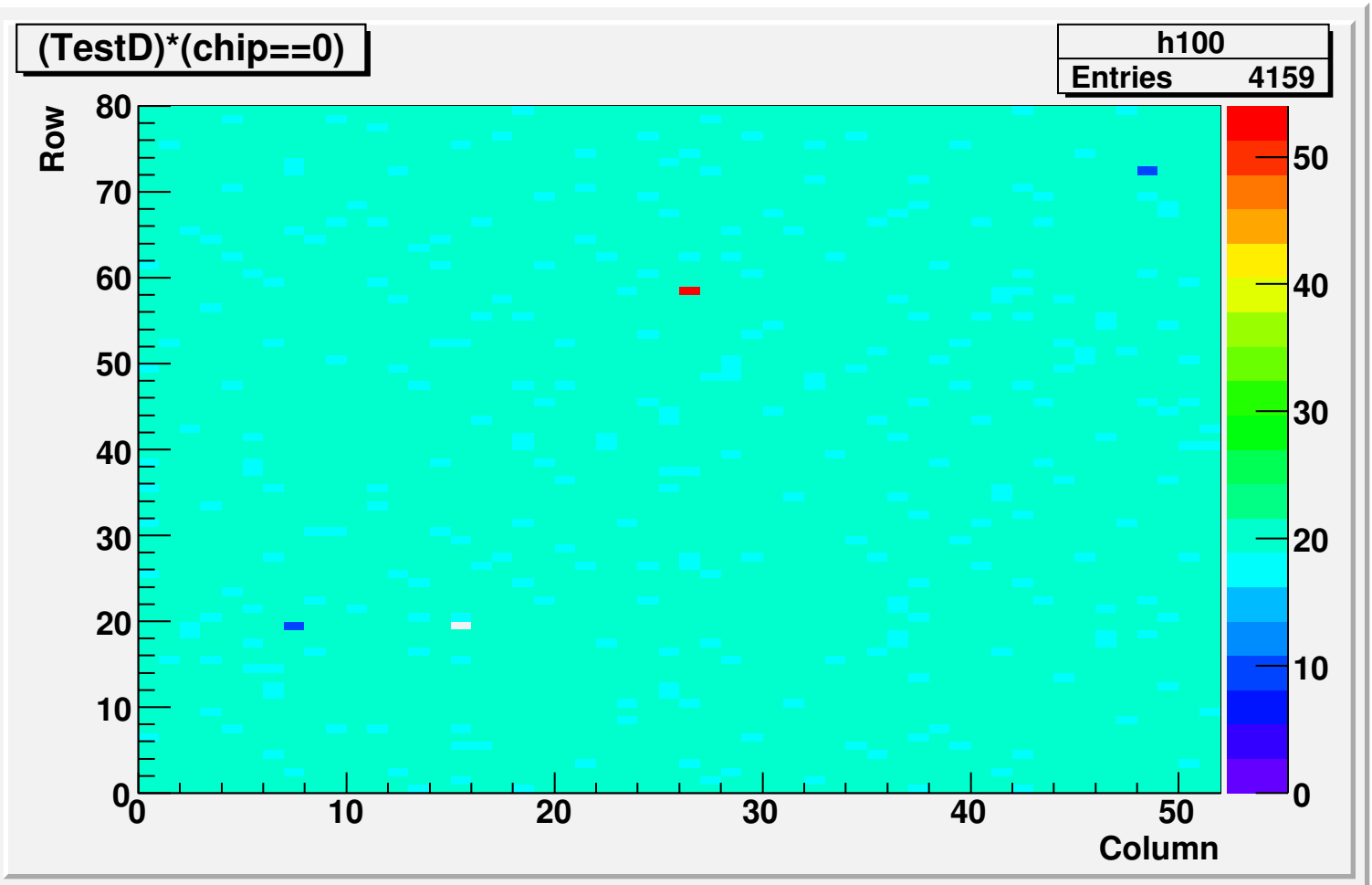
Chip # 1
Chip # 2
Chip # 3
Chip # 4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Entries 4159
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Column
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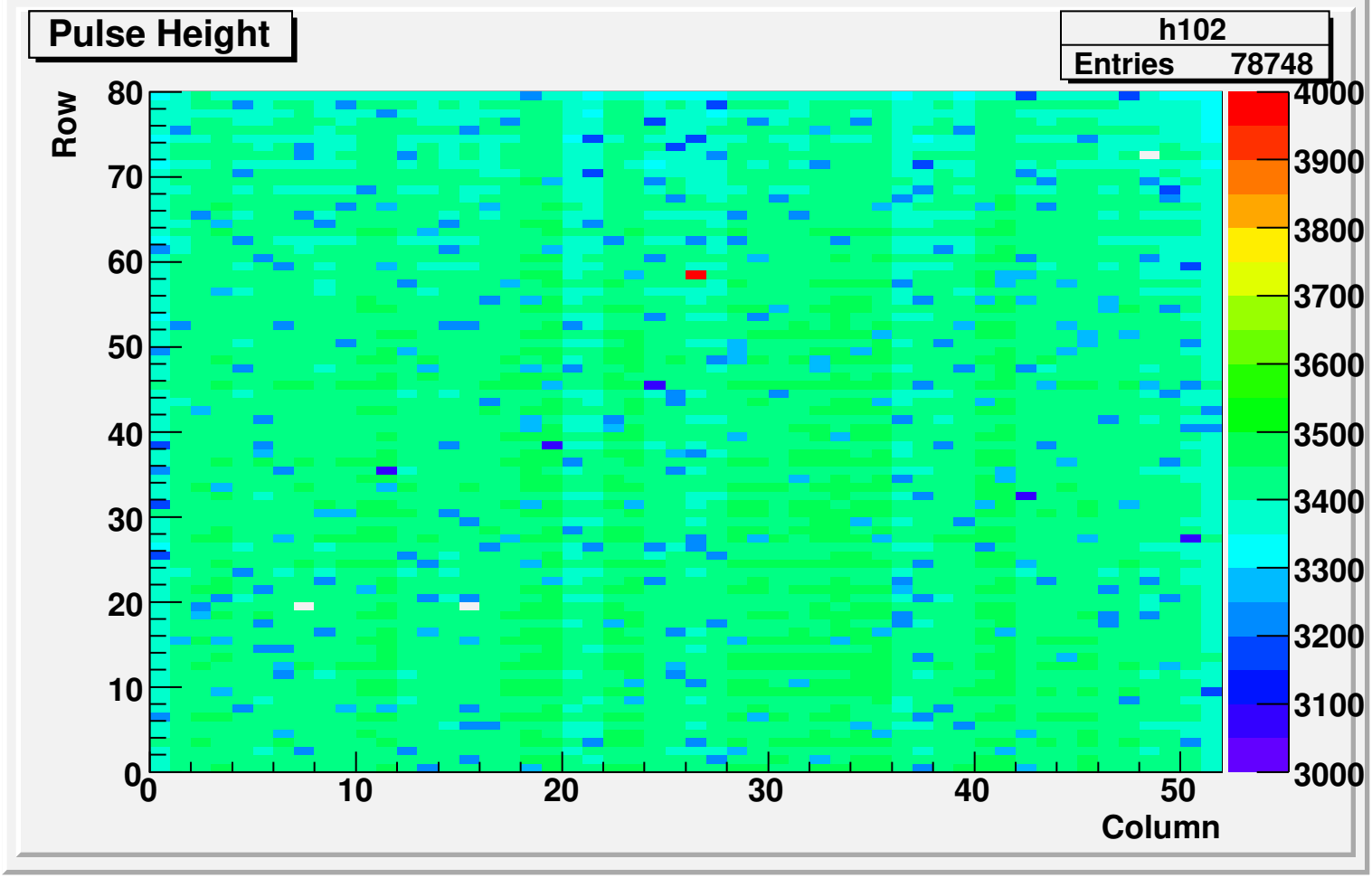
Pulse Height
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Entries 78748
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Column
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3900
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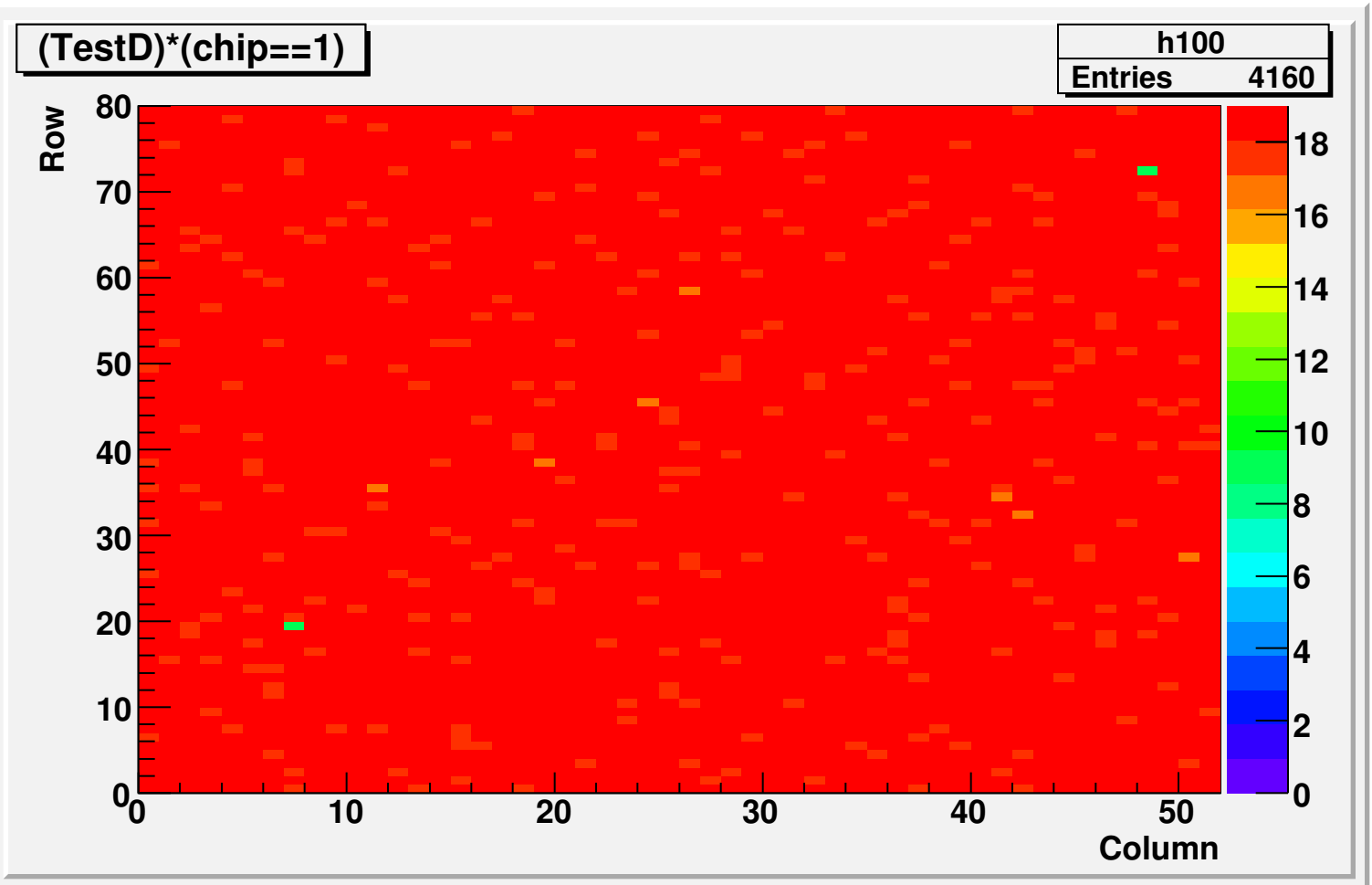
(TestD)*(chip==1)
h100
Entries 4160
Row
Column
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12
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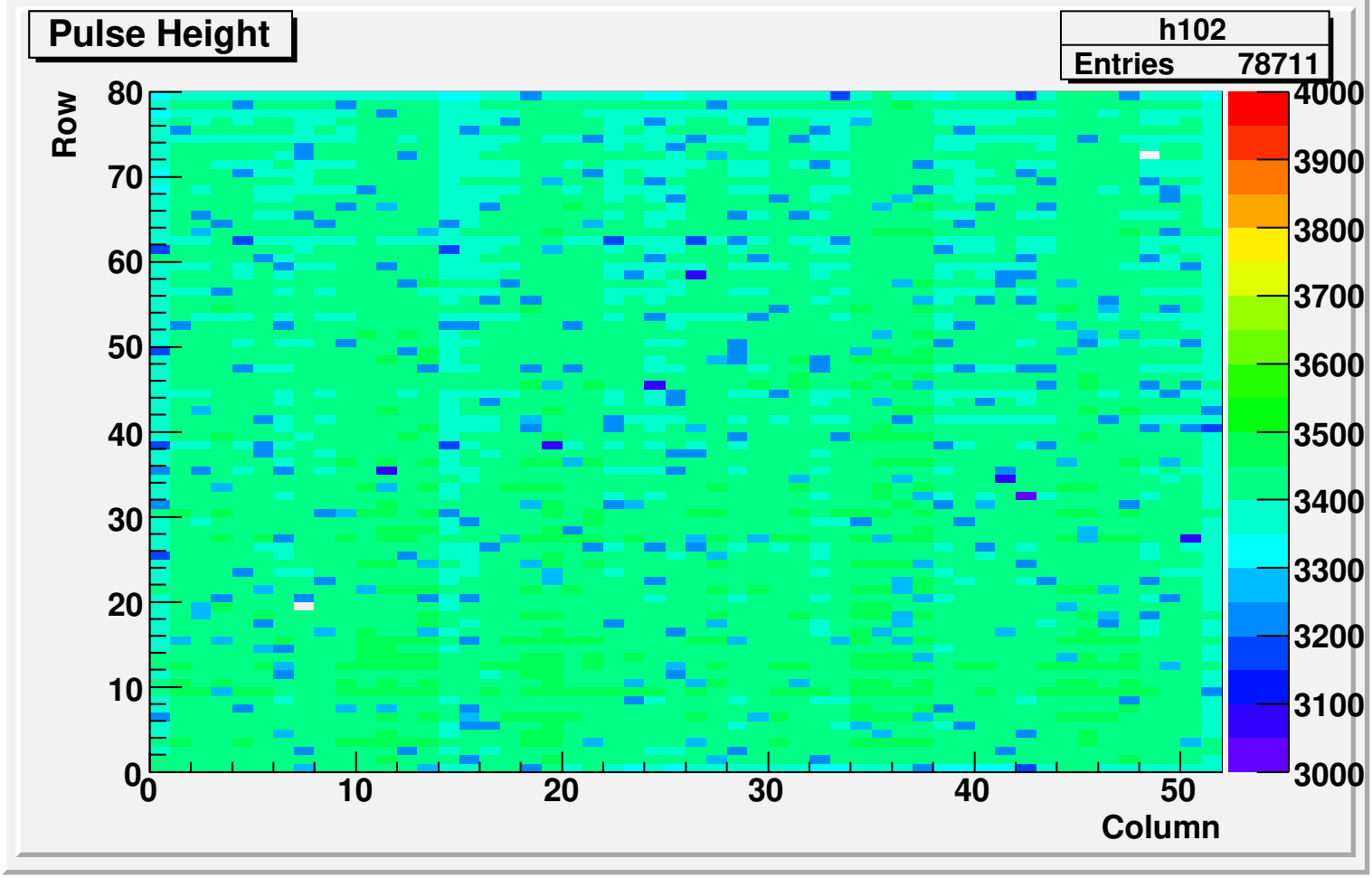
16
18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Entries 78711
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Column
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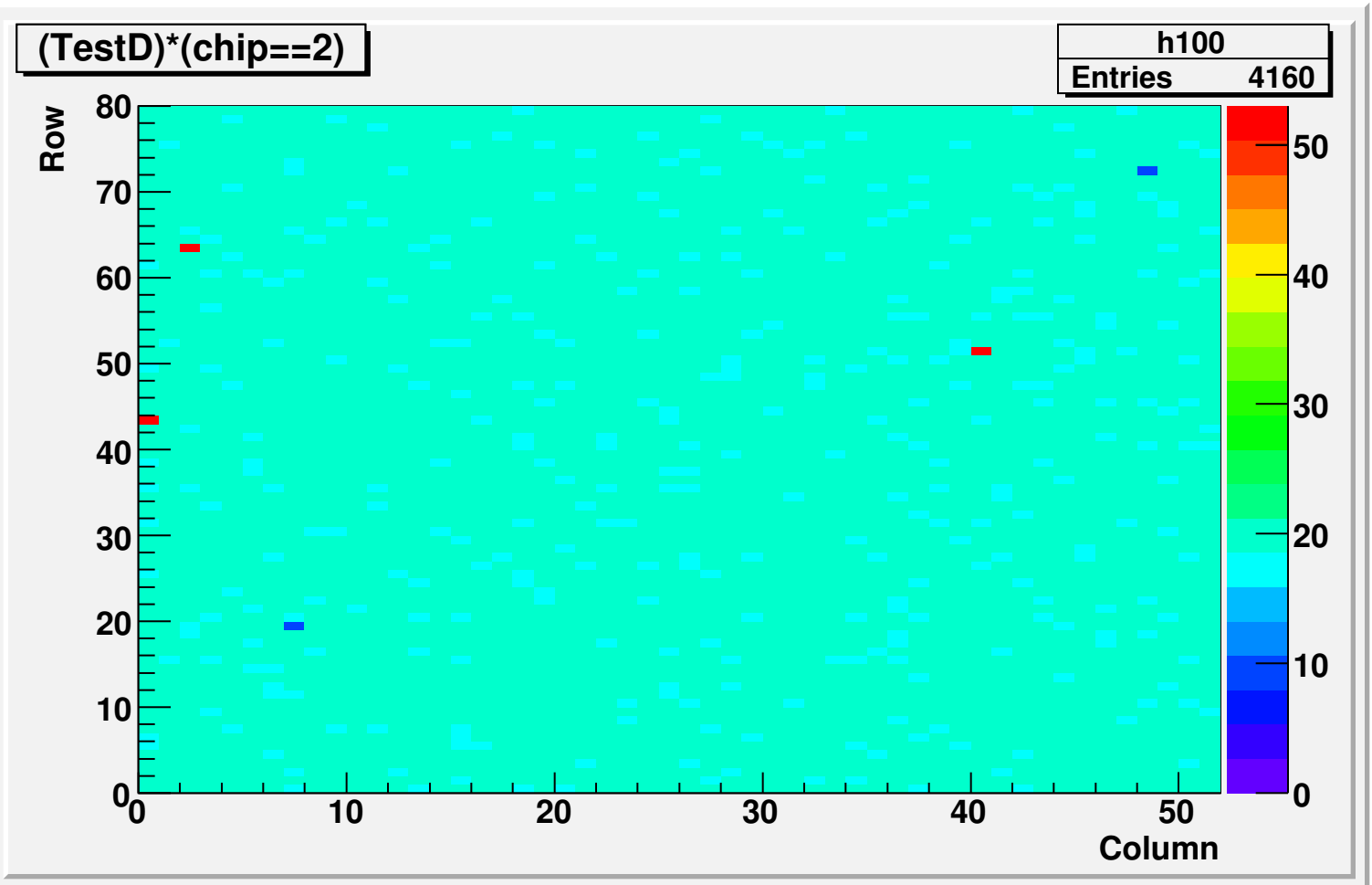
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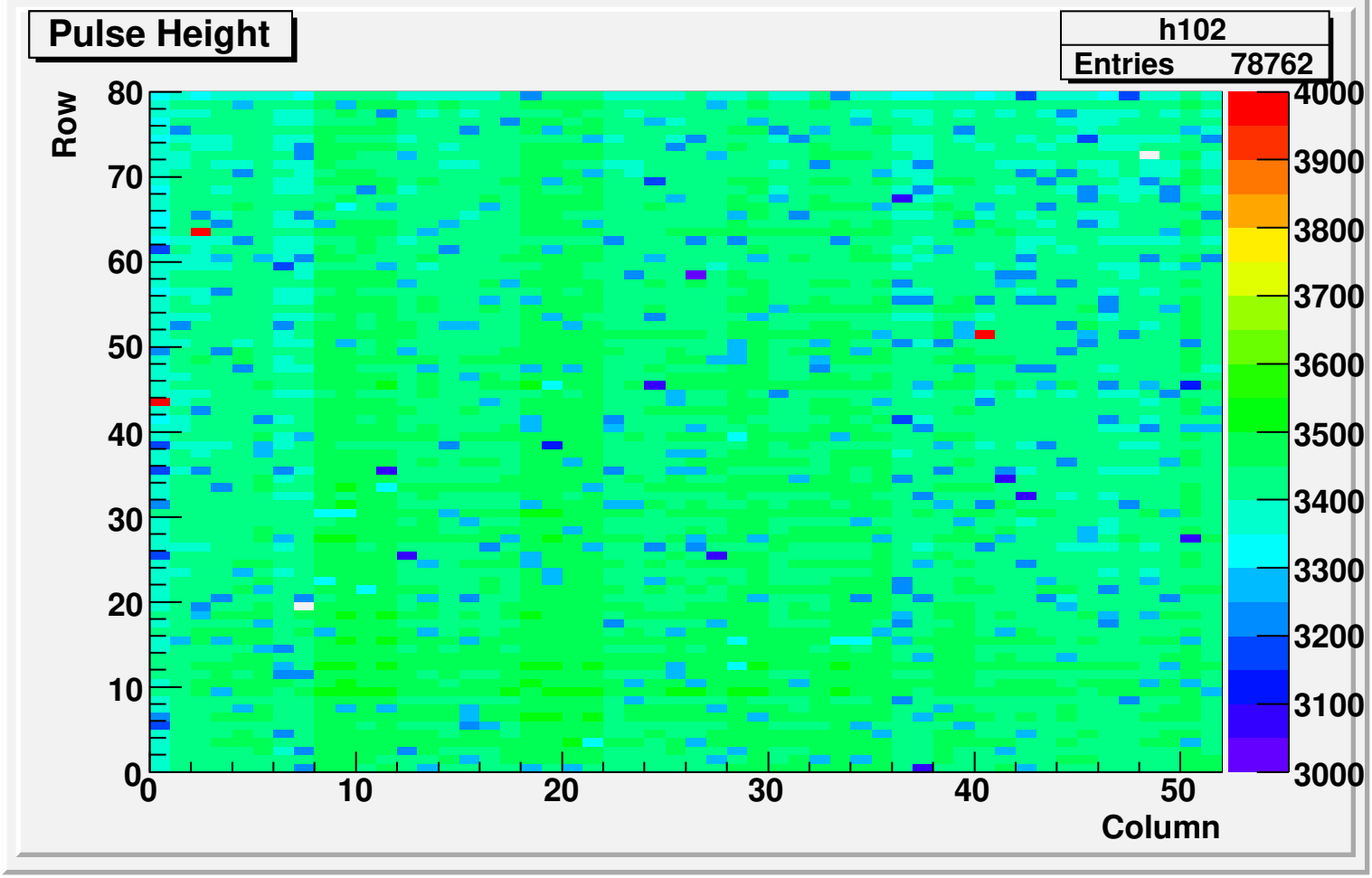
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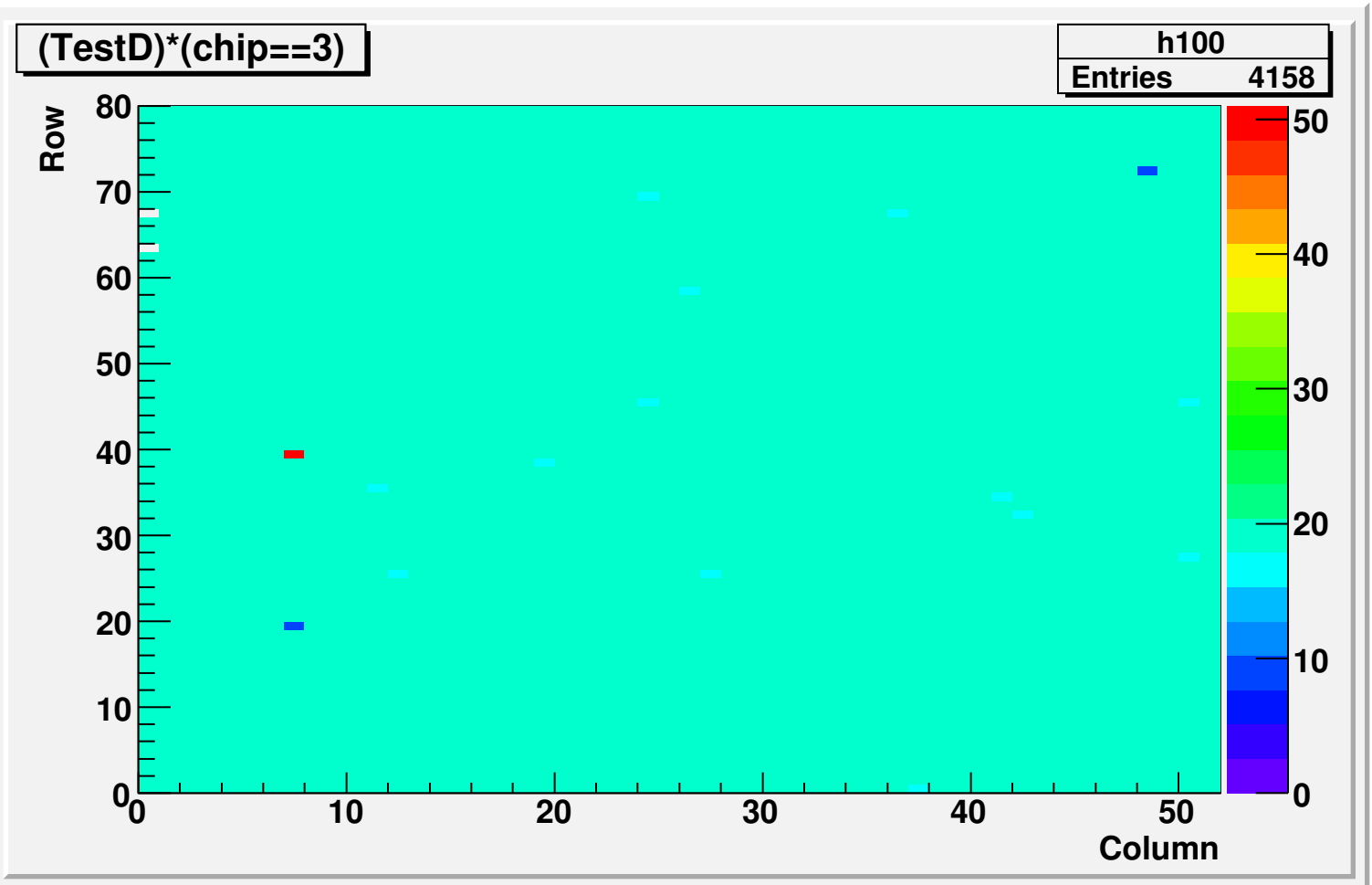
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Entries 4158
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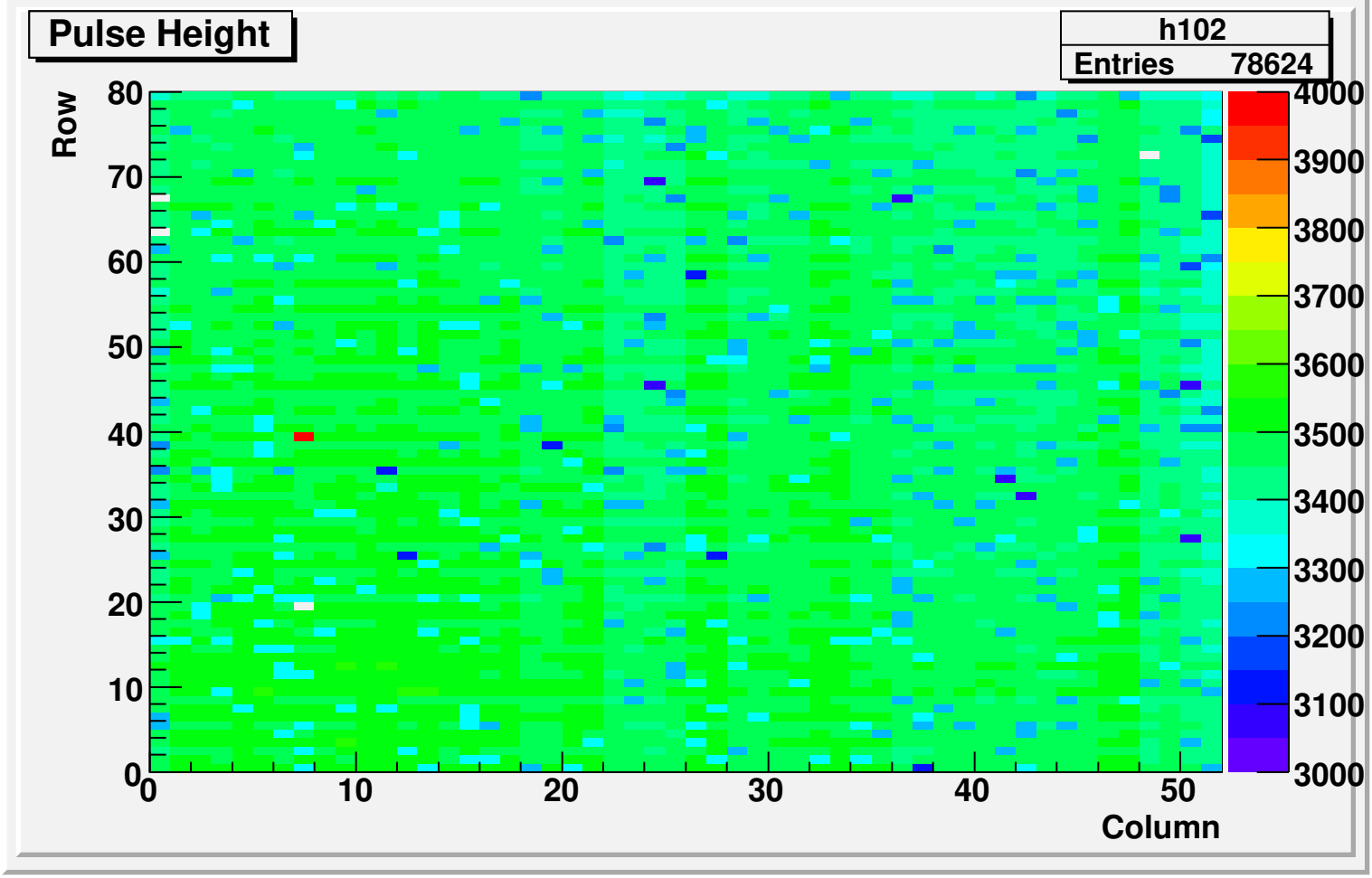
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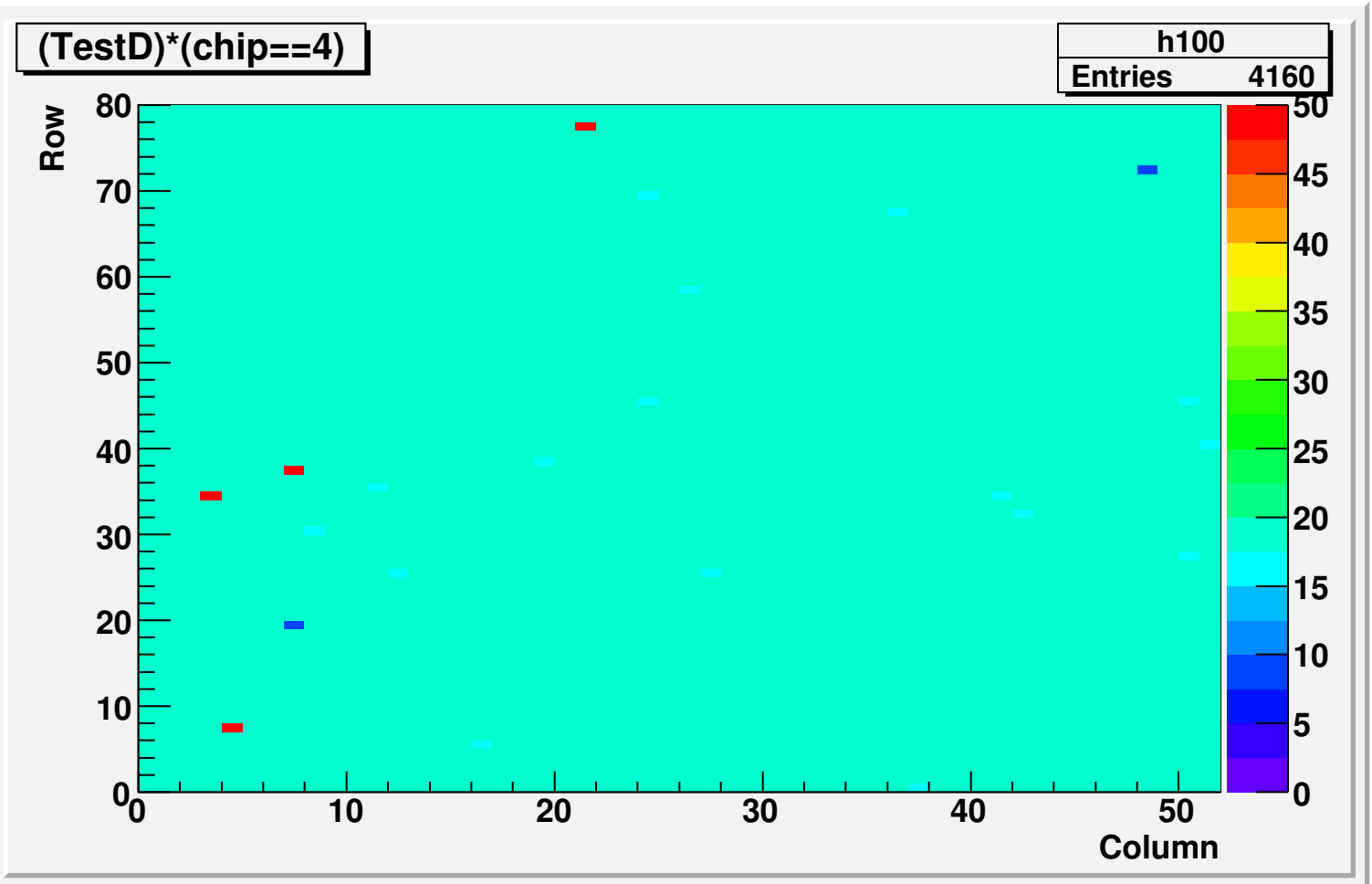
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Column
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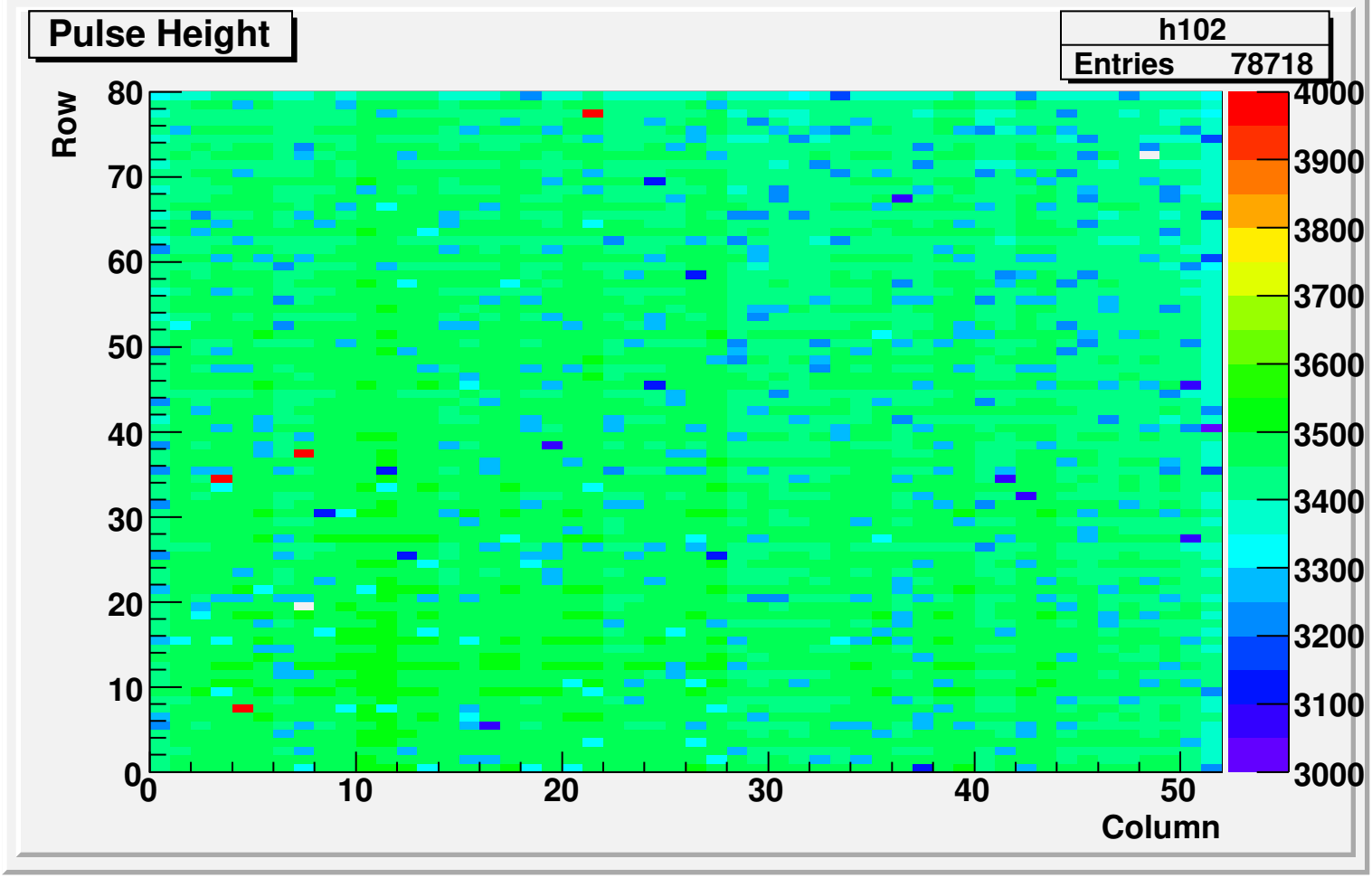
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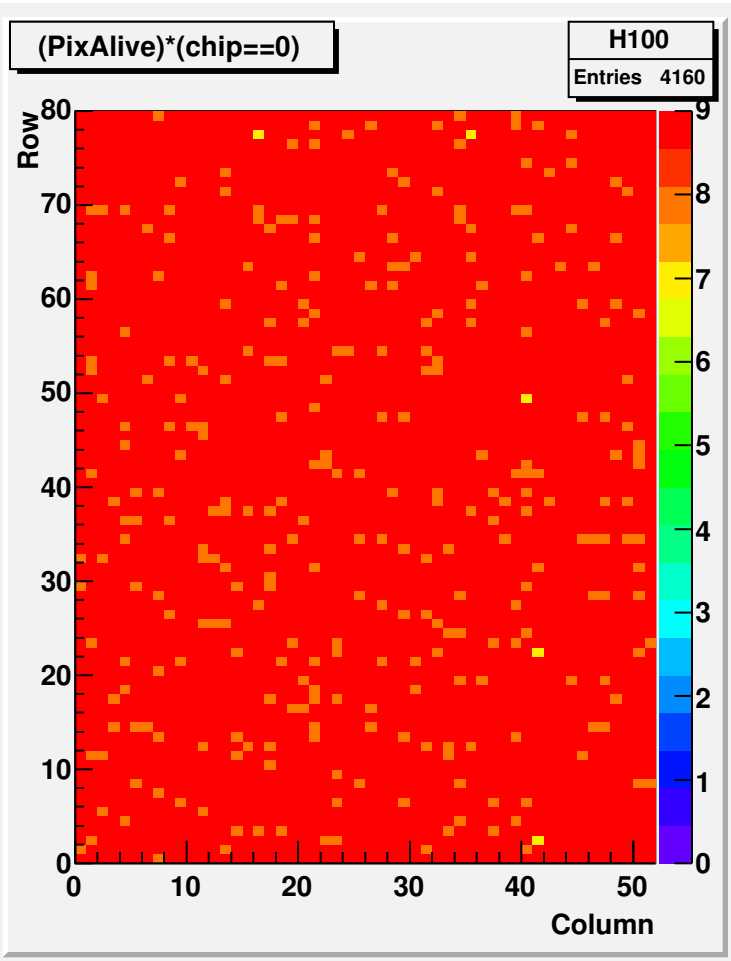
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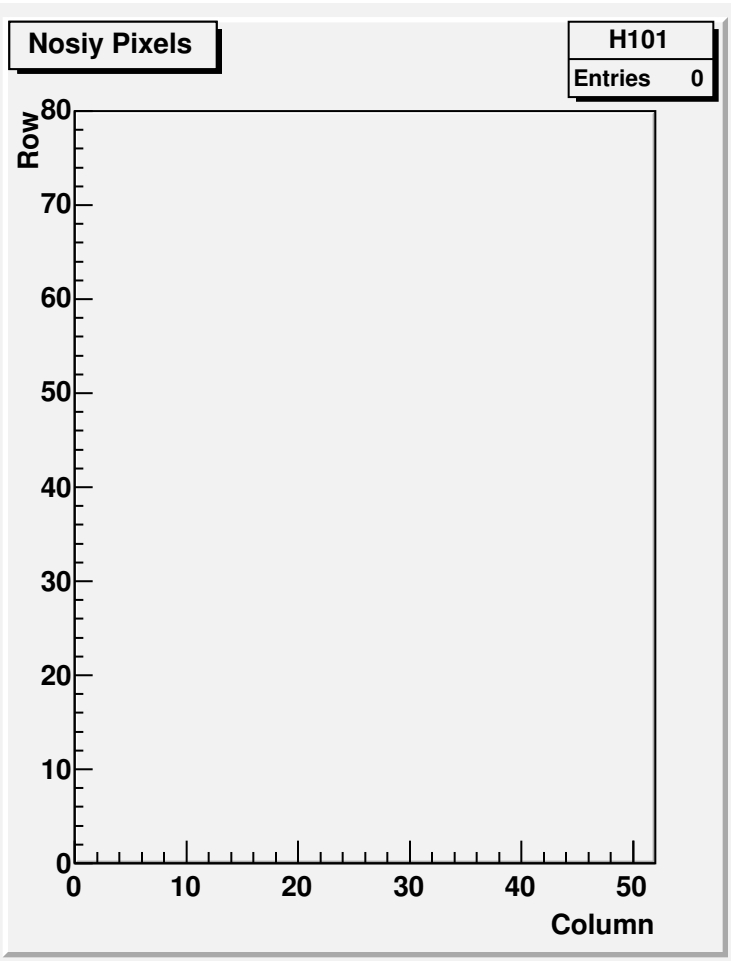
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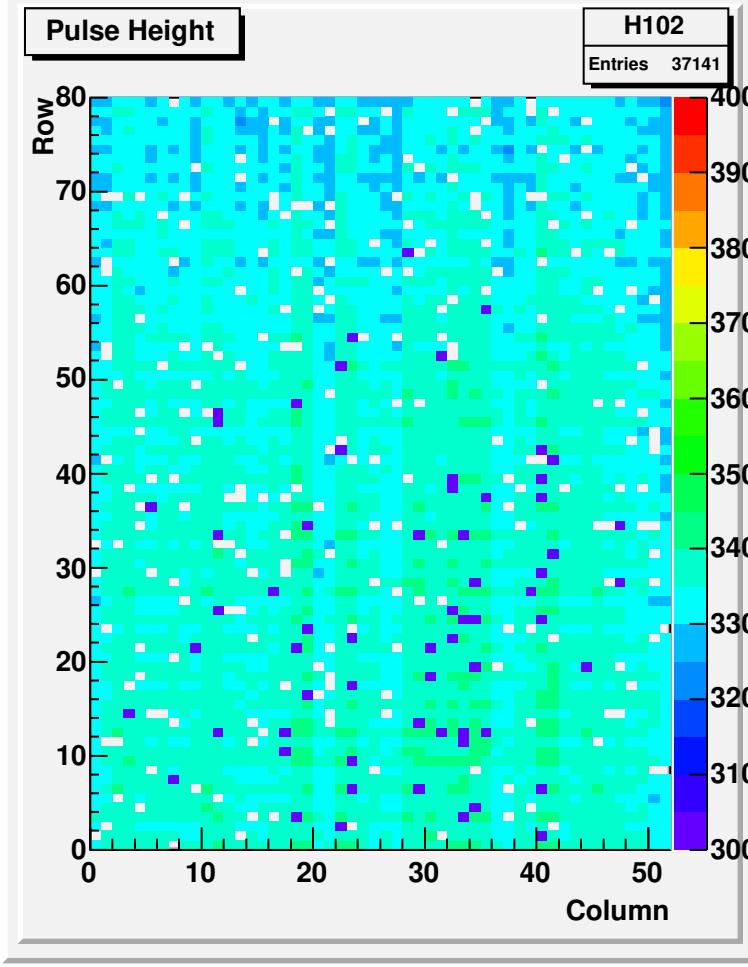
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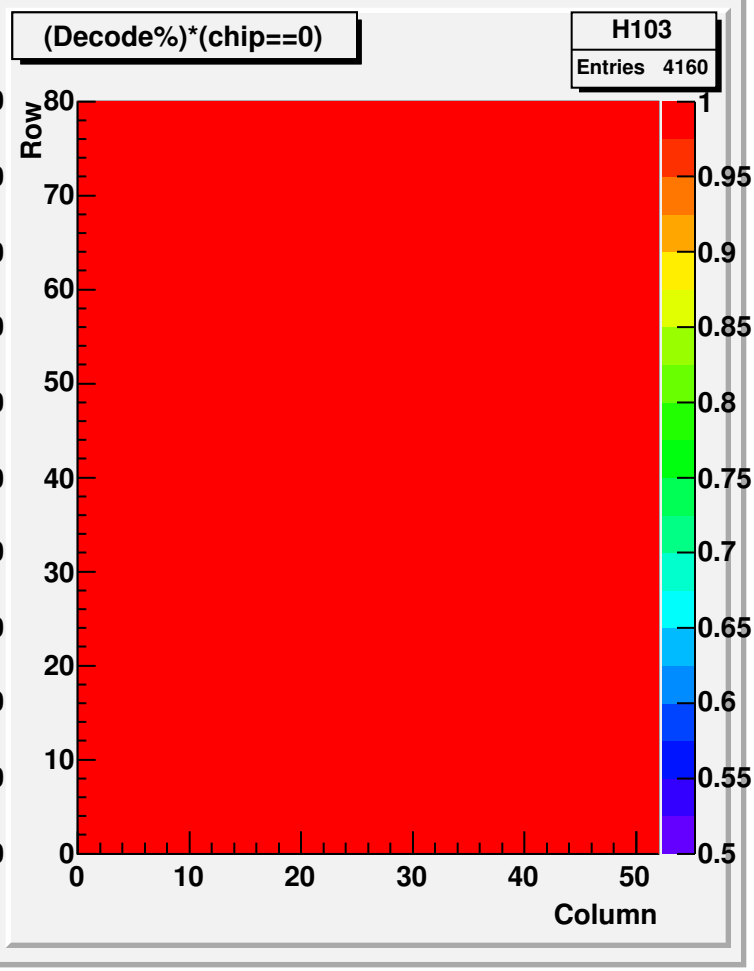
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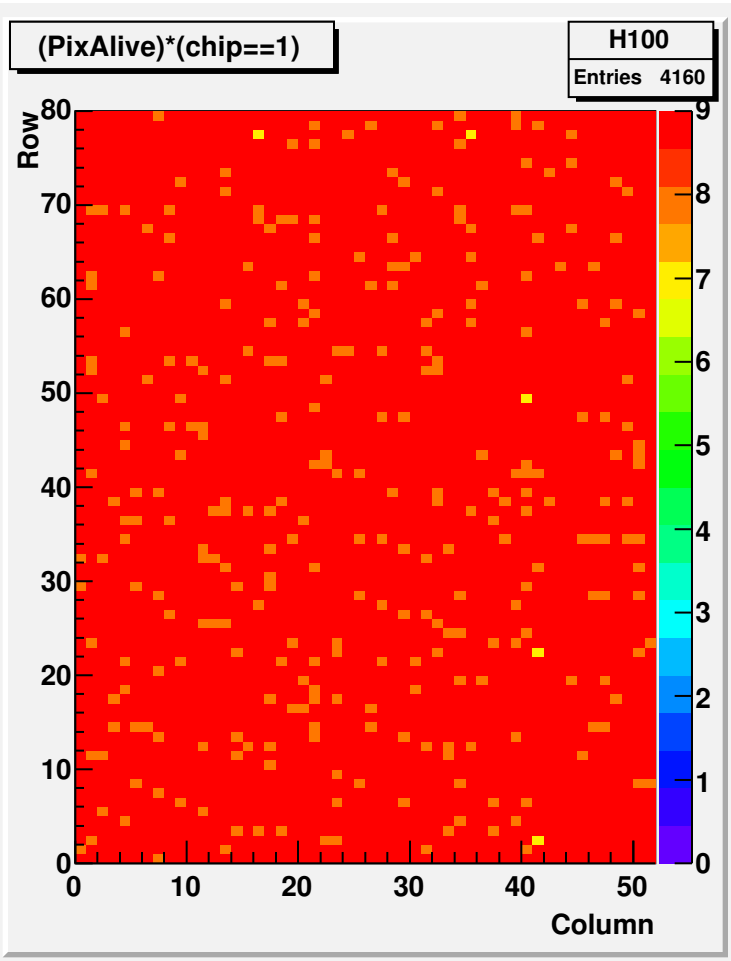
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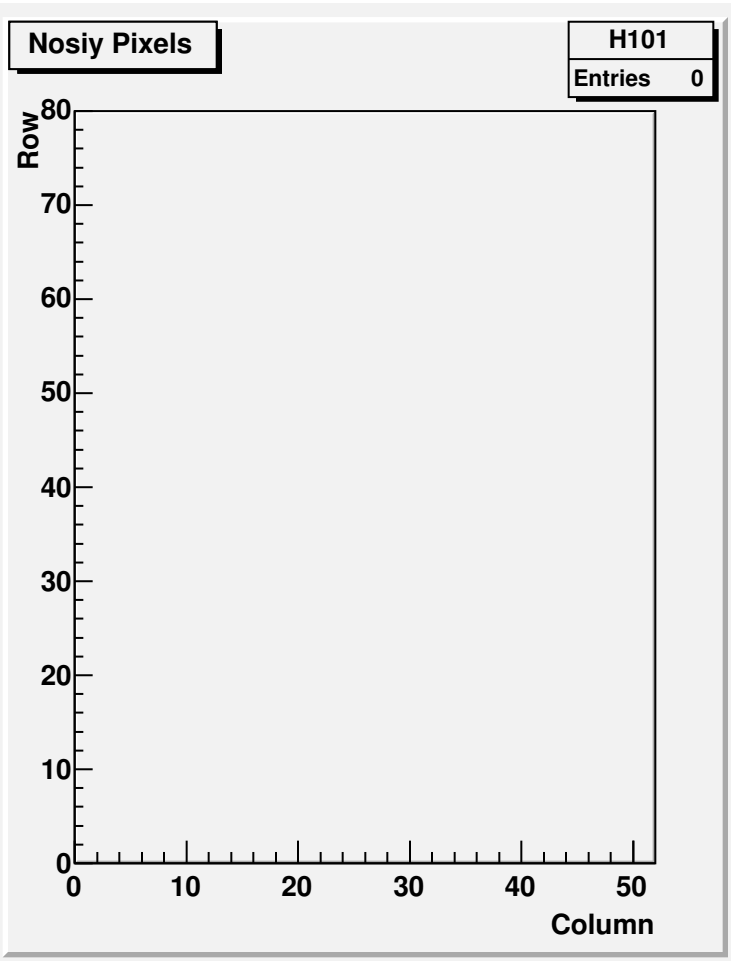
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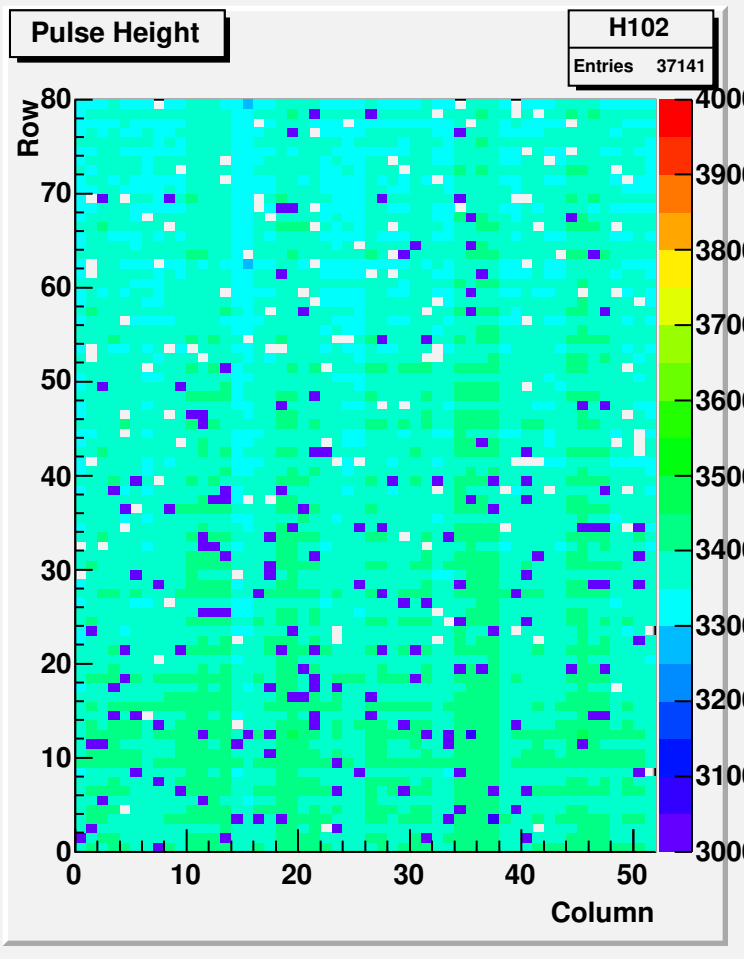
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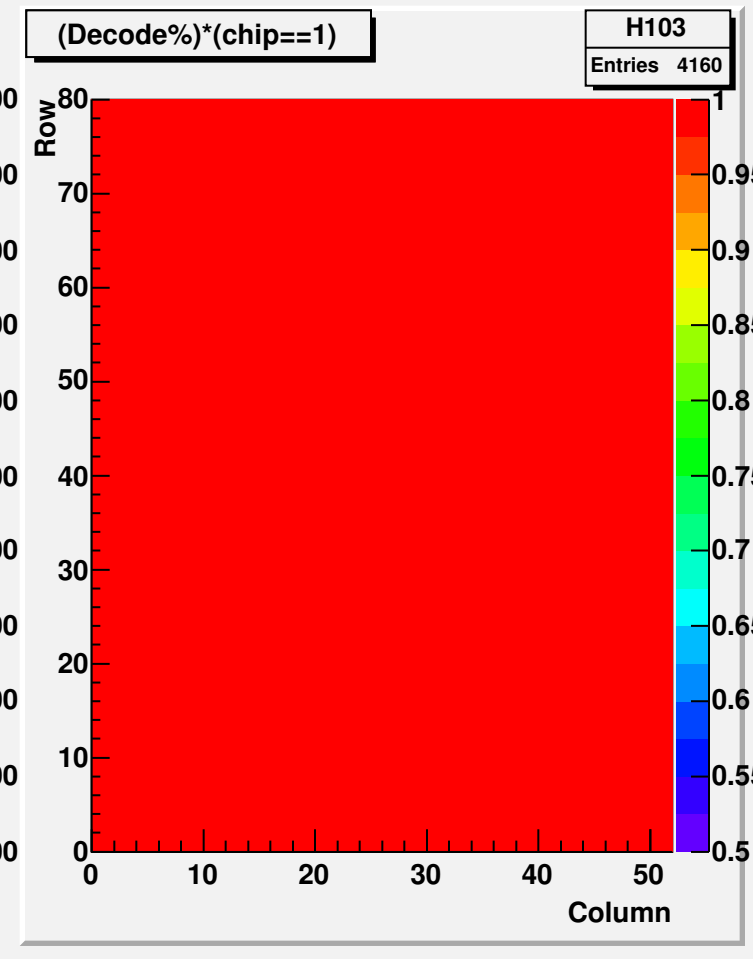
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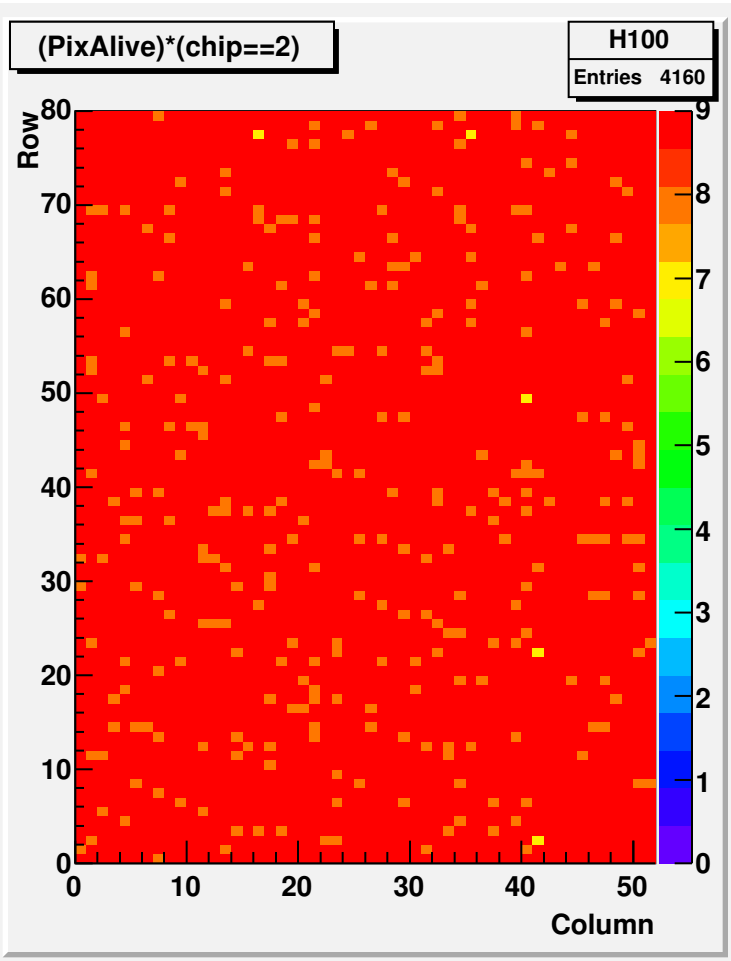
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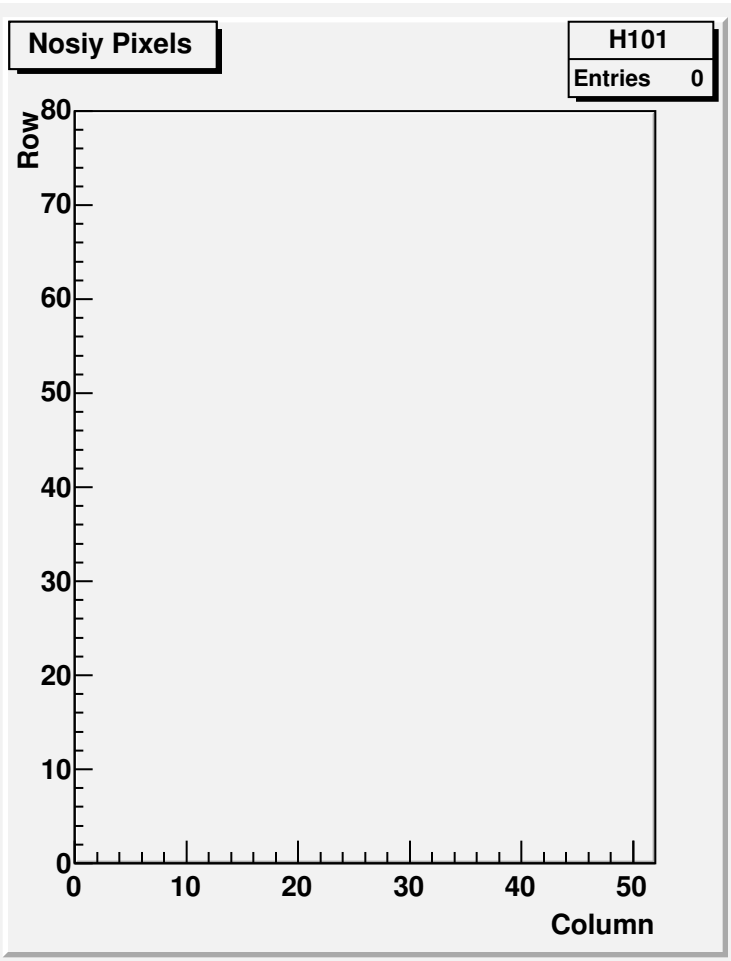
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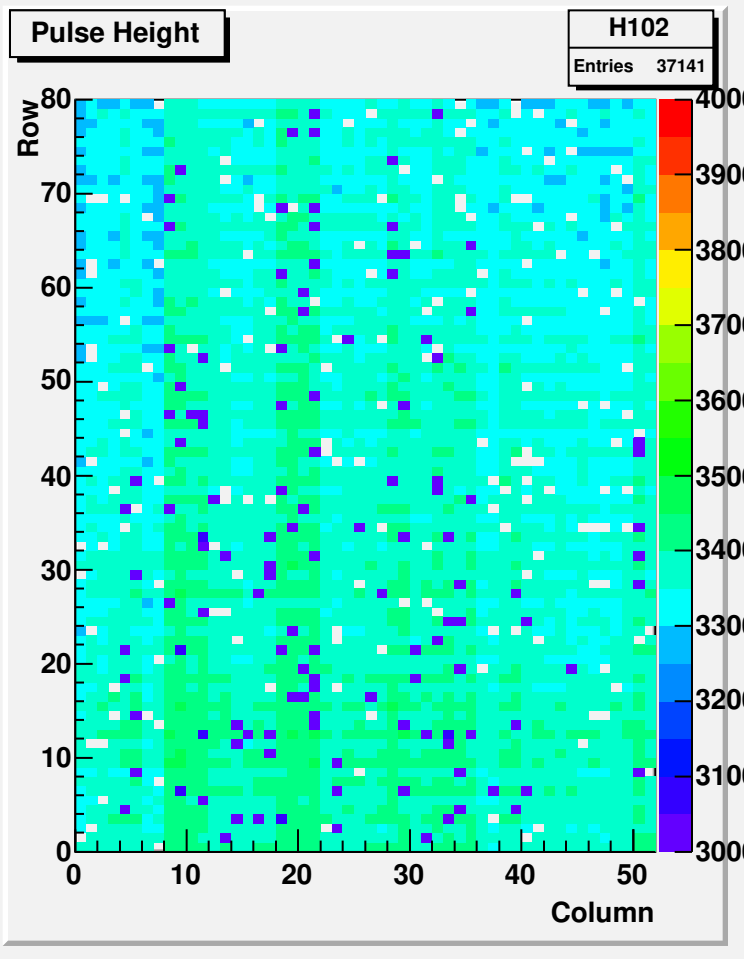
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H102
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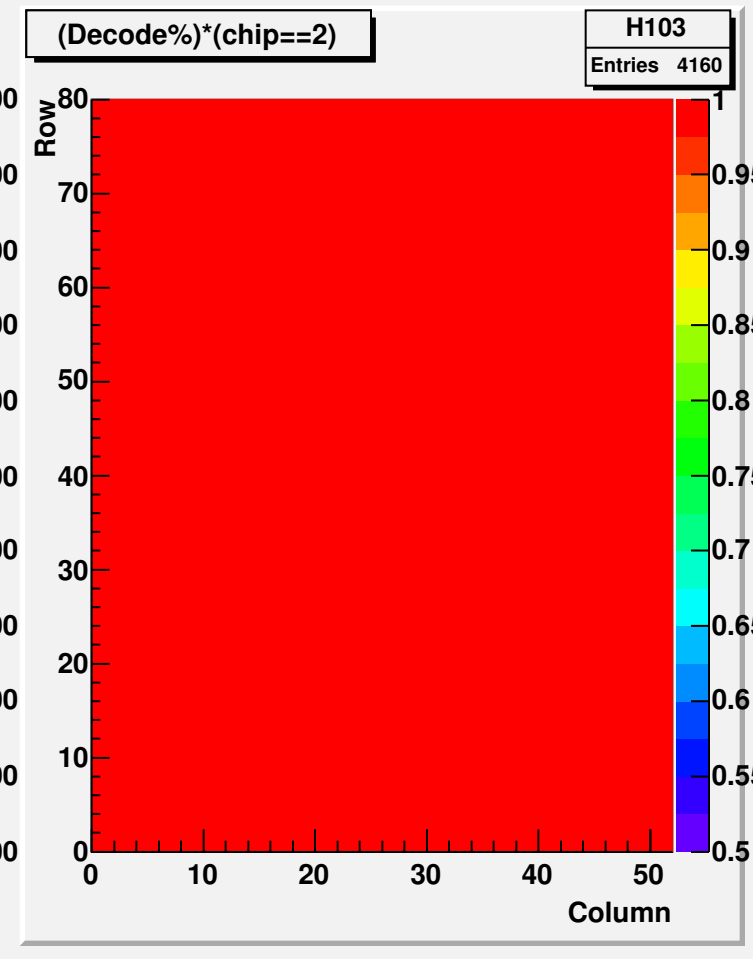
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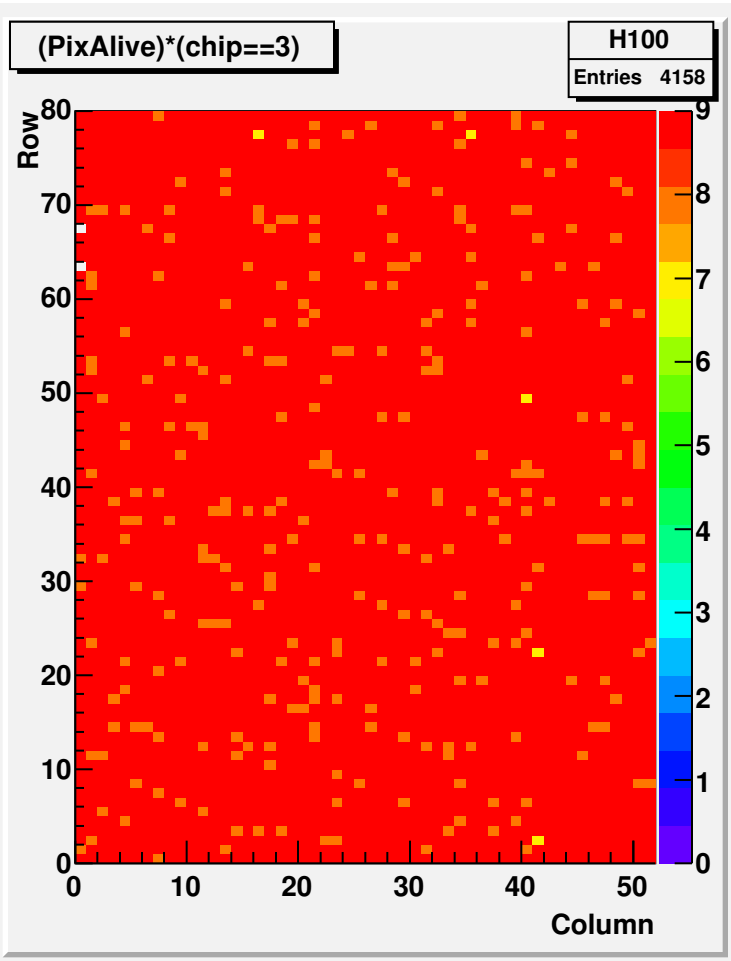
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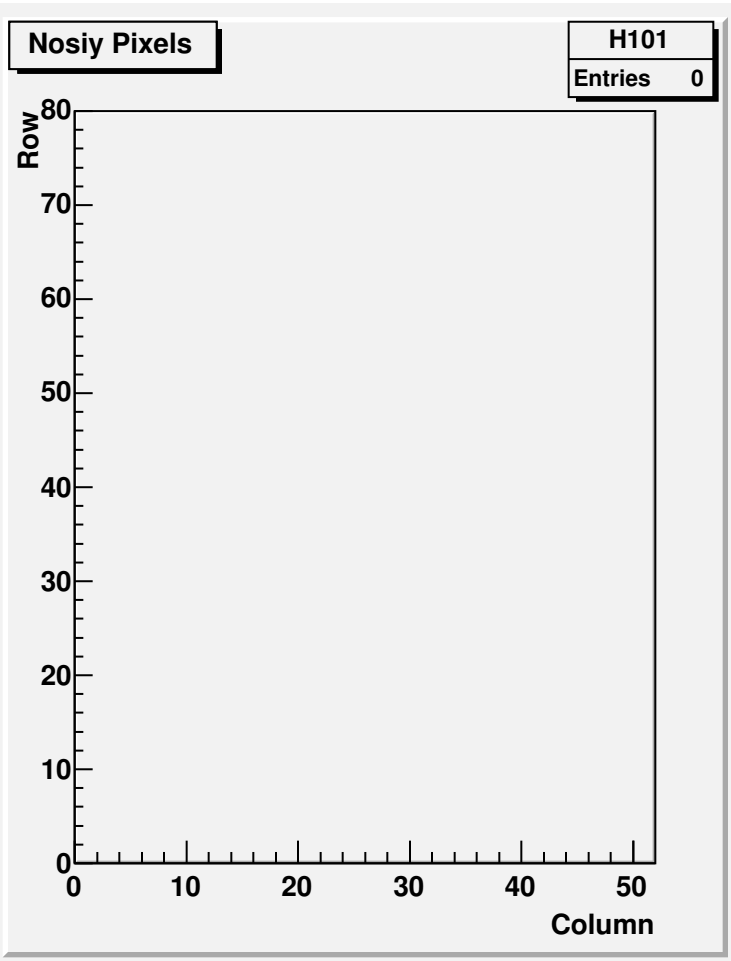
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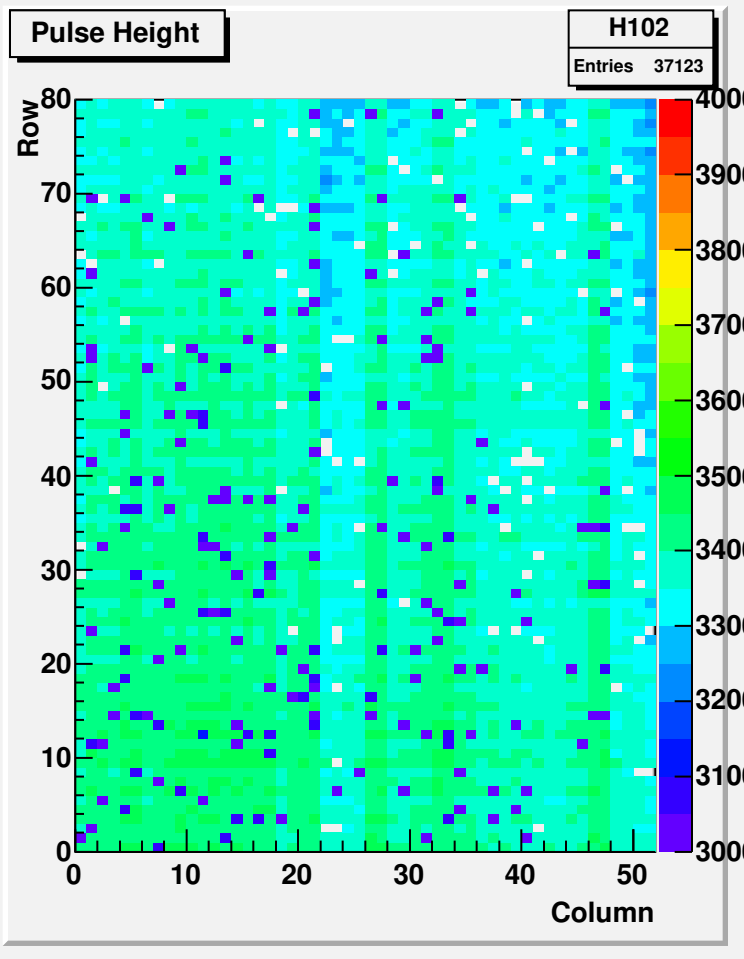
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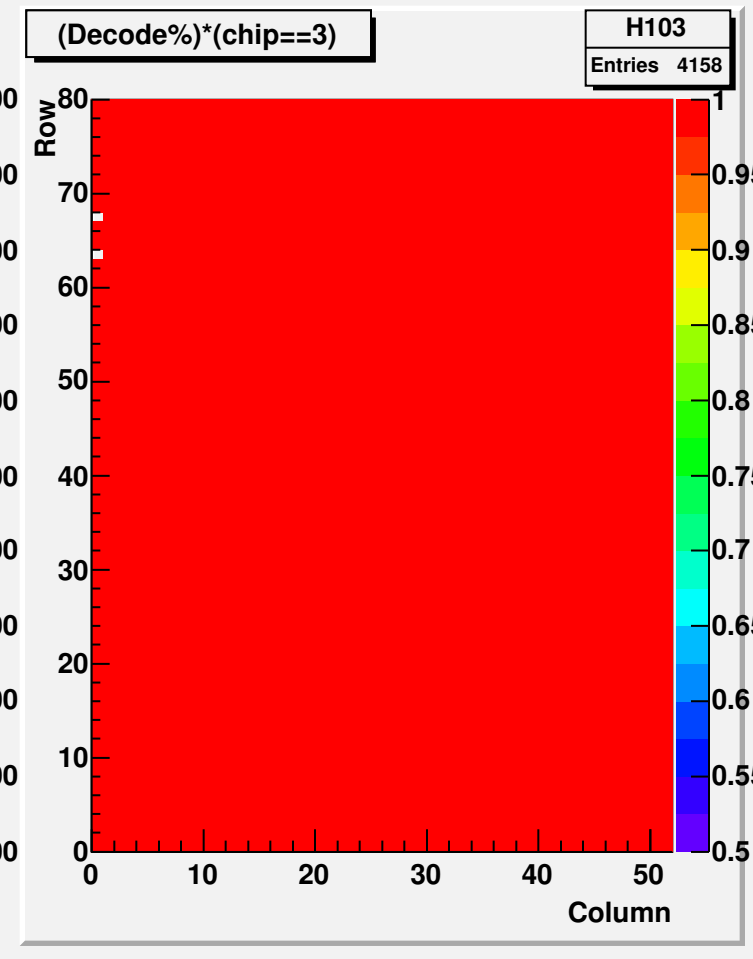
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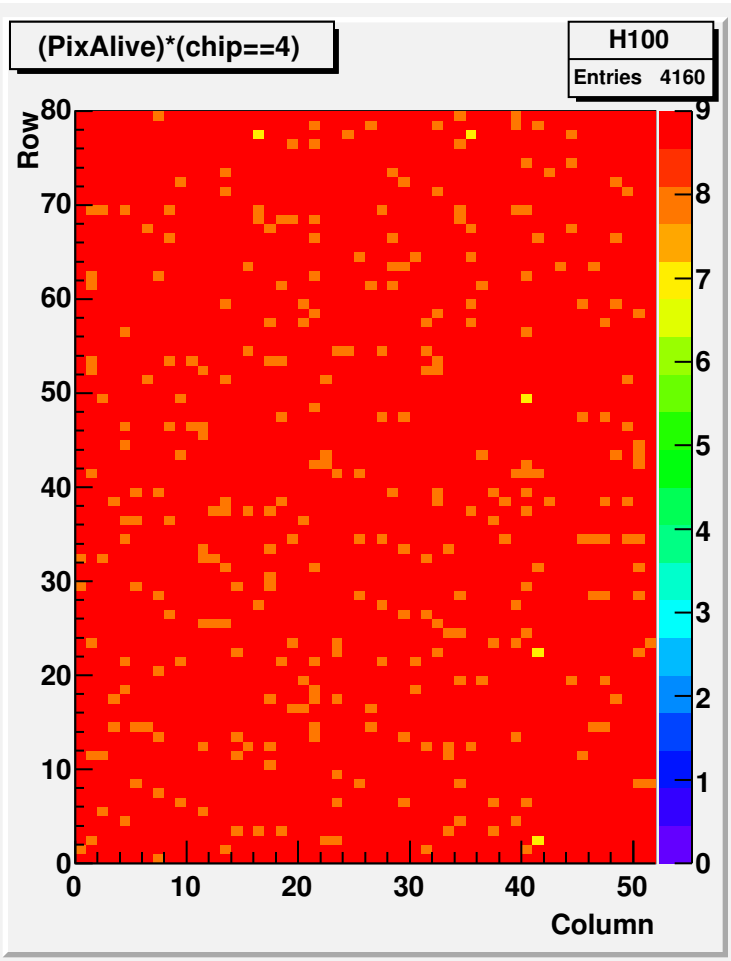
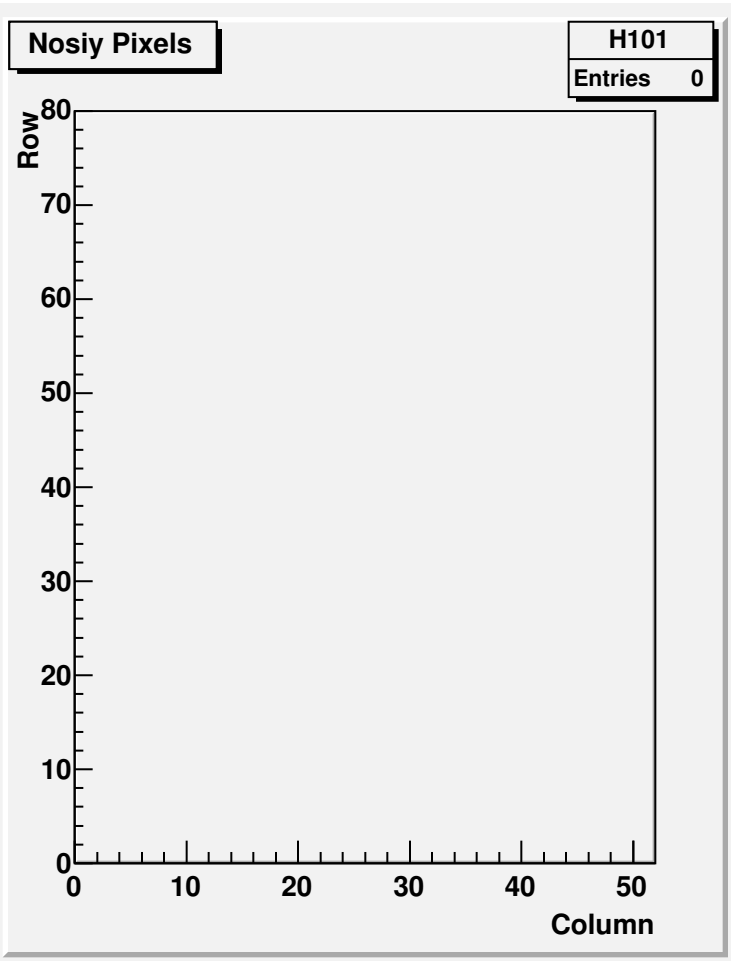
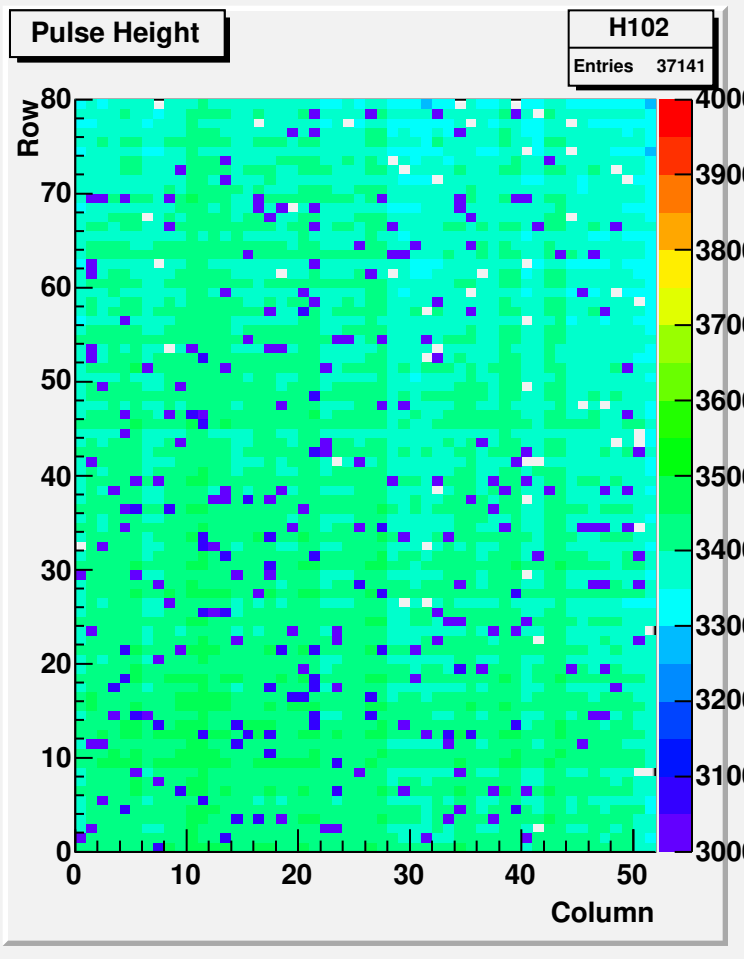
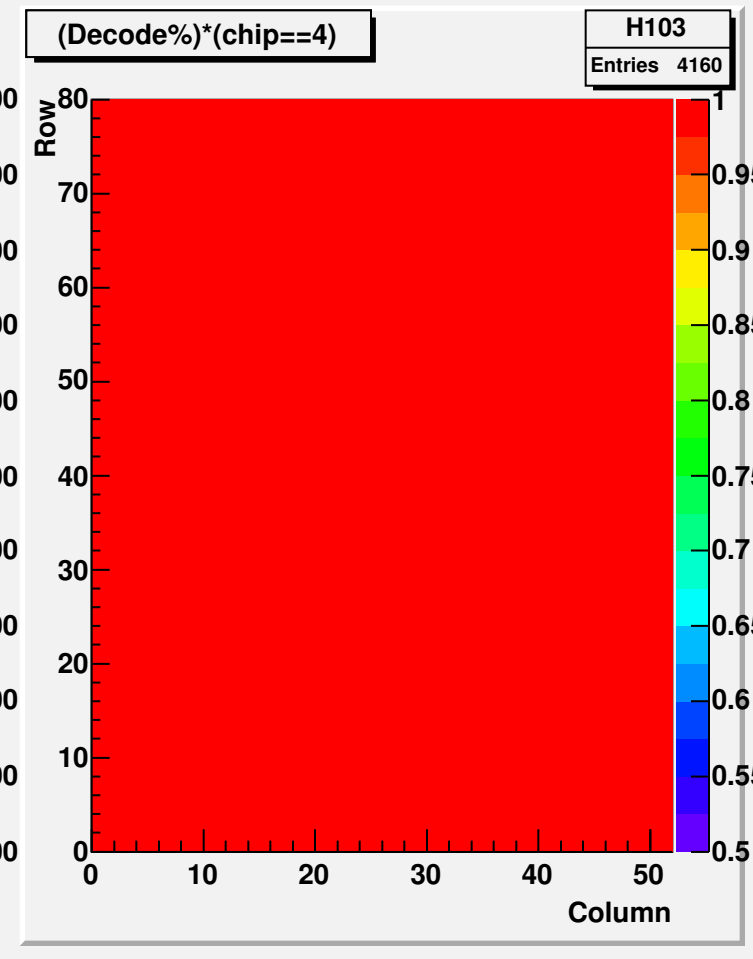
Row
Column


(PixAlive)*(chip==4)
H100
Entries 4160
Row
Column


Nosiy Pixels
H101
Entries 0
Row
Column


Pulse Height
H102
Entries 37141
Row
Column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H103
Entries 4160
Row
Column