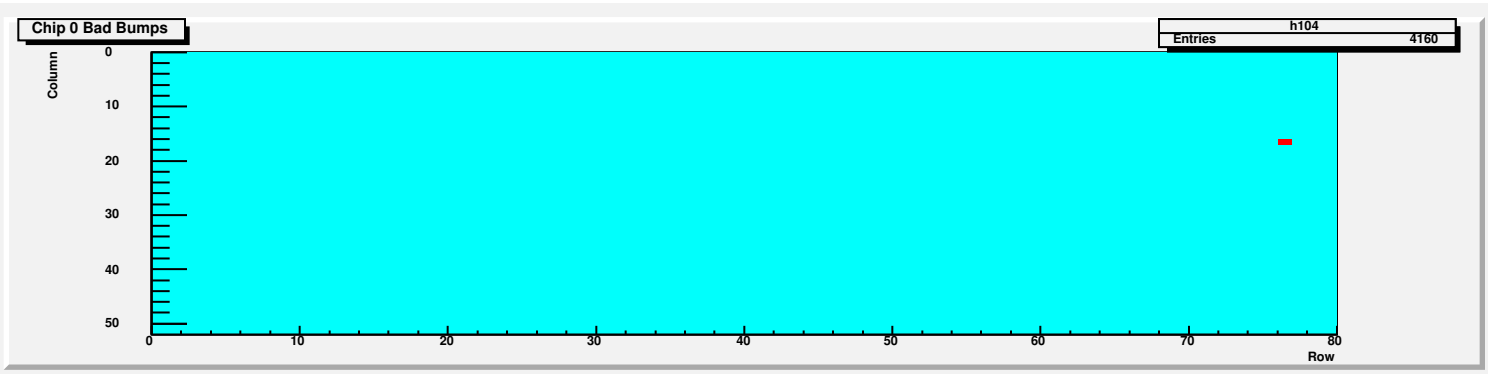
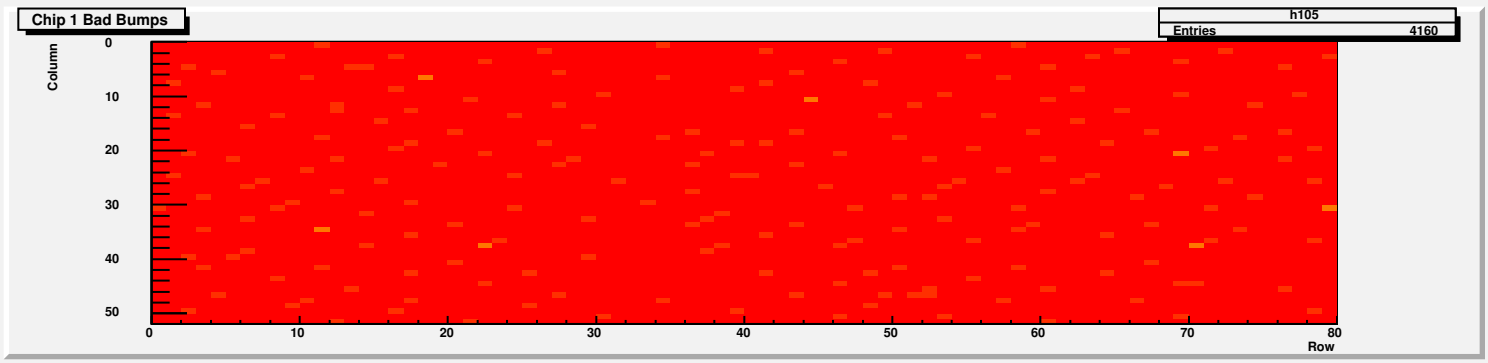

Chip 0 Bad Bumps
h104
Entries 4160
Column
Row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h105
Entries 4160
Column
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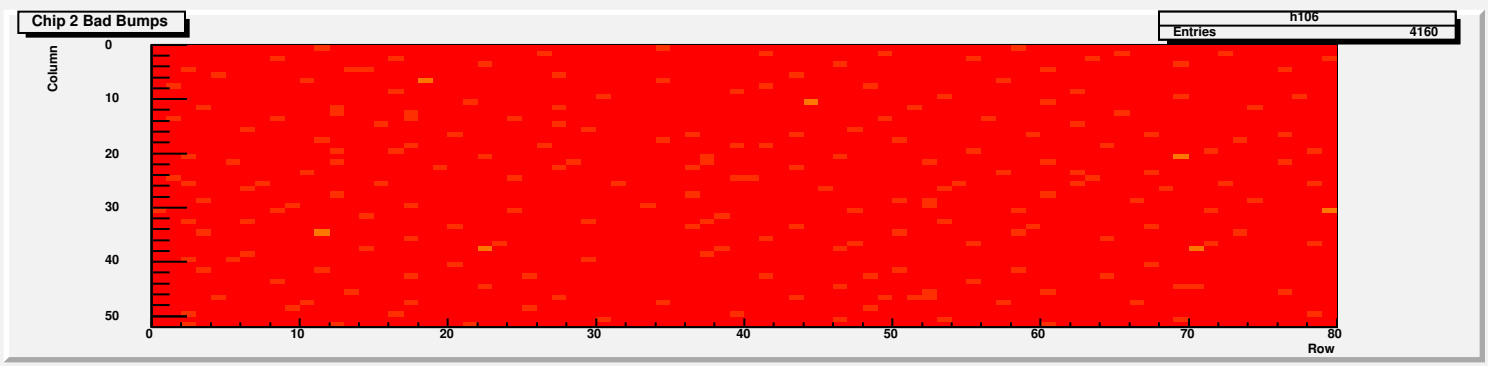
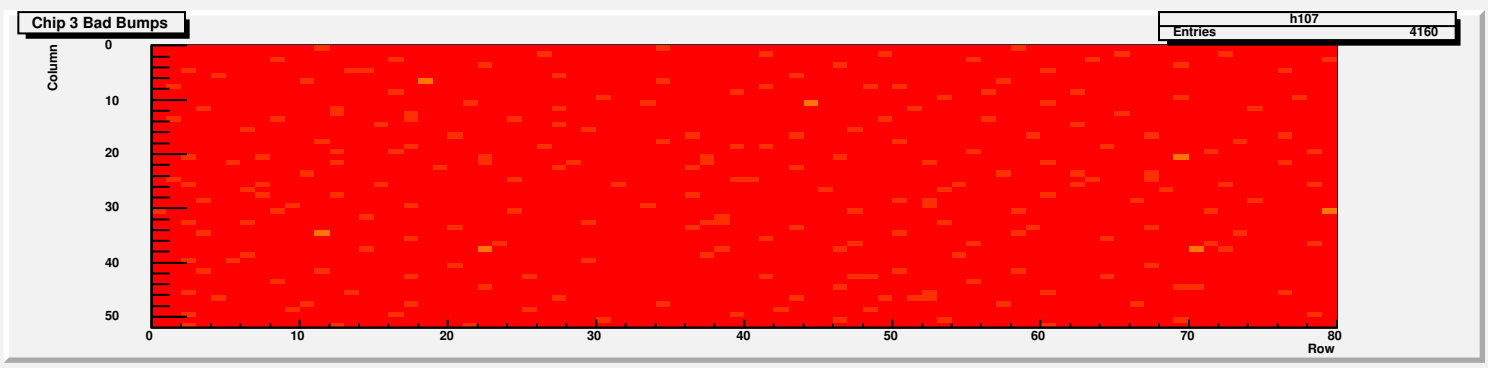

Chip 2 Bad Bumps
h106
Entries 4160
Column
Row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h107
Entries 4160
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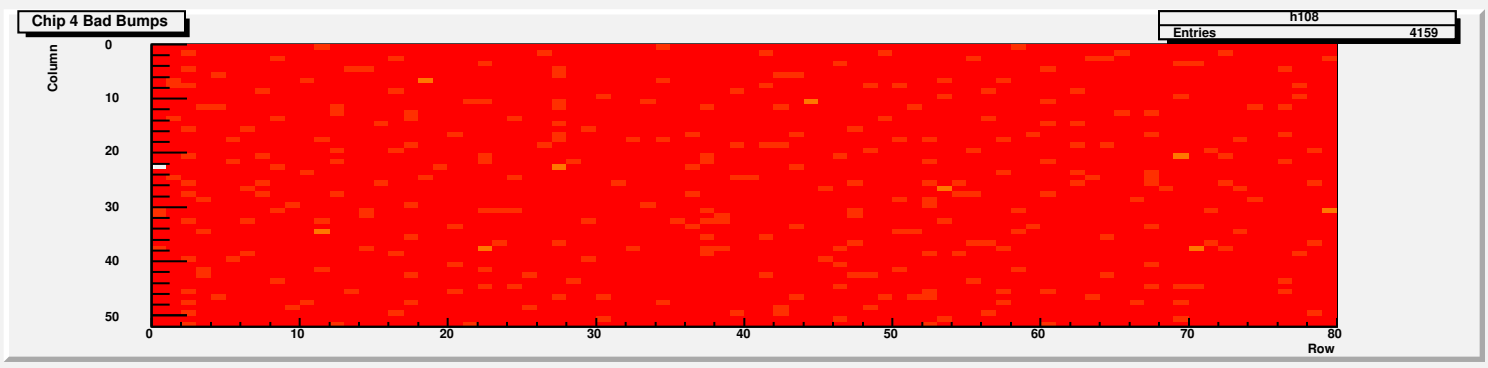
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h108
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Column
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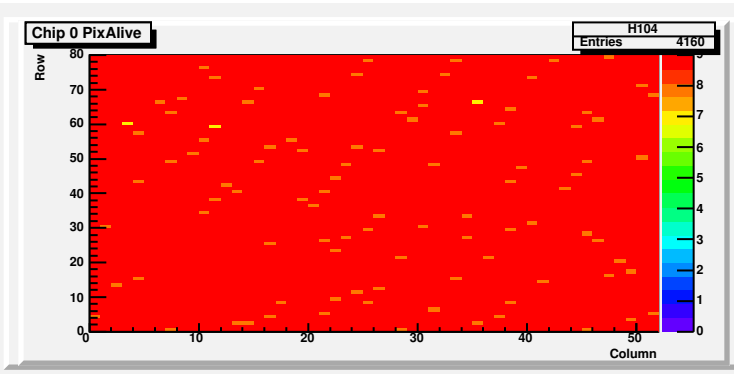
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Entries 4160
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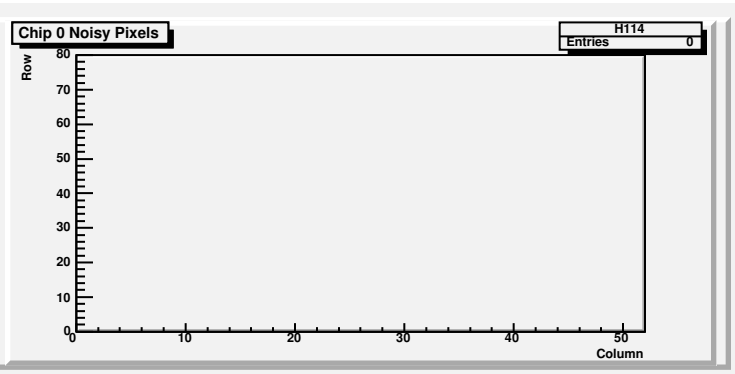
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H114
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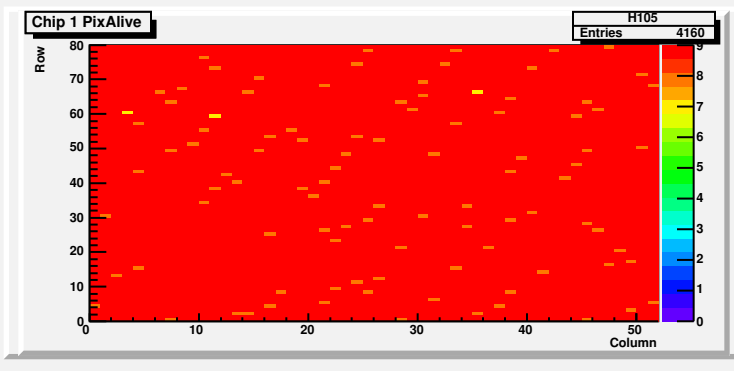
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Entries 4160
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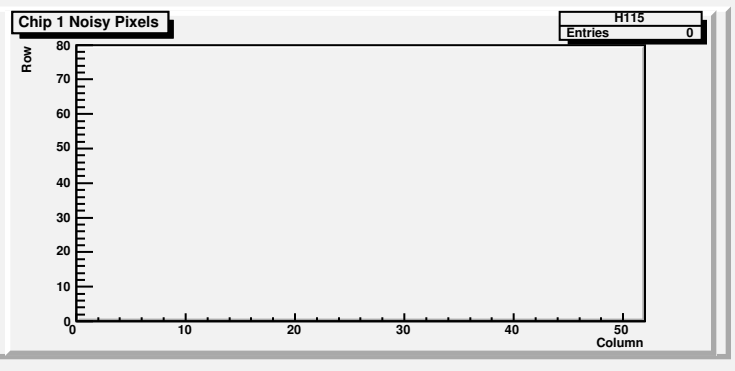
Column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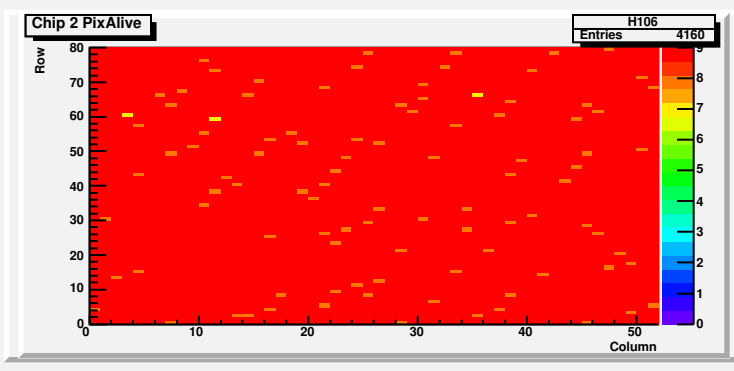
Chip 2 PixAlive
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Entries 4160
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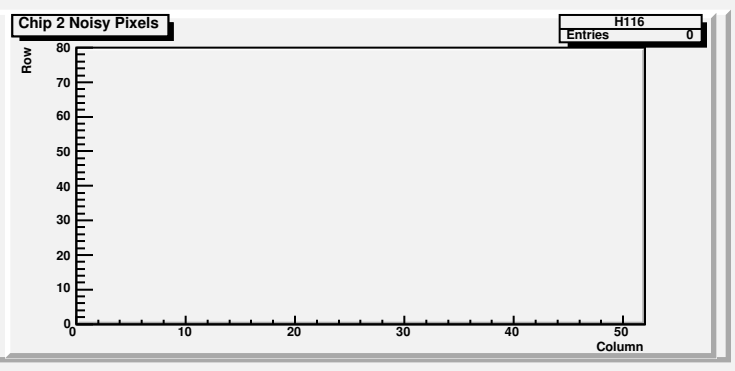
Column


Chip 2 Noisy Pixels
H116
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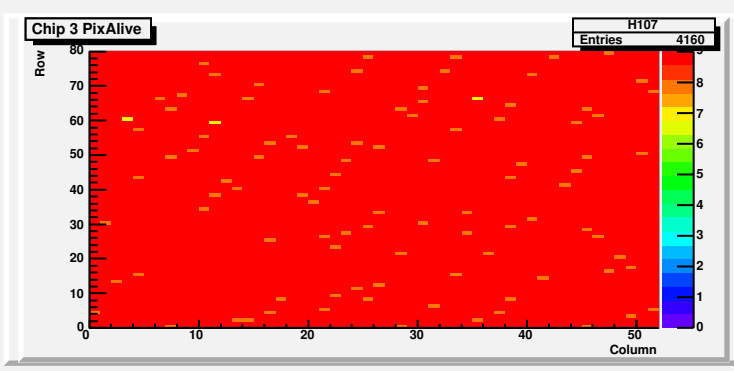
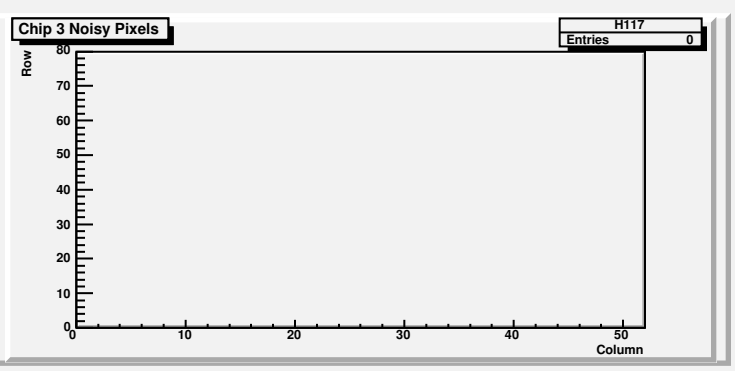

Chip 3 PixAlive
H107
Entries 4160
Row
Column


Chip 3 Noisy Pixels
H117
Entries 0
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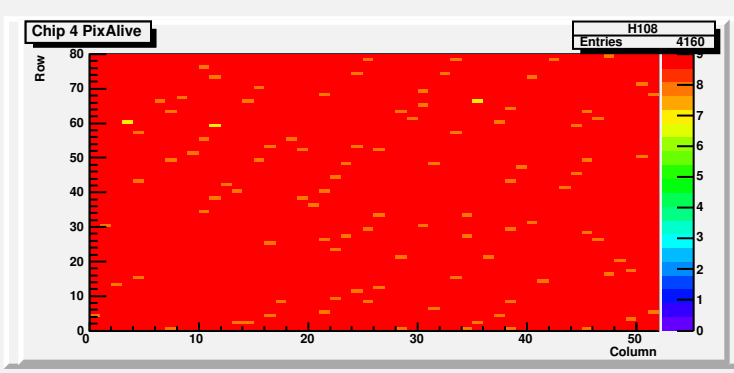
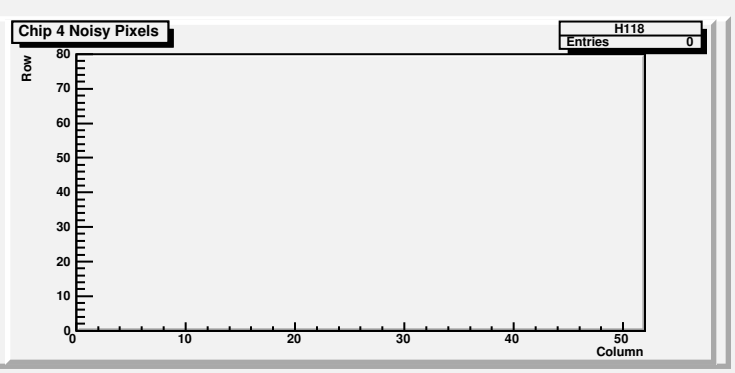
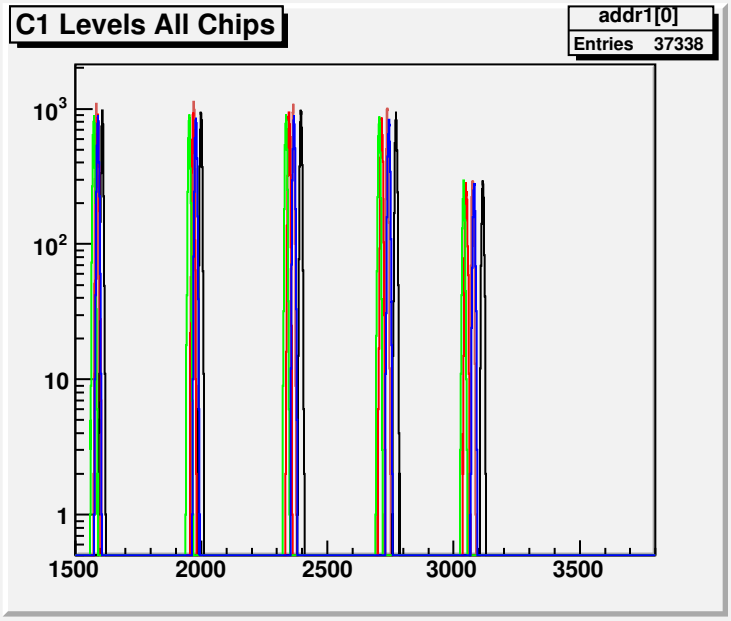
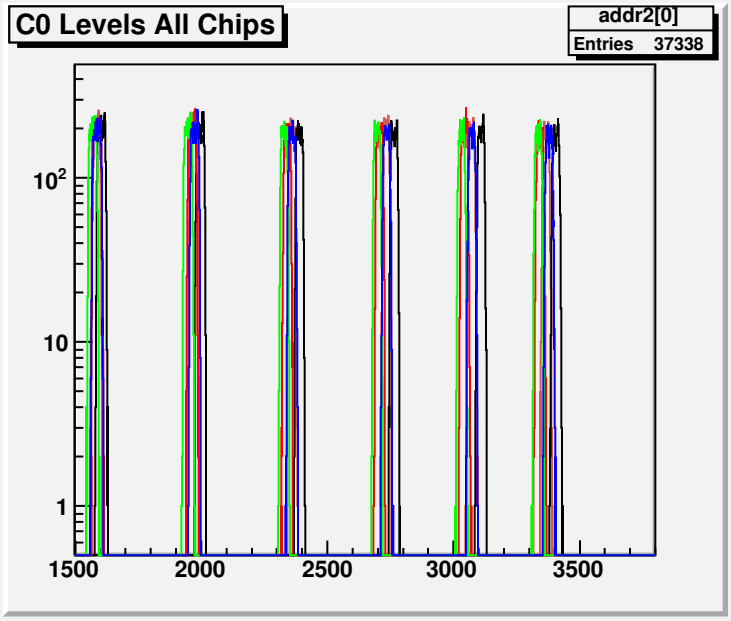
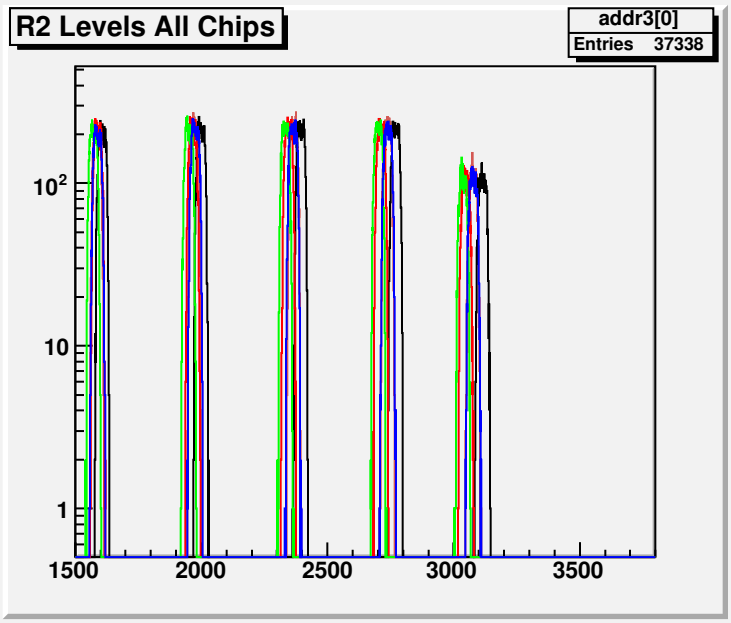
Column

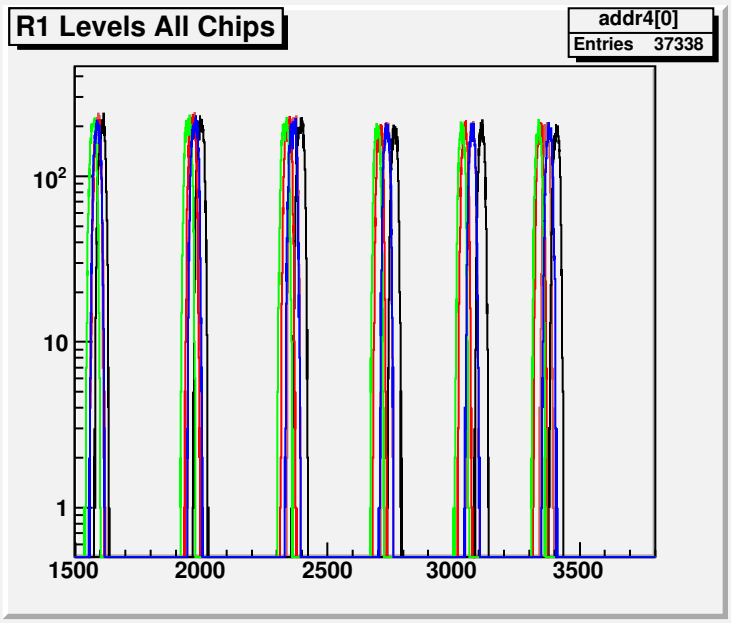

Chip 4 PixAlive
H108
Entries 4160
Row
Column


Chip 4 Noisy Pixels
H118
Entries 0
Row
Column


C1 Levels All Chips
addr1[0]
Entries 37338
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37338
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37338
10^2
10
1
1500
2000
2500
3000
3500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Entries 37338
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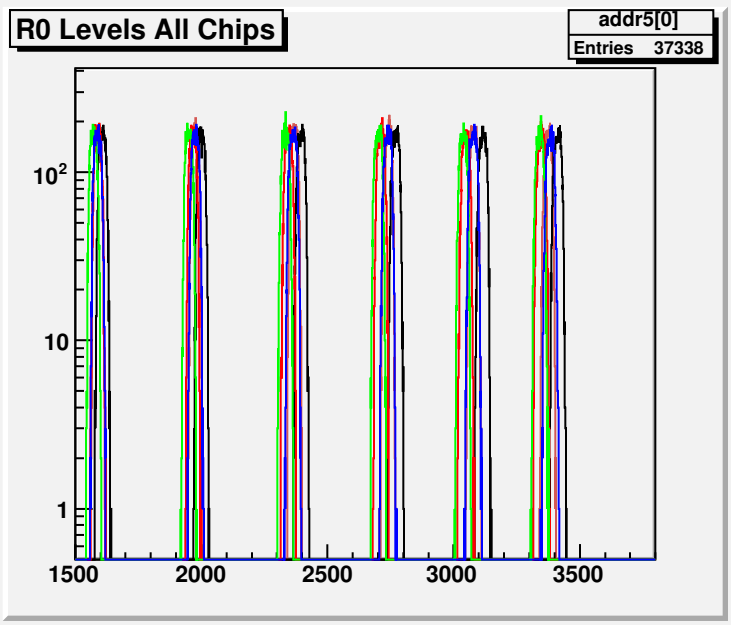
1
1500
2000
2500
3000
3500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addr5[0]
Entries 37338
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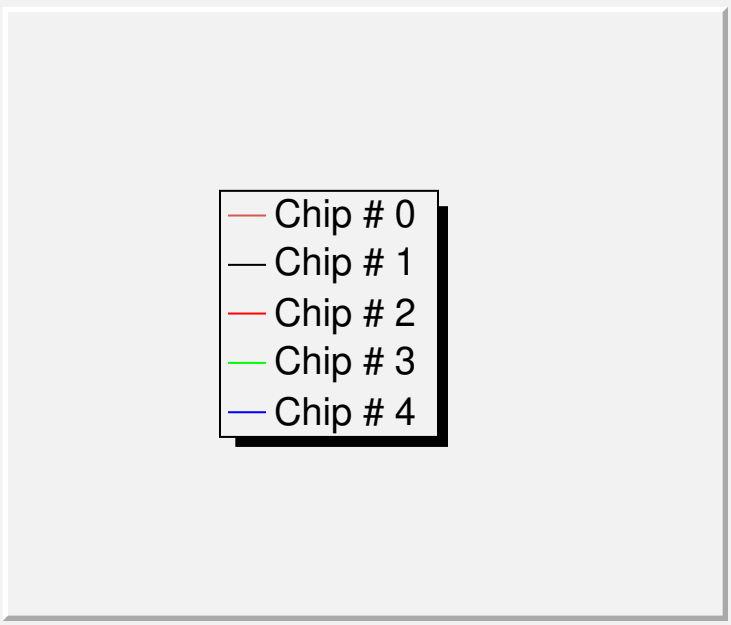
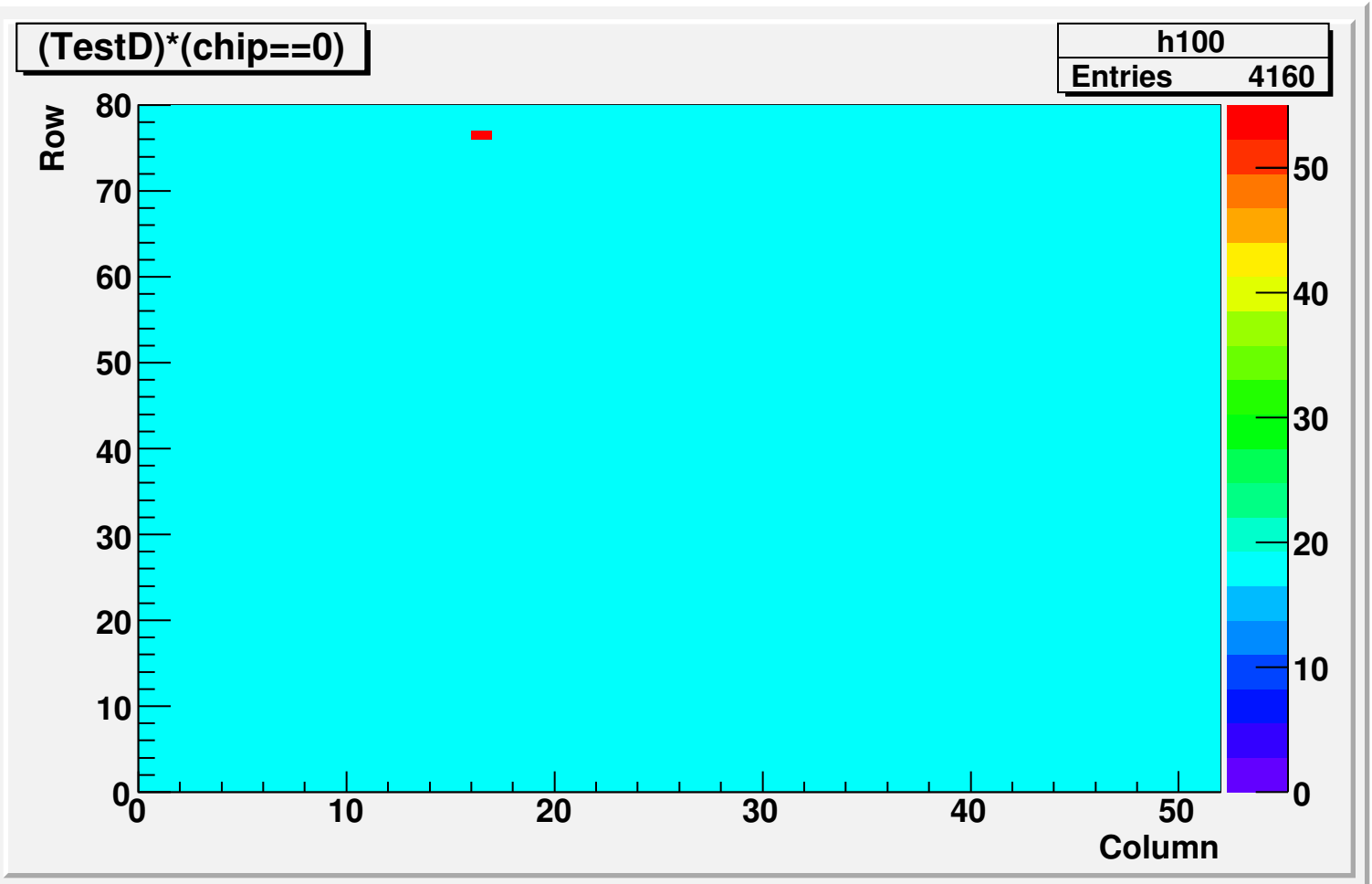
1
1500
2000
2500
3000
3500


Chip # 0
Chip # 1
Chip # 2
Chip # 3
Chip # 4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h100
Entries 4160
Row
Column
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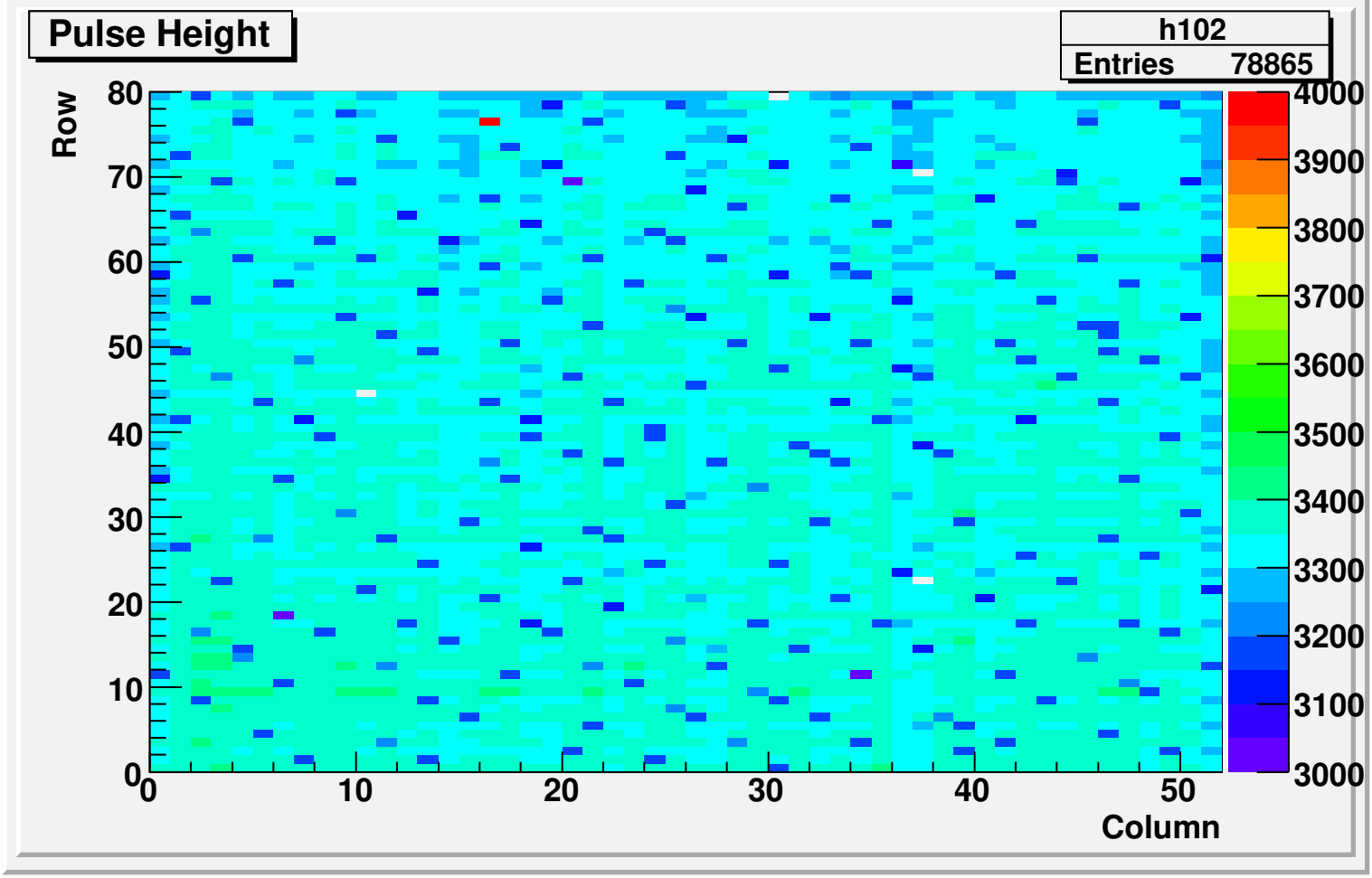
Pulse Height
h102
Entries 78865
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Column
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3100
3200
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3900
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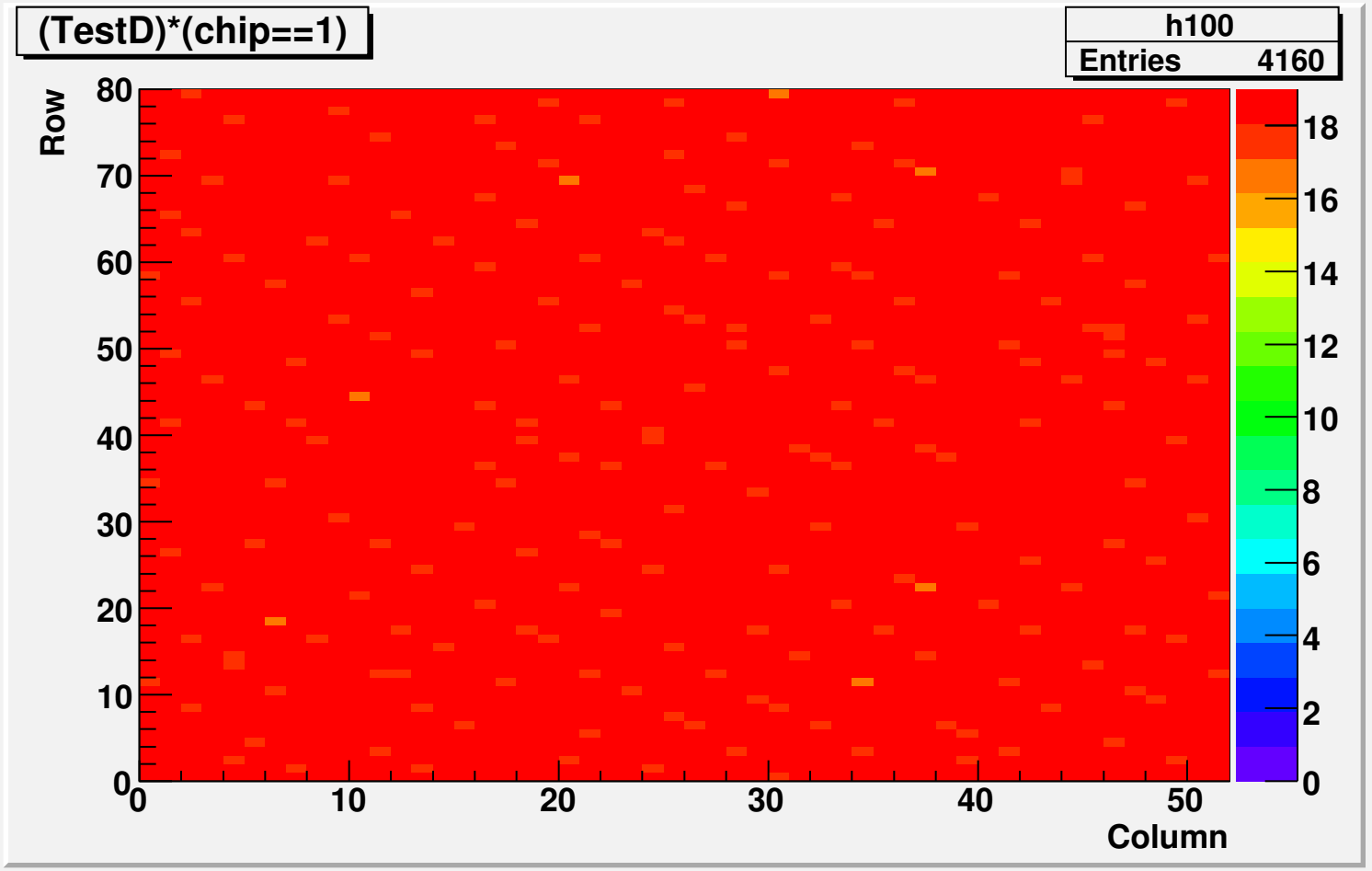
(TestD)*(chip==1)
h100
Entries 4160
Row
Column
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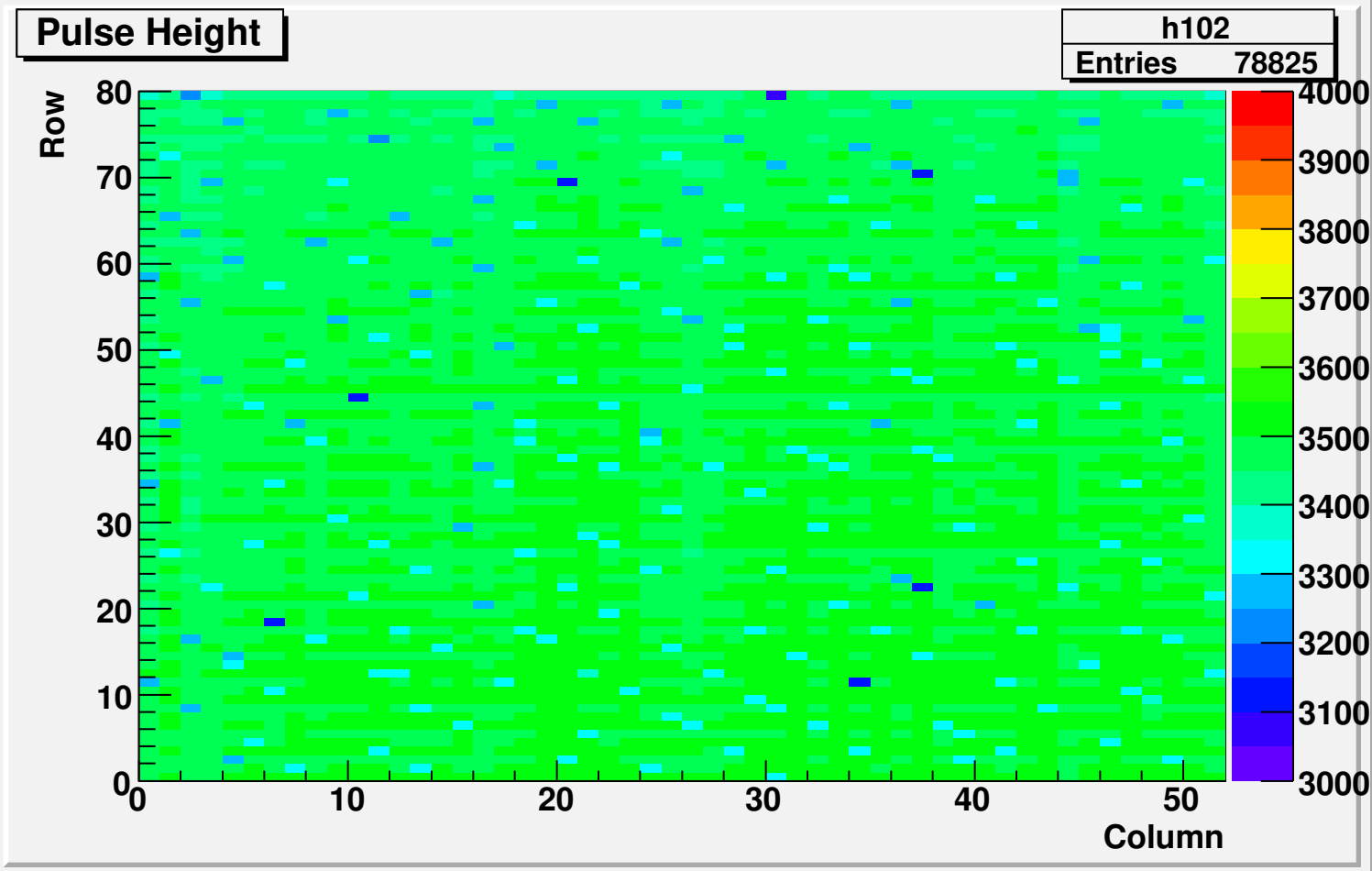
6
8
10
12
14
16
18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Entries 78825
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Column
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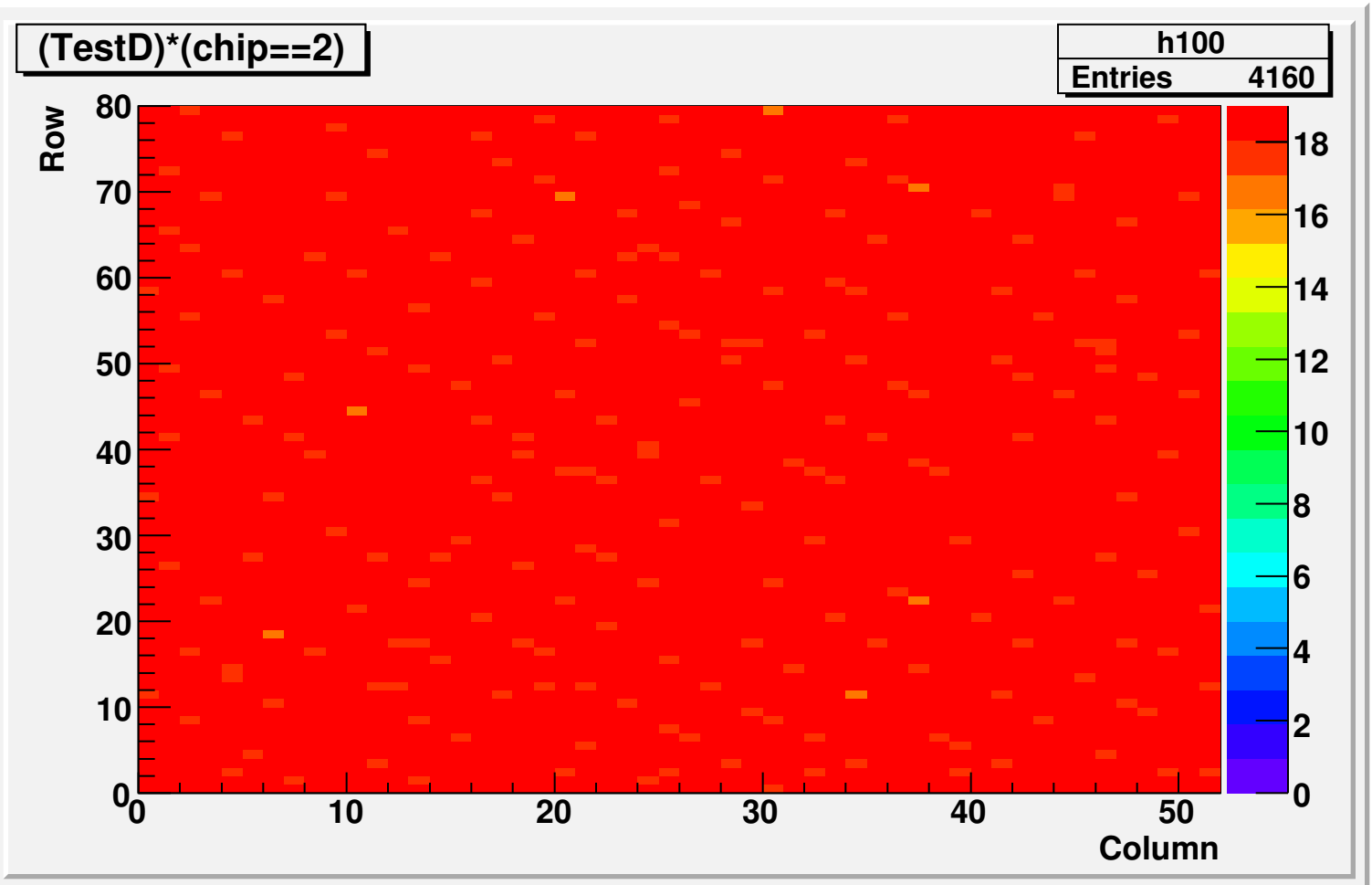
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Entries 4160
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Column
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8
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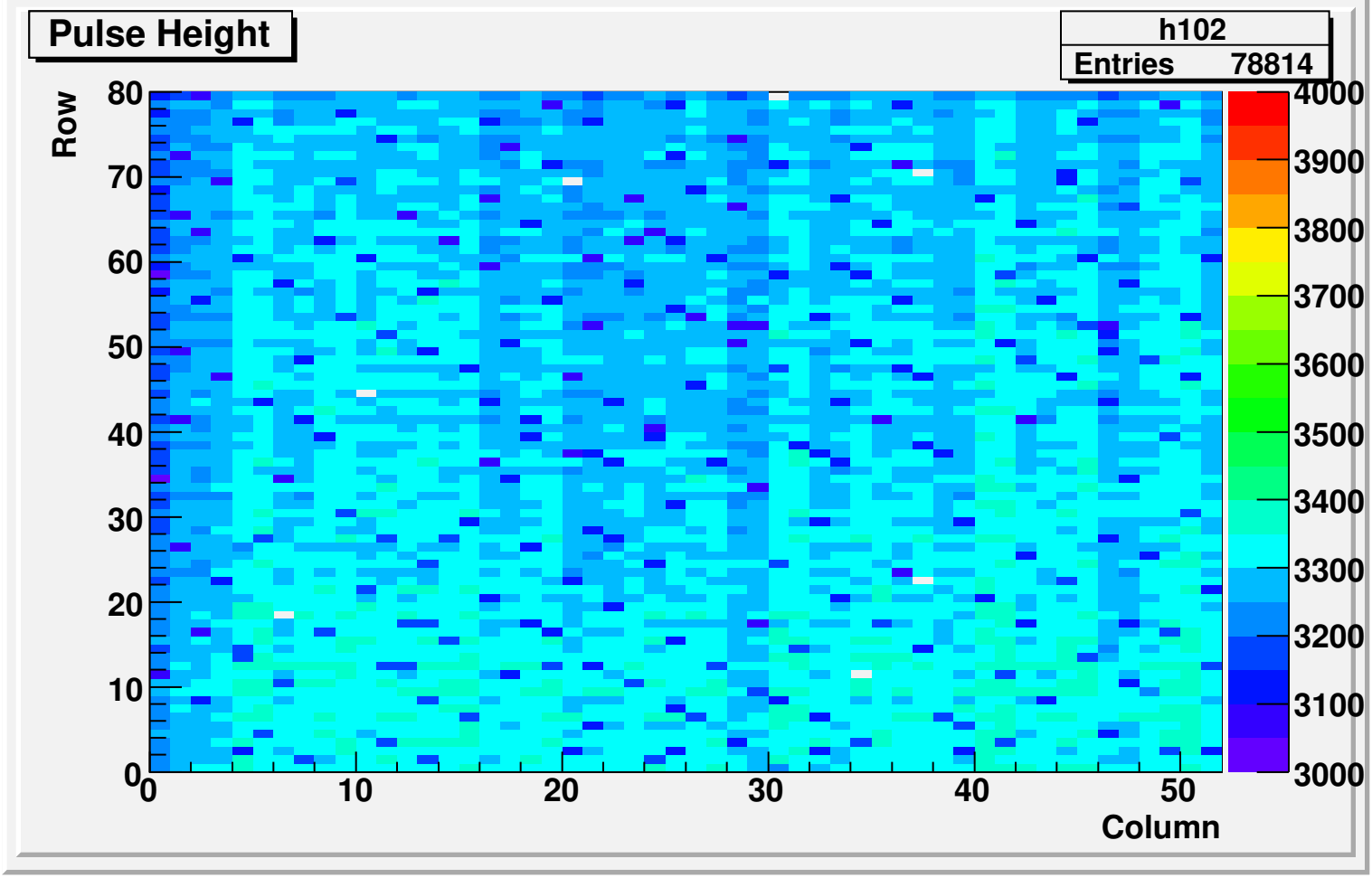
12
14
16
18


Pulse Height
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Entries 78814
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Column
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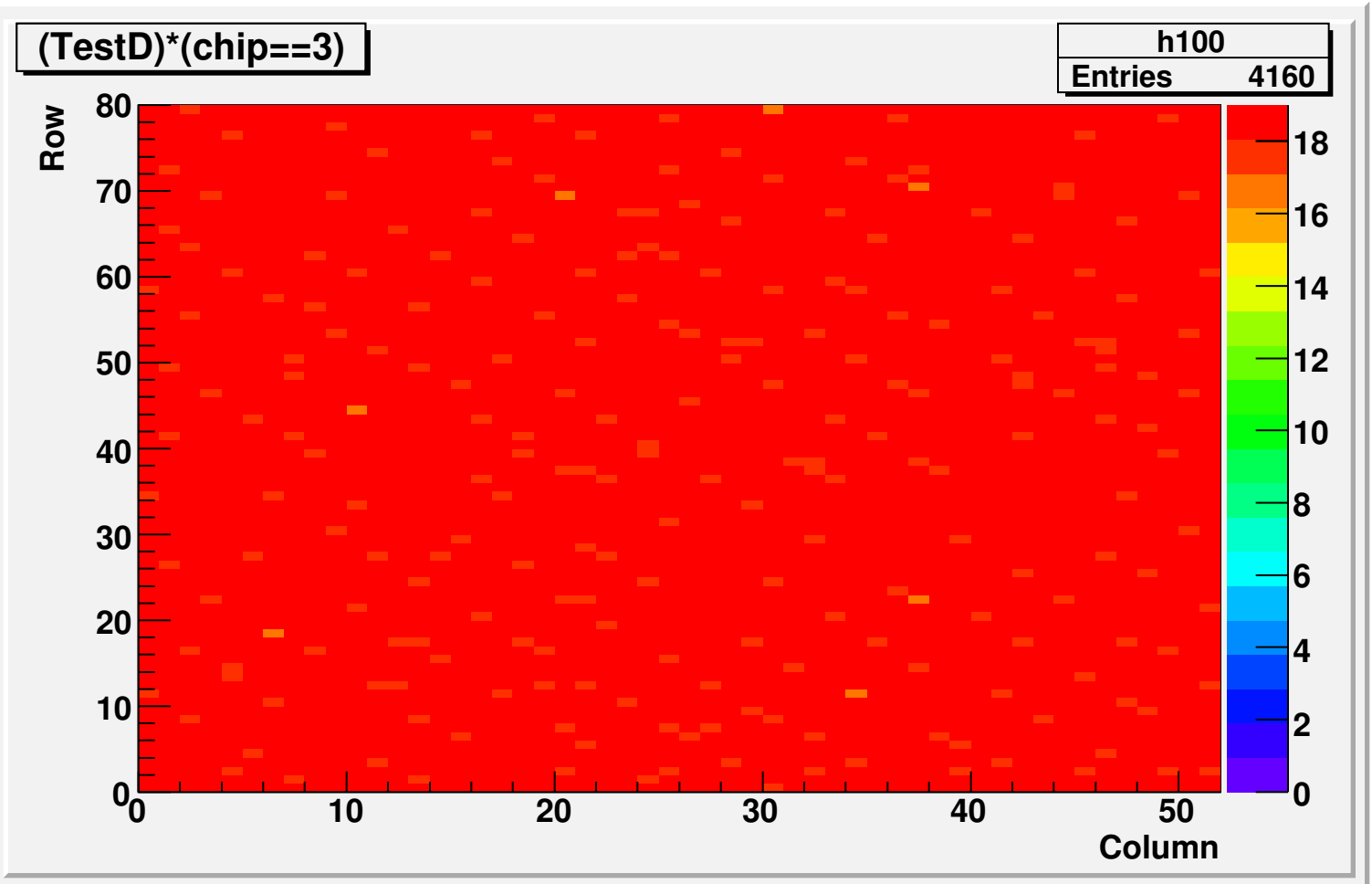
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Entries 4160
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Column
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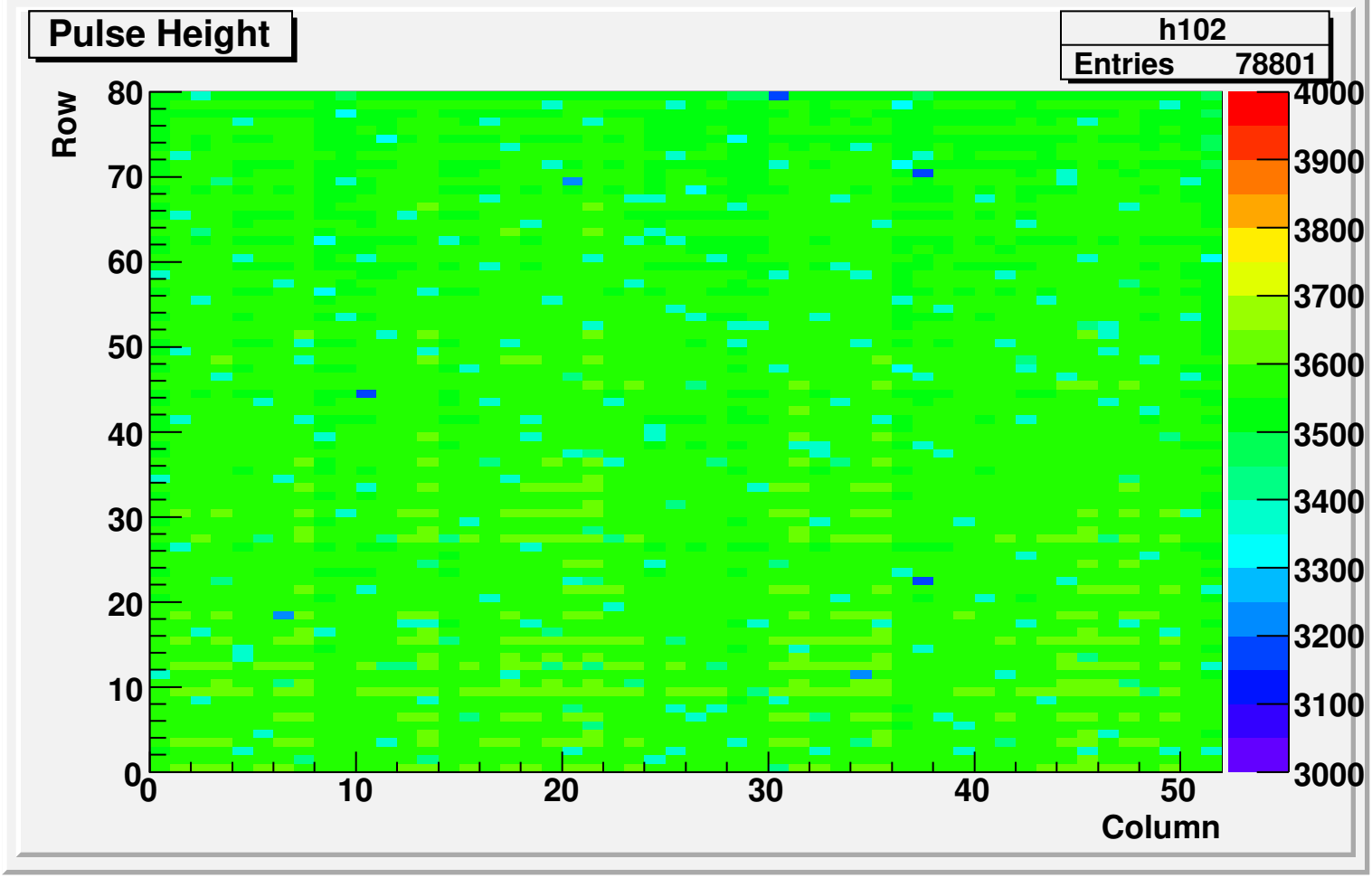
Pulse Height
h102
Entries 78801
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Column
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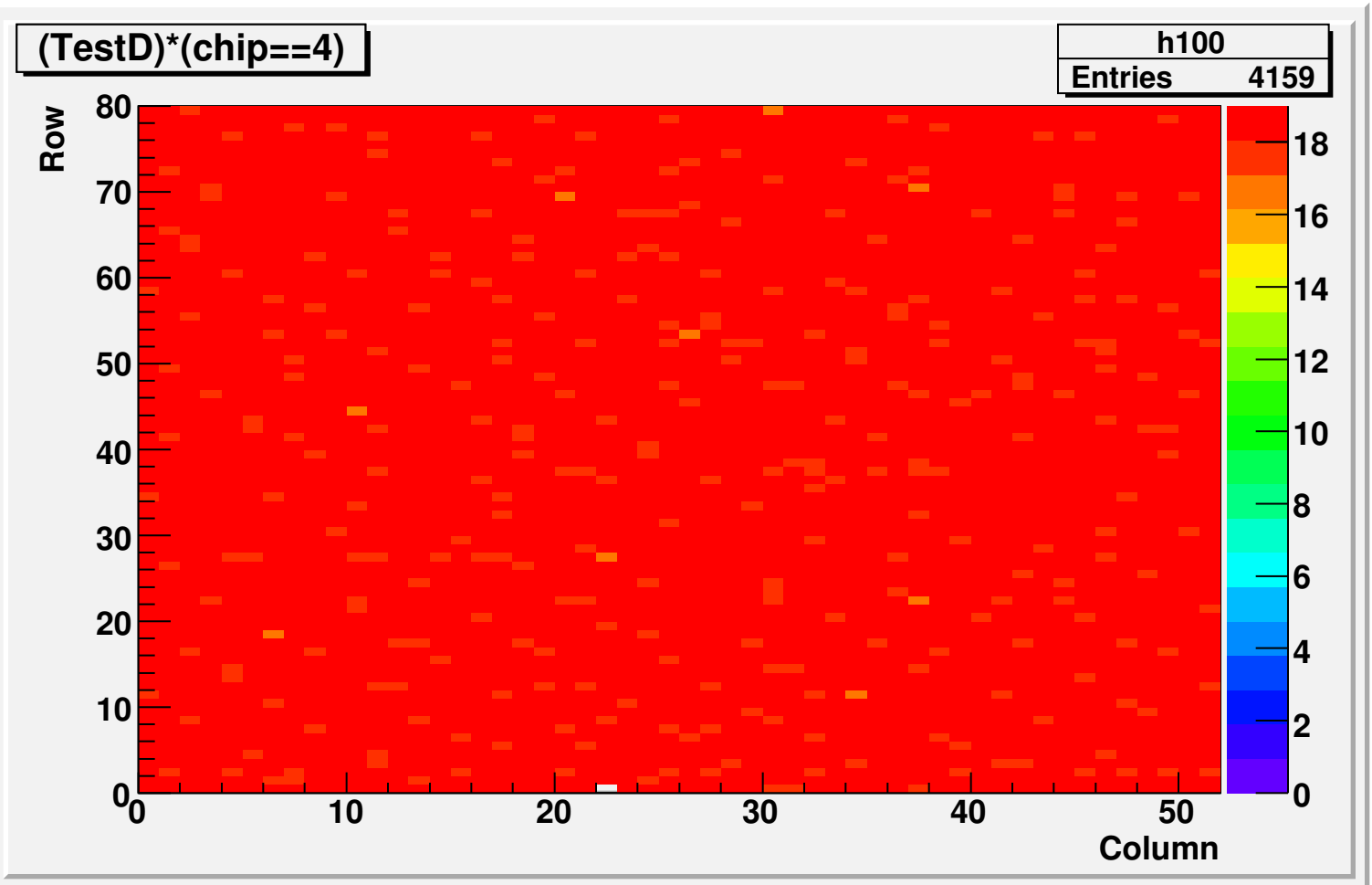
(TestD)*(chip==4)
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Entries 4159
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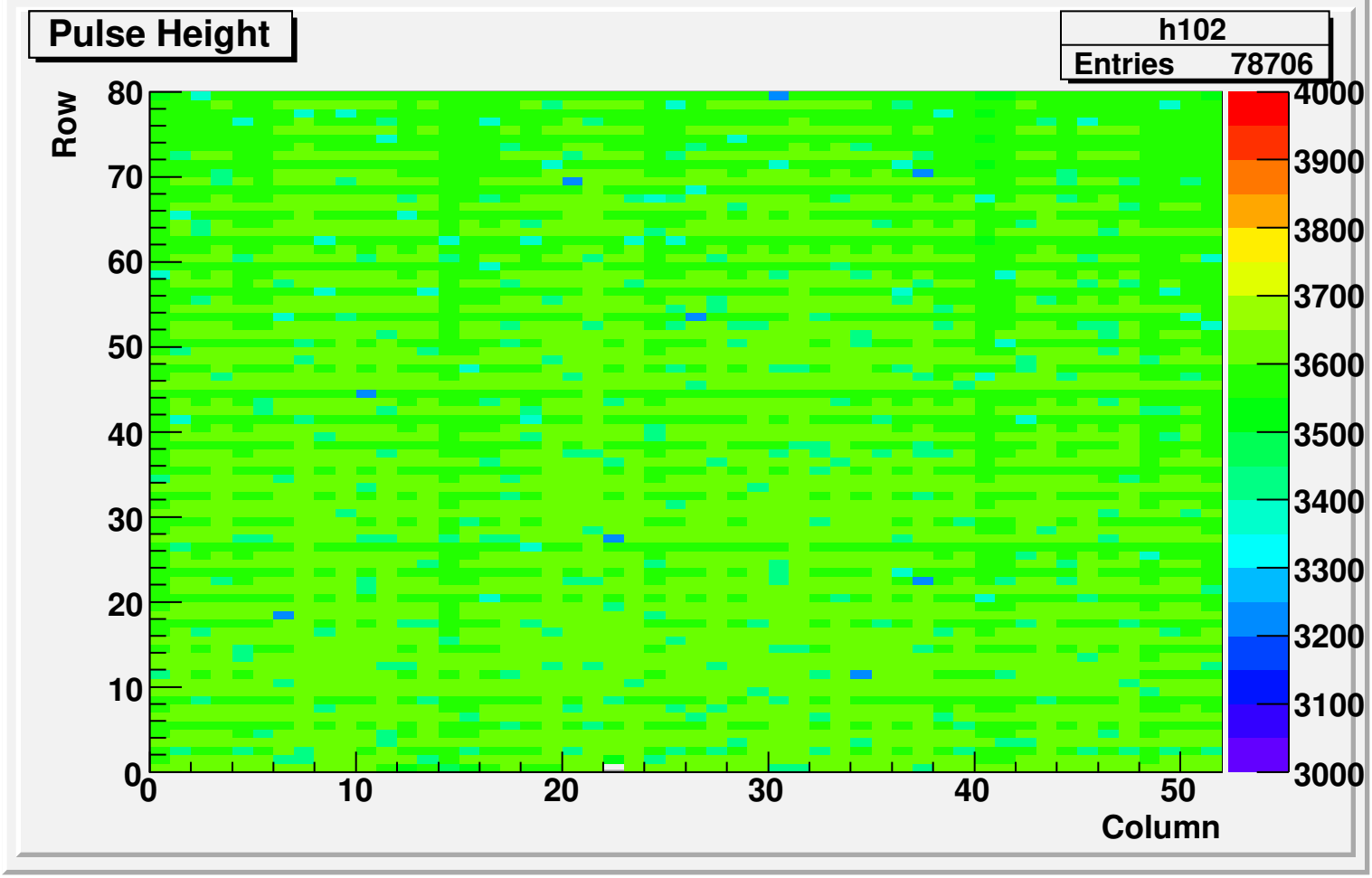
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Entries 78706
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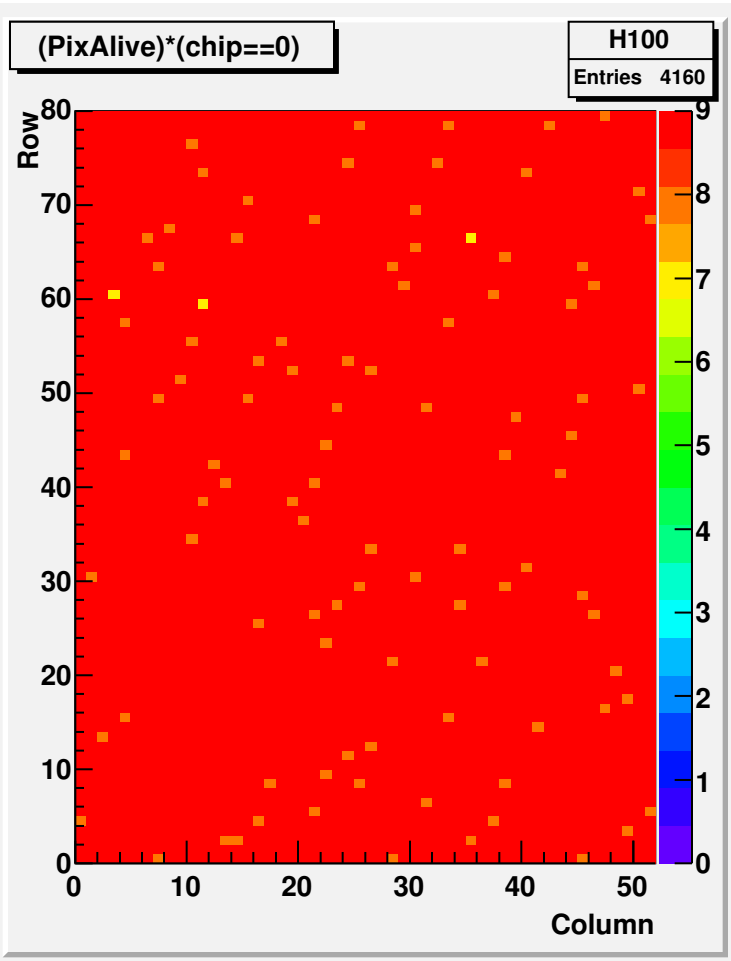
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Entries 4160
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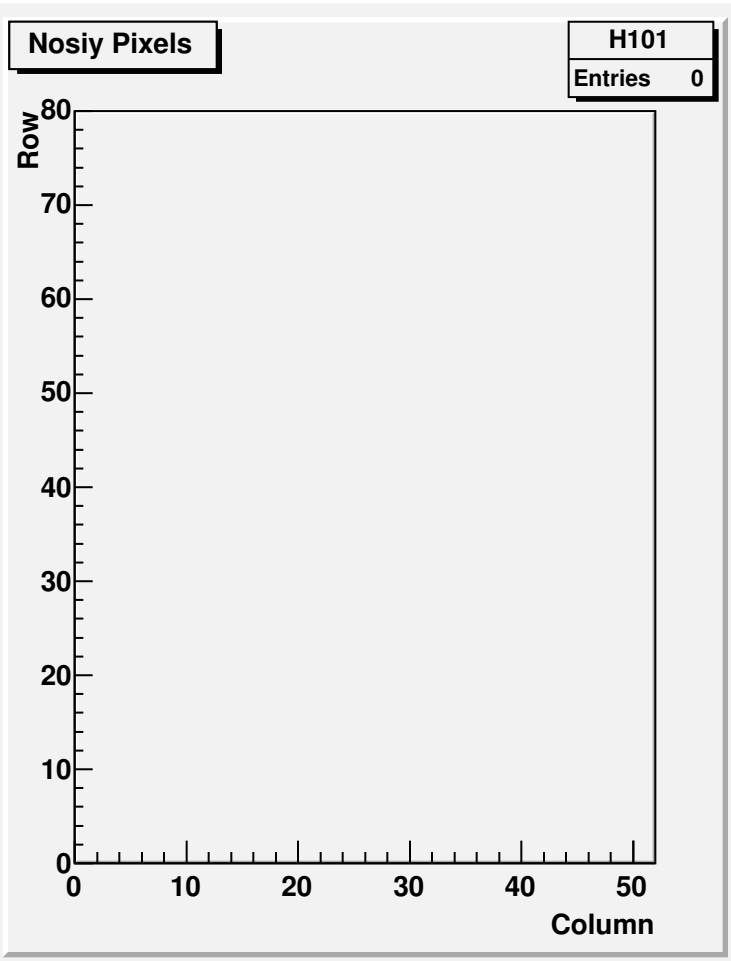
Column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H101
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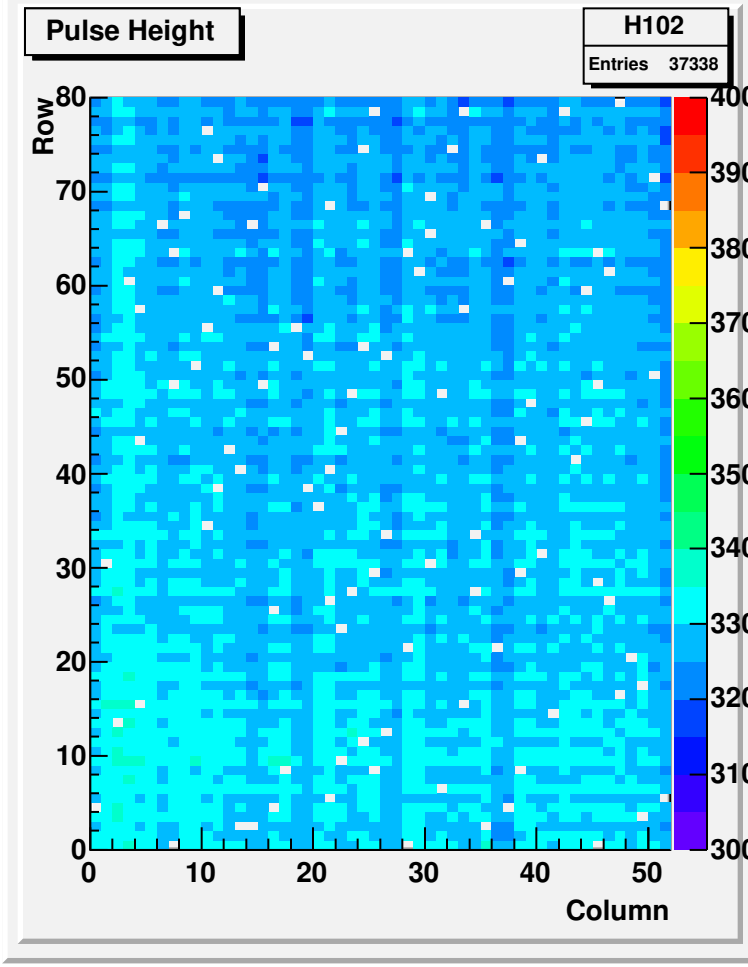
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H102
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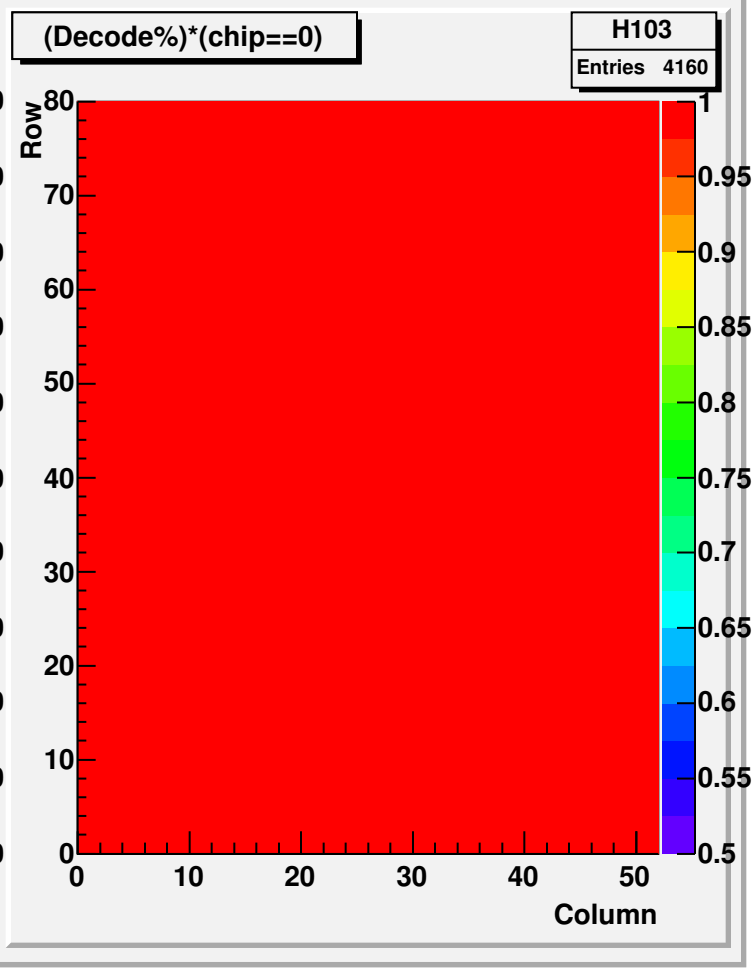
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H103
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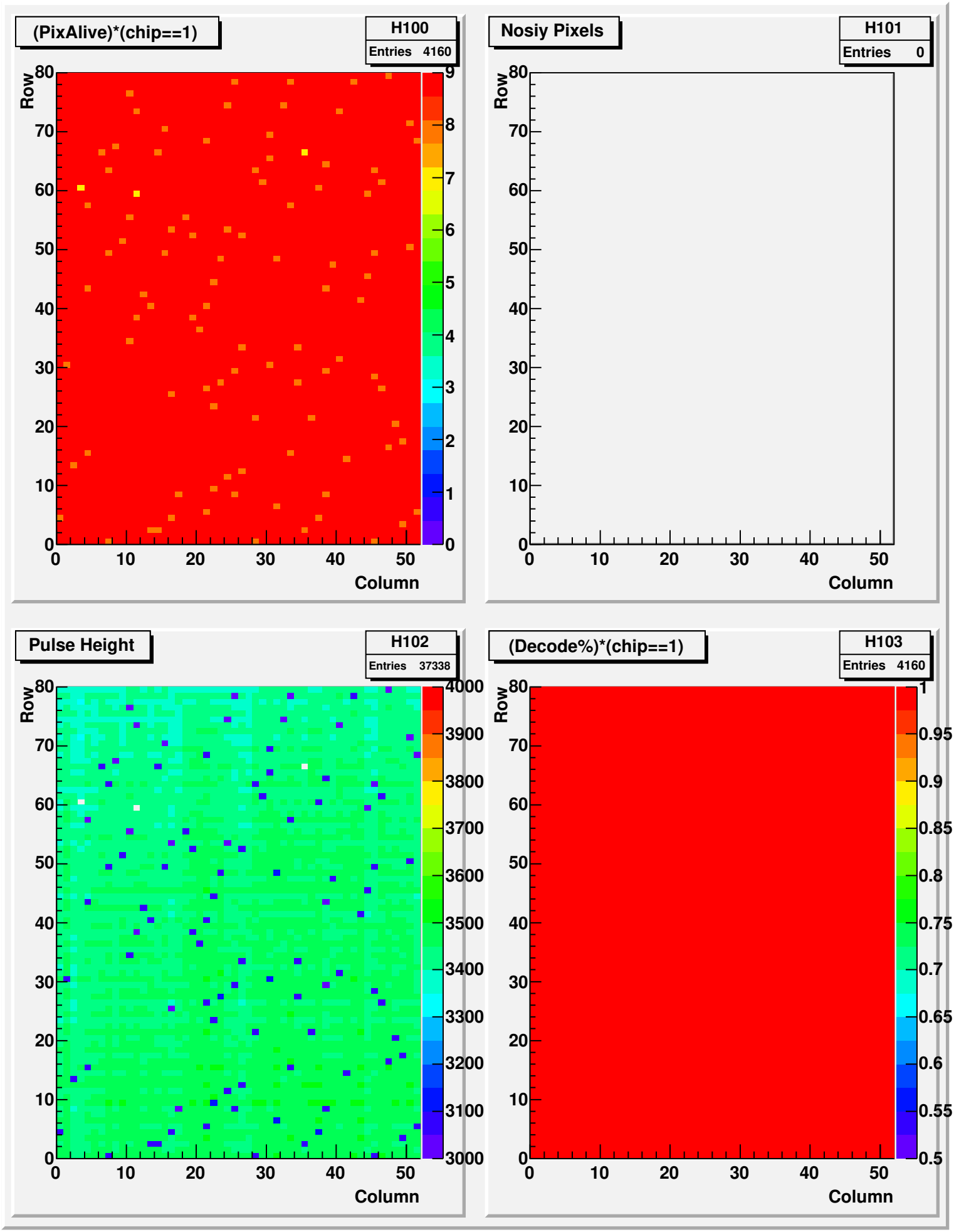
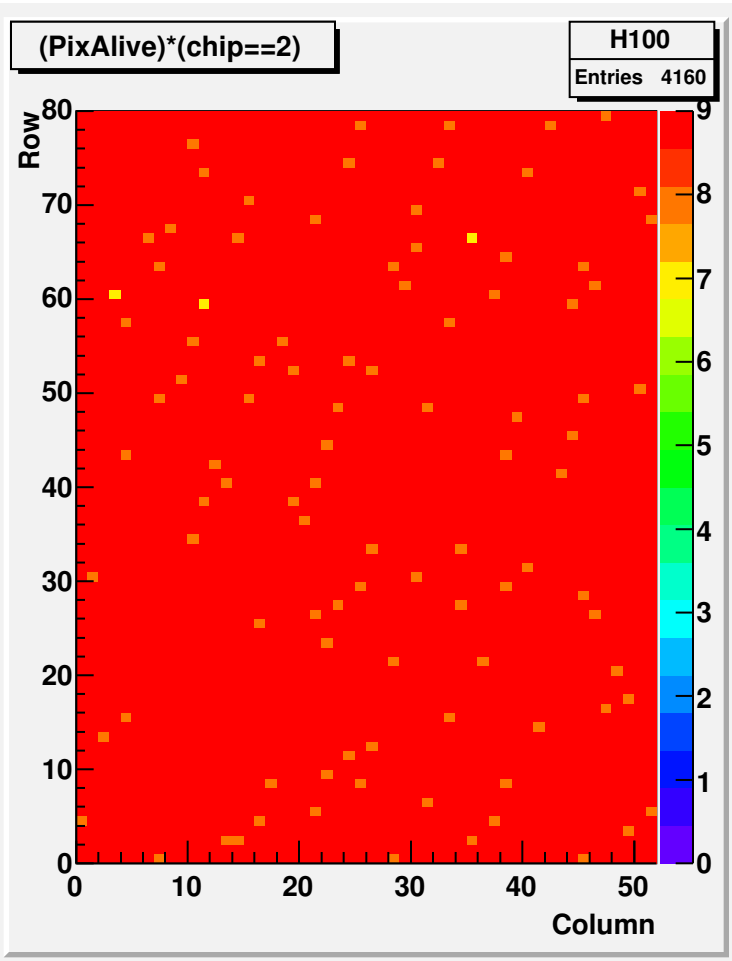

(PixAlive)*(chip==1)
H100
Entries 4160
Row
Column
Nosiy Pixels
H101
Entries 0
Row
Column
Pulse Height
H102
Entries 37338
Row
Column
(Decode%)*(chip==1)
H103
Entries 4160
Row
Column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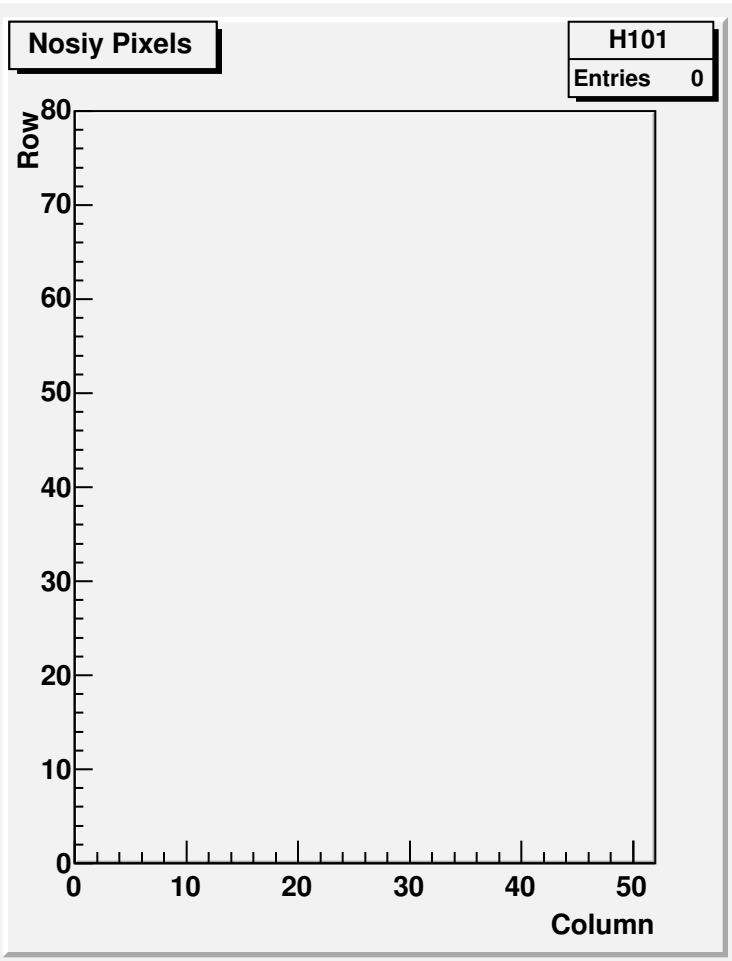
Column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H101
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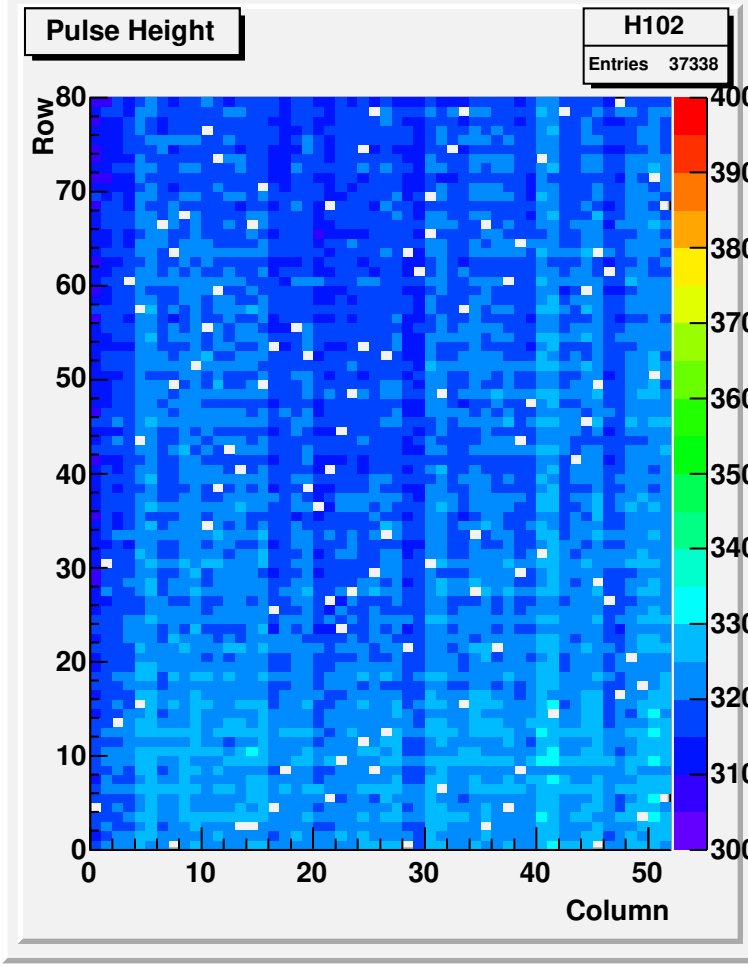
Column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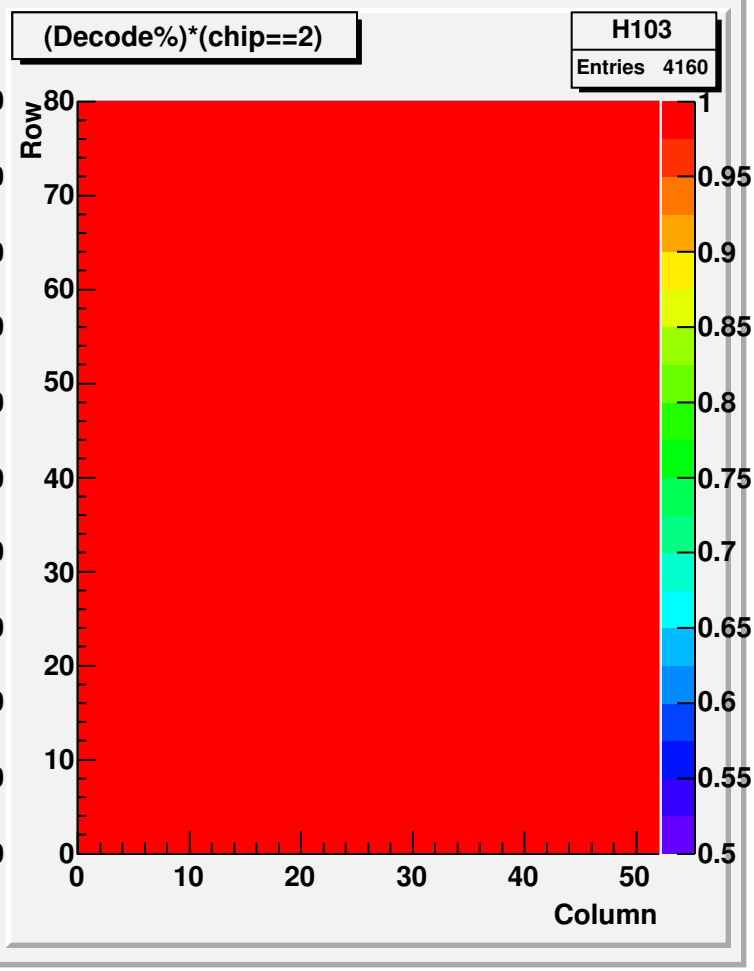
H102
Entries 37338
Row
Column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Entries 4160
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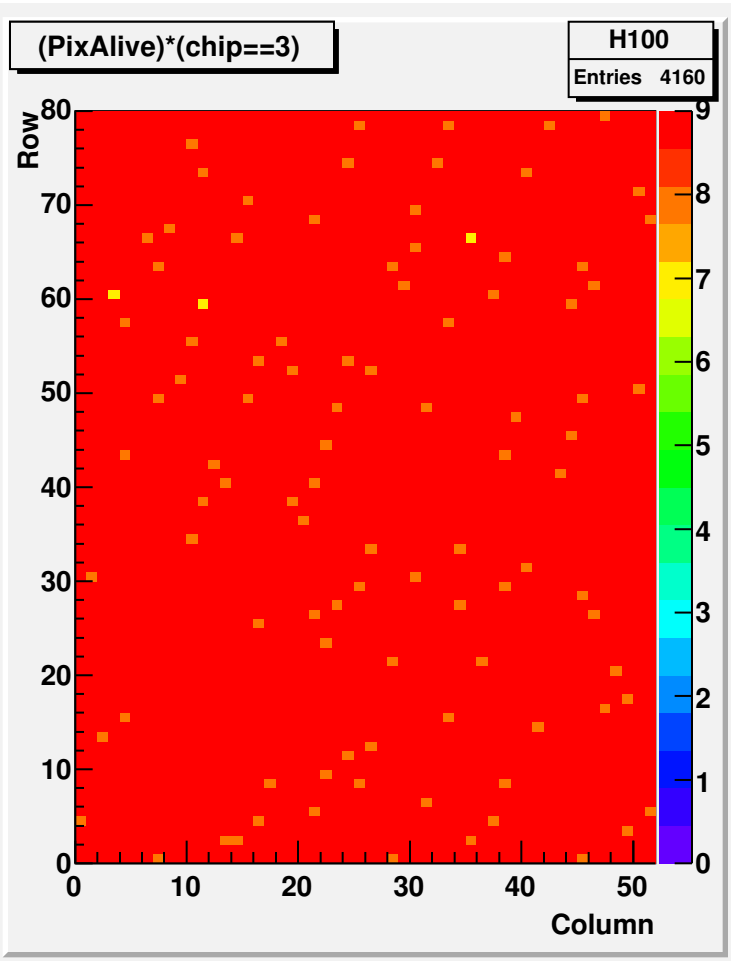
Column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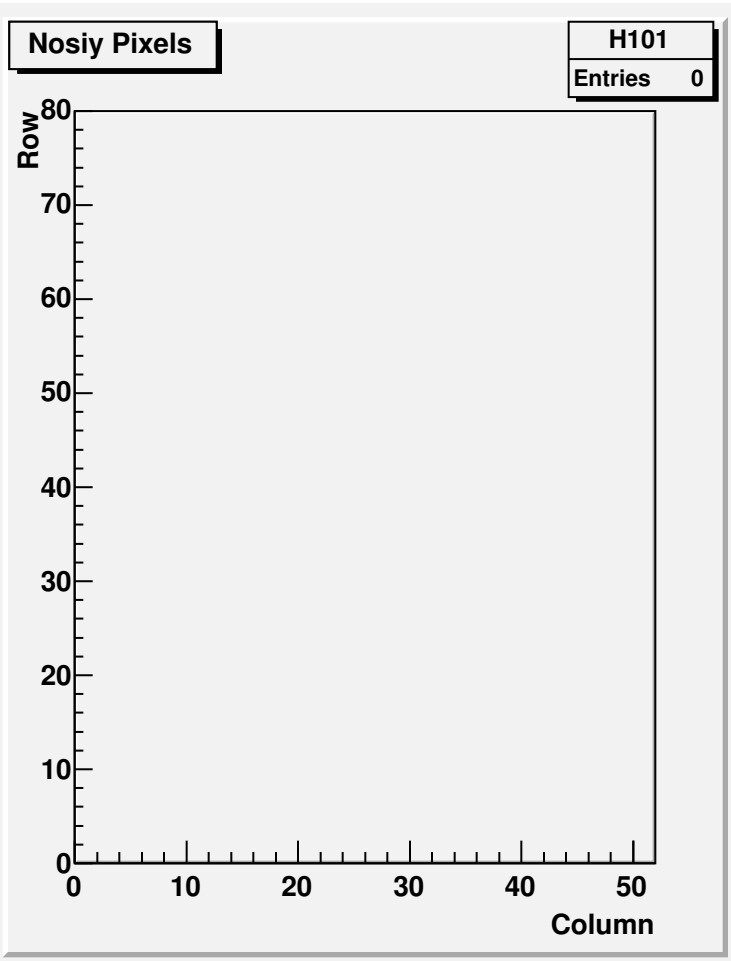
H100
Entries 4160
Row
Column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H101
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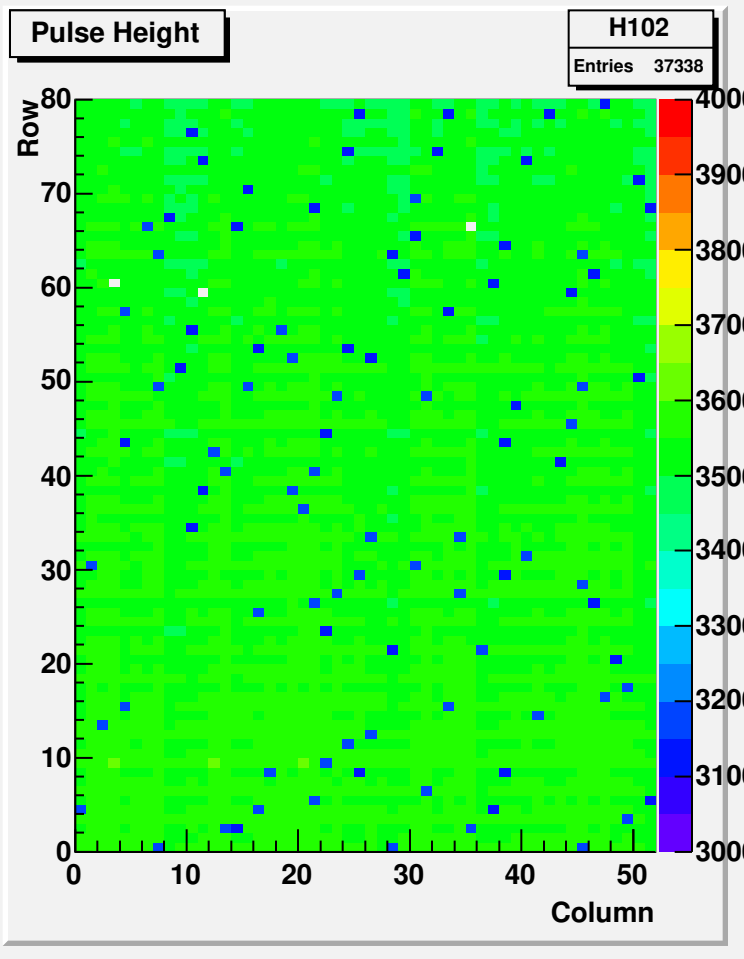
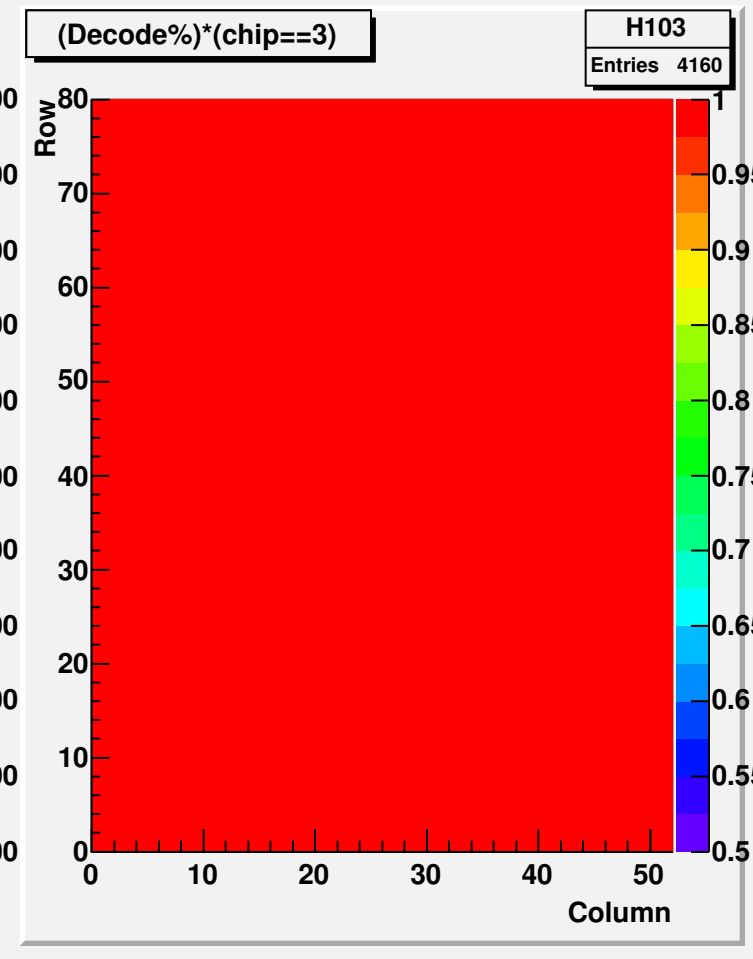
Column


Pulse Height
H102
Entries 37338
Row
Column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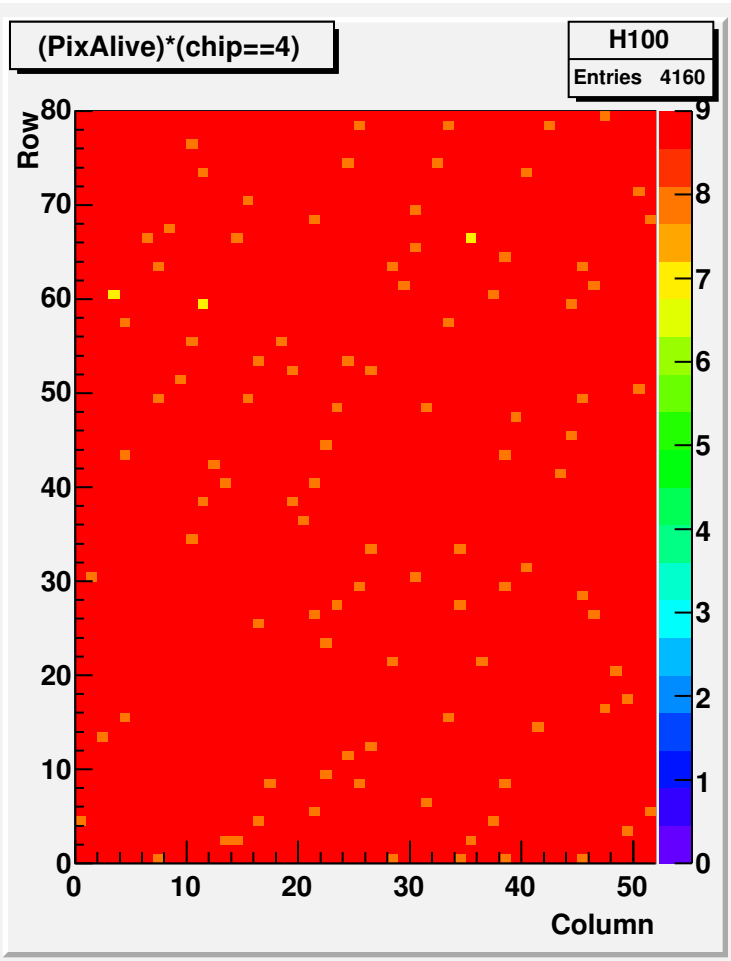
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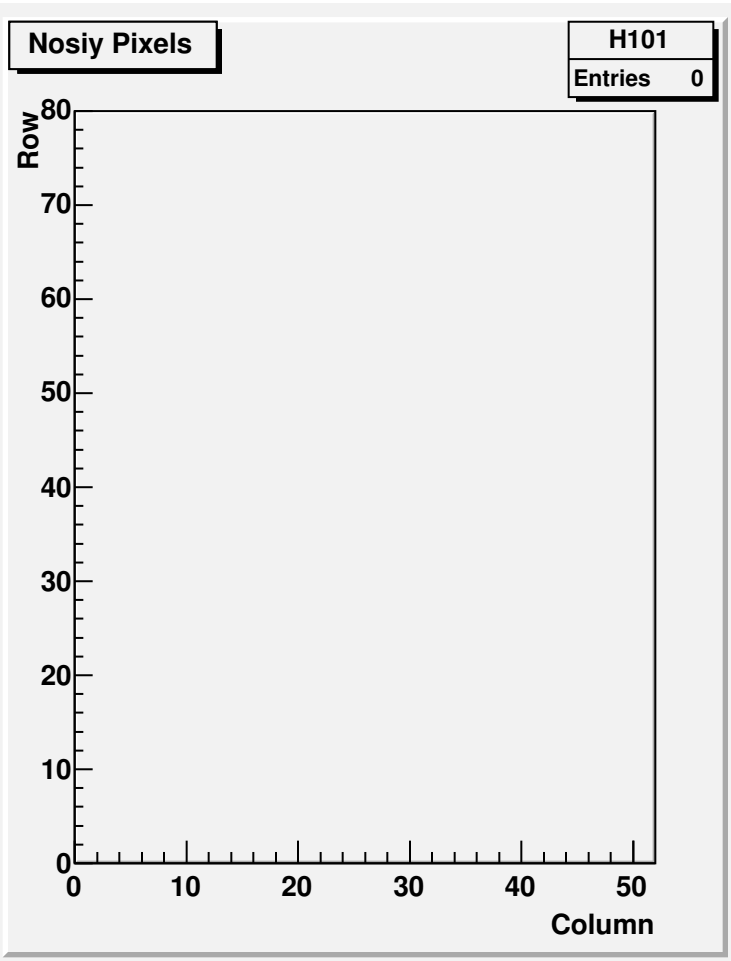
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H101
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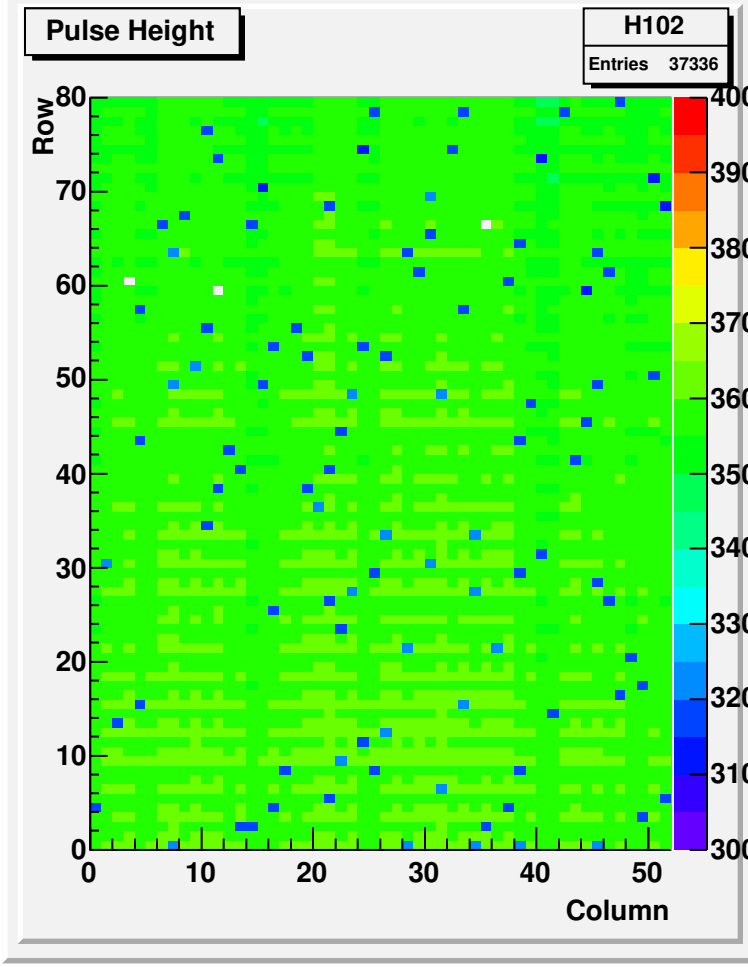
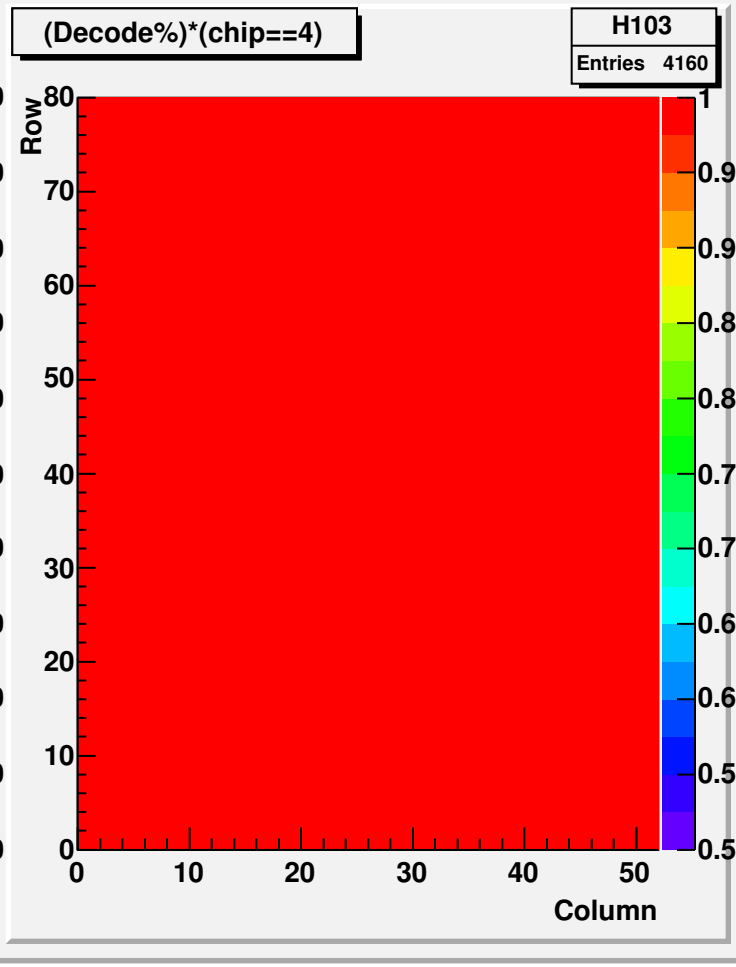

Pulse Height
H102
Entries 37336
Row
Column


(Decode%)*(chip==4)
H103
Entries 4160
Row
Column