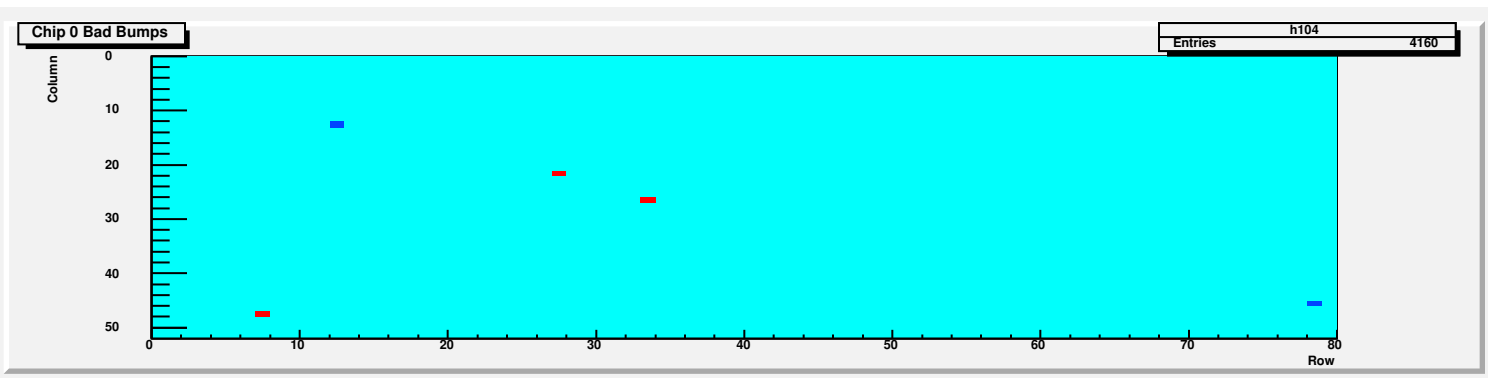
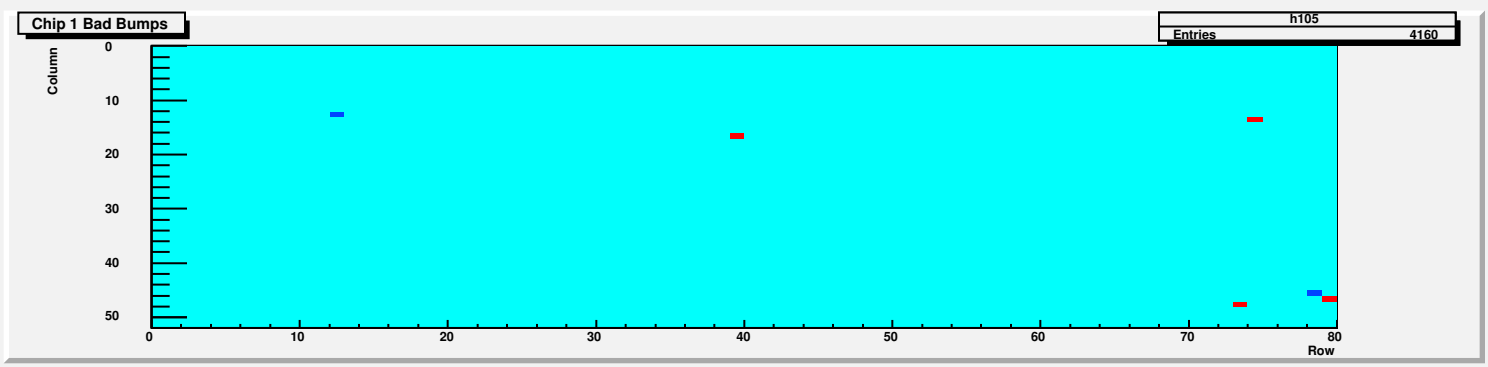
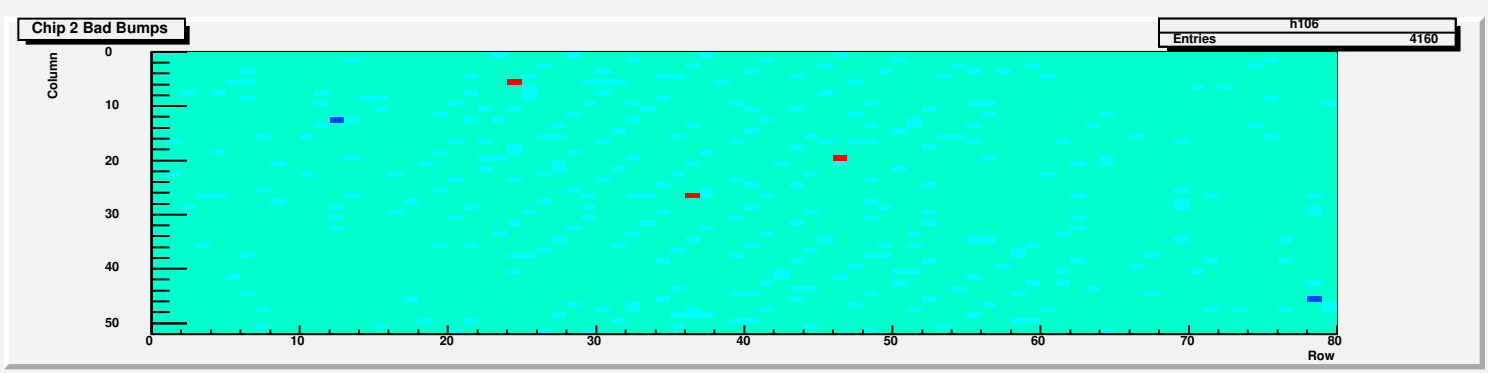
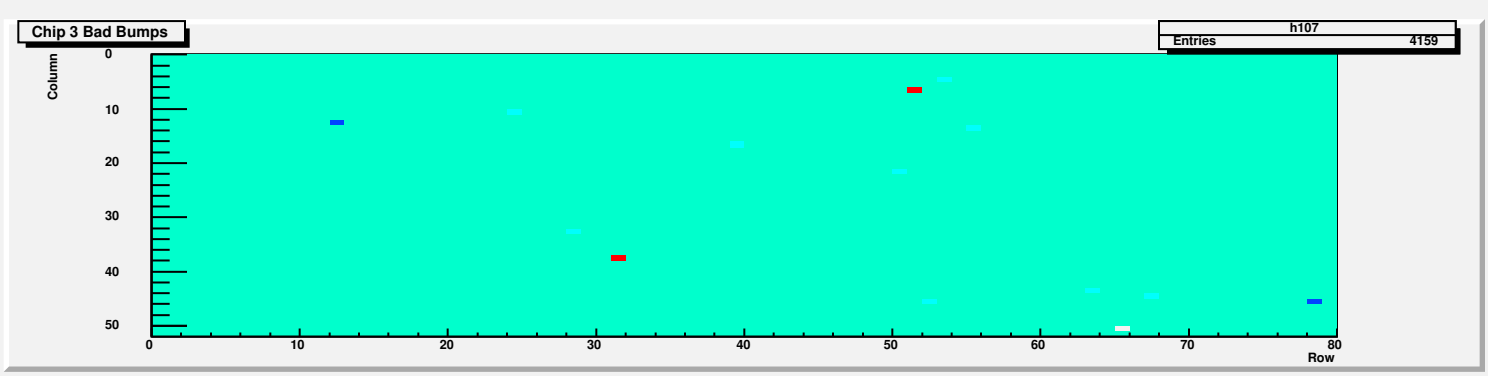

Chip 0 Bad Bumps
h104
Entries 4160
Column
Row


Chip 1 Bad Bumps
h105
Entries 4160
Column
Row


Chip 2 Bad Bumps
h106
Entries 4160
Column
Row


Chip 3 Bad Bumps
h107
Entries 4159
Column
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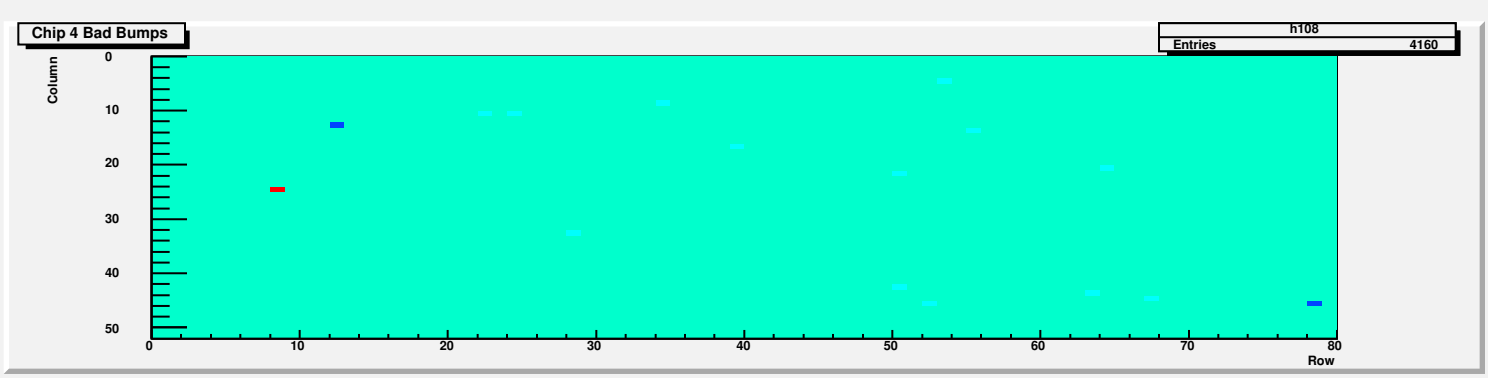
Chip 4 Bad Bumps
h108
Entries 4160
Column
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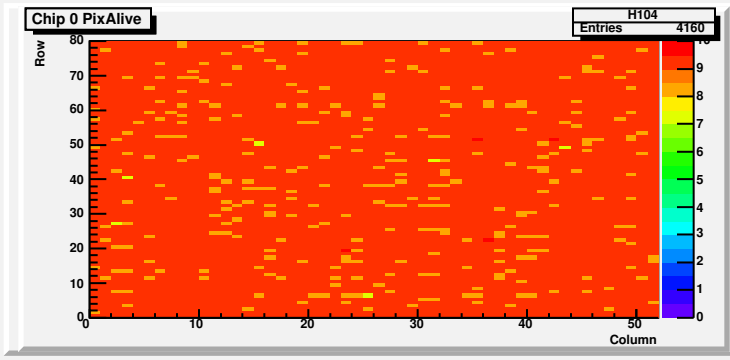
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H104
Entries 4160
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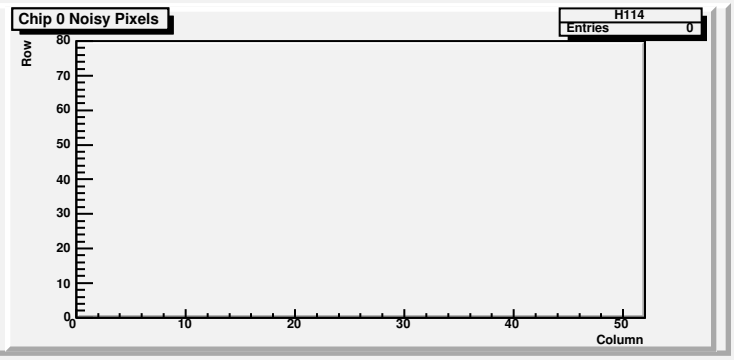
Column


Chip 0 Noisy Pixels
H114
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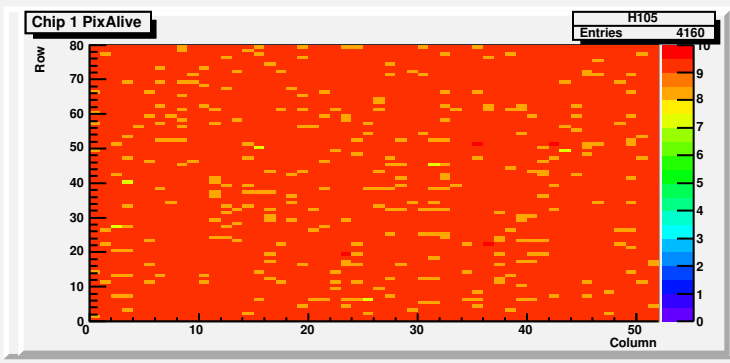
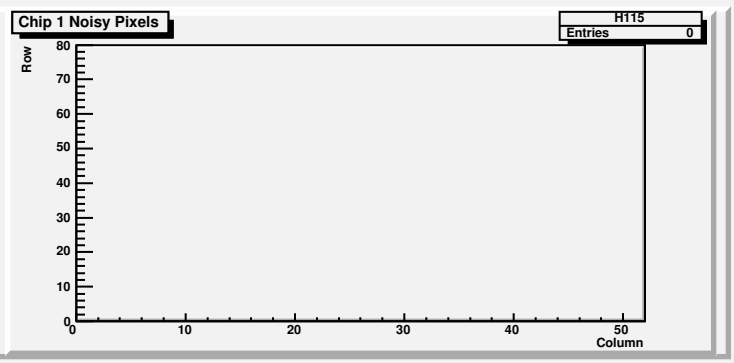
Column


Chip 1 PixAlive
H105
Entries 4160
Row
Column


Chip 1 Noisy Pixels
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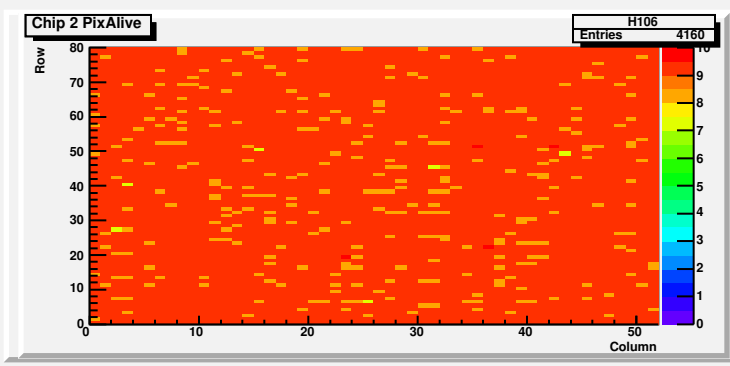
Column


Chip 2 PixAlive
H106
Entries 4160
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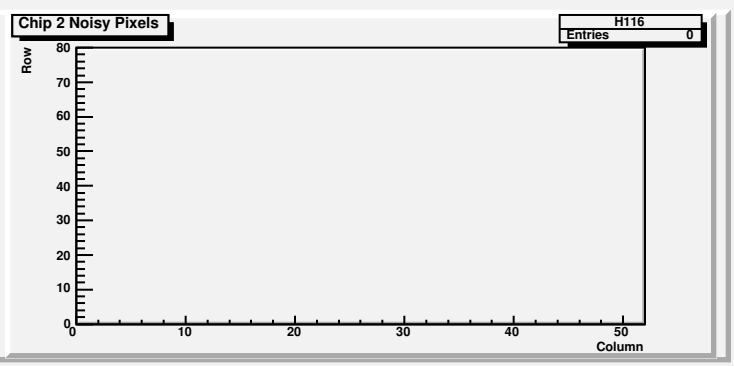
Column


Chip 2 Noisy Pixels
H116
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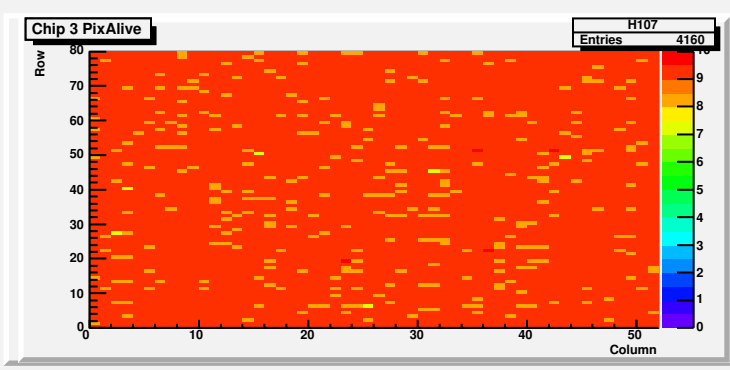
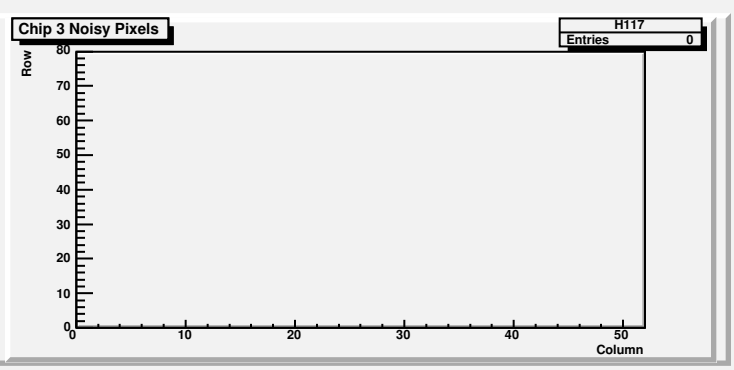
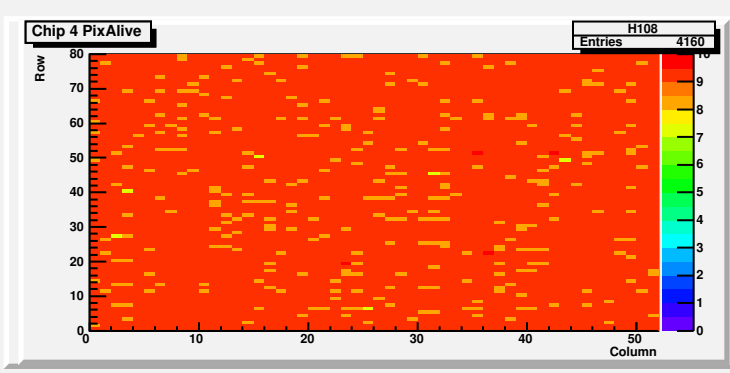
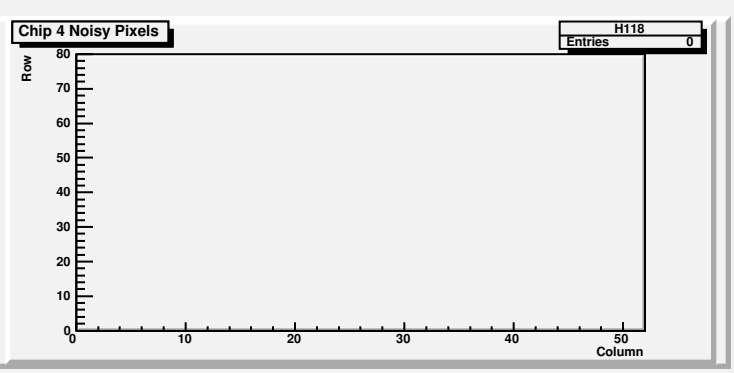
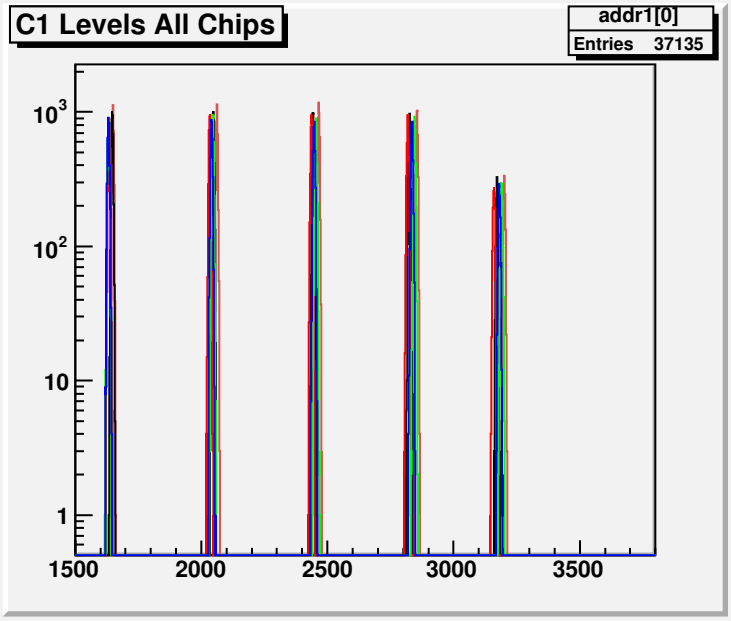
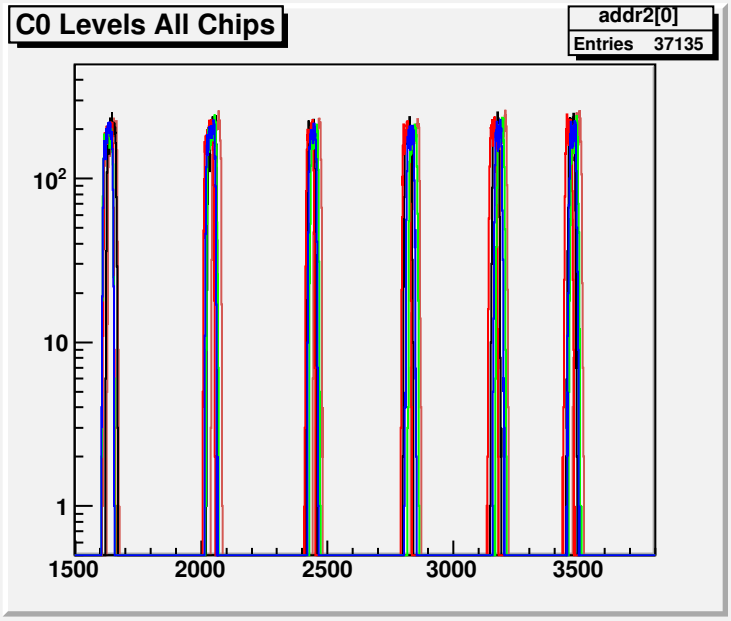

Chip 3 PixAlive
H107
Entries 4160
Row
Column

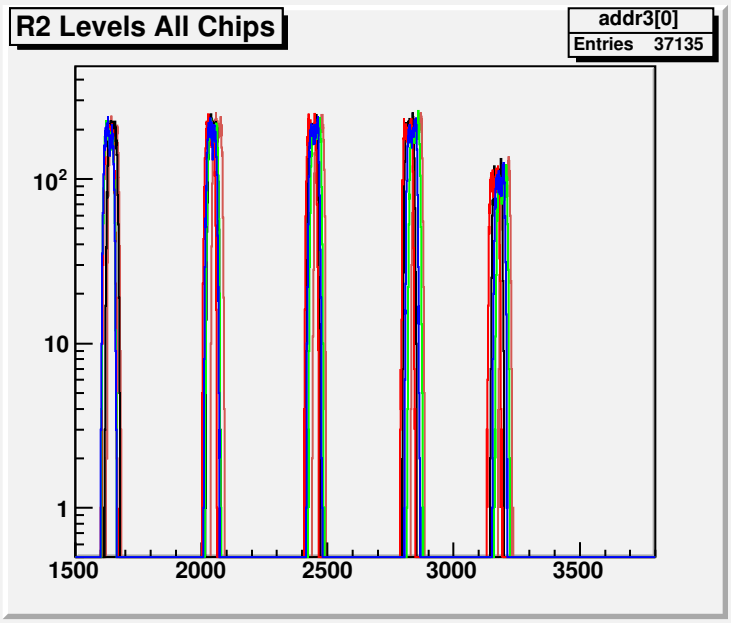

Chip 3 Noisy Pixels
H117
Entries 0
Row
Column

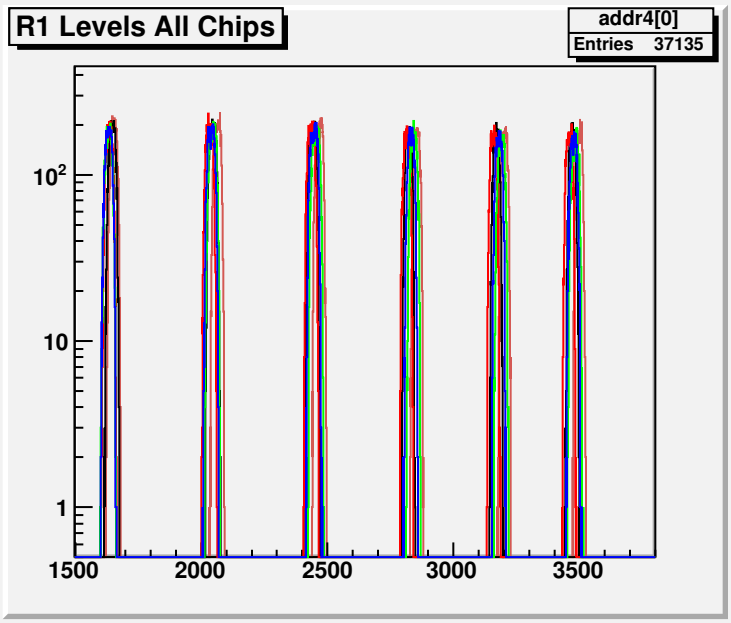

Chip 4 PixAlive
H108
Entries 4160
Row
Column


Chip 4 Noisy Pixels
H118
Entries 0
Row
Column


C1 Levels All Chips
addr1[0]
Entries 37135
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37135
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37135
10^2
10
1
1500
2000
2500
3000
3500


R1 Levels All Chips
addr4[0]
Entries 37135
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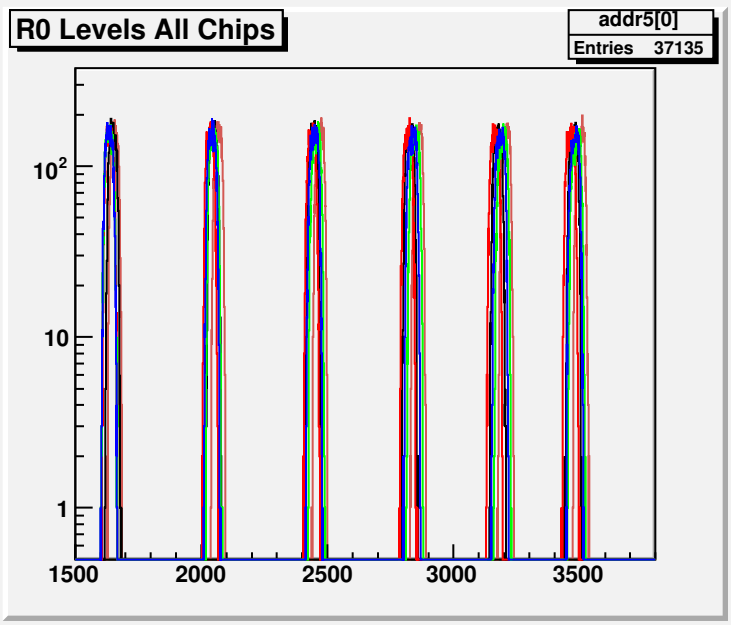
10
1
1500
2000
2500
3000
3500


R0 Levels All Chips
addr5[0]
Entries 37135
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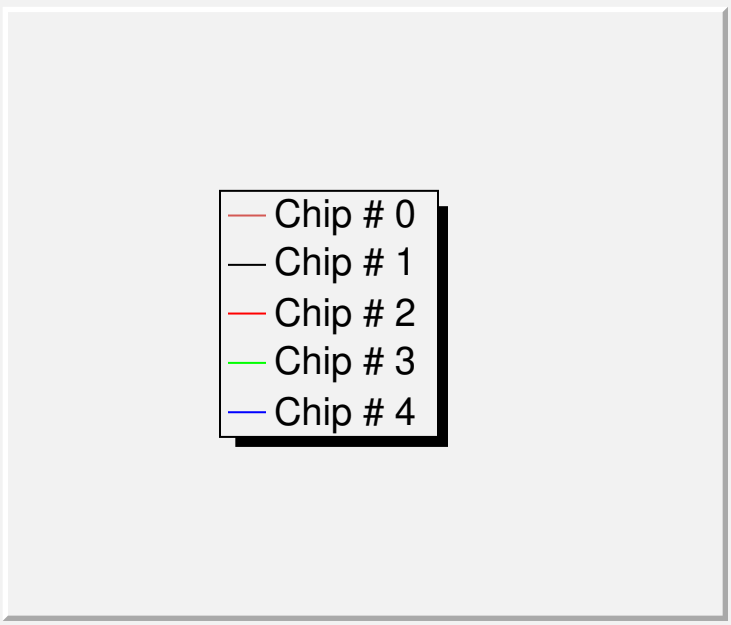
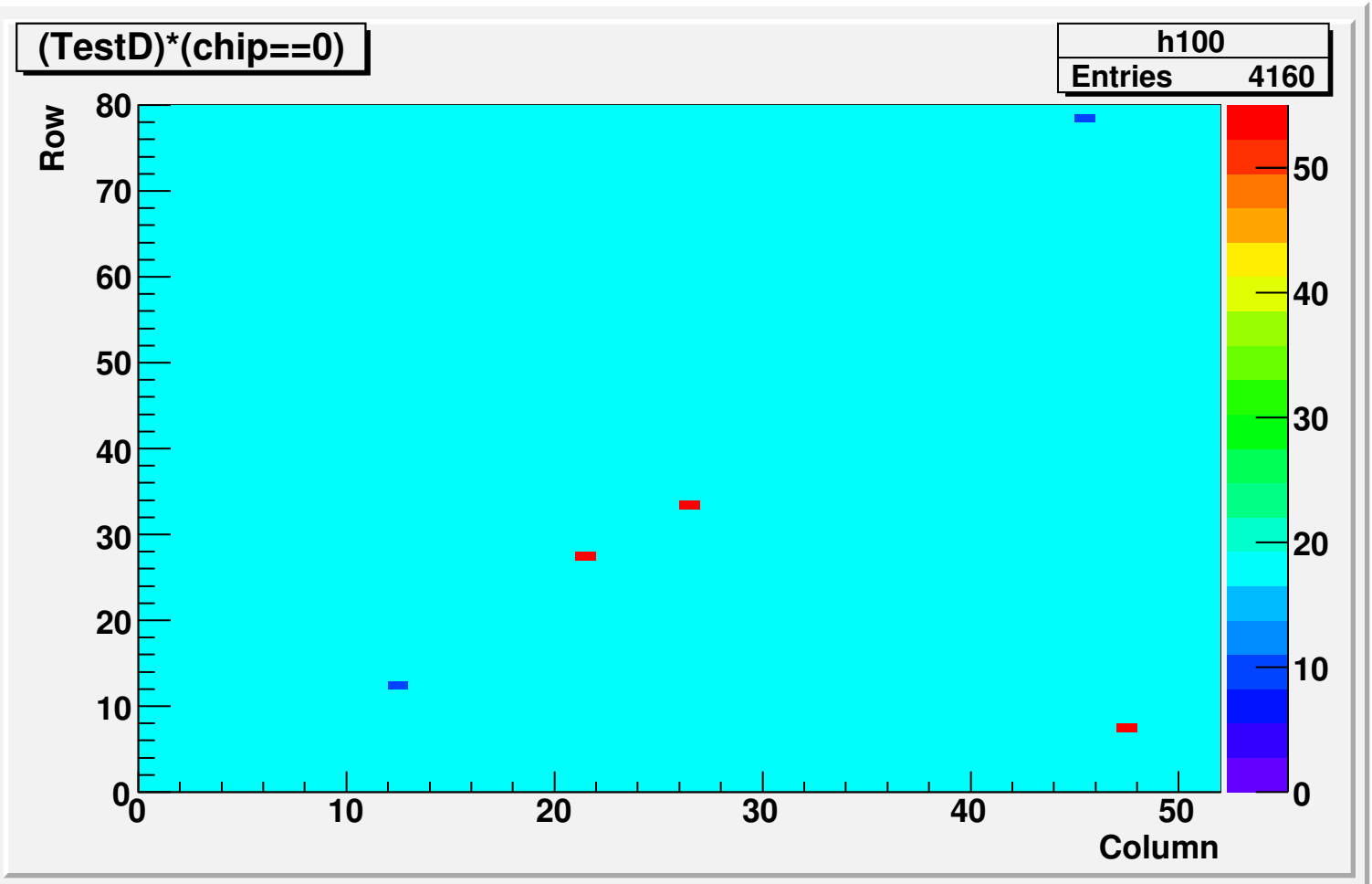
1
1500
2000
2500
3000
3500


Chip # 0
Chip # 1
Chip # 2
Chip # 3
Chip # 4


(TestD)*(chip==0)
h100
Entries 4160
Row
Column
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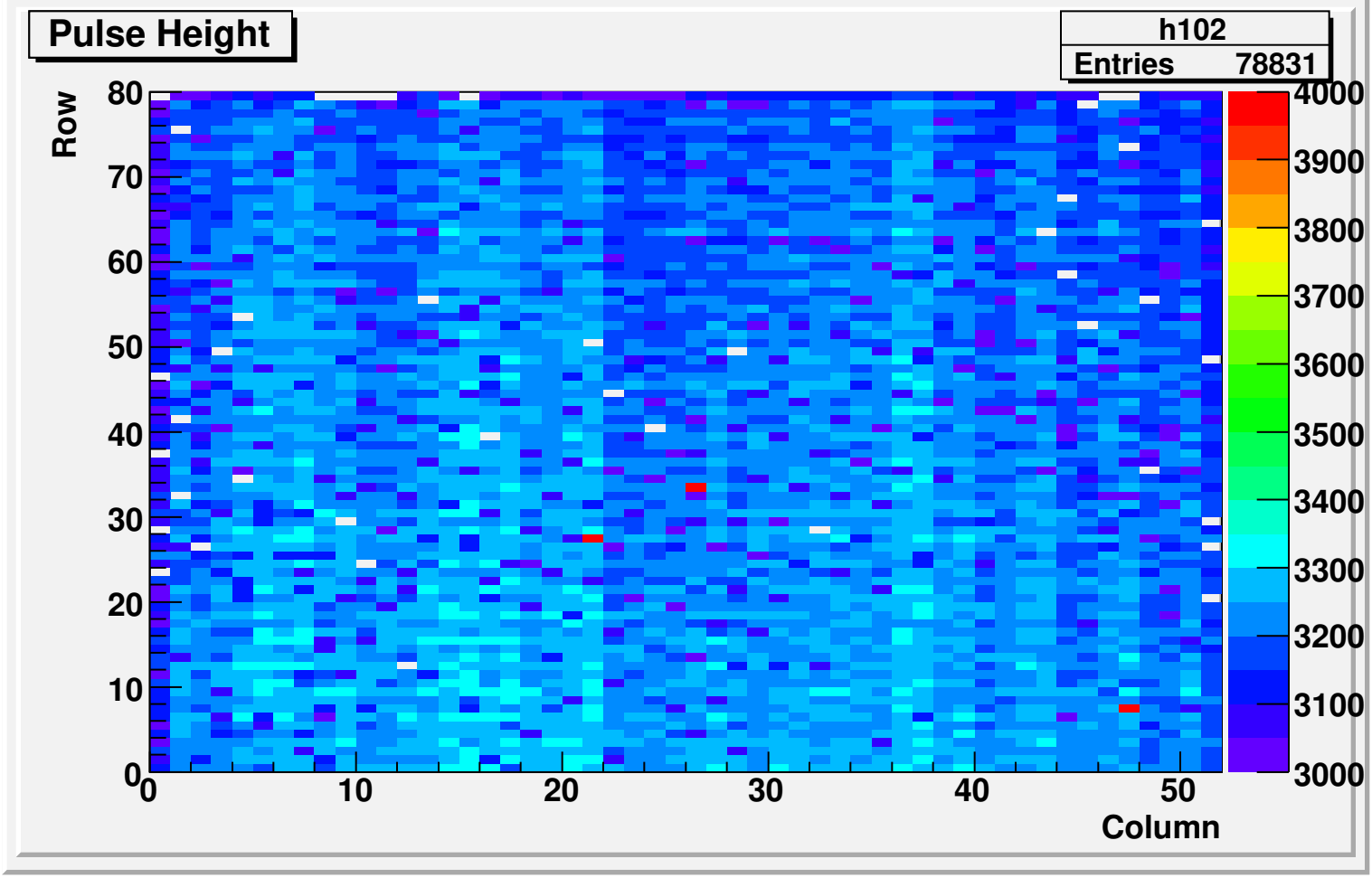
70
80


Pulse Height
h102
Entries 78831
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Column
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3100
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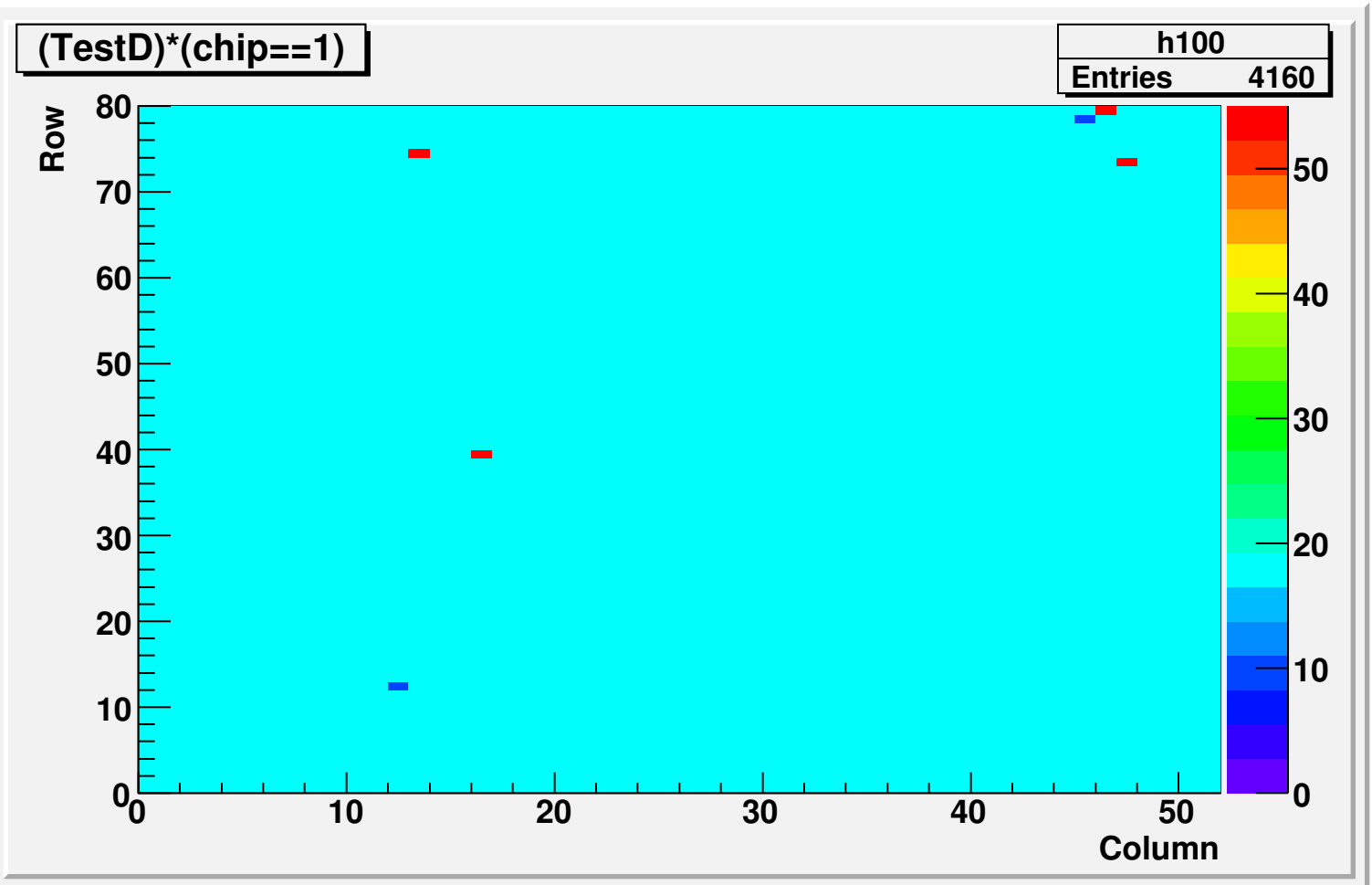
(TestD)*(chip==1)
h100
Entries 4160
Row
Column
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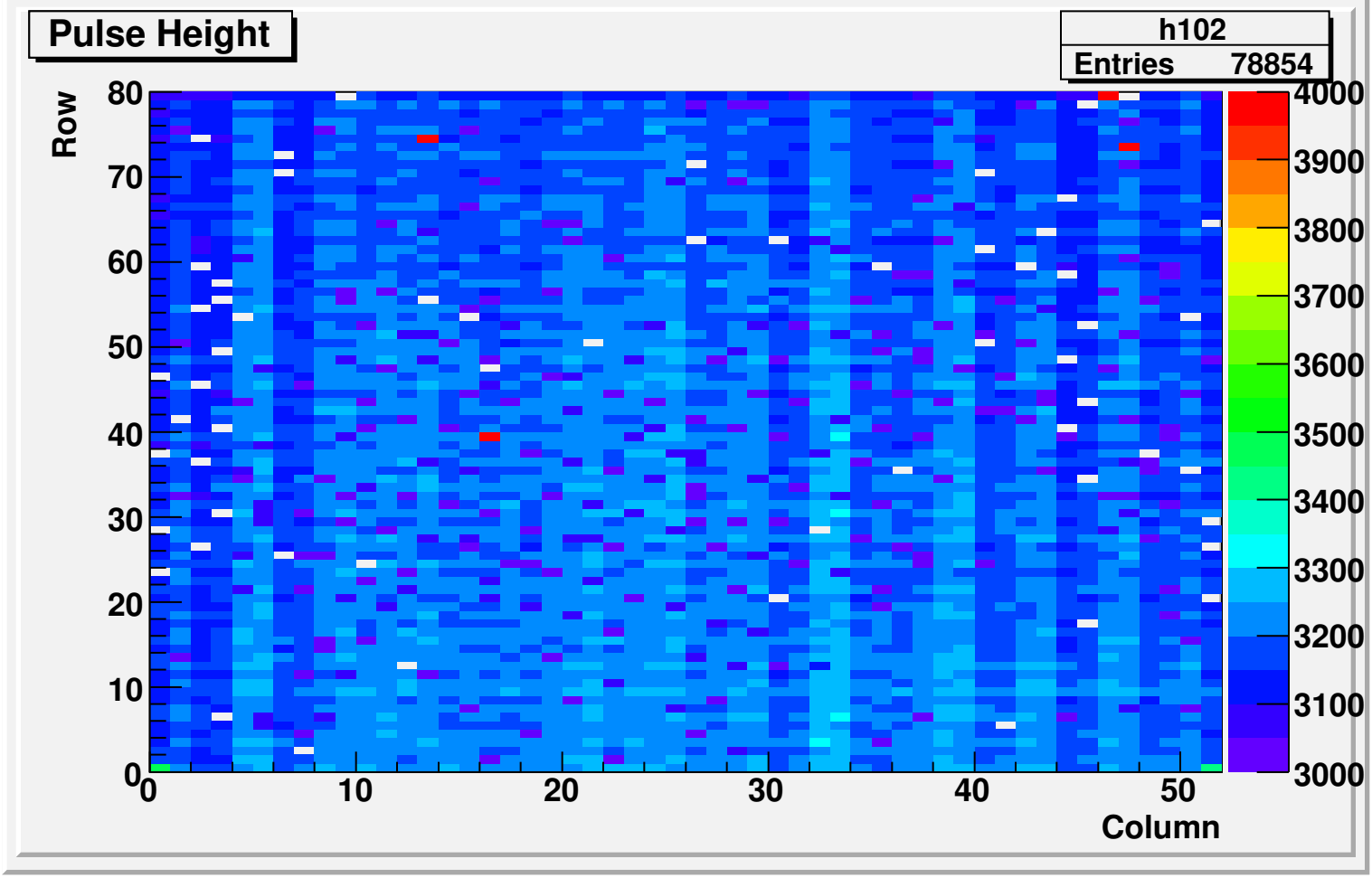
70
80


Pulse Height
h102
Entries 78854
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Column
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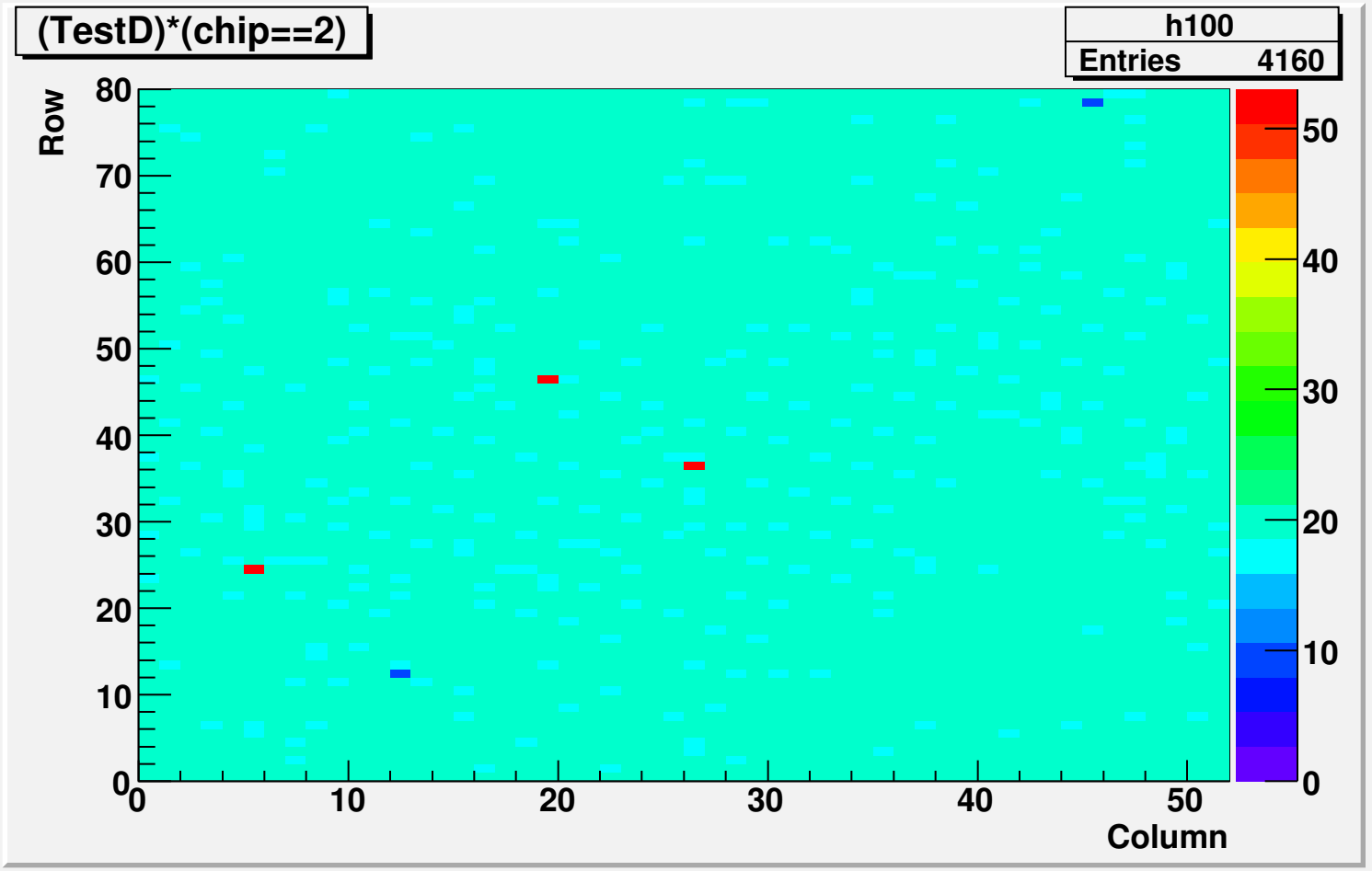
(TestD)*(chip==2)
h100
Entries 4160
Row
Column
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80
0
10
20
30
40
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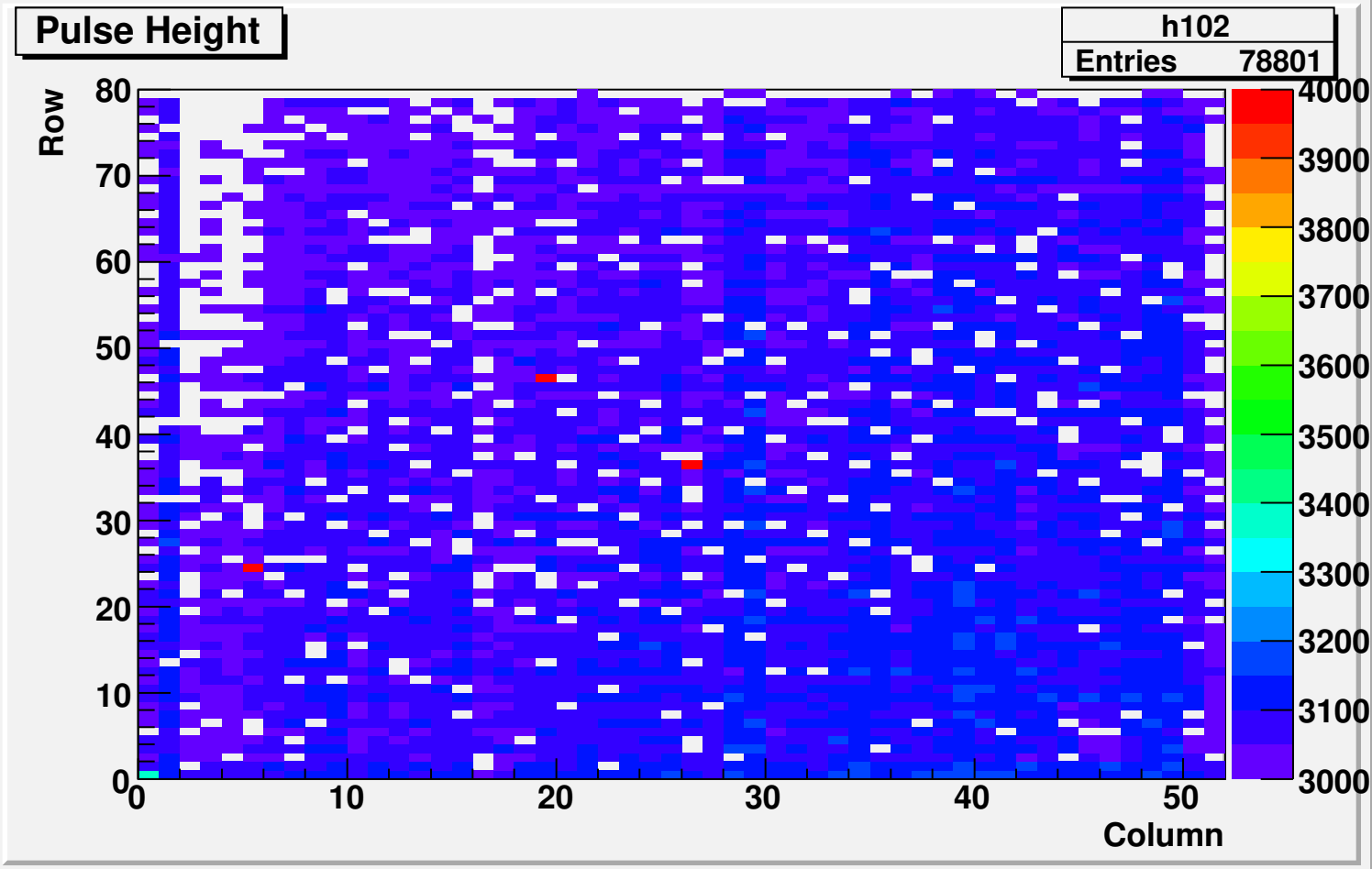
Pulse Height
h102
Entries 78801
Row
Column
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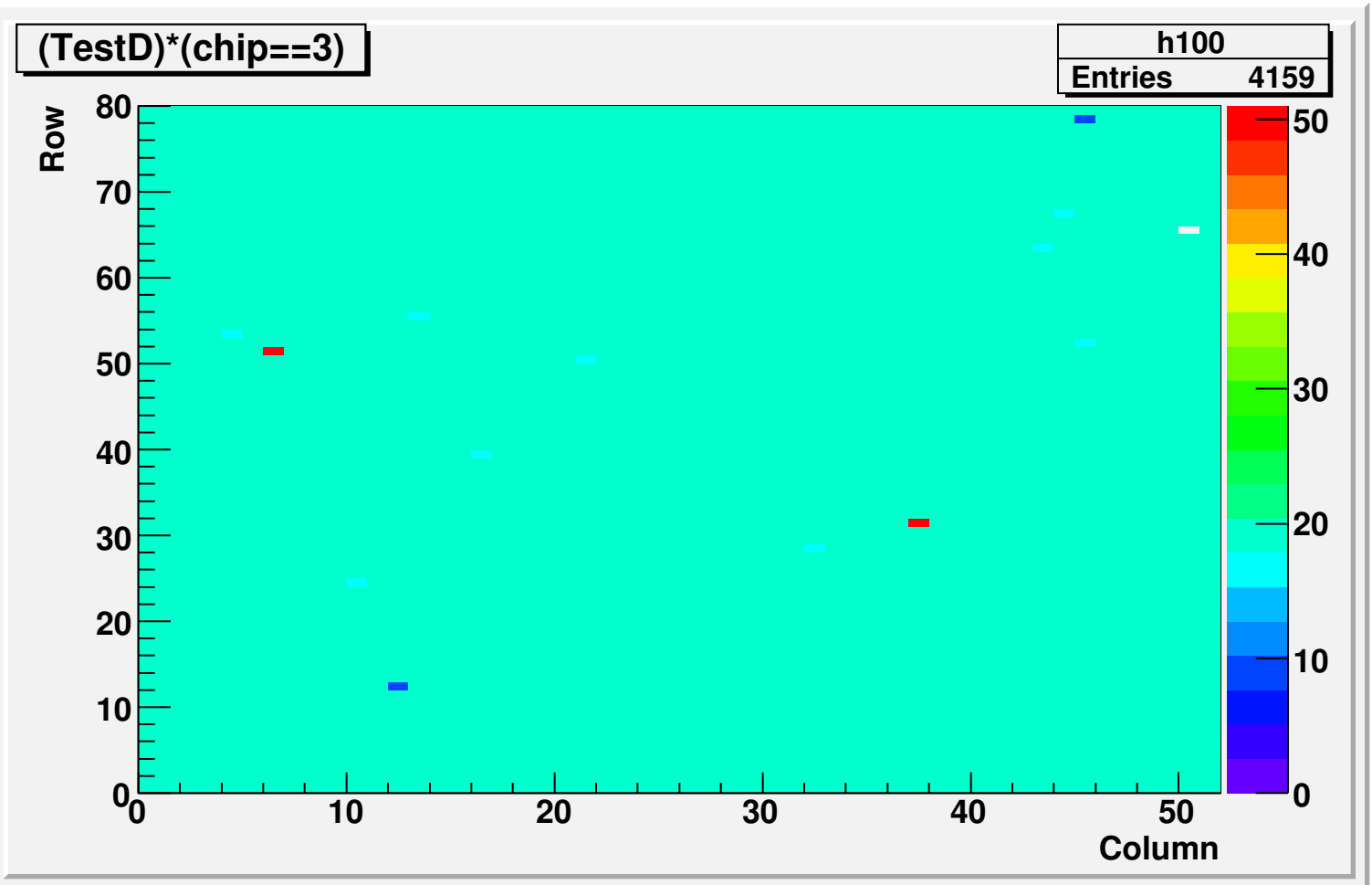
(TestD)*(chip==3)
h100
Entries 4159
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Column
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40
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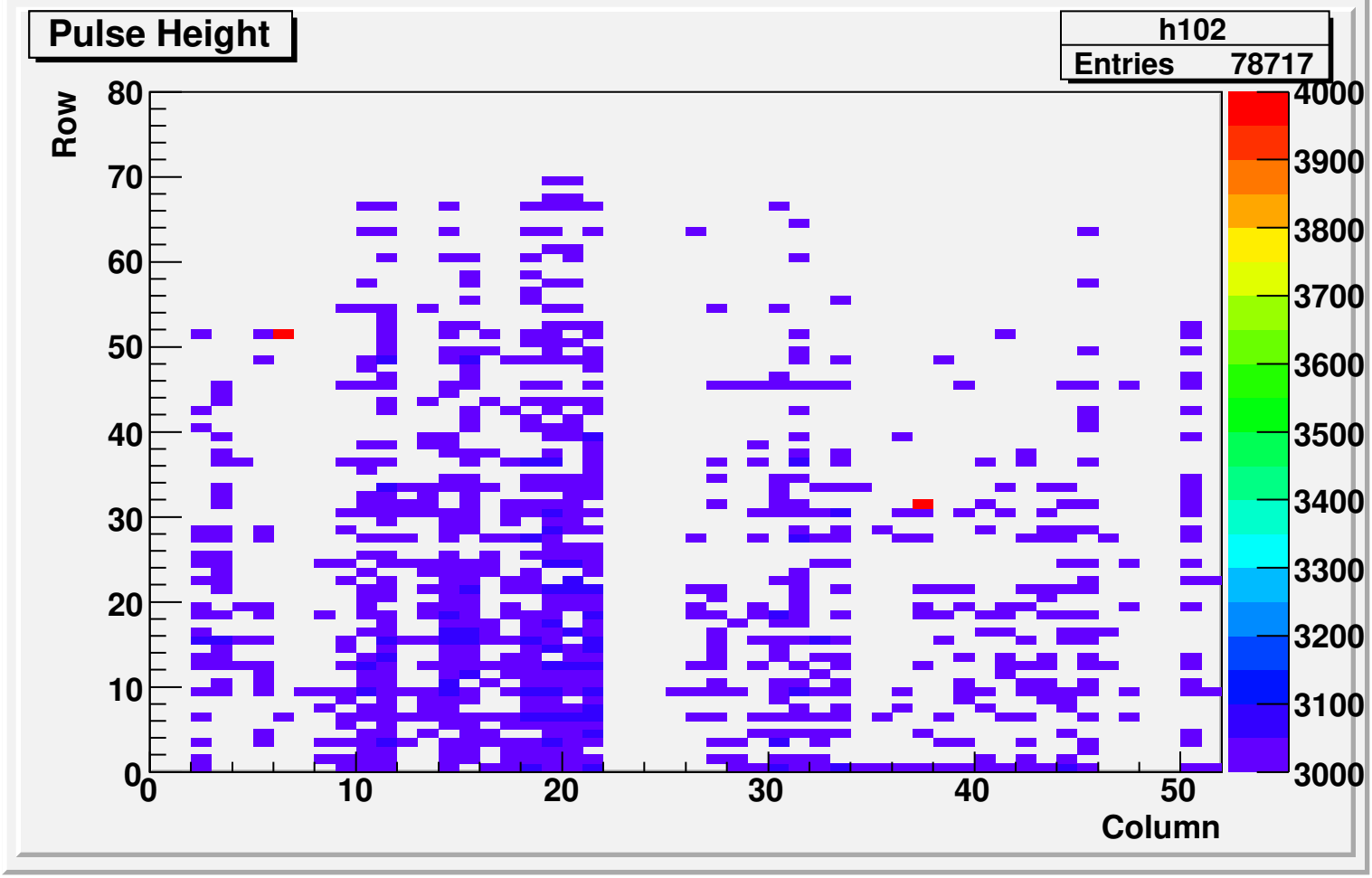
Pulse Height
h102
Entries 78717
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Column
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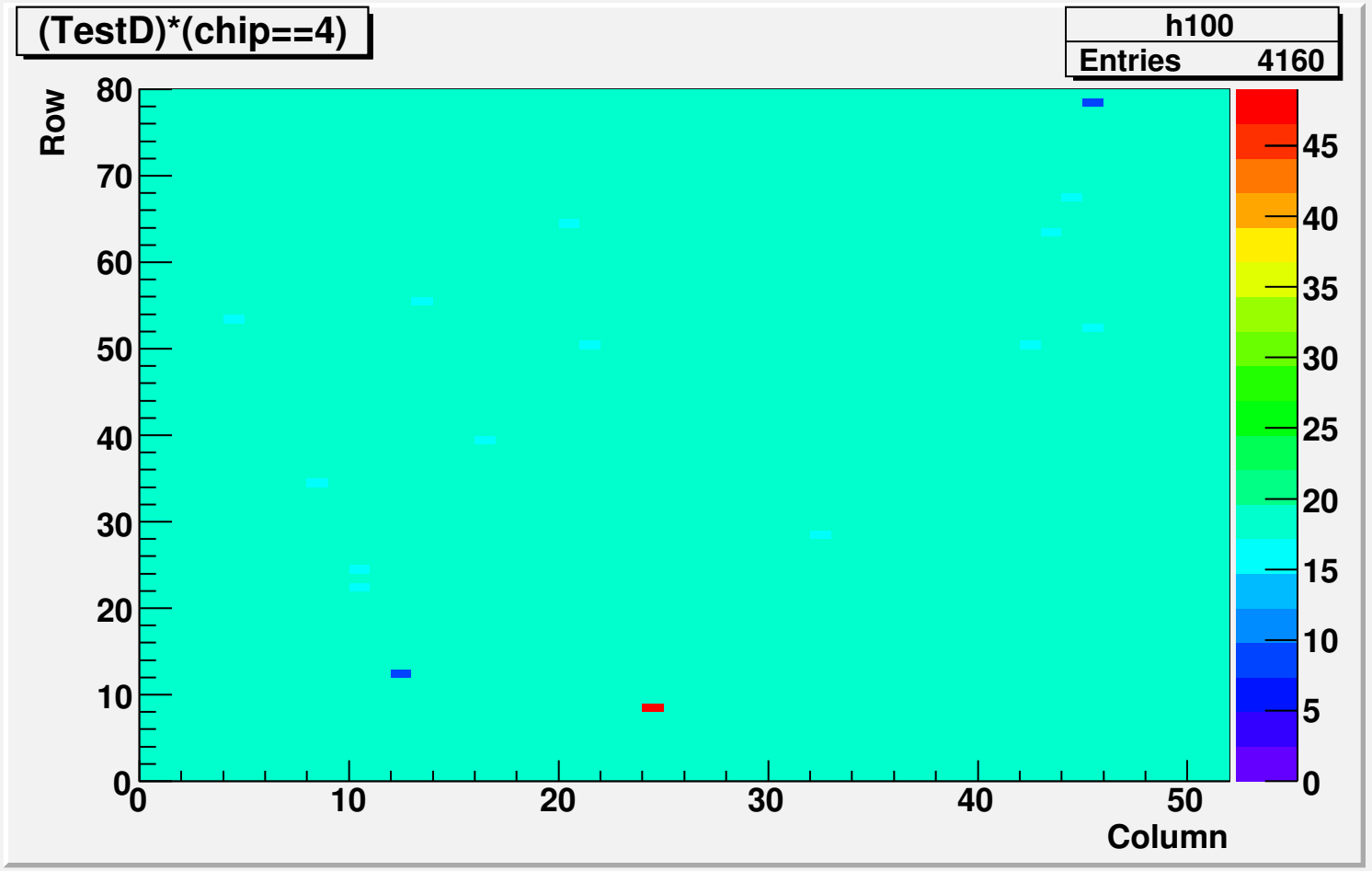
(TestD)*(chip==4)
h100
Entries 4160
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Column
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40
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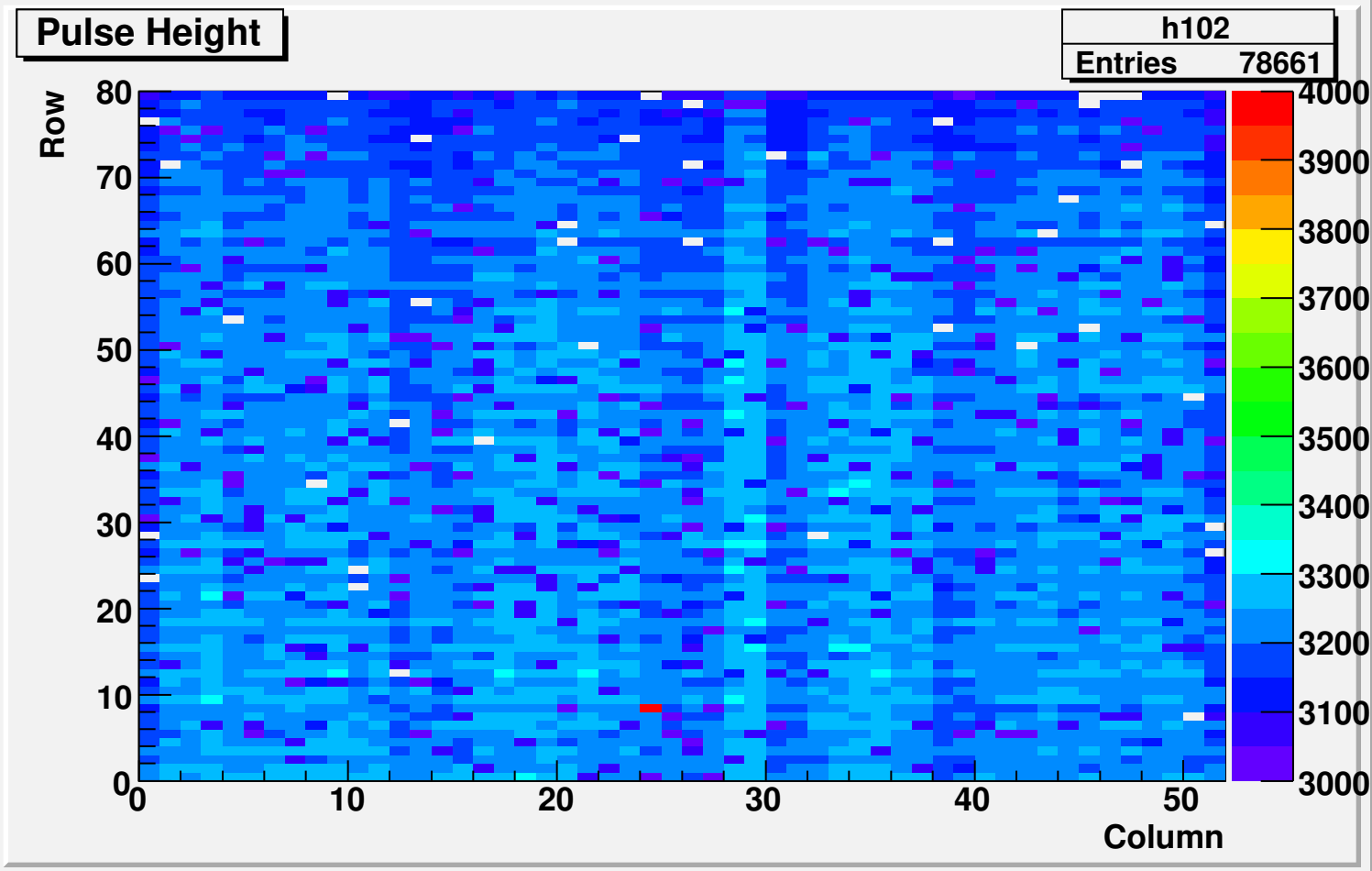
Pulse Height
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Entries 78661
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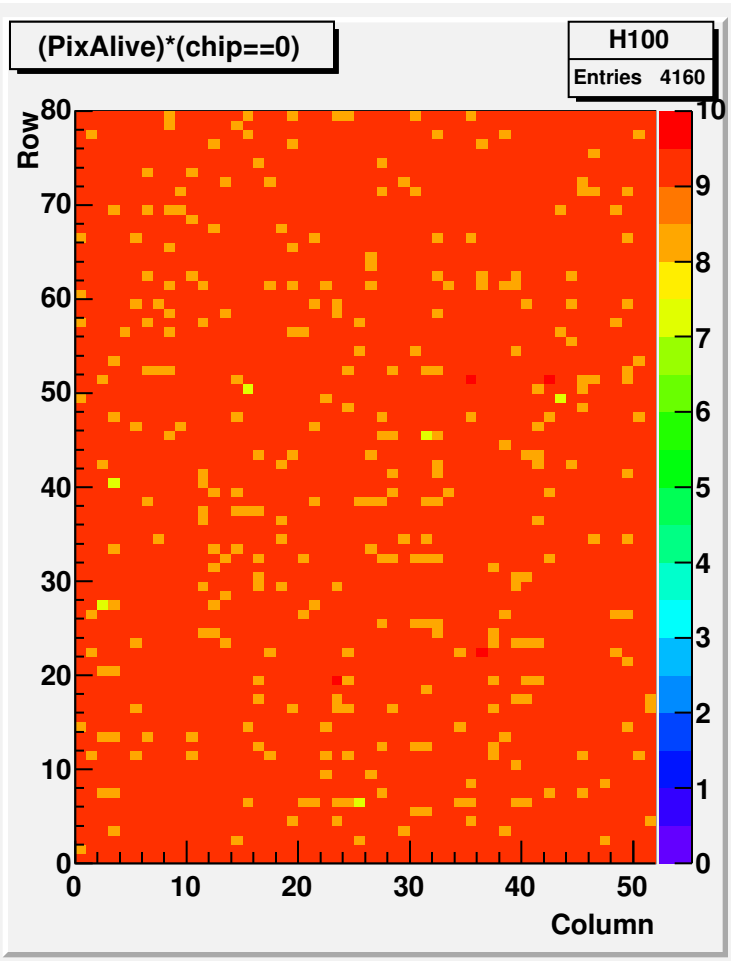
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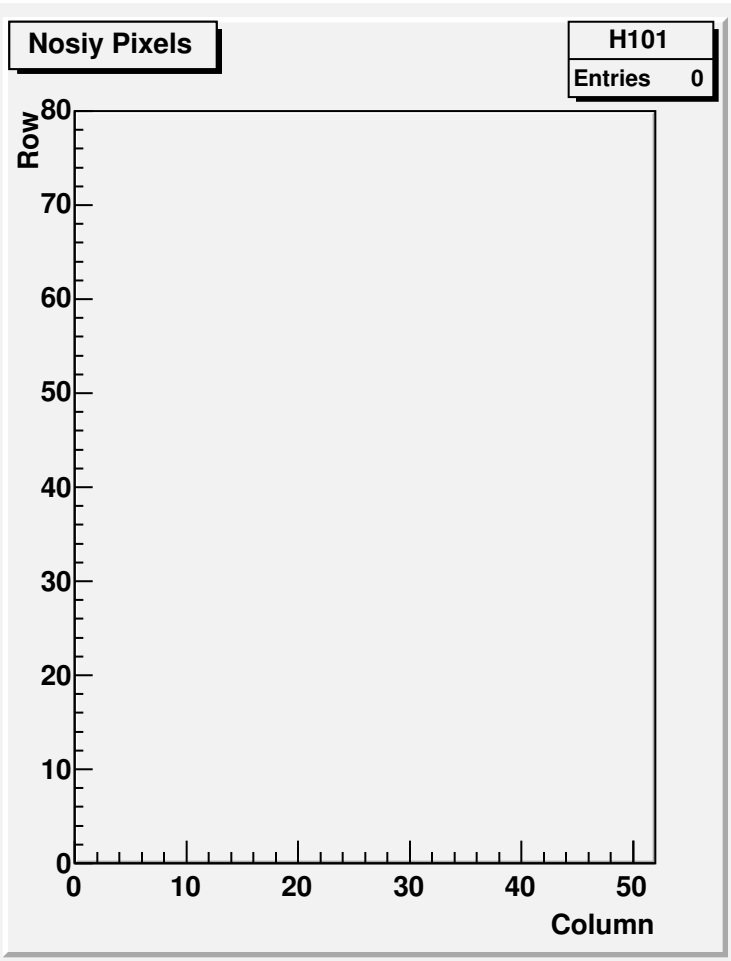
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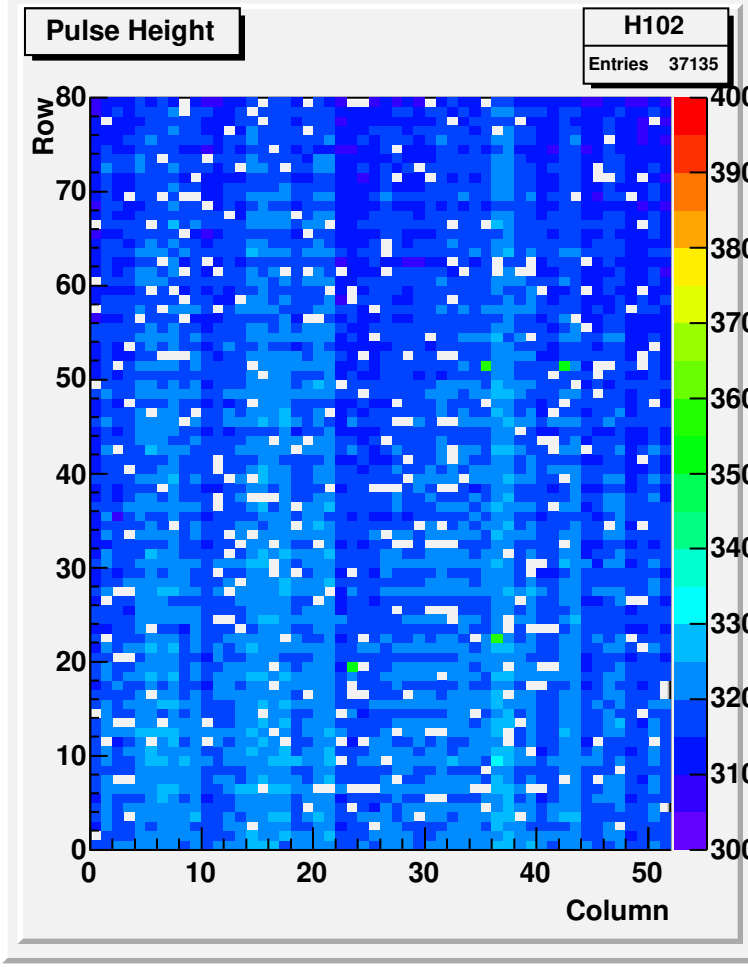
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H102
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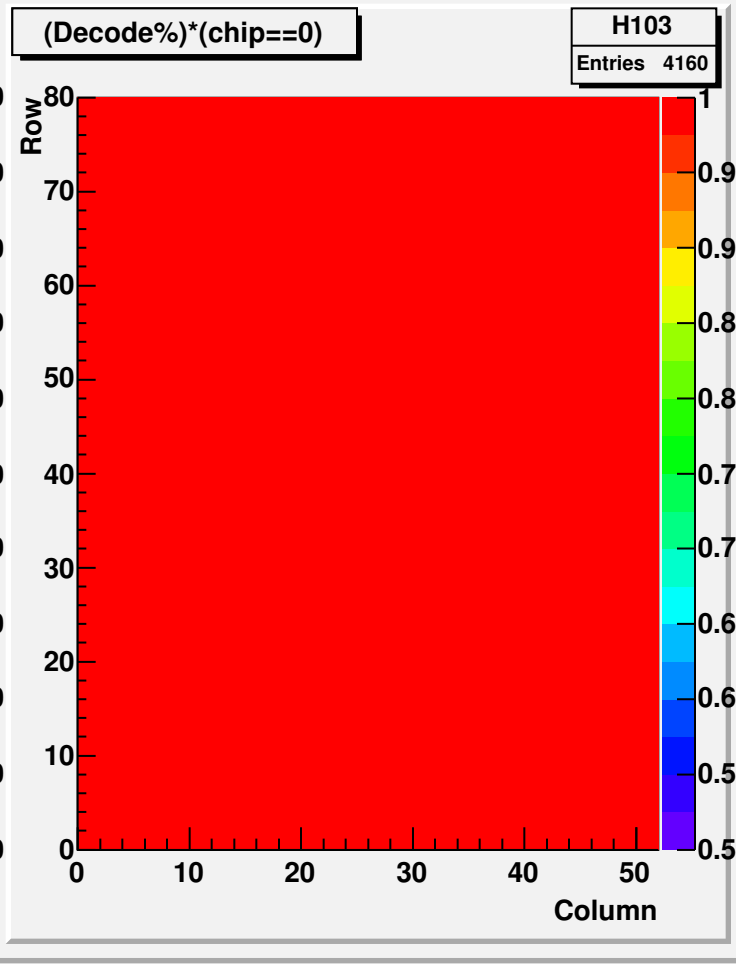
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H103
Entries 4160
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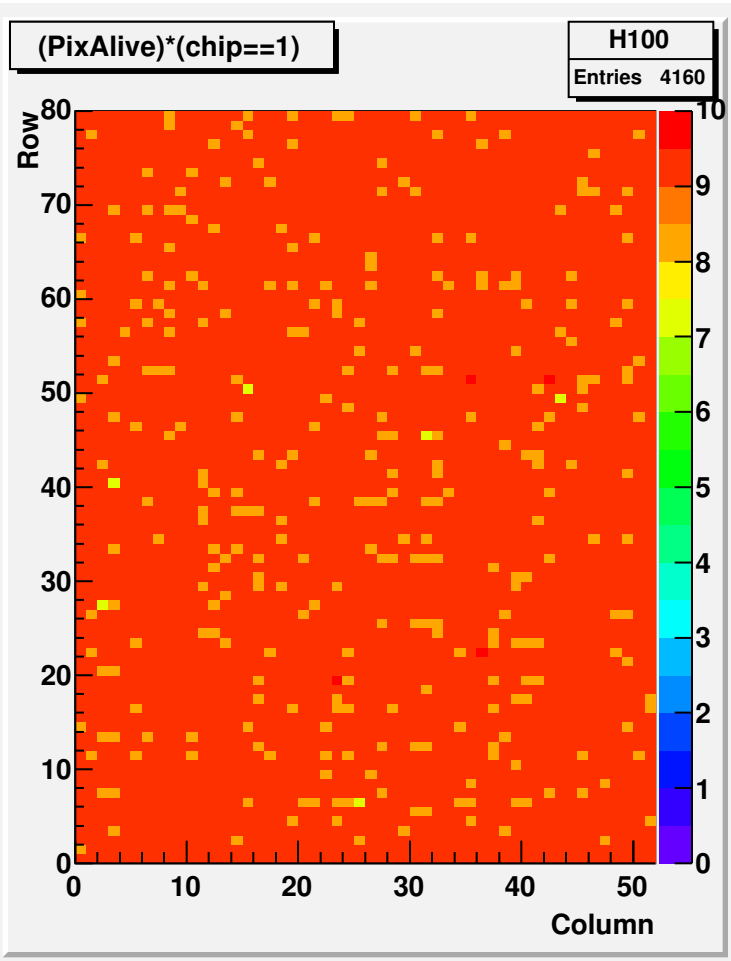
Column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H100
Entries 4160
Row
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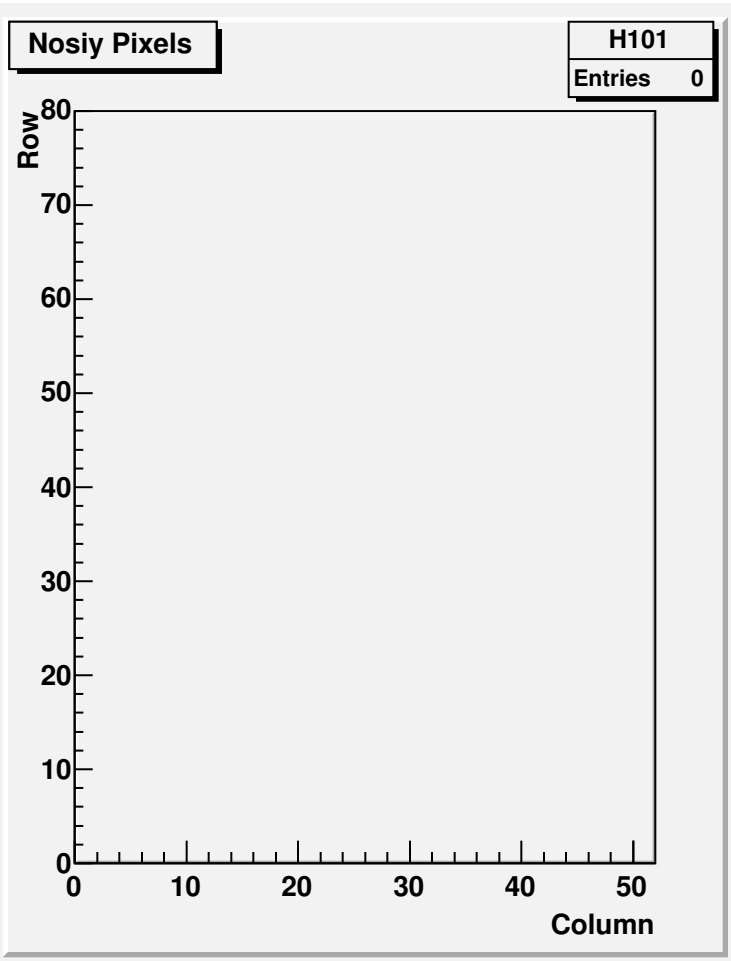
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H101
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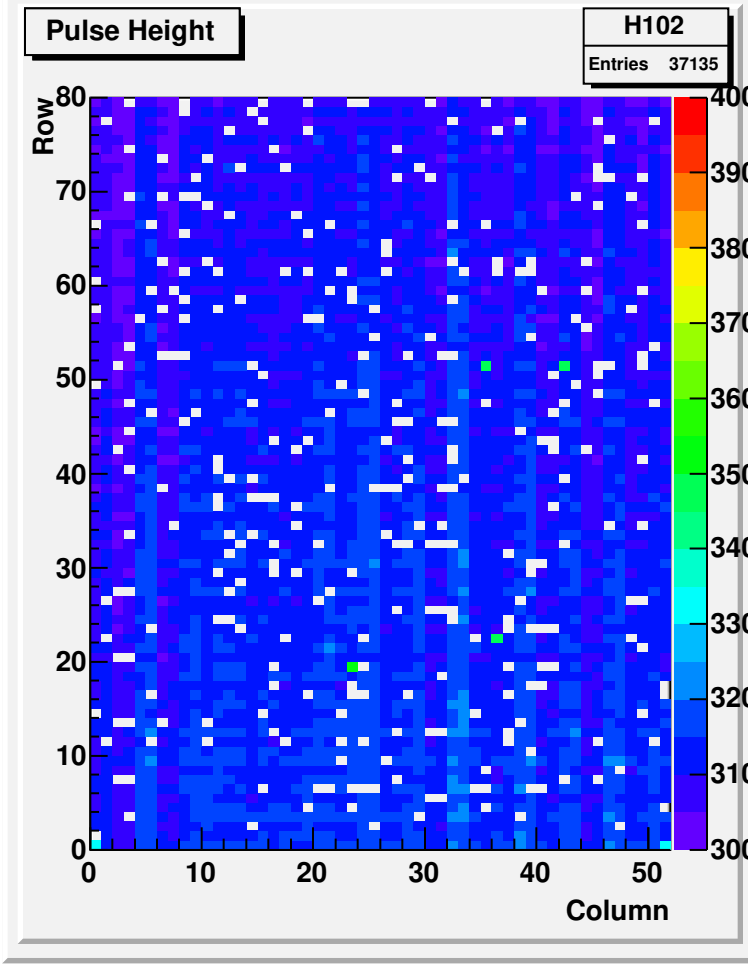
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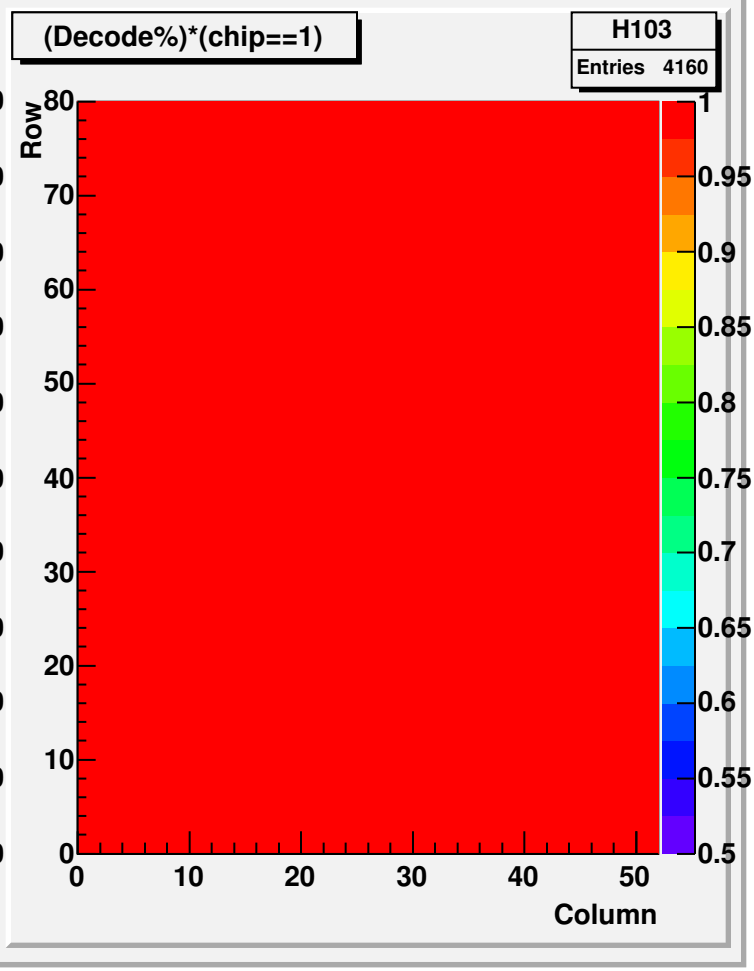
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Entries 4160
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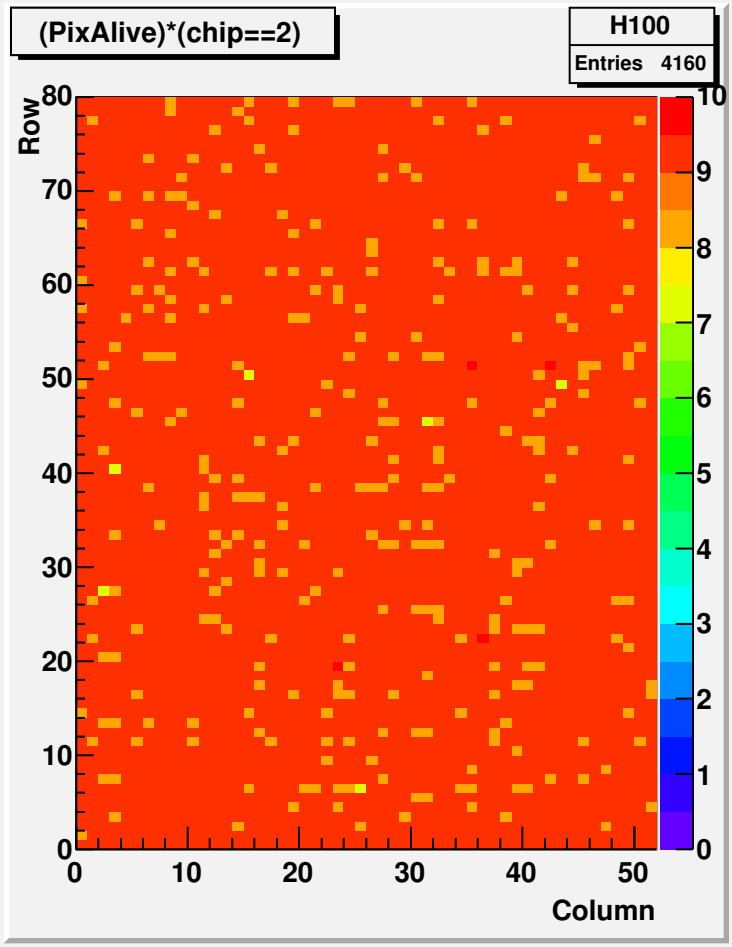
Column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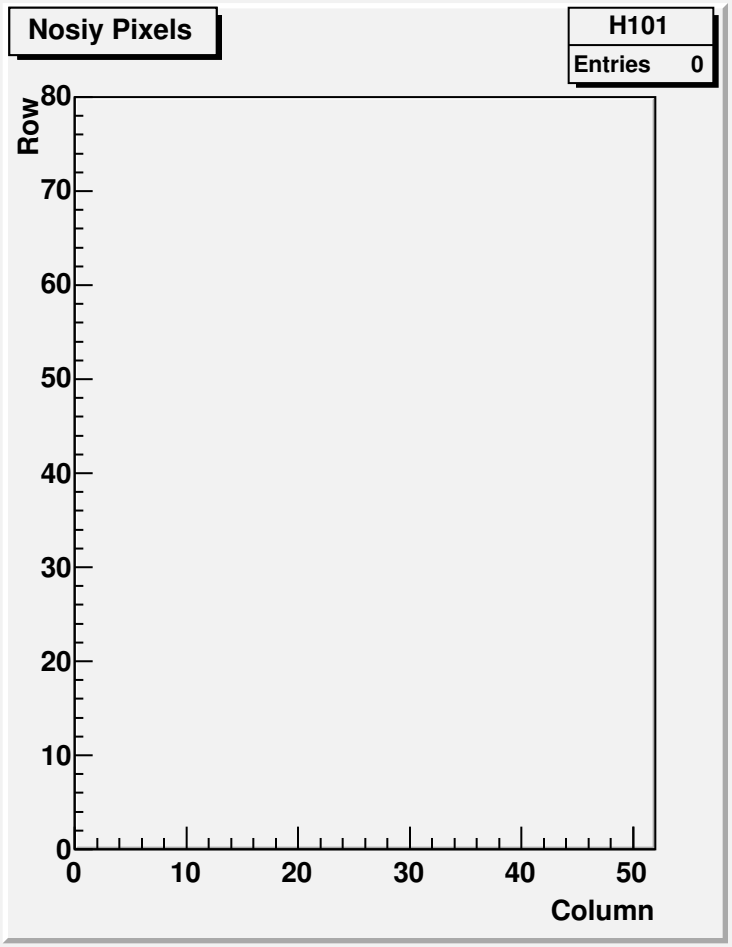
H100
Entries 4160
Row
Column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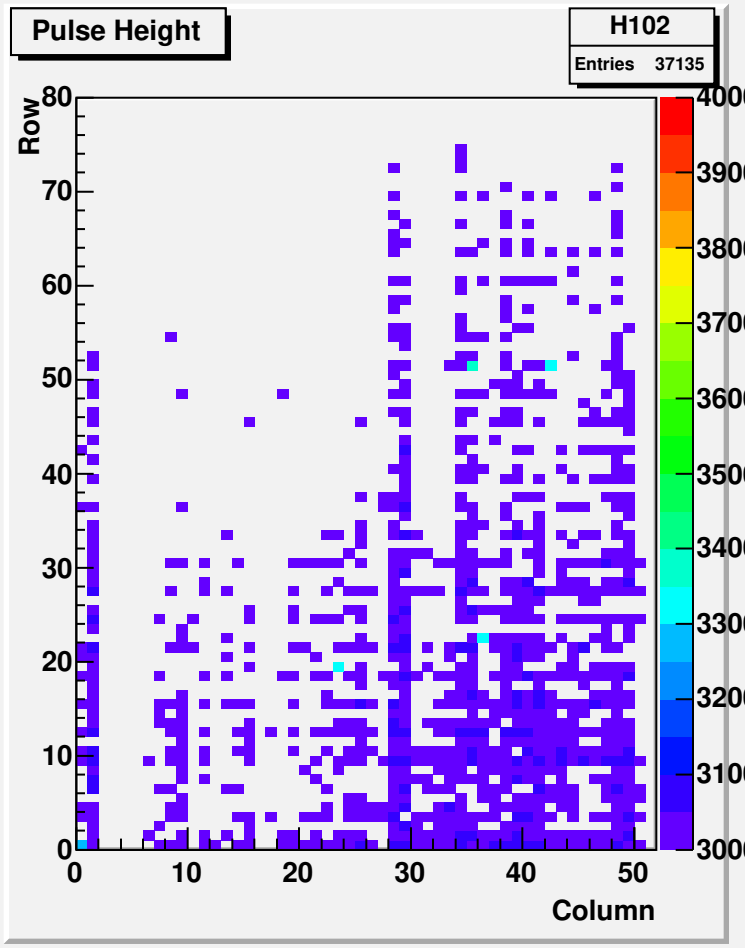
H101
Entries 0
Row
Column


Pulse Height
H102
Entries 37135
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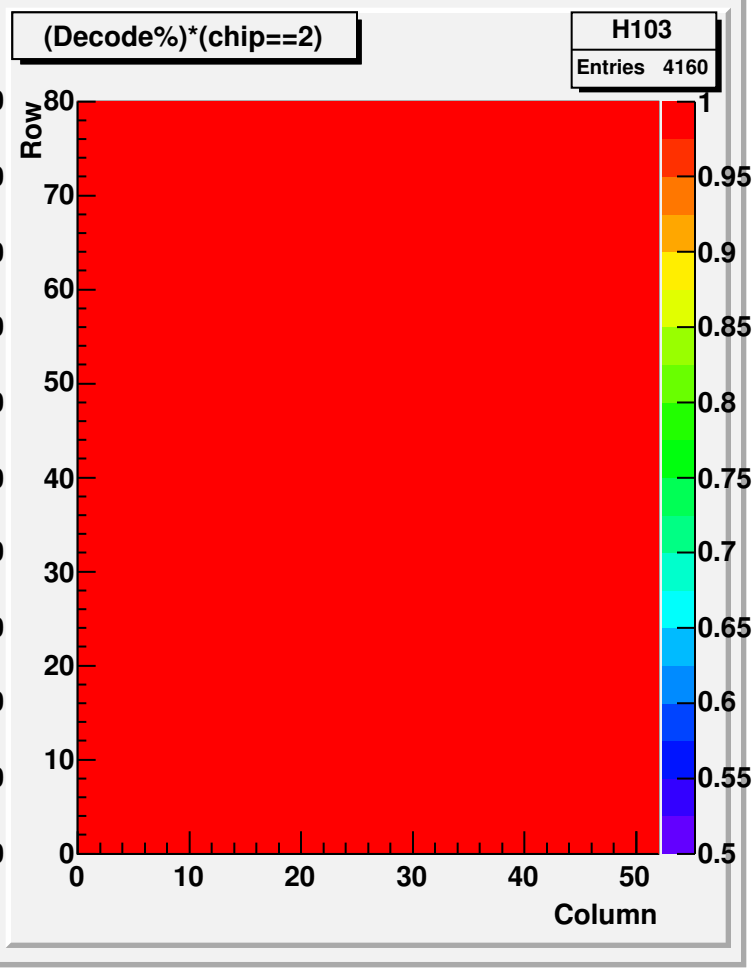
Column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H103
Entries 4160
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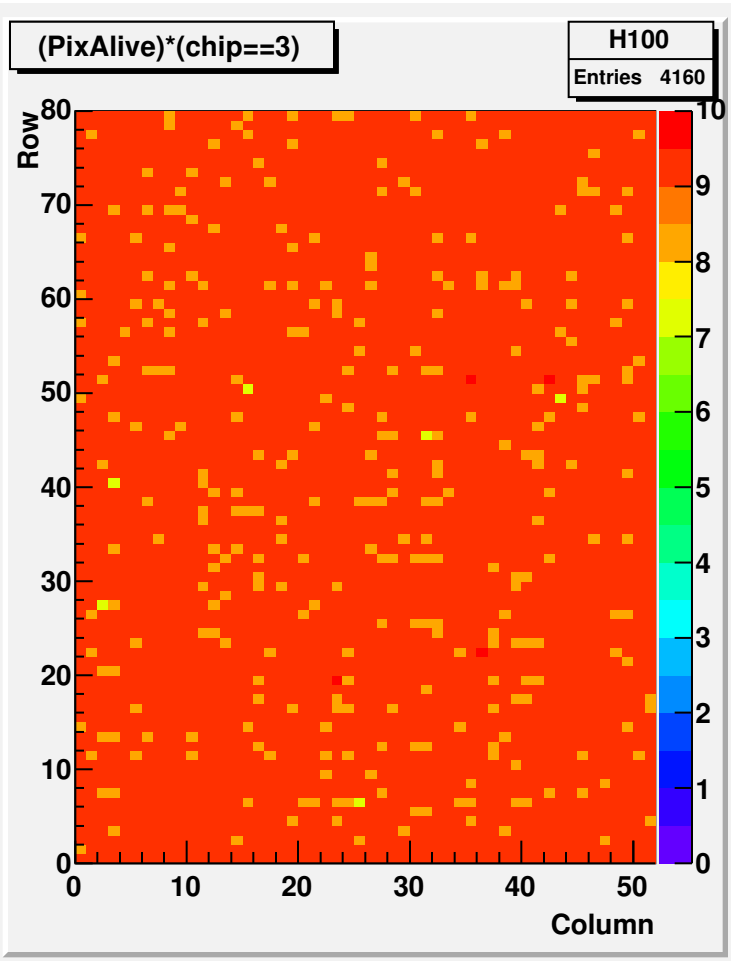
Column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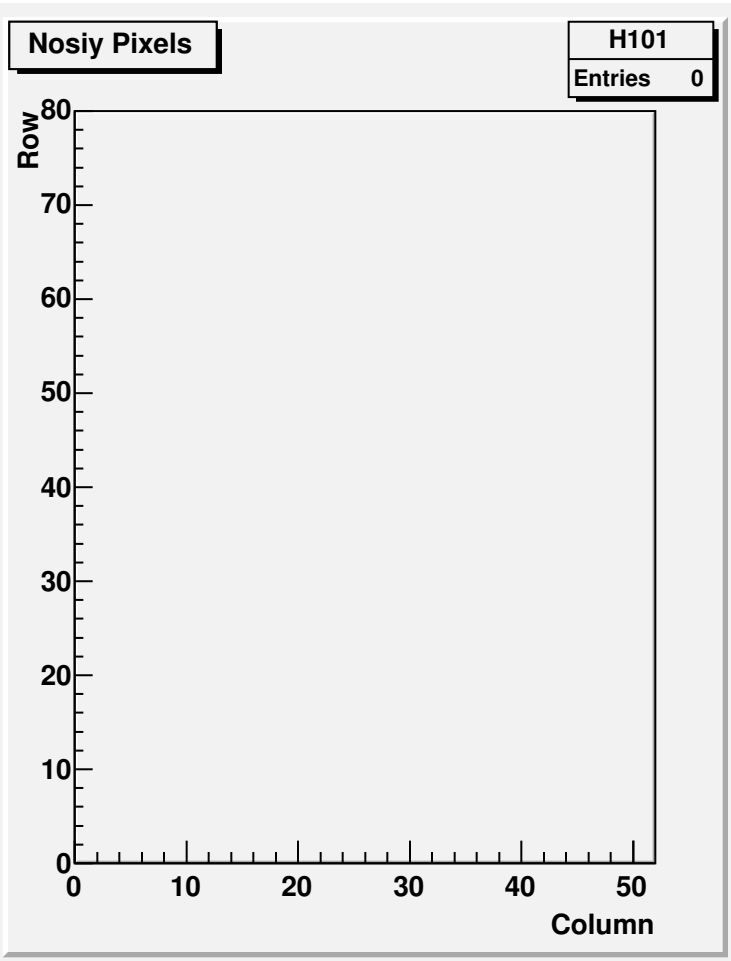
Entries 4160
Row
Column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H101
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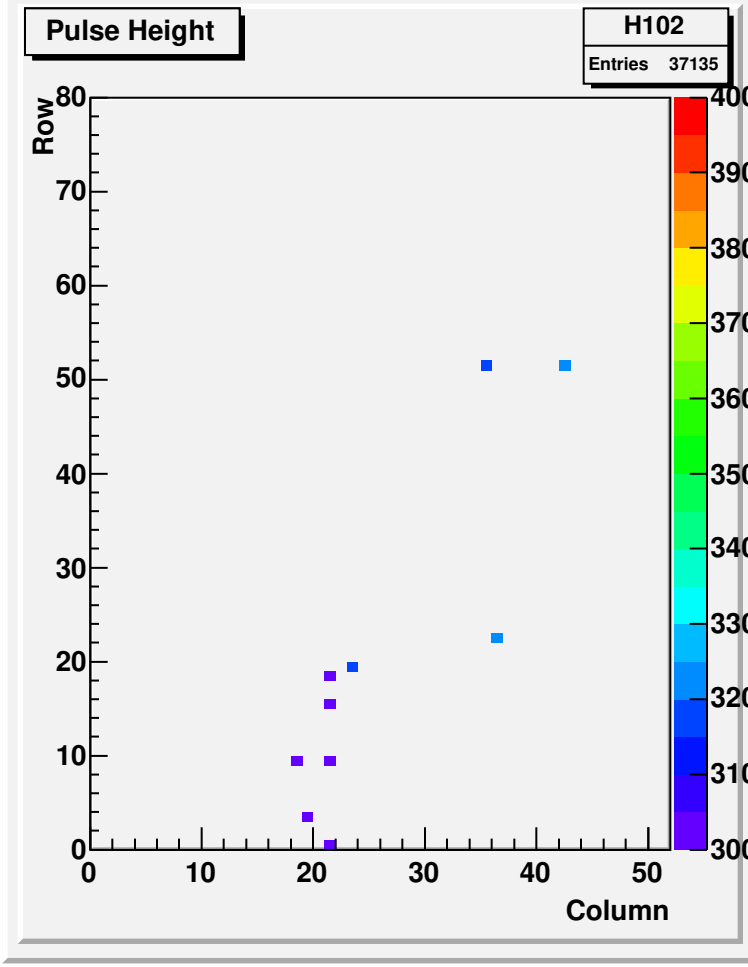
Column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H102
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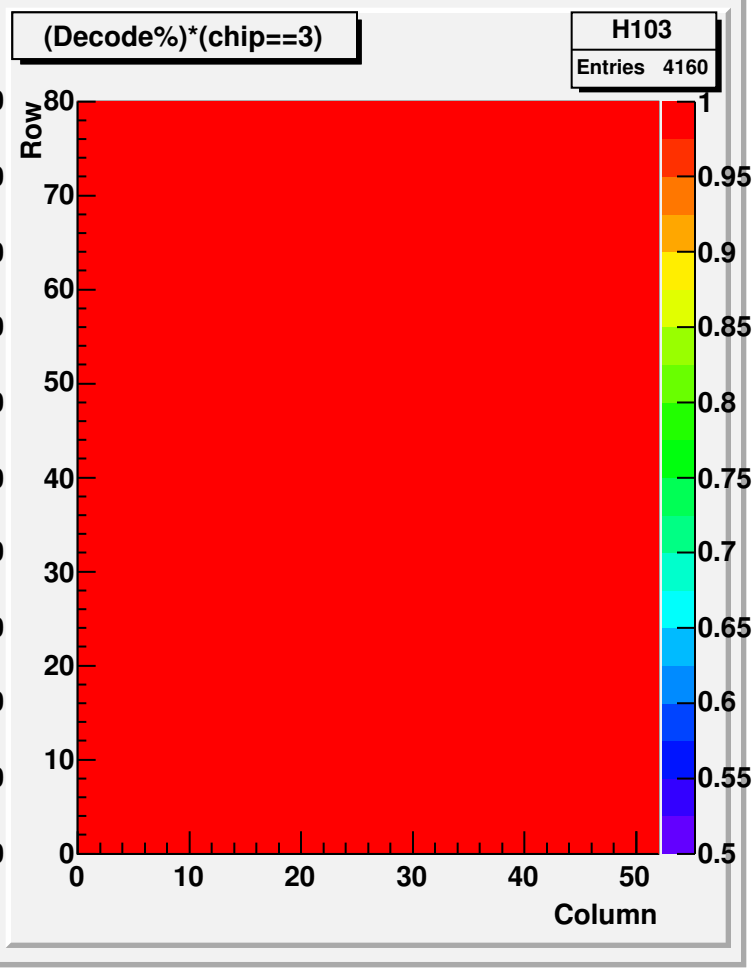
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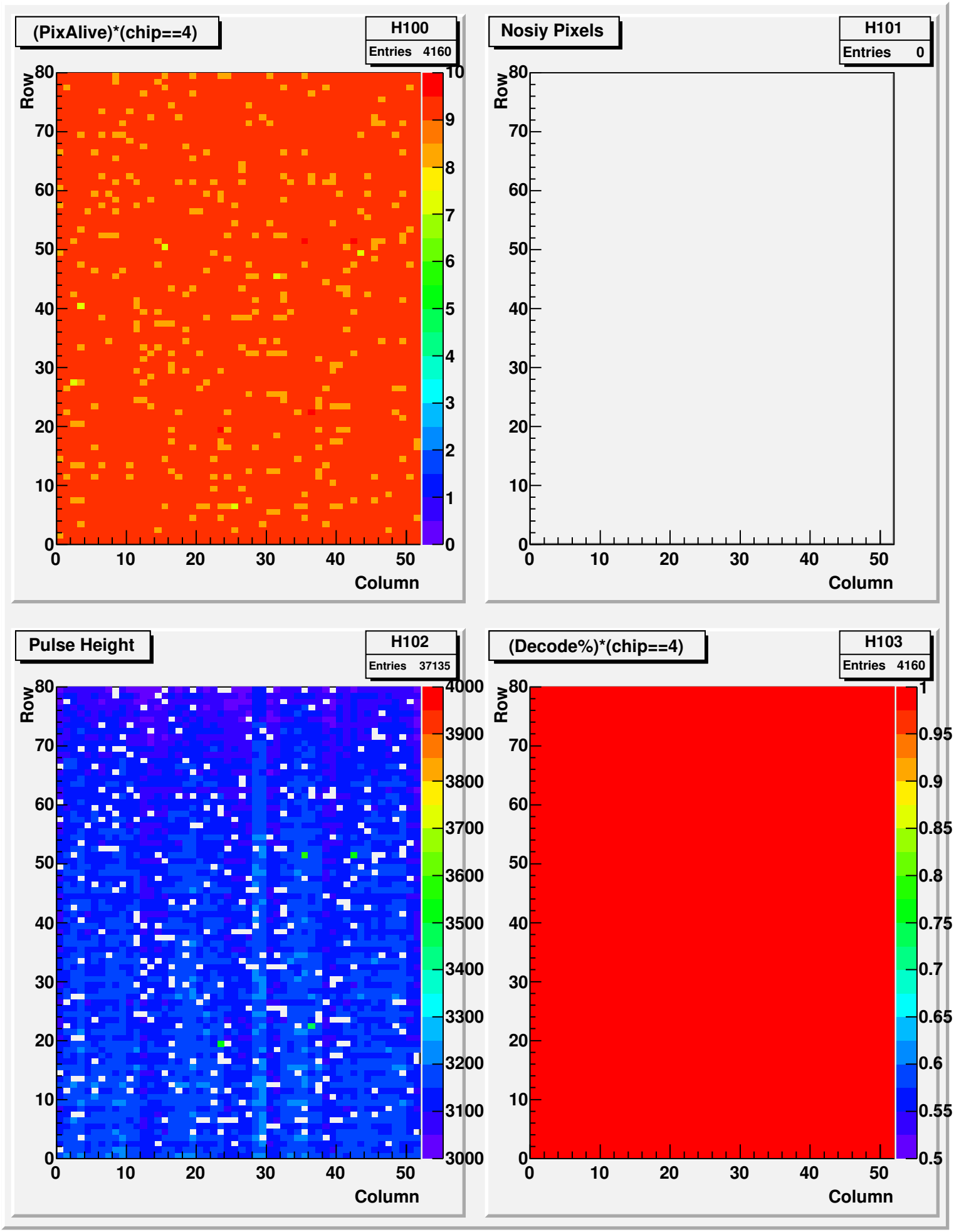
(PixAlive)*(chip==4)
H100
Entries 4160
Row
Column
Nosiy Pixels
H101
Entries 0
Row
Column
Pulse Height
H102
Entries 37135
Row
Column
(Decode%)*(chip==4)
H103
Entries 4160
Row
Column