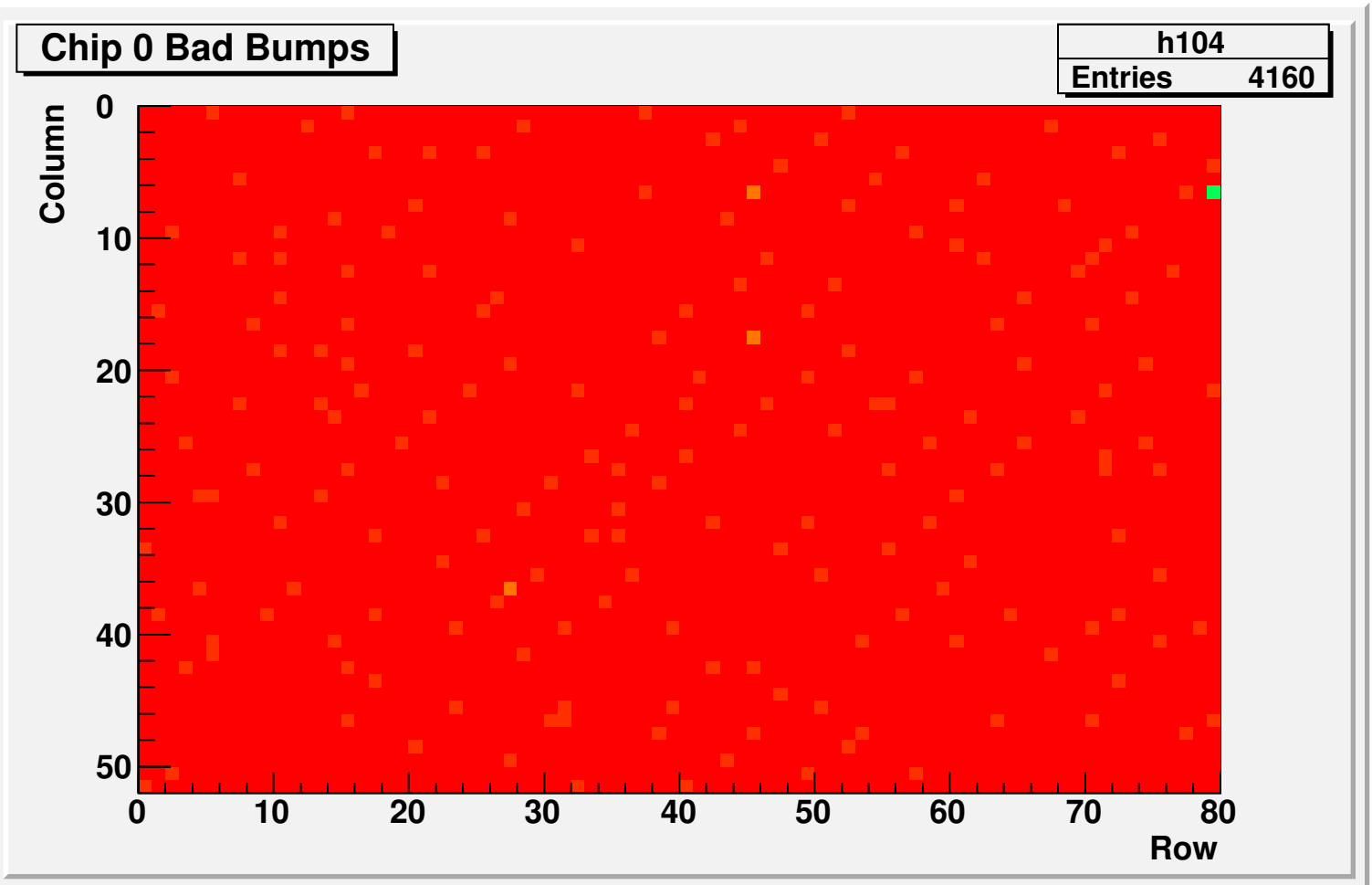
Chip 0 Bad Bumps
h104
Entries 4160
Column
0
10
20
30
40
50
0
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20
30
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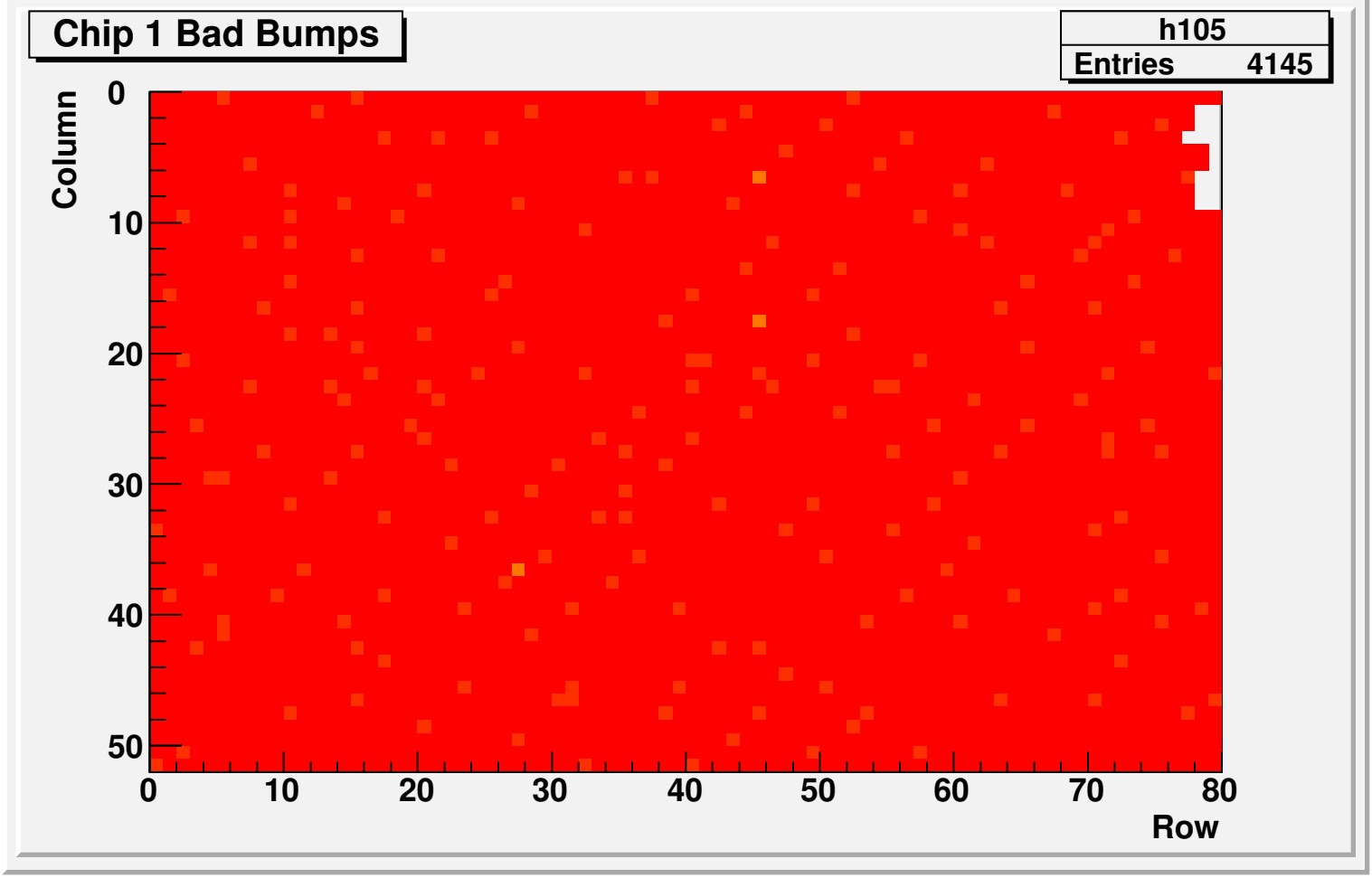
50
60
70
80
Row


Chip 1 Bad Bumps
h105
Entries 4145
Column
0
10
20
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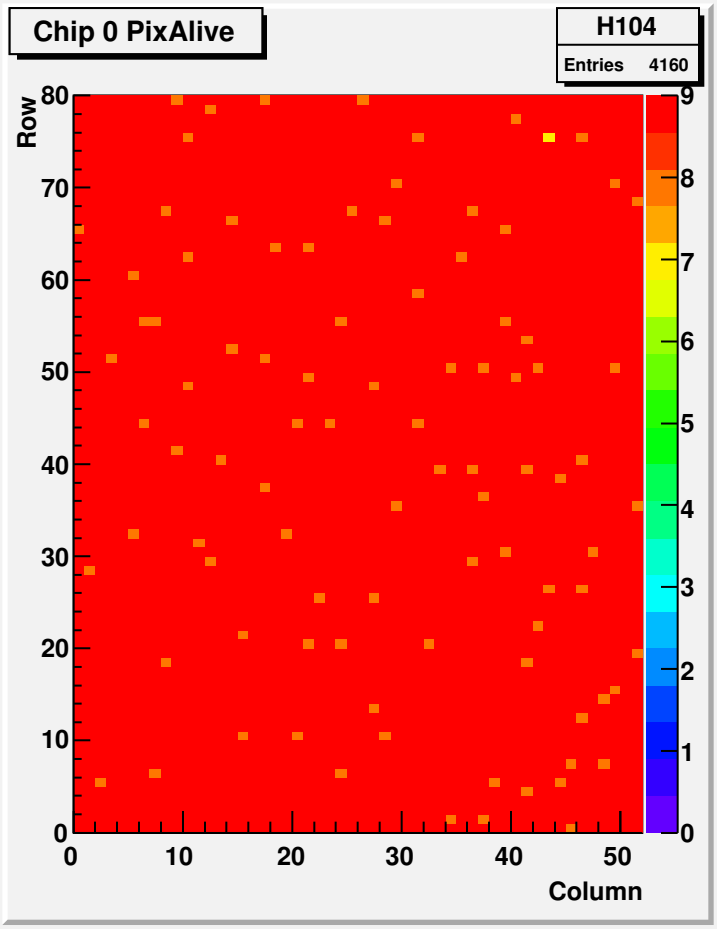
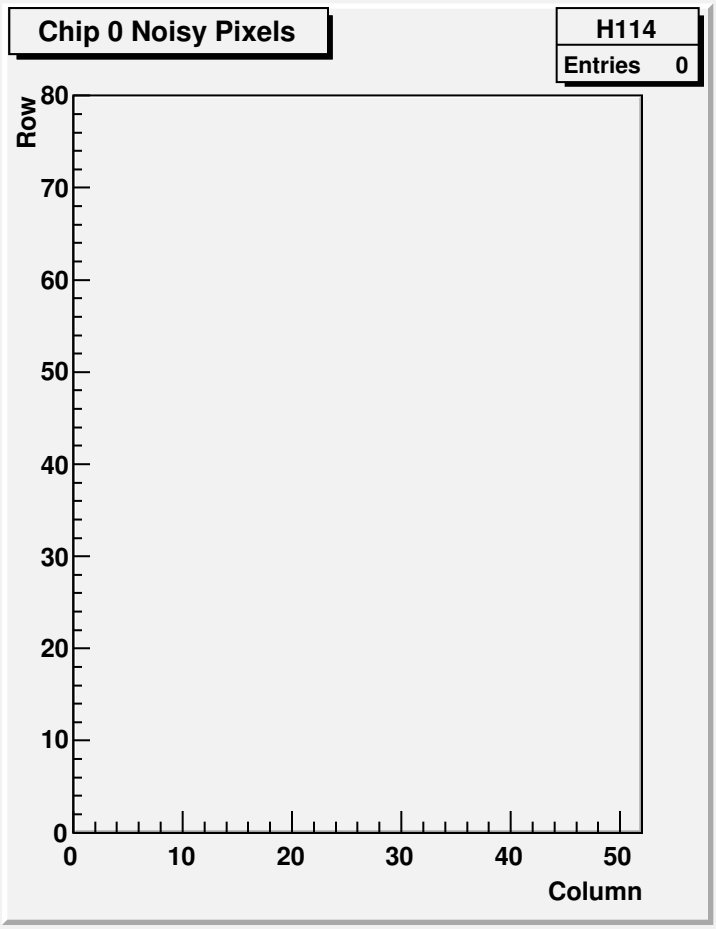
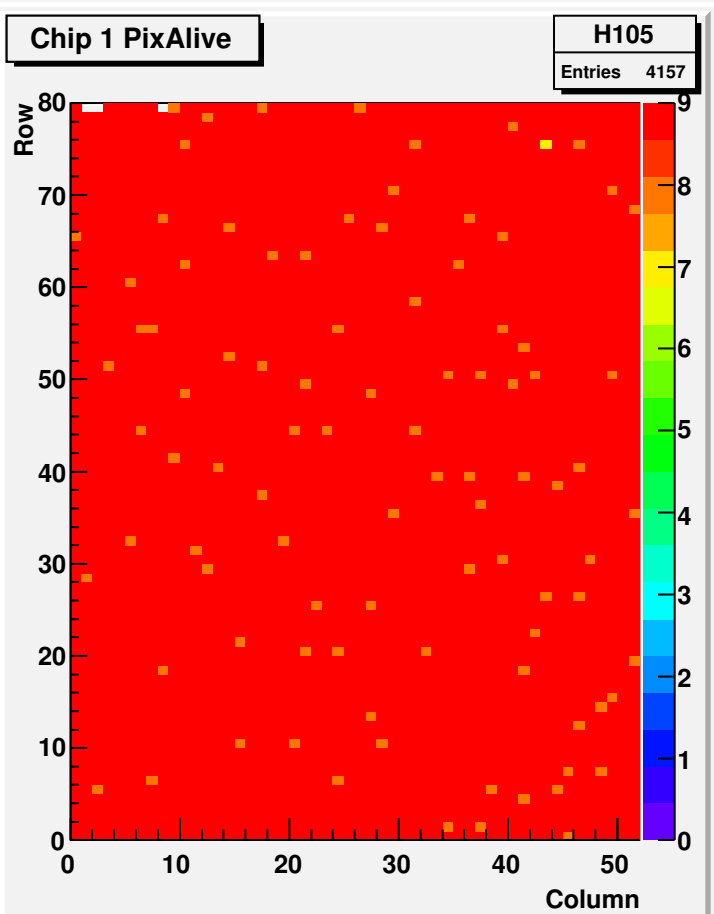
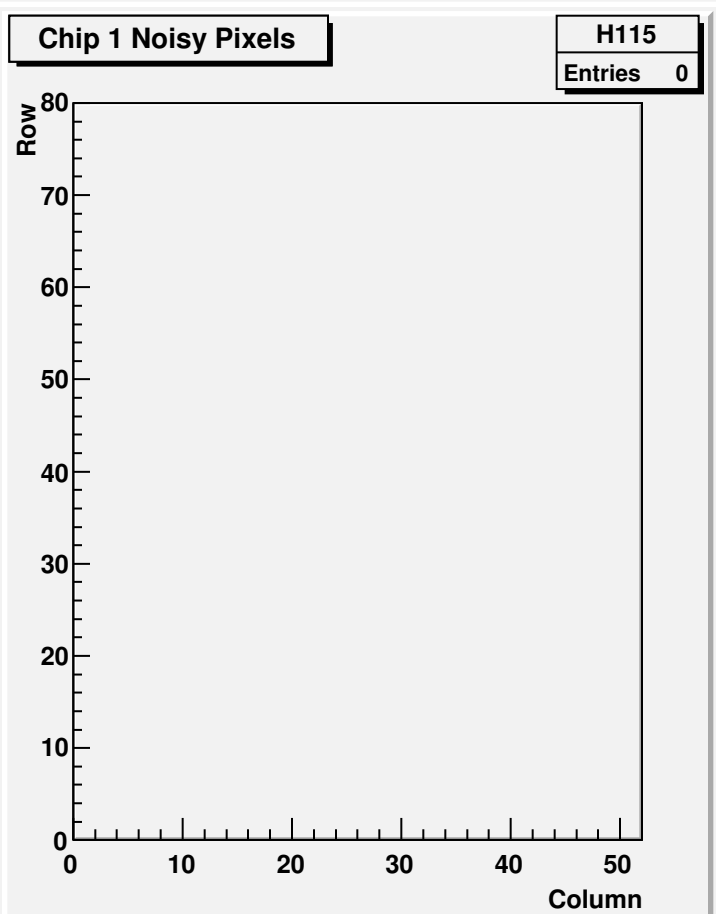
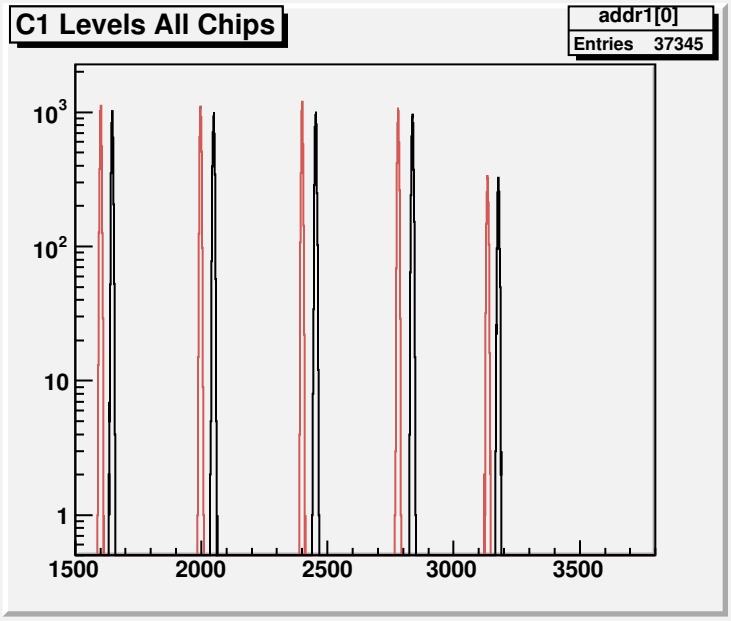
70
80
Row

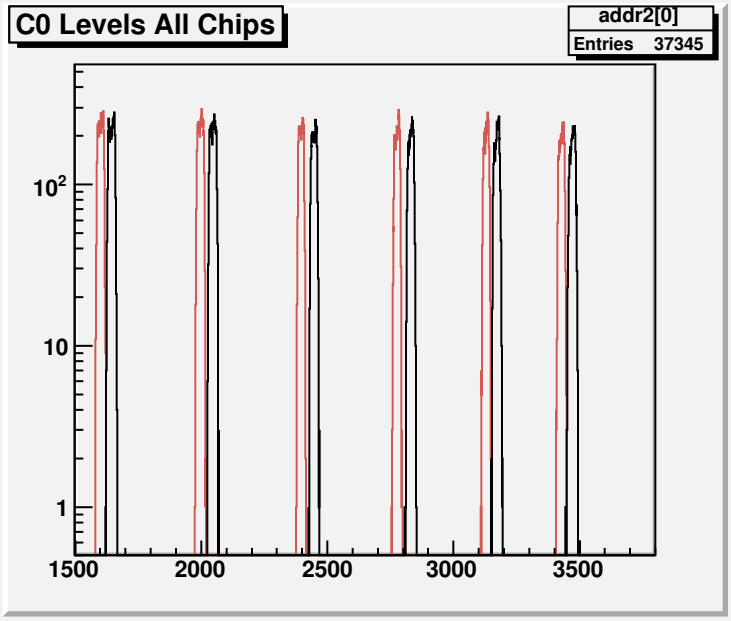

Chip 0 PixAlive
H104
Entries 4160
Row
Column

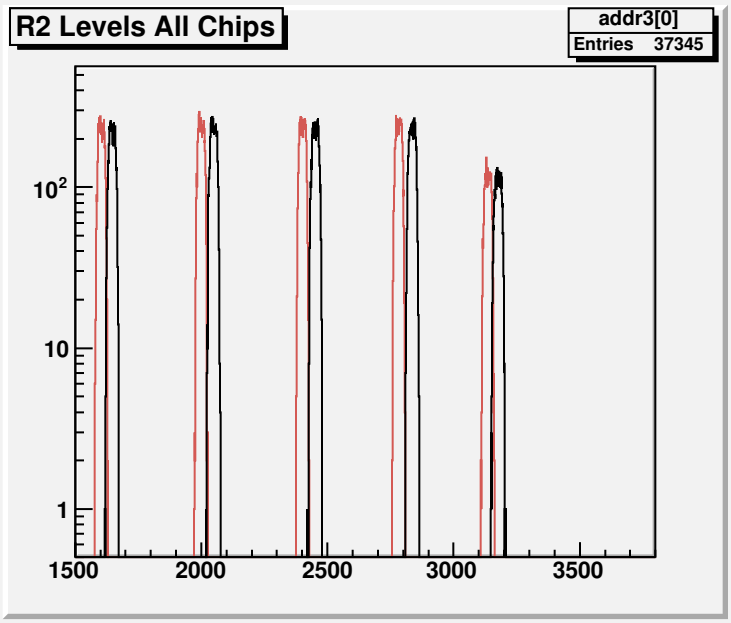

Chip 0 Noisy Pixels
H114
Entries 0
Row
Column

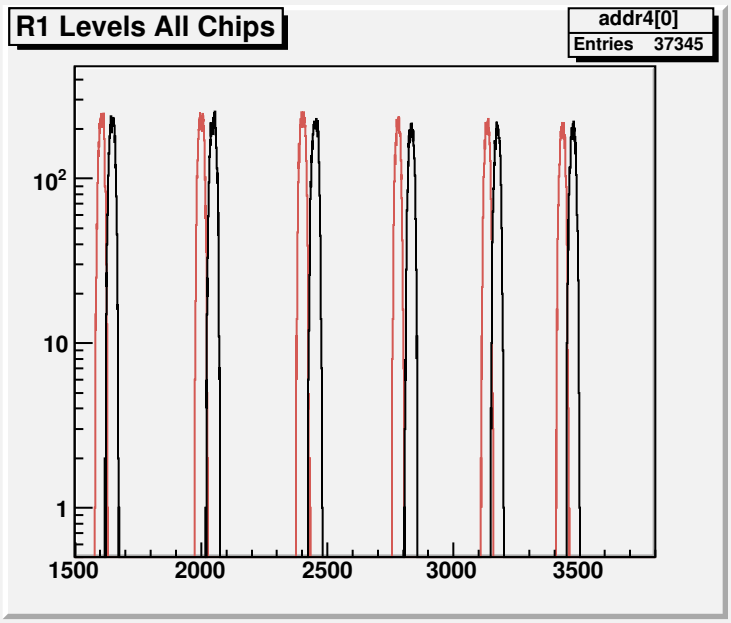

Chip 1 PixAlive
H105
Entries 4157
Row
Column

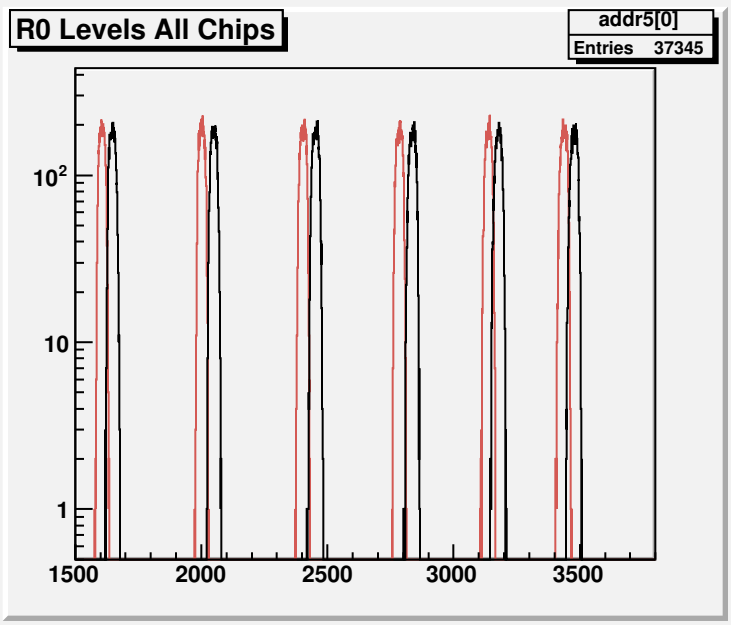

Chip 1 Noisy Pixels
H115
Entries 0
Row
Column

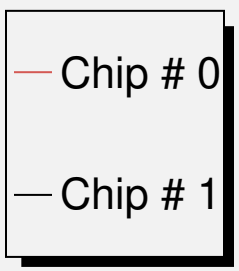

C1 Levels All Chips
addr1[0]
Entries 37345
10^3
10^2
10
1
1500
2000
2500
3000
3500

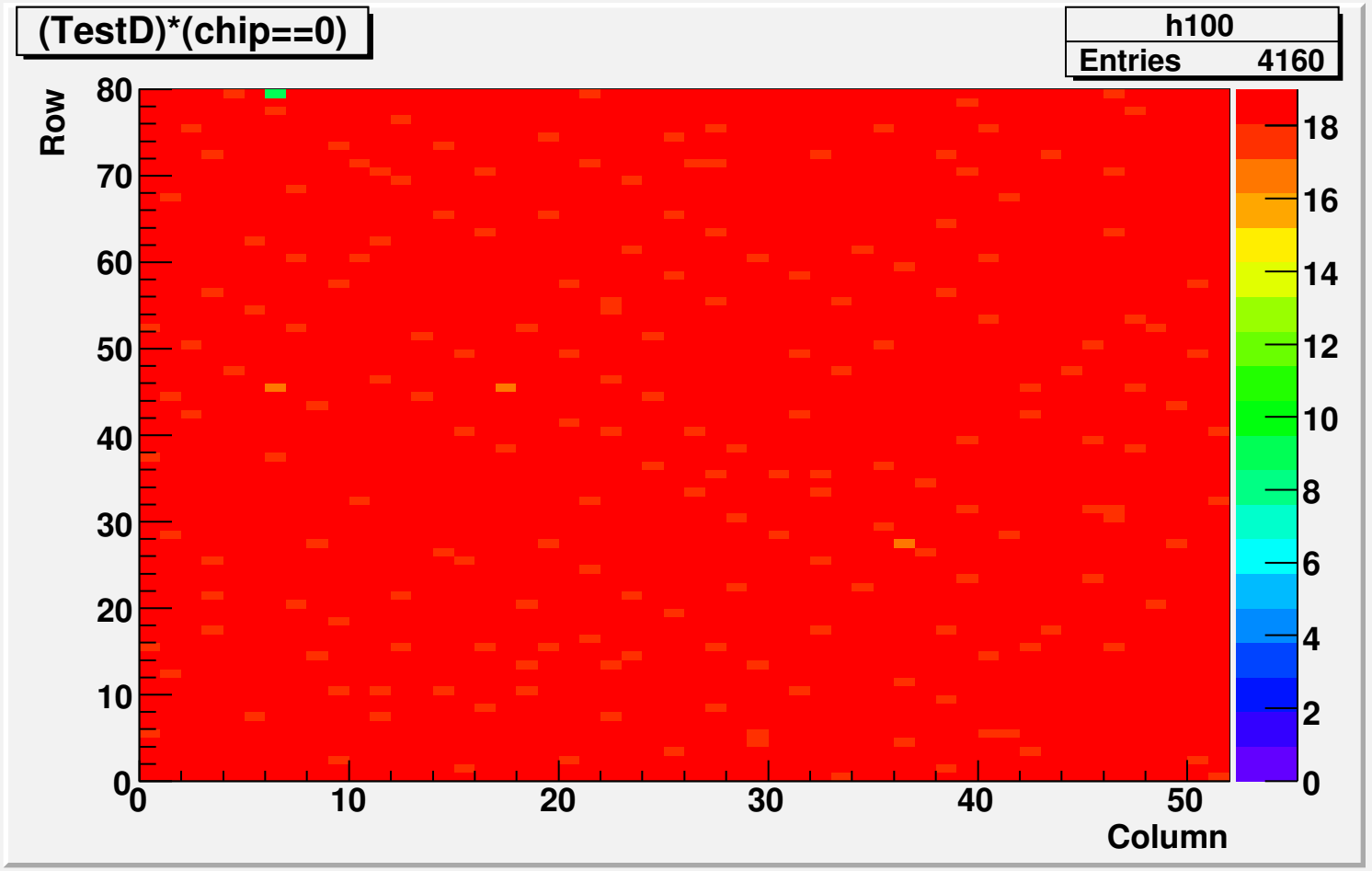

C0 Levels All Chips
addr2[0]
Entries 37345
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37345
10^2
10
1
1500
2000
2500
3000
3500


R1 Levels All Chips
addr4[0]
Entries 37345
10^2
10
1
1500
2000
2500
3000
3500


R0 Levels All Chips
addr5[0]
Entries 37345
10^2
10
1
1500
2000
2500
3000
3500


Chip # 0
Chip # 1


(TestD)*(chip==0)
h100
Entries 4160
Row
Column
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60
70
80
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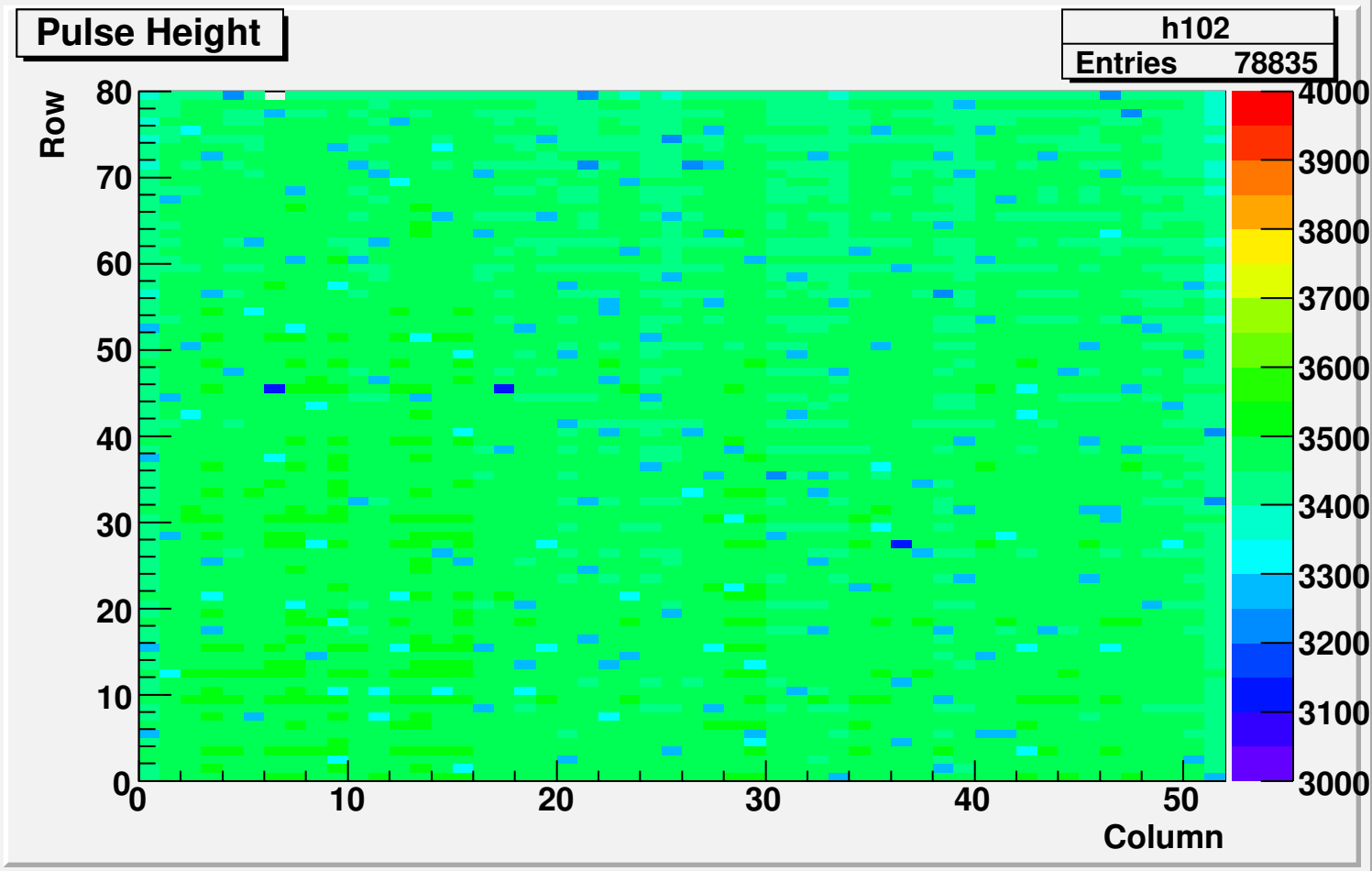
4
6
8
10
12
14
16
18


Pulse Height
h102
Entries 78835
Row
Column
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80
3000
3100
3200
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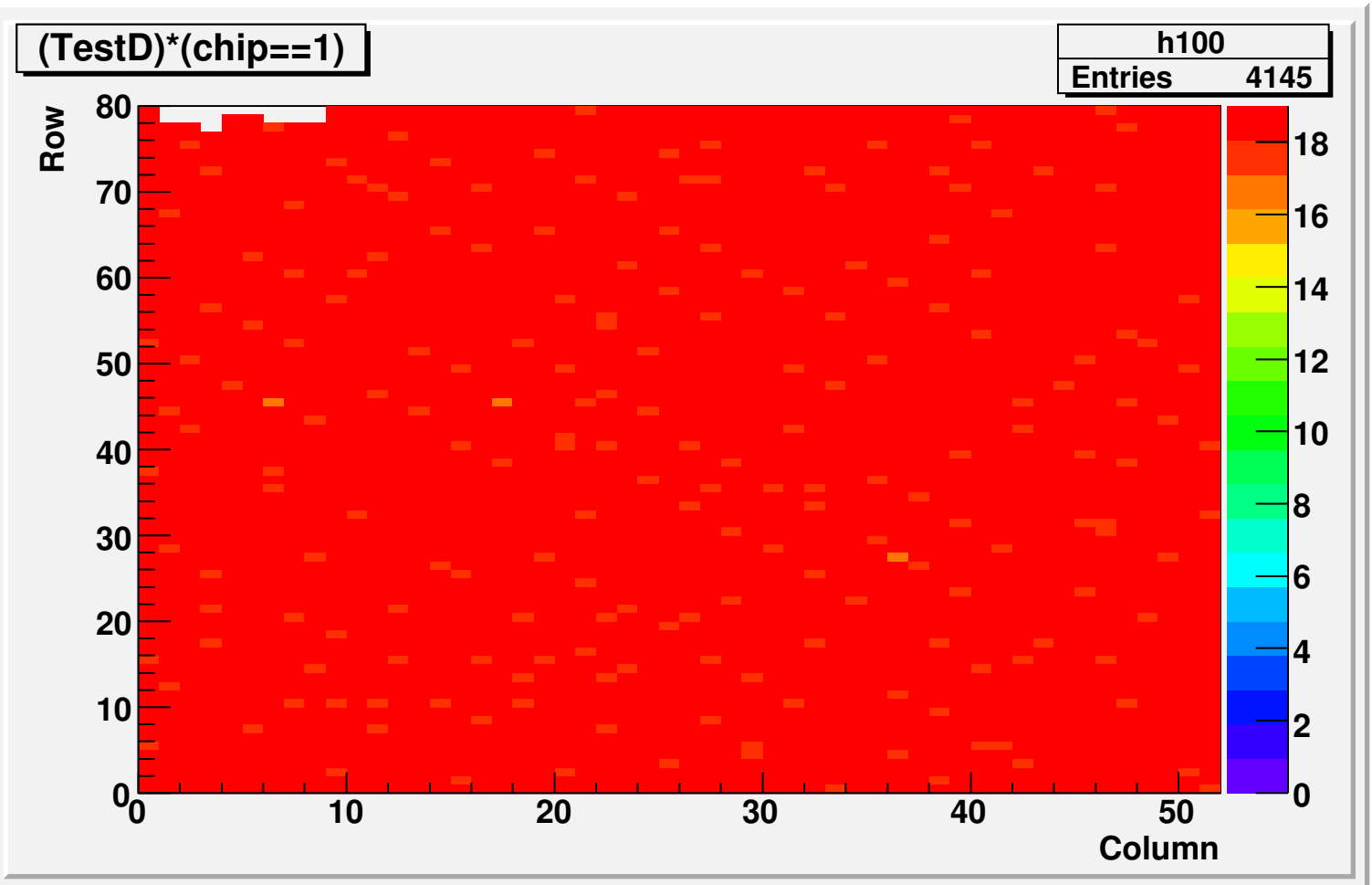
3600
3700
3800
3900
4000


(TestD)*(chip==1)
h100
Entries 4145
Row
Column
0
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30
40
50
60
70
80
0
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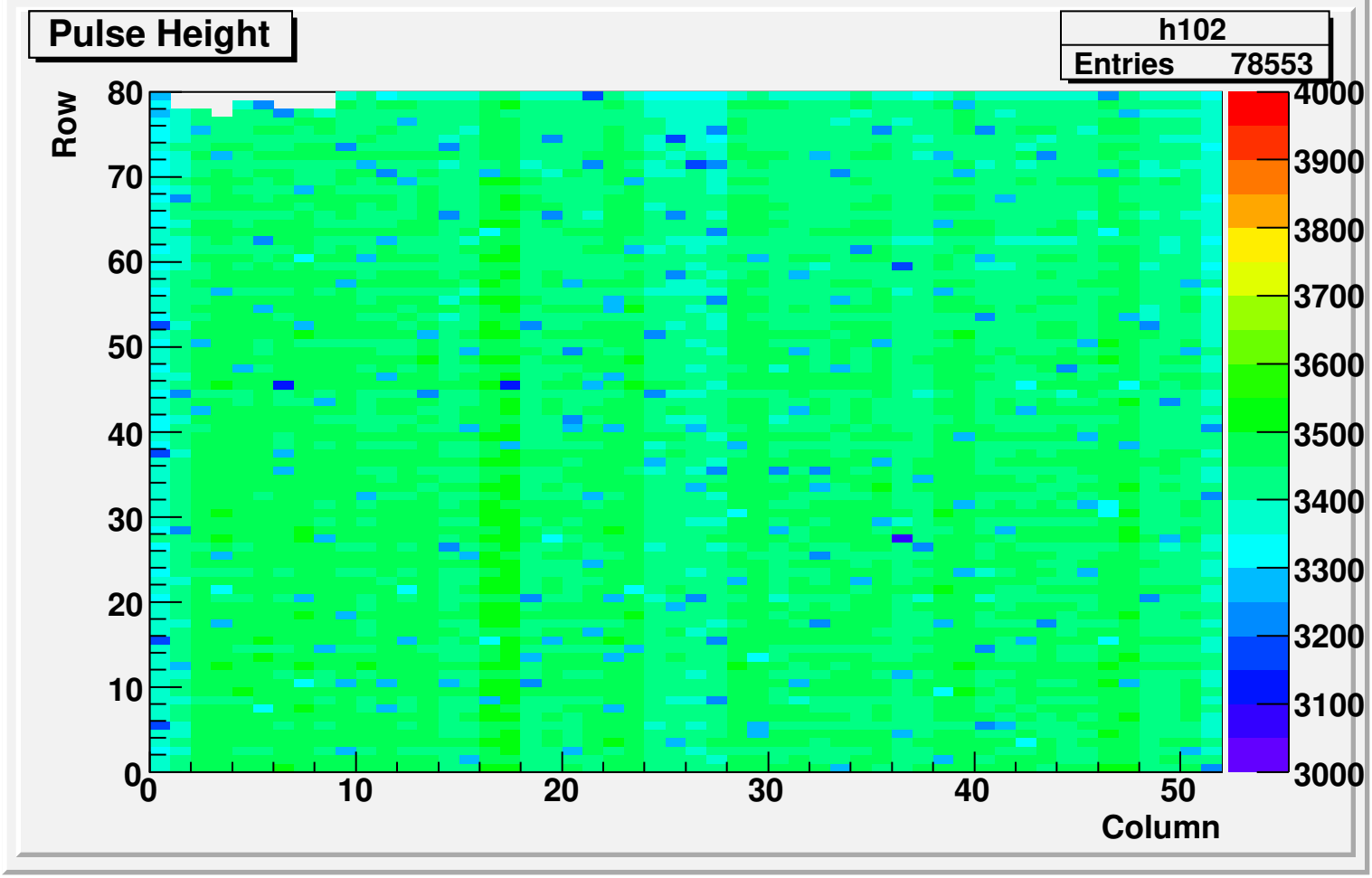
12
14
16
18


Pulse Height
h102
Entries 78553
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Column
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3800
3900
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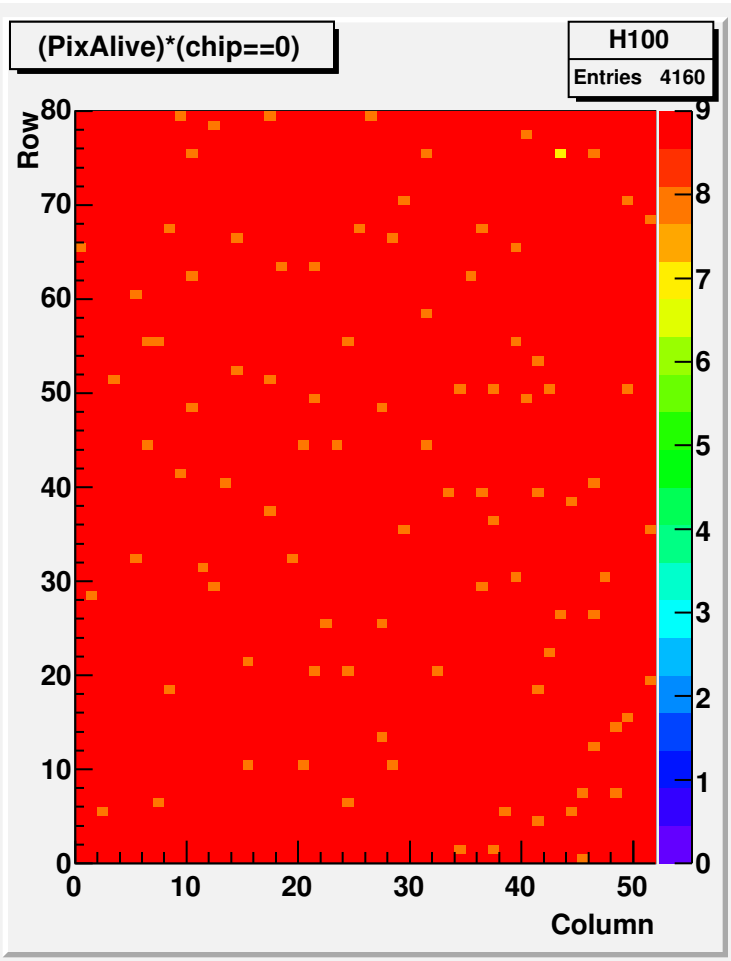
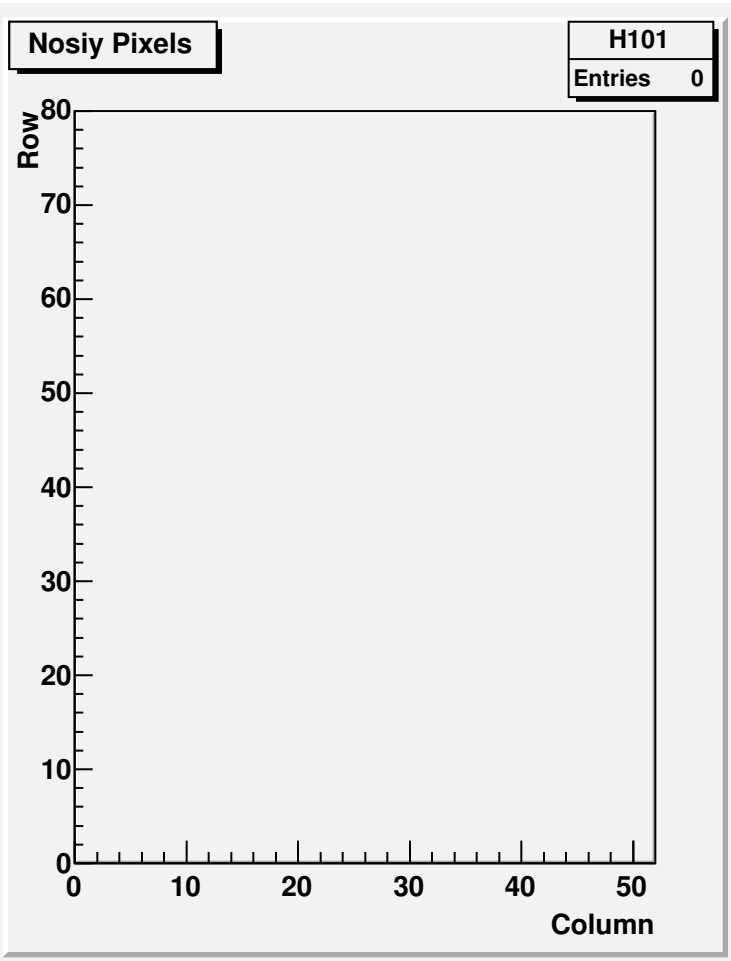
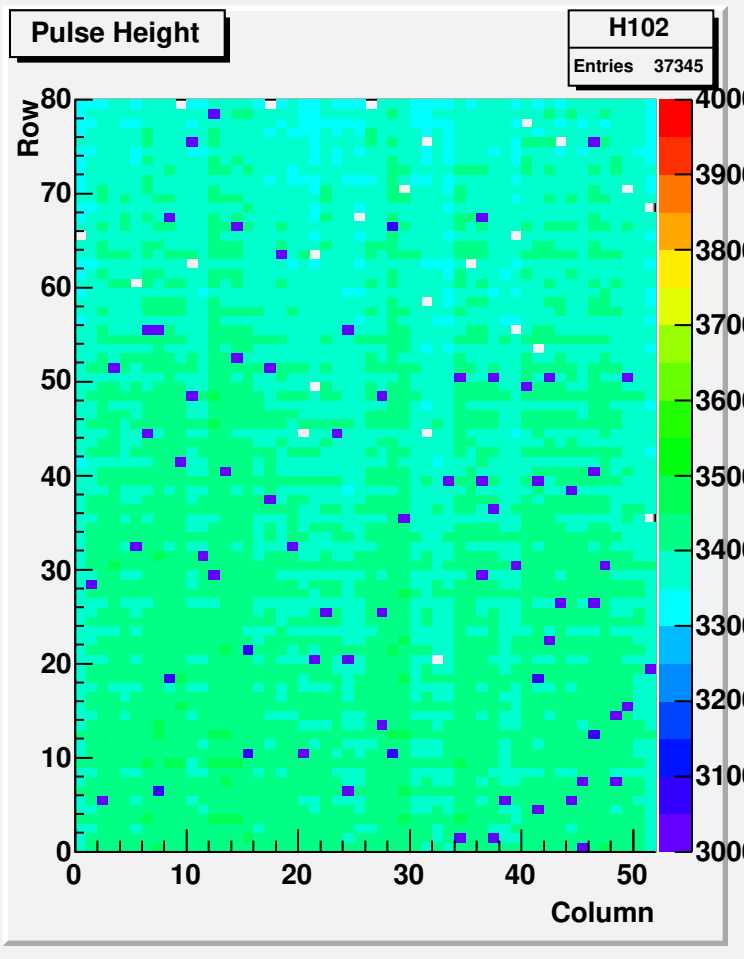
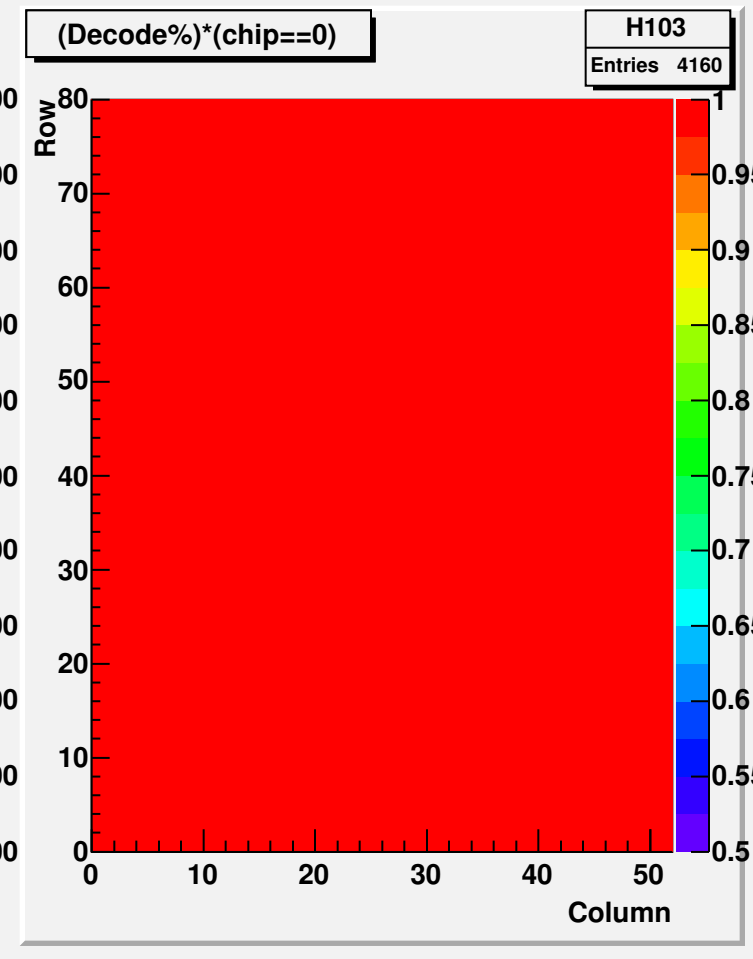
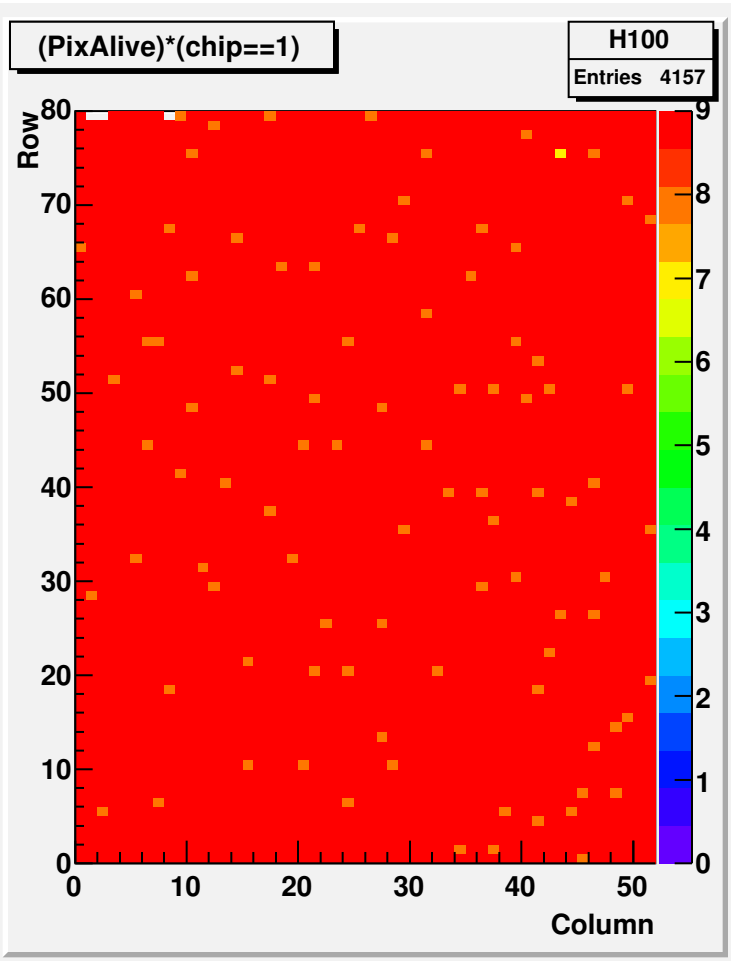
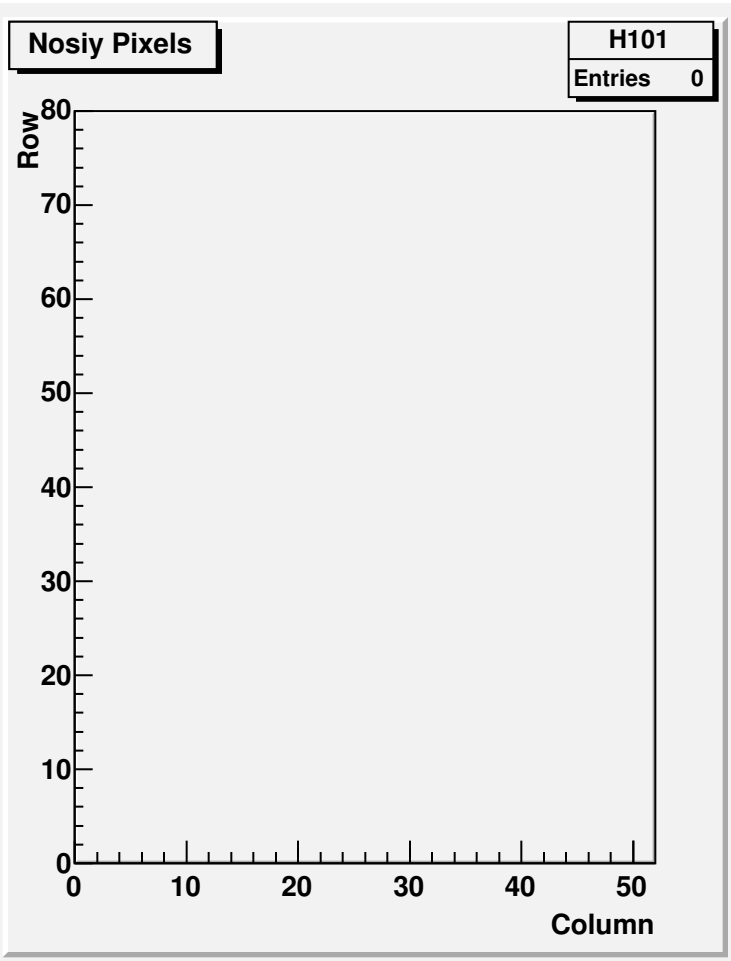

(PixAlive)*(chip==0)
H100
Entries 4160
Row
Column

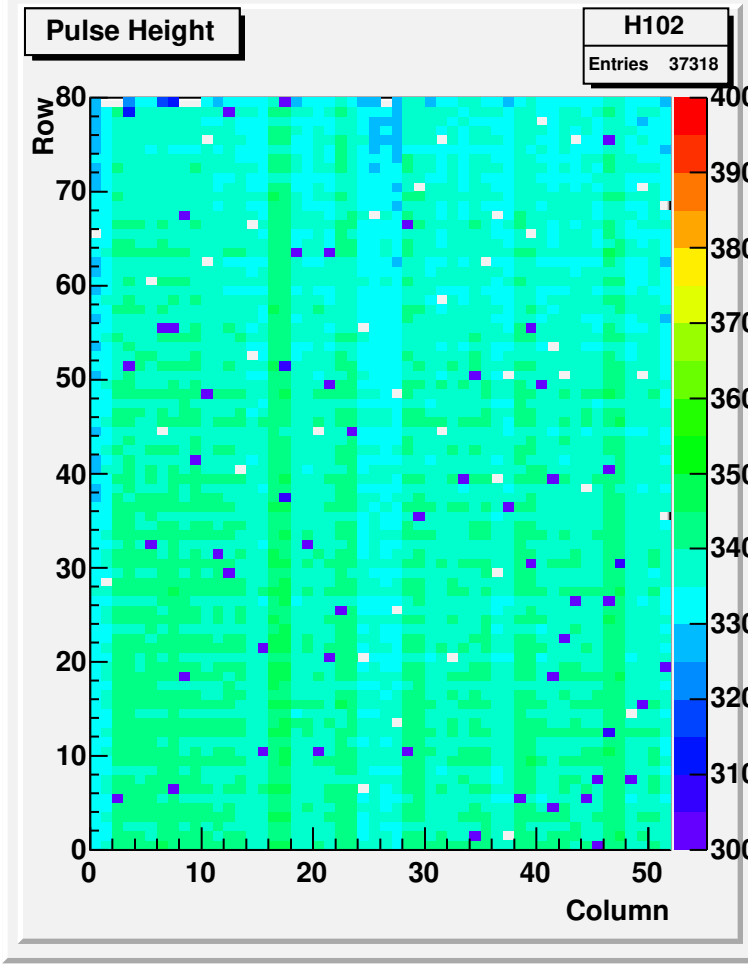

Nosiy Pixels
H101
Entries 0
Row
Column

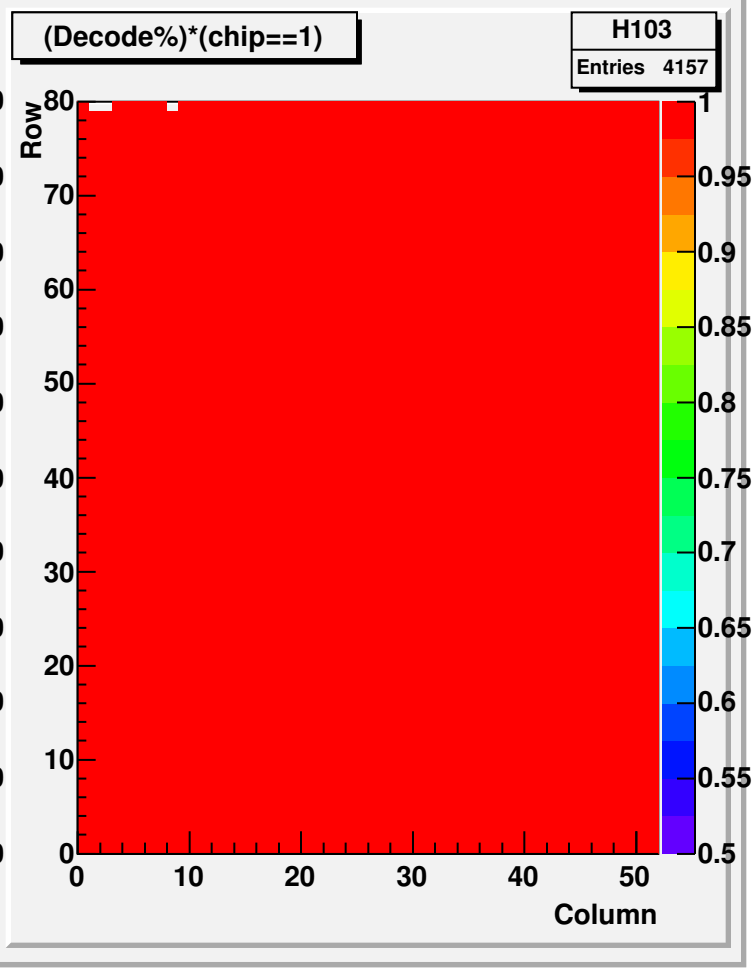

Pulse Height
H102
Entries 37345
Row
Column


(Decode%)*(chip==0)
H103
Entries 4160
Row
Column


(PixAlive)*(chip==1)
H100
Entries 4157
Row
Column


Nosiy Pixels
H101
Entries 0
Row
Column


Pulse Height
H102
Entries 37318
Row
Column


(Decode%)*(chip==1)
H103
Entries 4157
Row
Column