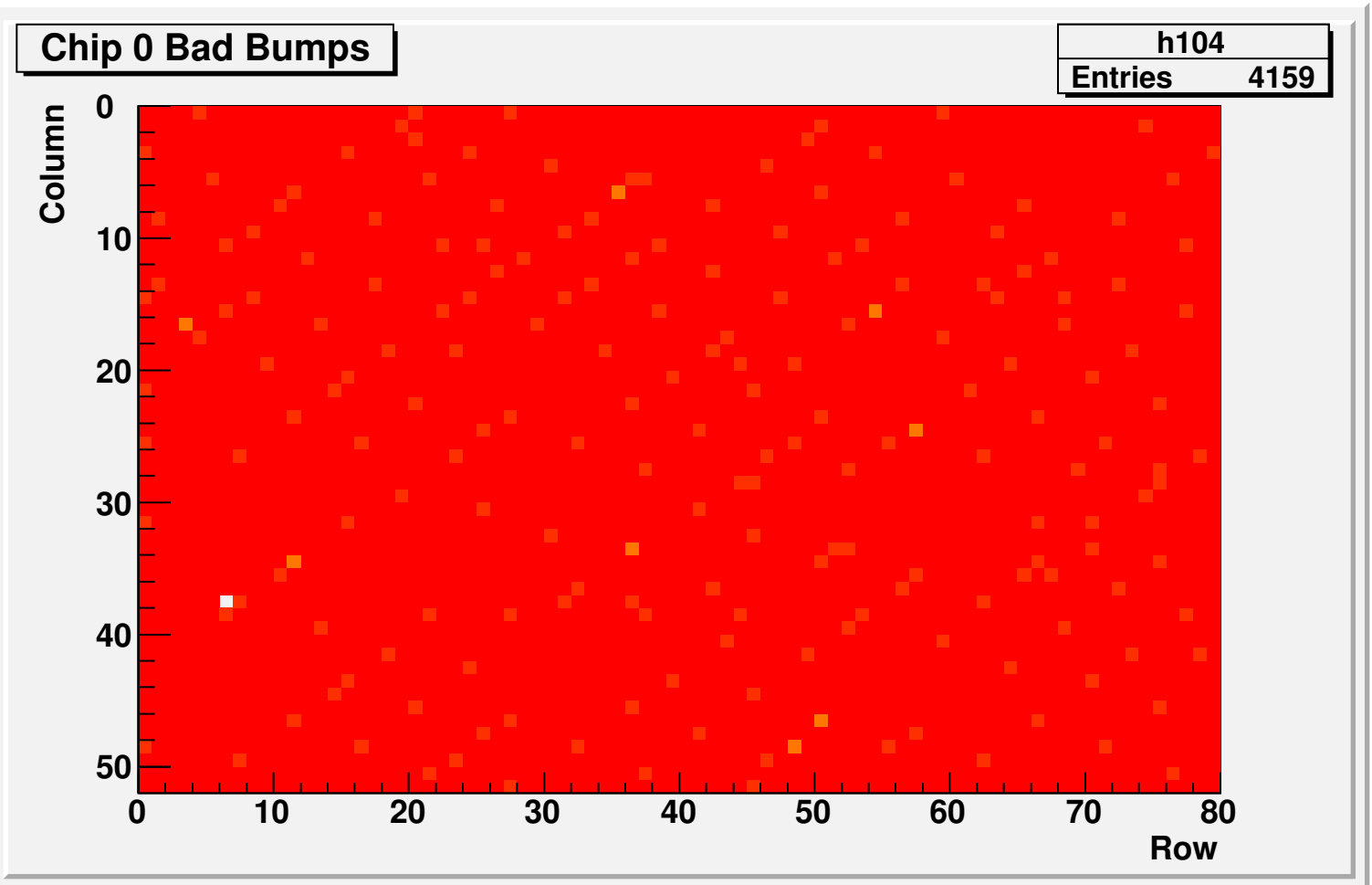
Chip 0 Bad Bumps
h104
Entries 4159
Column
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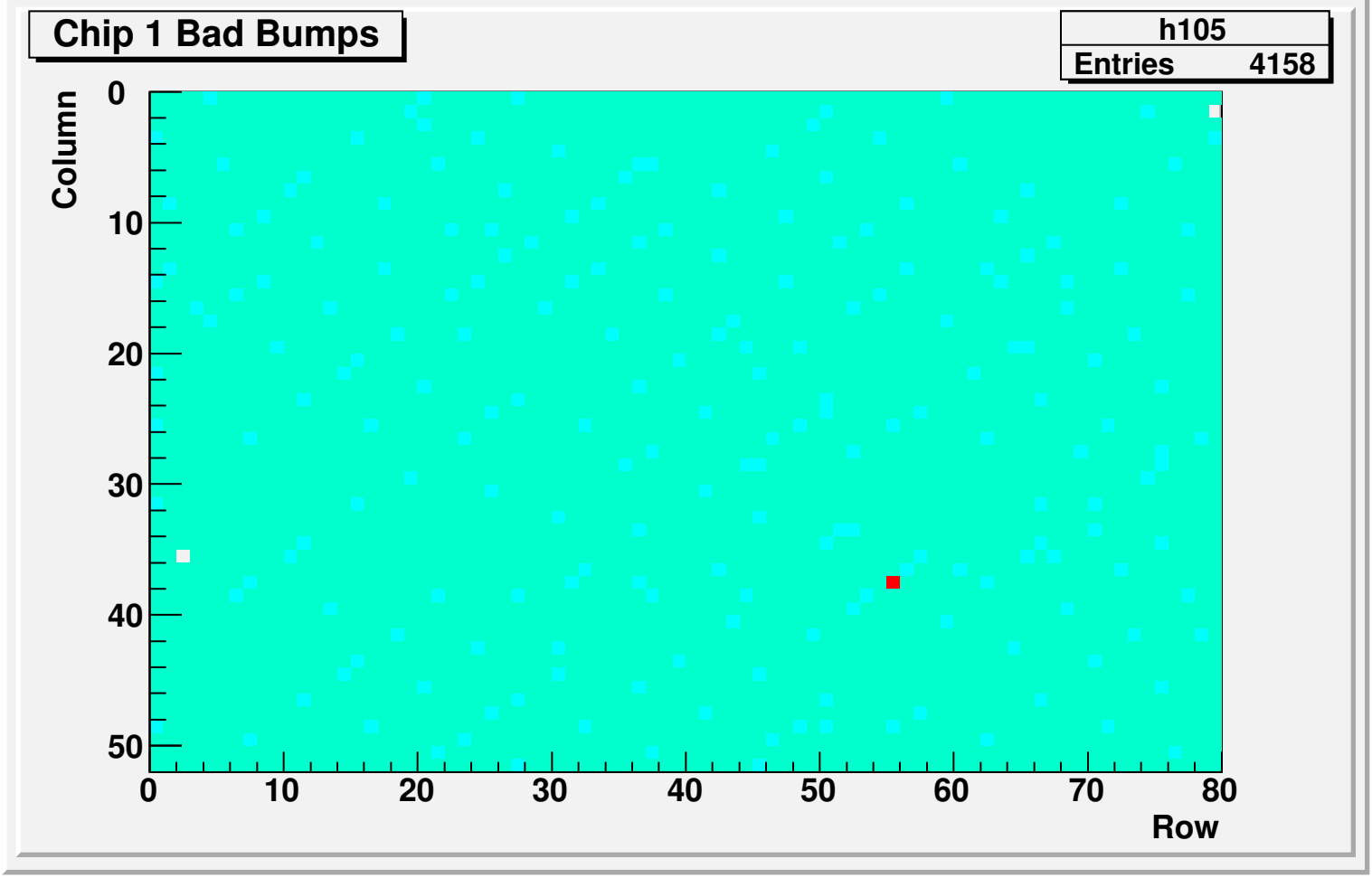
Row


Chip 1 Bad Bumps
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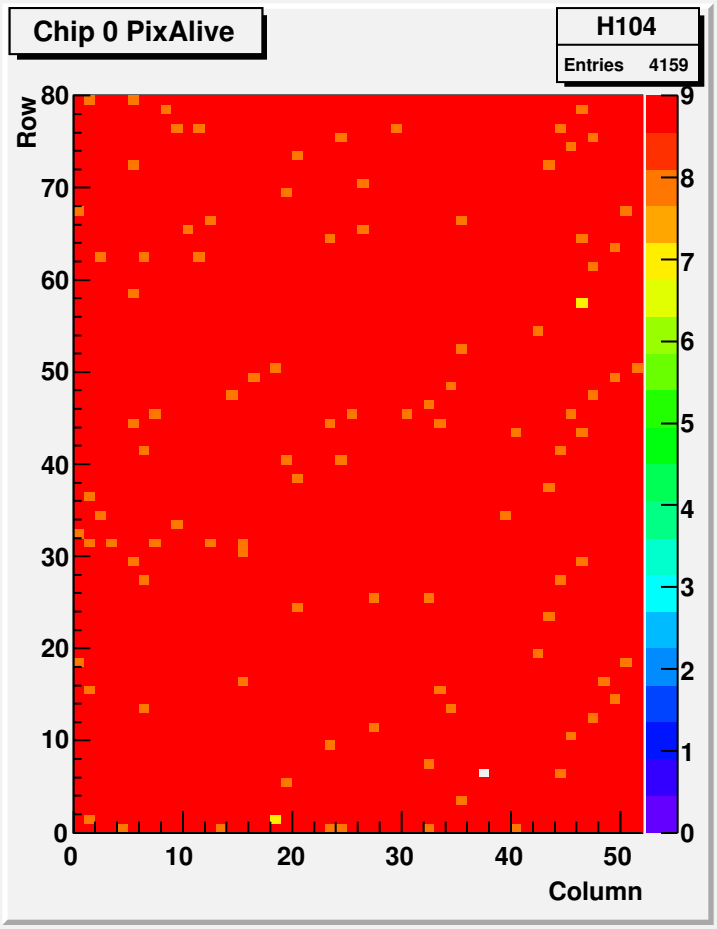
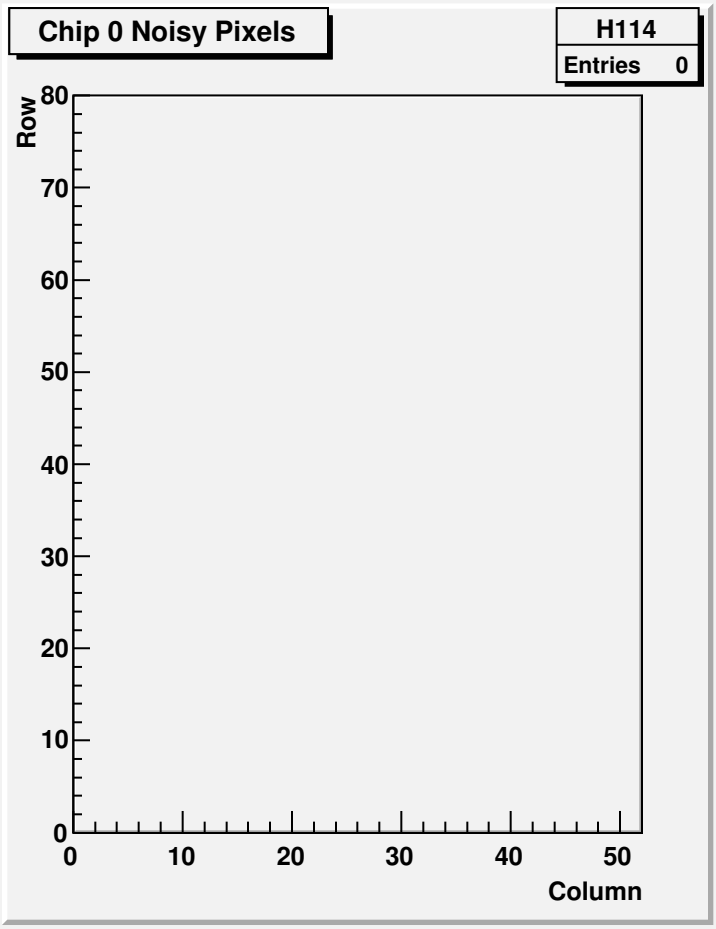
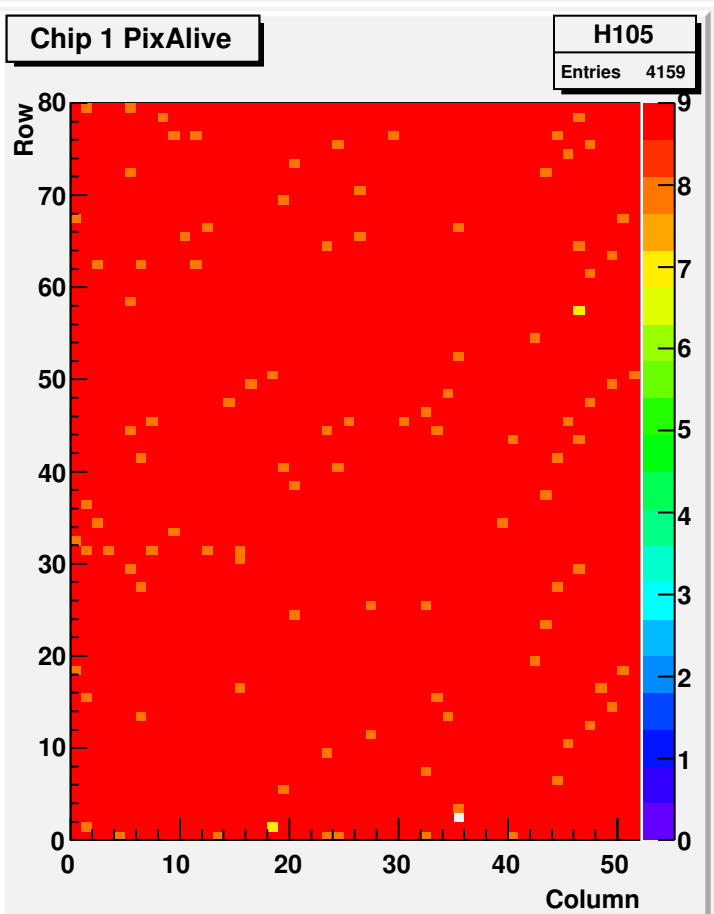
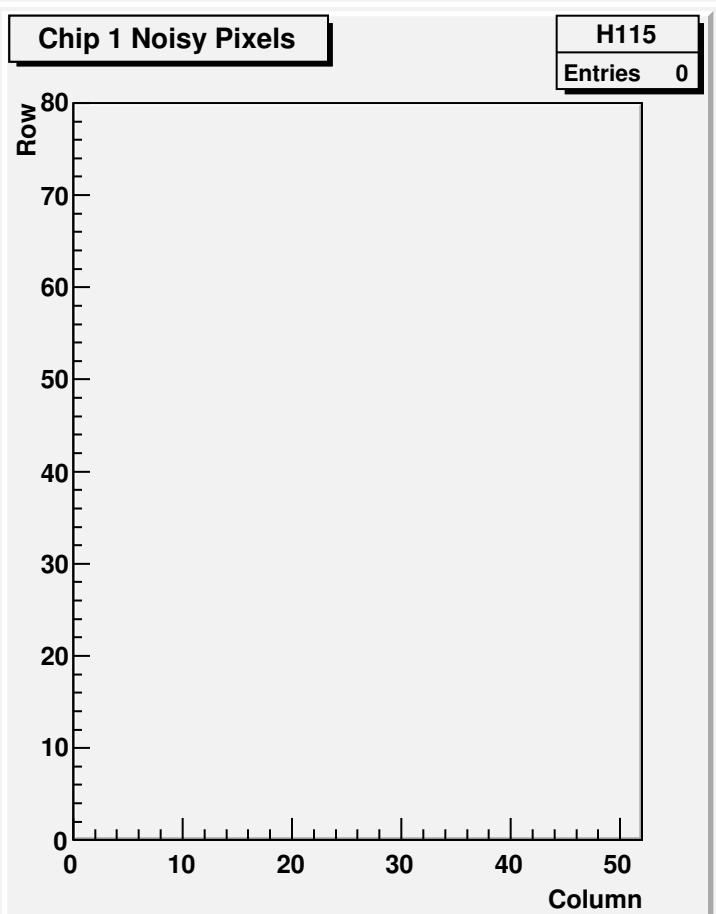

Chip 0 PixAlive
H104
Entries 4159
Row
Column


Chip 0 Noisy Pixels
H114
Entries 0
Row
Column


Chip 1 PixAlive
H105
Entries 4159
Row
Column


Chip 1 Noisy Pixels
H115
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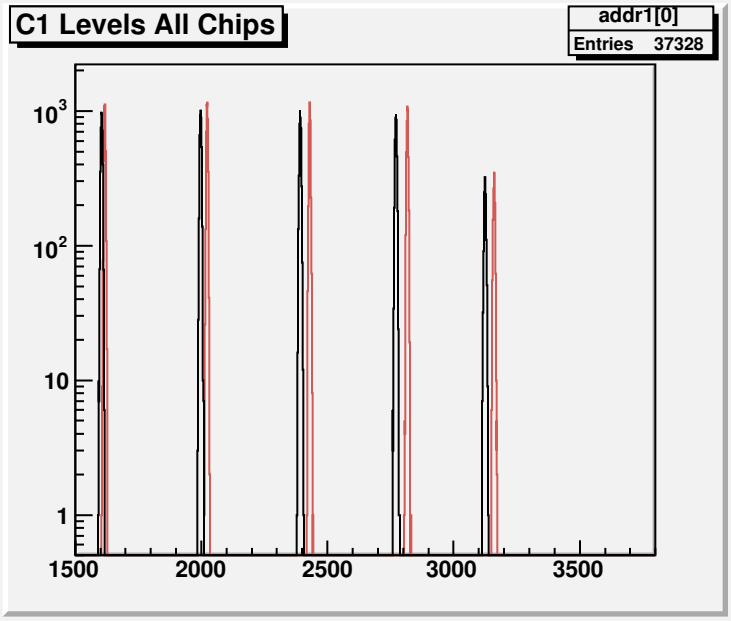
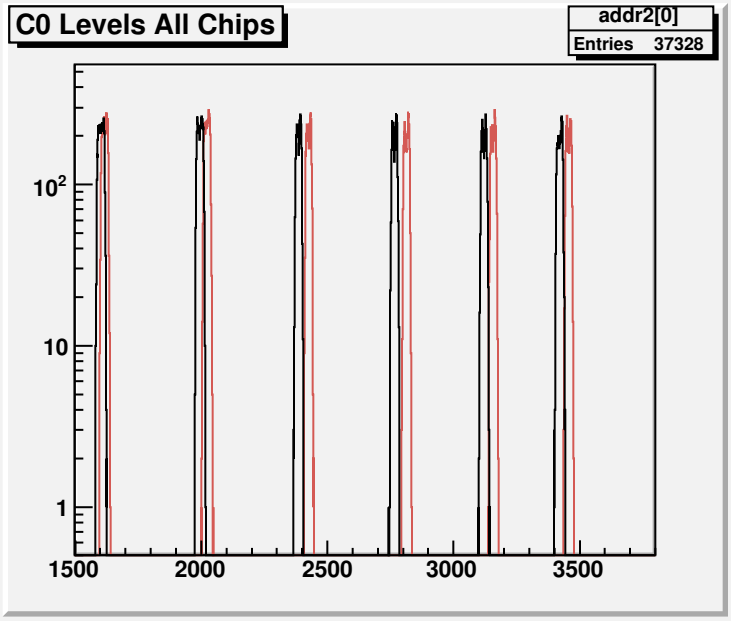
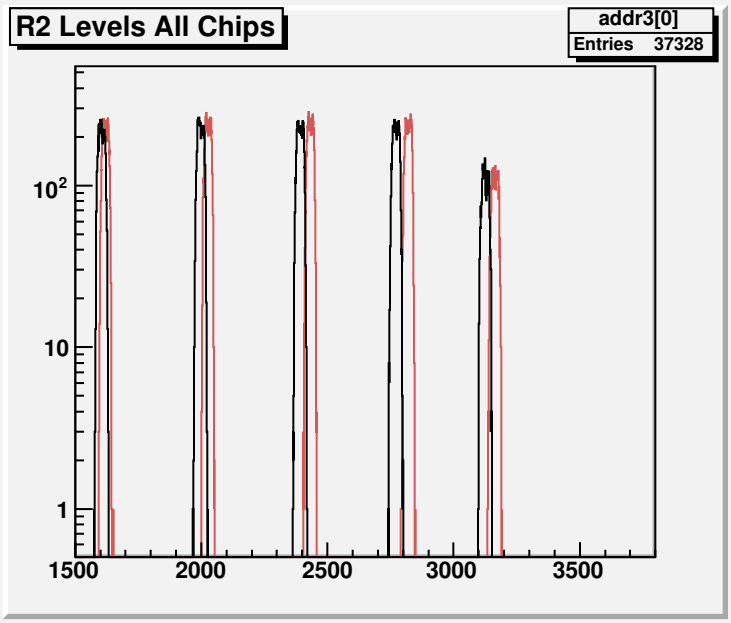
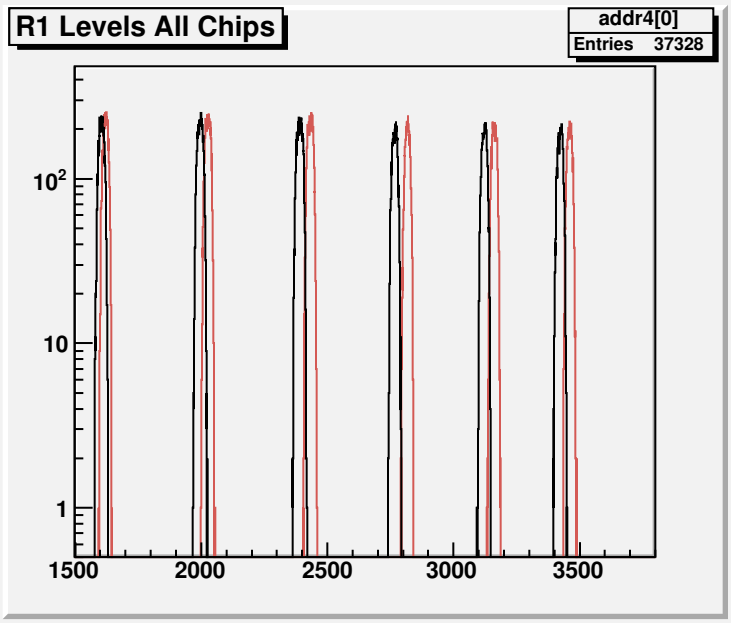
Row
Column


C1 Levels All Chips
addr1[0]
Entries 37328
10^3
10^2
10
1
1500
2000
2500
3000
3500


C0 Levels All Chips
addr2[0]
Entries 37328
10^2
10
1
1500
2000
2500
3000
3500


R2 Levels All Chips
addr3[0]
Entries 37328
10^2
10
1
1500
2000
2500
3000
3500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addr4[0]
Entries 37328
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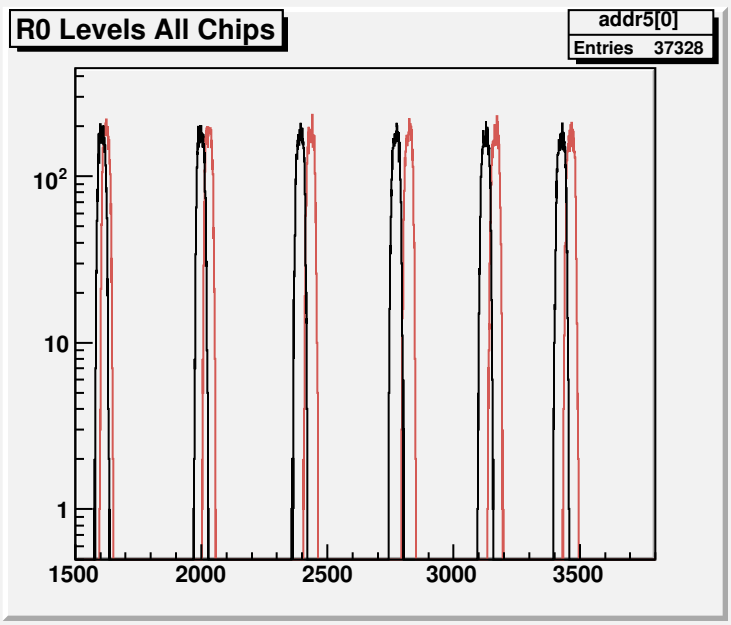
10
1
1500
2000
2500
3000
3500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Entries 37328
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2500
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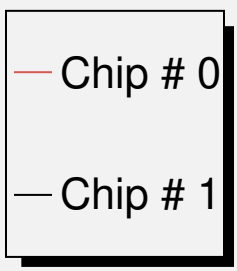
3500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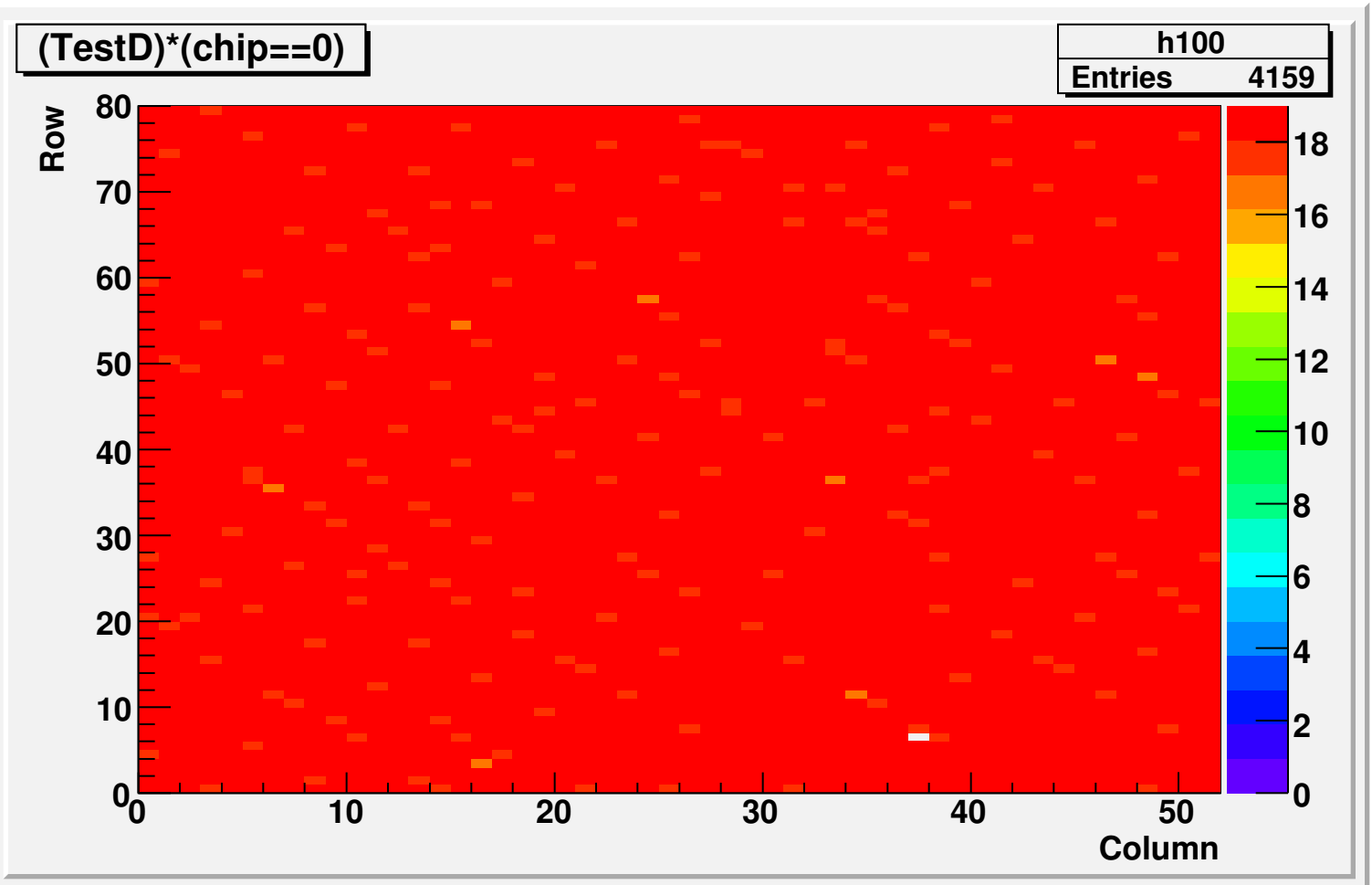
Chip # 1


(TestD)*(chip==0)
h100
Entries 4159
Row
Column
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70
80
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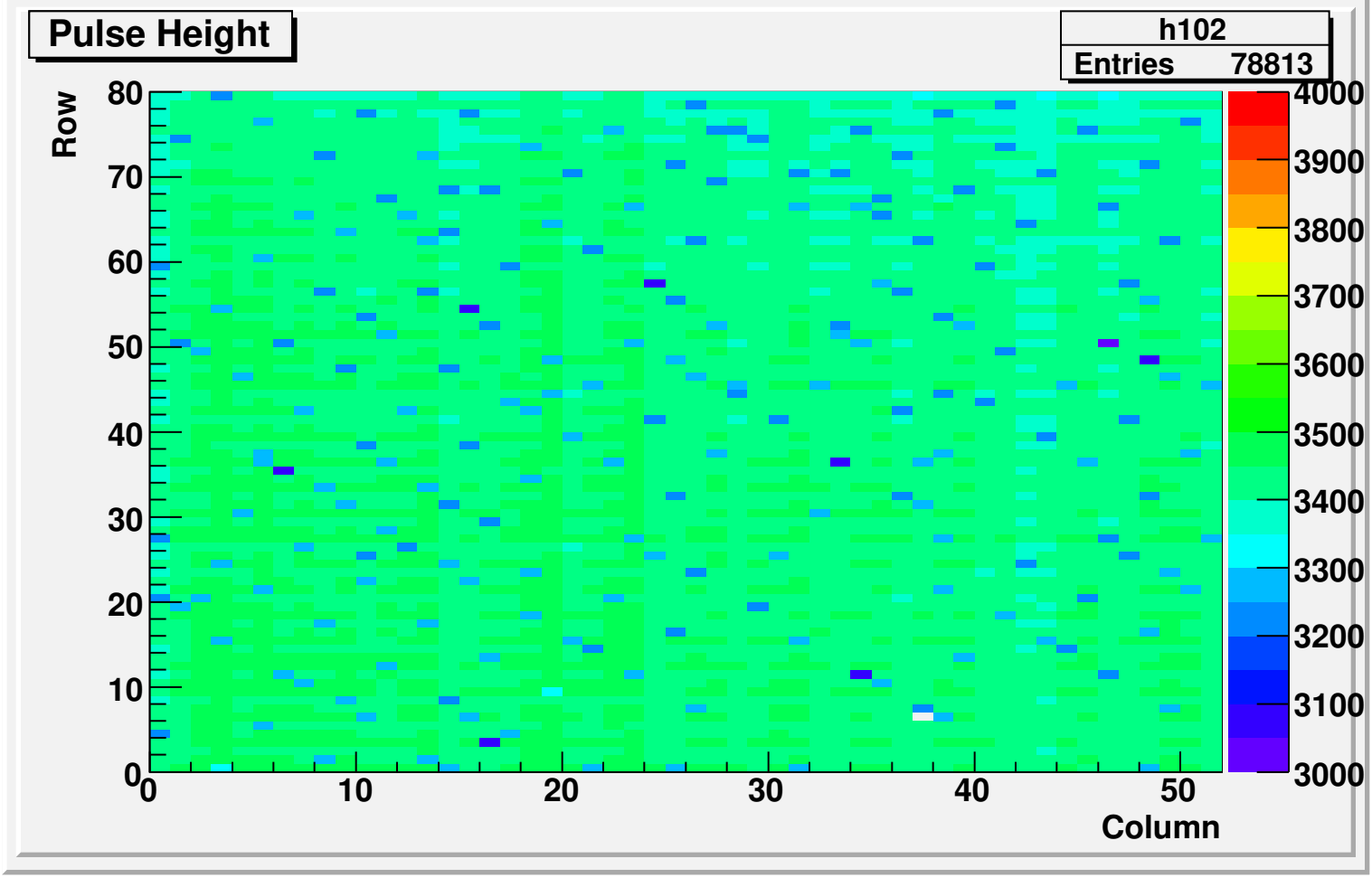
6
8
10
12
14
16
18


Pulse Height
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Entries 78813
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Column
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3900
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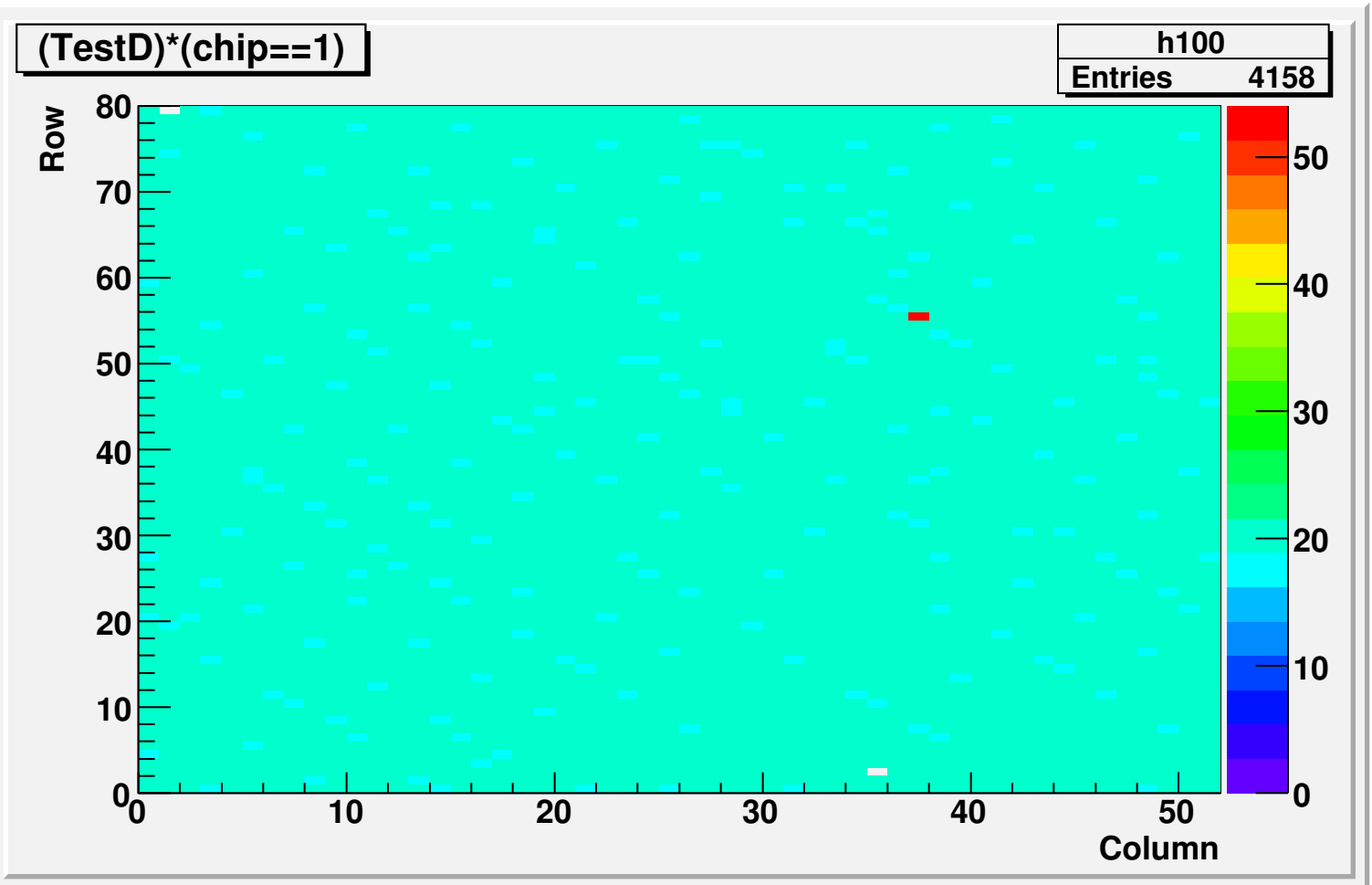
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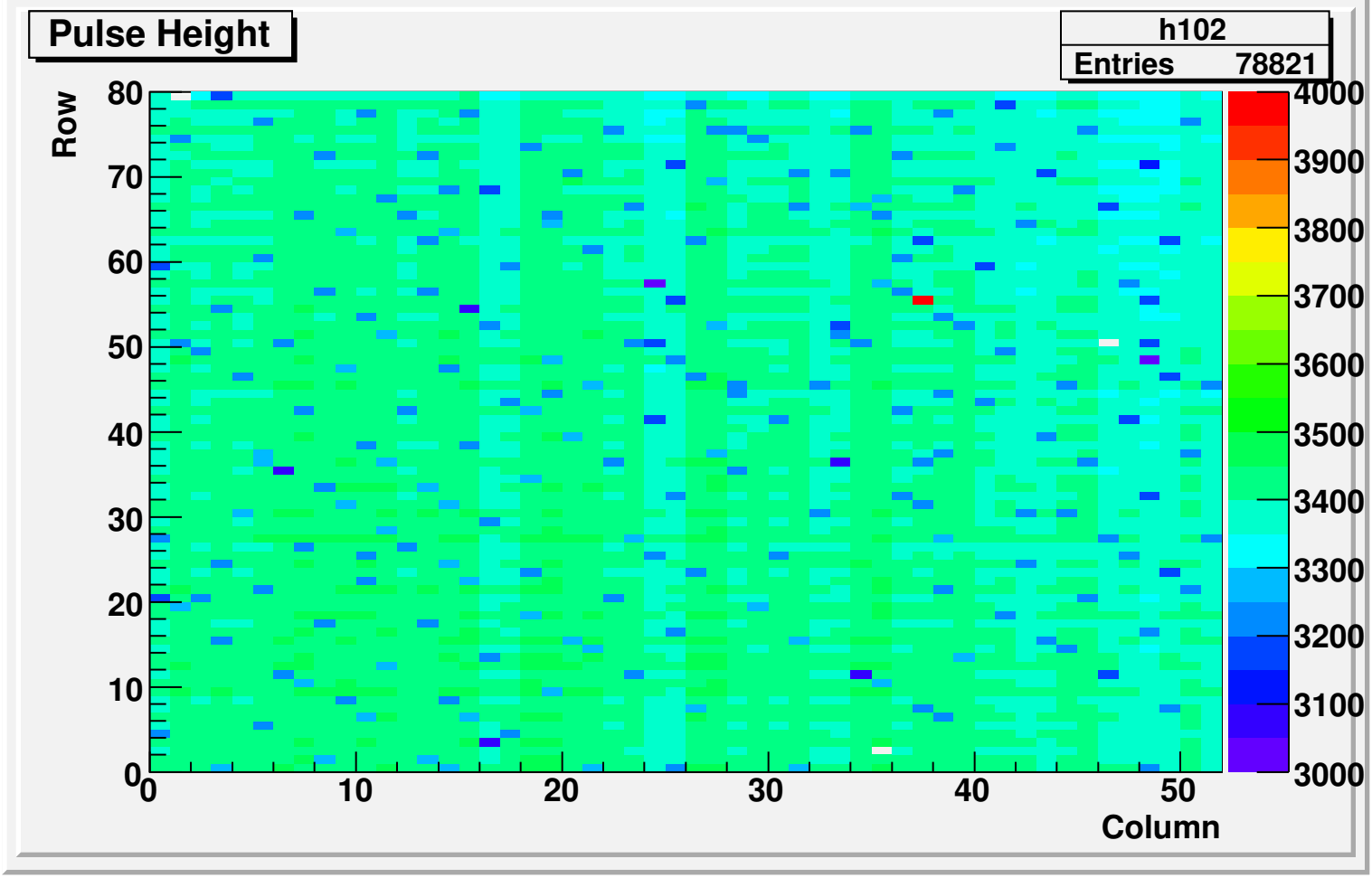
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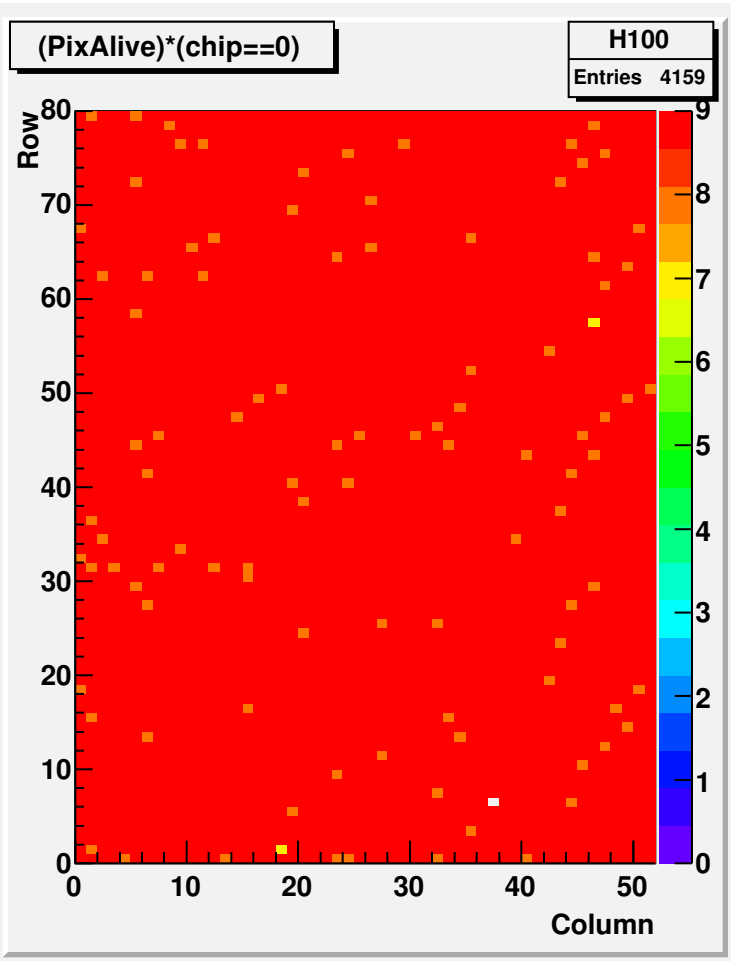
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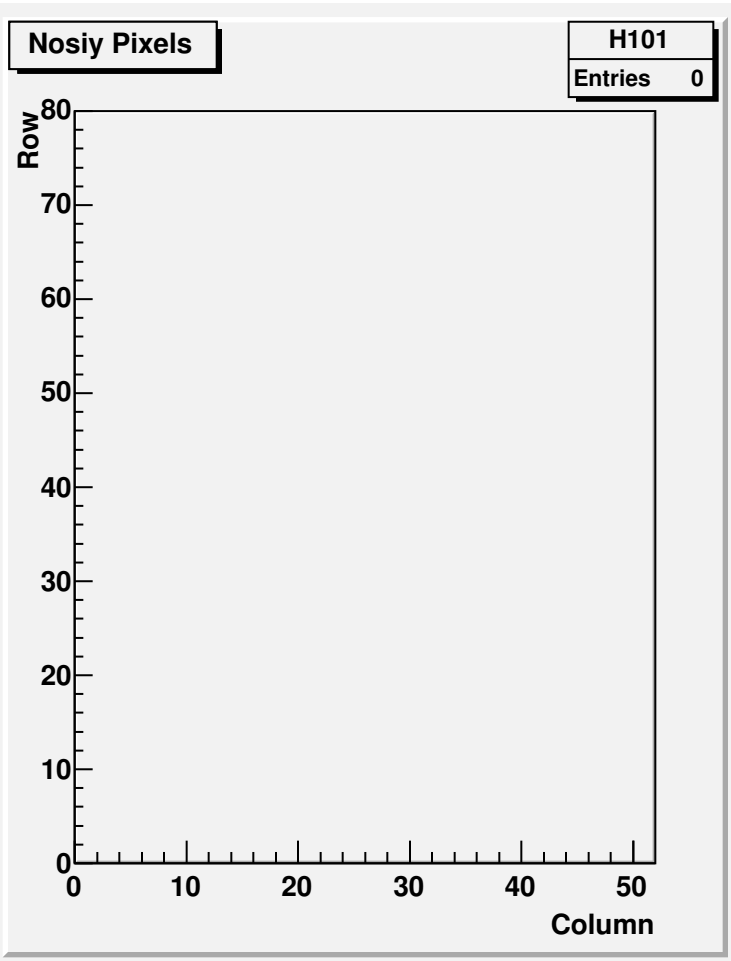
Column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H101
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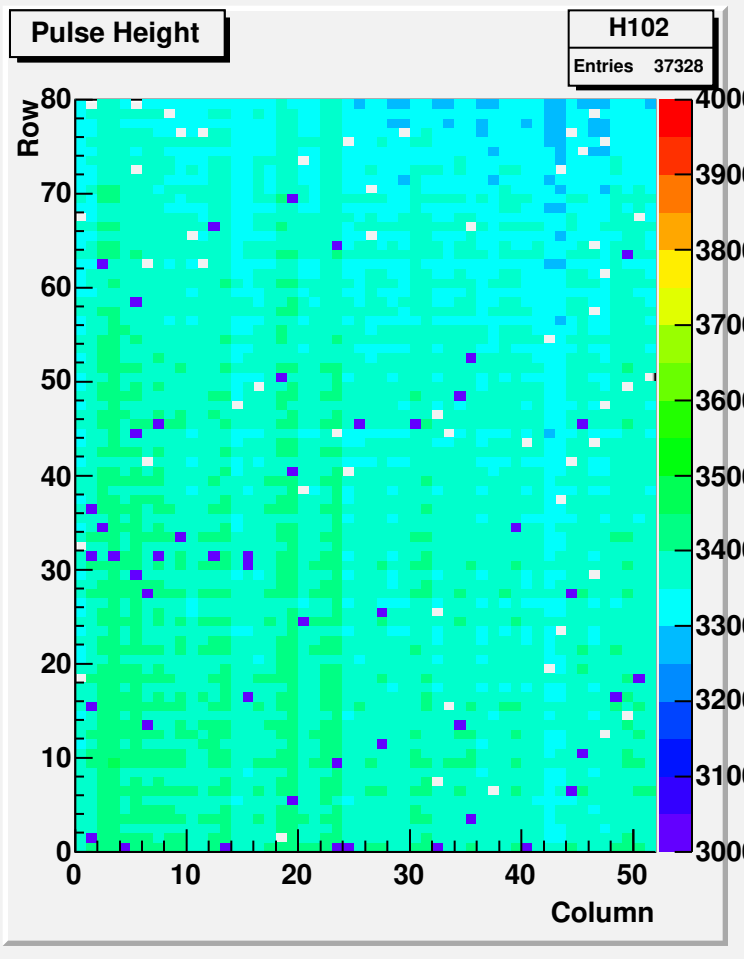
Row
Column


Pulse Height
H102
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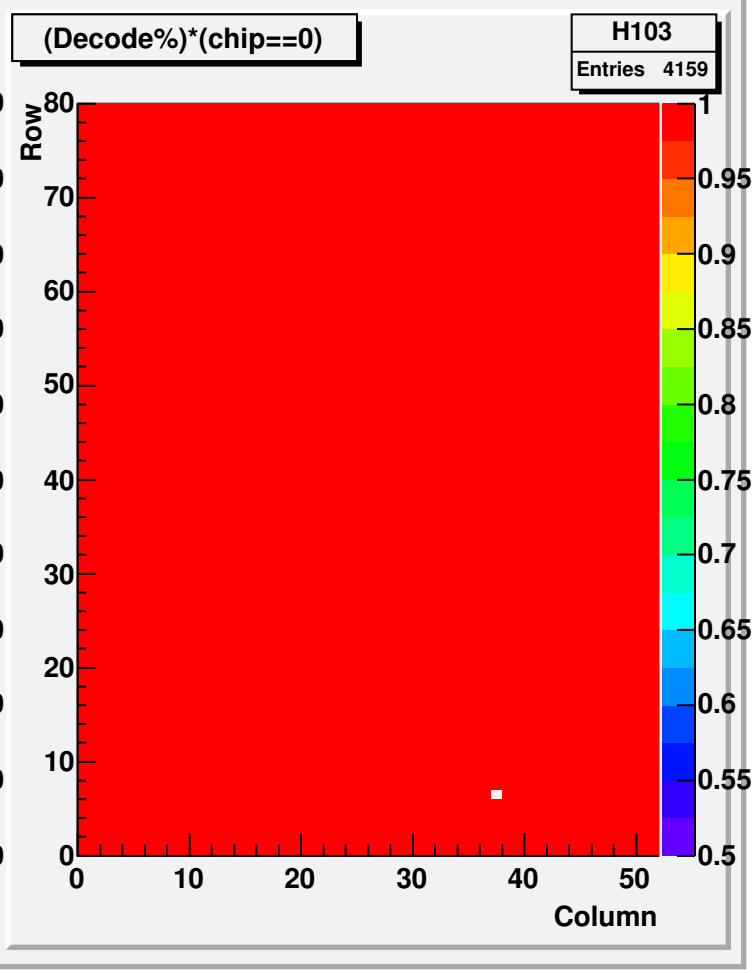
Column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H103
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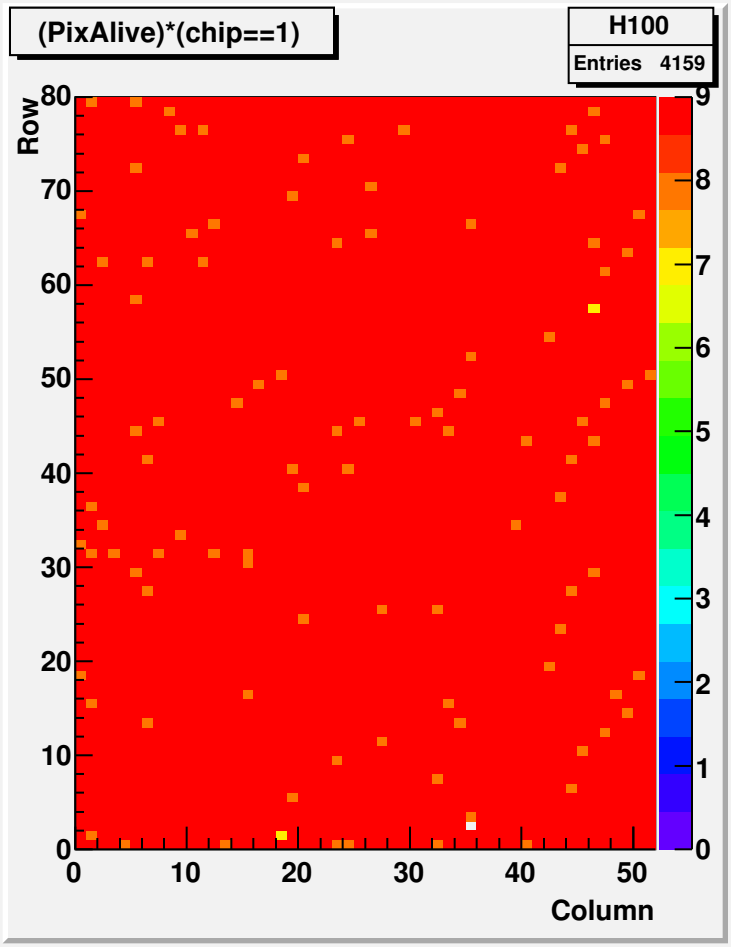
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H100
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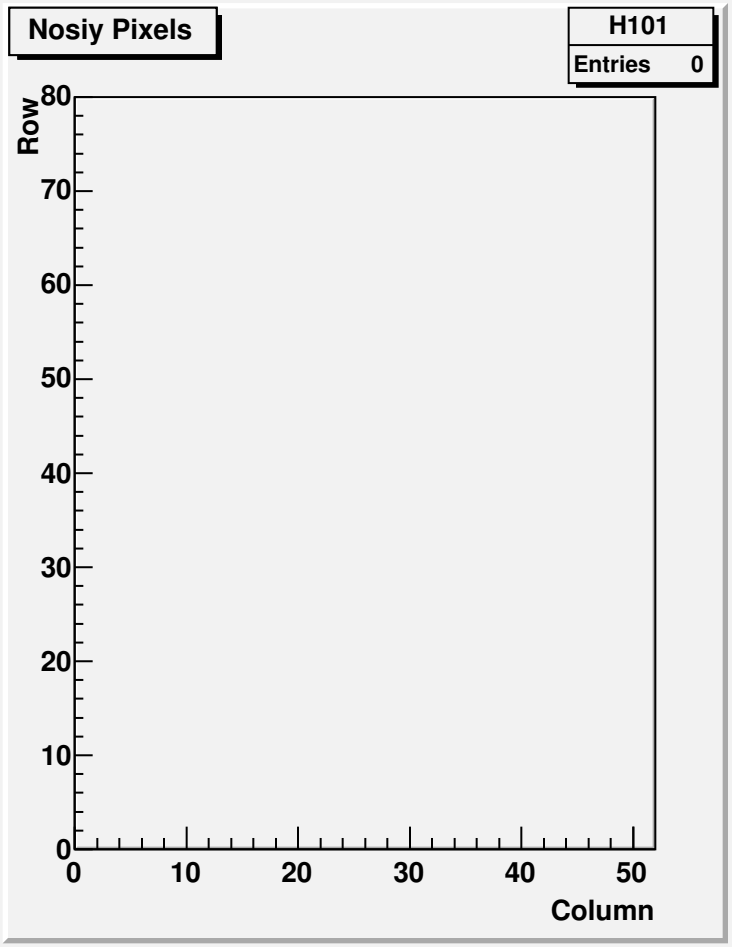
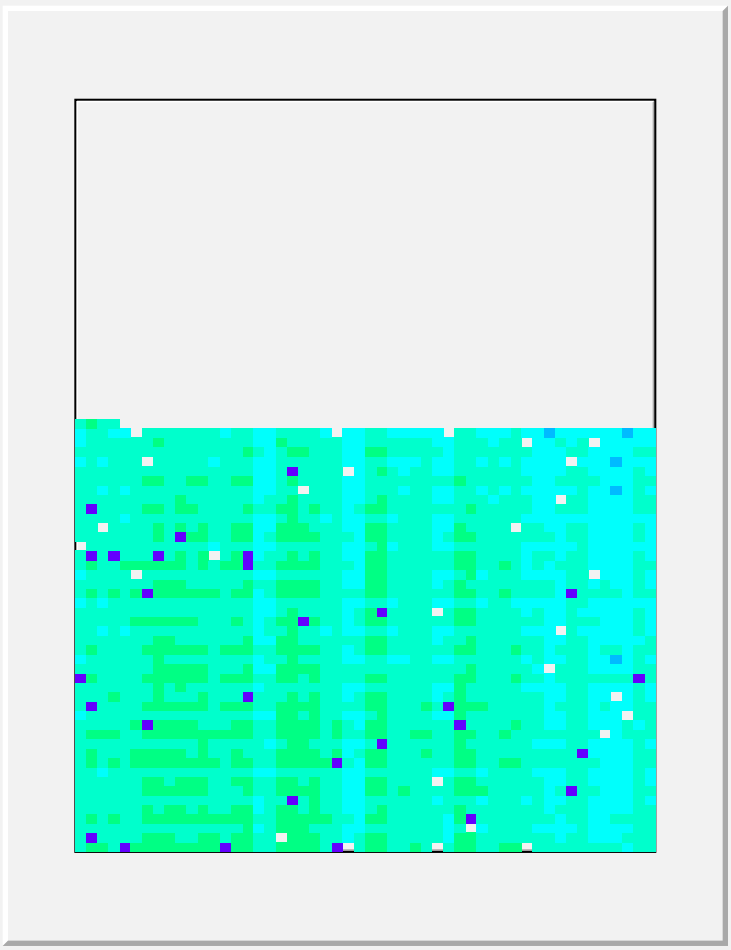
9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H101
Entries 0
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Column
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